

SUBHANGINI V

#49,SRI LAKSHMI VENKATESHWARA NILAYA•1ST CROSS • VEMANNA LAYOUT

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Professional Profile:

- Hardware Testing Professional with 1 year 9 months of experience at different stages of testing Electronic Warfare System LRUs and Embedded Hardware used in the Electronic Voting Machine. I am currently employed with Bharat Electronics, where I am responsible for testing of RF modules used in Satellite Systems.
- Motivated achiever with One year Nine months of experience in understanding client's needs especially in embedded hardware testing, worked in the DRDO and interacted with scientists & different team members to arrive at a globally acceptable product solution.

Key Skills:

- Hands on Experience in MIL1553B BUS interfaces.
- Application Programming – Matlab, C, Labview.
- Experience in Handling Functional Test Procedure (FTP), Qualification Test Procedure (QTP)
- Doing the Documentation as part of the testing like ATR, FTR preparation.
- Hands on Experience in Mass production of various electronics modules manufactured for the EVM.

Technical Experience: Familiar with a vast array of programming languages, concepts and technologies, including, but not limited to

- High Level Languages: Matlab.
- Electronic Drafting/Simulation Software's: OrCAD.
- OS: MS-DOS, LINUX.
- Communication Standards: RS232, RS485, MIL STD 1553B.
- Virtual Instrumentation Tools: Labview

Education:

- VSB Engineering College, Karur. B.E in Electronics and Communication with CGPA of 7.4, June 2018.

Overall Work Experience:

- Working as Trainee Engineer in Bharat Electronics Ltd, Bangalore (Jan 2023 to till date).
- Worked as Quality Assurance Engineer in Quest, Bangalore (Worked in Client location CASDIC, DRDO, Bangalore (Jan 2022 to Jan 2023)).

Professional Profile:

Project Summary#1- Electronic warfare system testing in the Rig

Team Size: 6.

Role: EW System testing with Software.

Duration: 1 Year.

Tools: Monitor, Dynamic Threat Simulator, Avionics Mudbugs Simulator.

Responsibilities:

EW System LRUs standalone mode testing

EW system Integrated mode testing

Threat Library creation, Antenna pattern creation, Static profile creation and evaluation using the Dynamic Threat Simulator

Dynamic scenario generation, Event definition and analysis for the scenario using the Dynamic Threat Simulator

Projects Summary #2 – Mass manufacturing Testing

Team Size: 3.

Role: Hardware Testing.

Duration: 8 months Tools:

TRNG, CAS.

Responsibilities:

Sensor Testing

Module Level testing

Final Assembly and Testing

ESS Testing for the EVM

EMI/EMC for the EVM

Roles & Responsibility:

- Handling System level Testing & Integration along with the Development Team.
- Production testing Line In-charge for the mass manufacturing of the various electronic assembly.
- Good team player.

Personal Details:

- Fathers name - Vellai Durai K
- Date of Birth - 02/09/1996.
- Passport No - R7403988
- Languages Known - English, Kannada and Tamil.
- Present address - #49, Sri Lakshmi Venkateshwara Nilaya,
1st Cross, Vemanna Layout,
Dodda BOMA Sandra,
Vidyaranyapura Post, Bangalore-97.
- Permanent address - Manaparai, Trichy.
Tamil Nadu-621306.

Date:

Place: Bangalore

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