

Address XOR translations:

Output 1 (JP1):

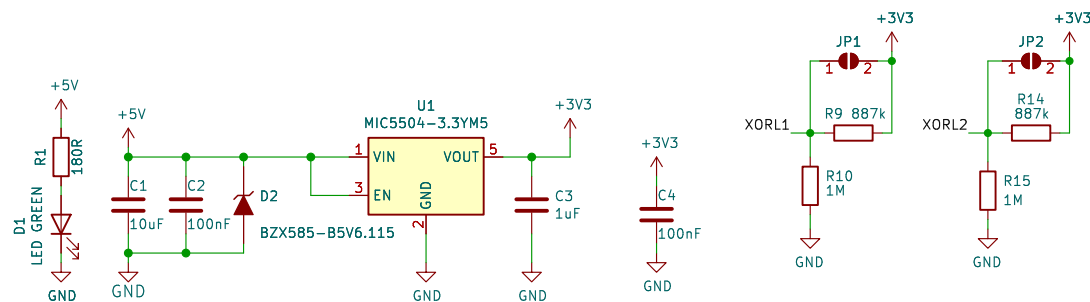
Open – 0x08

Shorted – 0x0f

Output 2 (JP2):

Open – 0x78

Shorted – 0x7f



TFI2CEXT01A

I2C address translator  
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