

CLOCK GENERATOR VVC – Quick Reference

For general information see UVVM Essential Mechanisms located in uvvm_vvc_framework/doc.

start_clock (VVCT, vvc_instance_idx, msg)

Example: start_clock(CLOCK_GENERATOR_VVCT, 1, "Start clock generator");

VVC ←√*

stop_clock (VVCT, vvc_instance_idx, msg)

Example: stop_clock(CLOCK_GENERATOR_VVCT, 1, "Stop clock generator");

clock_generator_vvc.vhd

set_clock_period (VVCT, vvc_instance_idx, clock_period, msg)

Example: set_clock_period(CLOCK_GENERATOR_VVCT, 1, 10 ns, "Change clock period to 10 ns");

set_clock_high_time(VVCT, vvc_instance_idx, clock_high_time, msg)

Example: set_clock_high_time(CLOCK_GENERATOR_VVCT, 1, 5 ns, "Change duty cycle to 50%");

CLOCK GENERATOR VVC Configuration record 'vvc_config' -- accessible via

shared_clock_generator_vvc_config

Record element	Туре	C_CLOCK_GENERATOR_VVC_CONFIG_DEFAULT
inter_bfm_delay	t_inter_bfm_delay	C_CLOCK_GENERATOR_INTER_BFM_DELAY_DEFAULT
cmd_queue_count_max	natural	C_CMD_QUEUE_COUNT_MAX
cmd_queue_count_threshold	natural	C_CMD_QUEUE_COUNT_THRESHOLD
cmd_queue_count_threshold_severity	t_alert_level	C_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY
result_queue_count_max	natural	C_RESULT_QUEUE_COUNT_MAX
result_queue_count_threshold	natural	C_RESULT_QUEUE_COUNT_THRESHOLD
result_queue_count_threshold_severit	y t_alert_level	C_RESULT_QUEUE_COUNT_THERSHOLD_SEVERITY
bfm_config	t_bfm_config	C_CLOCK_GENERATOR_CONFIG_DEFAULT
msg_id_panel	t_msg_id_panel	C_VVC_MSG_ID_PANEL_DEFAULT

Clock Generator VVC Status record signal 'vvc_status' -- accessible via

shared_clock_generator_vvc_status

Record element	Туре	
current_cmd_idx	natural	
previous_cmd_idx	natural	
pending_cmd_cnt	natural	

Common VVC procedures applicable for this VVC

- See UVVM Methods QuickRef for details.

await_completion()

enable_log_msg()

disable_log_msg()

fetch_result()

flush_command_queue()

terminate_current_command()

terminate_all_commands()

insert_delay()

get_last_received_cmd_idx()



Copyright © 2017 by Bitvis AS. All rights reserved.



VVC target parameters

Name	Туре	Example(s)	Description
VVCT	t_vvc_target_record	CLOCK_GENERATOR_VVCT	VVC target type compiled into each VVC in order to differentiate between VVCs.
vvc_instance_idx	integer	1	Instance number of the VVC

VVC functional parameters

Name	Type	Example(s)	Description
clock_period	time	10 ns	Clock period
clock_high_time	time	5 ns	Time of the clock cycle that is '1'. Value have to be less than clock_period.
msg	string	"Read from DUT"	A custom message to be appended in the log/alert

VVC entity signals

Name	Туре	Direction	Description	
clk	std_logic	Output	VVC Clock signal	

VVC entity generic constants

Name	Туре	Default	Description
GC_INSTANCE_IDX	natural	1	Instance number to assign the VVC
GC_CLOCK_GENERATOR_CONFIG	t_bfm_config	C_CLOCK_GENERATOR_CONFIG_DEFAULT	Configuration for the Clock Generator.
GC_CMD_QUEUE_COUNT_MAX	natural	1000	Absolute maximum number of commands in the VVC command
			queue
GC_CMD_QUEUE_COUNT_THRESHOLD	natural	950	An alert will be generated when reaching this threshold to indicate
			that the command queue is almost full. The queue will still accept
			new commands until it reaches C_CMD_QUEUE_COUNT_MAX.
GC_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY	t_alert_level	WARNING	Alert severity which will be used when command queue reaches
			GC_CMD_QUEUE_COUNT_THRESHOLD.
GC_RESULT_QUEUE_COUNT_MAX	natural	1000	Maximum number of unfetched results before result_queue is full.
GC_RESULT_QUEUE_COUNT_THRESHOLD	natural	950	An alert with severity 'result_queue_count_threshold_severity' will
			be issued if result queue exceeds this count. Used for early
			warning if result queue is almost full. Will be ignored if set to 0.
GC_RESULT_QUEUE_COUNT_THRESHOLD_SEVERITY	t_alert_level	WARNING	Severity of alert to be initiated if exceeding
			result_queue_count_threshold



VVC details

All VVC procedures are defined in vvc_methods_pkg (dedicated this VVC), and uvvm_vvc_framework.td_vvc_framework_common_methods_pkg (common VVC procedures) It is also possible to send a multicast to all instances of a VVC with ALL_INSTANCES as parameter for vvc_instance_idx.

1 VVC procedure details and examples

Procedure	Description				
start_clock()	start_clock(VVCT, vvc_instance_idx, msg)				
	This procedure adds a start_clock command to the Clock Generator VVCs executor queue, which will run as soon as all preceding commands have completed. When the start_clock command is scheduled to run, the executor activates the clock generator process in the VVC.				
	<pre>Example: start_clock(CLOCK_GENERATOR_VVCT, 1, "Start clock generator");</pre>				
stop_clock()	stop_clock (VVCT, vvc_instance_idx, msg)				
	This procedure adds a stop_clock command to the Clock Generator VVCs executor queue, which will run as soon as all preceding commands have completed. When the stop_clock command is scheduled to run, the executor deactivates the clock generator process in the VVC after finishing current clock cycle.				
	<pre>Example: stop_clock(CLOCK_GENERATOR_VVCT, 1 "Stop clock generator");</pre>				
set_clock_period()	set_clock_period (VVCT, vvc_instance_idx, clock_period, msg)				
	This procedure adds a set_clock_period command to the CLOCK GENERATOR VVCs executor queue, which will run as soon as all preceding commands have completed. When the set_clock_period command is scheduled to run, the executor will change the clock period instantly.				
	<pre>Examples: set_clock_period(CLOCK_GENERATOR_VVCT, 1, 10 ns, "Change clock period to 10 ns");</pre>				
set_clock_high_time()	set_clock_high_time (VVCT, vvc_instance_idx, clock_high_time, msg)				
	This procedure adds a set_clock_high_time command to the CLOCK_GENERATOR VVCs executor queue, which will run as soon as all preceding commands have completed. When the write command is scheduled to run, the executor changes the clock high time and the change will take effect from the next rising edge.				
	Examples:				

set_clock_high_time(CLOCK_GENERATOR_VVCT, 1, 6 ns, "Changing the duty cycle to 60%");



2 VVC Configuration

Record element	Туре	C_CLOCK_GENERATOR_CONFIG_DEFAULT	Description
inter_bfm_delay	t_inter_bfm_delay	C_CLOCK_GENERATOR_INTER_BFM_DELAY_DEFAULT	Delay between any requested BFM accesses towards the DUT.
			 TIME_START2START: Time from a BFM start to the next BFM start
			(A TB_WARNING will be issued if access takes longer than TIME_START2START).
			- TIME_FINISH2START: Time from a BFM end to the next BFM start.
			Any insert_delay() command will add to the above minimum delays,
			giving for instance the ability to skew the BFM starting time.
cmd_queue_count_max	natural	C_CMD_QUEUE_COUNT_MAX	Maximum pending number in command queue before queue is full.
			Adding additional commands will result in an ERROR.
cmd_queue_count_threshold	natural	C_CMD_QUEUE_COUNT_THRESHOLD	An alert with severity "cmd_queue_count_threshold_severity" will be
			issued if command queue exceeds this count. Used for early warning if
			command queue is almost full. Will be ignored if set to 0.
cmd_queue_count_threshold_severity	t_alert_level	C_CMD_QUEUE_COUNT_THRESHOLD_SEVERITY	Severity of alert to be initiated if exceeding cmd_queue_count_threshold
result_queue_count_max	natural	C_RESULT_QUEUE_COUNT_MAX	Maximum number of unfetched results before result_queue is full.
result_queue_count_threshold	natural	C_RESULT_QUEUE_COUNT_THRESHOLD	An alert with severity 'result_queue_count_threshold_severity' will be issued if
			result queue exceeds this count. Used for early warning if result queue is almost
			full. Will be ignored if set to 0.
result_queue_count_threshold_severity	t_alert_level	C_ RESULT_QUEUE_COUNT_THRESHOLD_SEVERITY	Severity of alert to be initiated if exceeding result_queue_count_threshold
bfm_config	t_bfm_config	C_CLOCK_GENERATOR_CONFIG_DEFAULT	Configuration for Clock Generator.
msg_id_panel	t_msg_id_panel	C_VVC_MSG_ID_PANEL_DEFAULT	VVC dedicated message ID panel

The configuration record can be accessed from the Central Testbench Sequencer through the shared variable array, e.g.:

 $\verb| shared_clock_generator_vvc_config(1).inter_bfm_delay.delay_in_time := 50 ns; \\$

3 VVC Status

The current status of the VVC can be retrieved during simulation. This is achieved by reading from the shared variable shared_clock_generator_vvc_status record from the test sequencer. The record contents can be seen below:

Record element	Туре	Description
current_cmd_idx	natural	Command index currently running
previous_cmd_idx	natural	Previous command index to run
pending_cmd_cnt	natural	Pending number of commands in the command queue



Additional Documentation

Additional documentation about UVVM and its features can be found under "/uvvm vvc framework/doc/".

Compilation 5

The Clock Generator VVC must be compiled with VHDL 2008.

It is dependent on the following libraries

- UVVM Utility Library (UVVM-Util), version 2.5.0 and up
- UVVM VVC Framework, version 2.3.0 and up

Before compiling the Clock Generator VVC, assure that uvvm_vvc_framework and uvvm_util have been compiled.

See UVVM Essential Mechanisms located in uvvm_vvc_framework/doc for information about compile scripts.

Compile order for the Clock Generator VVC:

Compile to library	File	Comment
bitvis_vip_clock_generator	vvc_cmd_pkg.vhd	Clock Generator VVC command types and operations
bitvis_vip_clock_generator	/uvvm_vvc_framework/src_target_dependent/td_target_support_pkg.vhd	UVVM VVC target support package, compiled into the Clock Generator VVC library.
bitvis_vip_clock_generator	/uvvm_vvc_framework/src_target_dependent/td_vvc_framework_common_methods_pkg.vhd	Common UVVM framework methods compiled into the Clock Generator VVC library
bitvis_vip_clock_generator	vvc_methods_pkg.vhd	Clock Generator VVC methods
bitvis_vip_clock_generator	/uvvm_vvc_framework/src_target_dependent/td_queue_pkg.vhd	UVVM queue package for the VVC
bitvis_vip_clock_generator	/uvvm_vvc_framework/src_target_dependent/td_vvc_entity_support_pkg.vhd	UVVM VVC entity support compiled into the Clock Generator VVC library
bitvis_vip_clock_generator	clock_generator_vvc.vhd	Clock Generator VVC

Simulator compatibility and setup

This VVC has been compiled and tested with Modelsim version 10.5b and Riviera-PRO version 2016.02.81. For required simulator setup see *UVVM-Util* Quick reference.



Disclaimer: This IP and any part thereof are provided "as is", without warranty of any kind, express or implied, including but not limited to the warranties of merchantability, fitness for a particular purpose and noninfringement. In no event shall the authors or copyright holders be liable for any claim, damages or other liability, whether in an action of contract, tort or otherwise, arising from, out of or in connection with this IP.