













ISE Project Navigator (M53d) - C:\Users\Administrator\Desktop\Xilinx\jatinkatihar\jatinkatihar\_xise - [mux2and1\_2 (RTL1)]

File Edit View Project Source Process Tools Window Layout Help

Design View: Implementation Simulation

Hierarchy

- t3 - or\_2 - Behavioral (or\_2.v)
- fullsubtractor\_11 - Behavioral (f...
- fullsubtractor\_2 - Behavioral (fu...
- halfadder\_11 - Behavioral (halfa...
- halfadder\_2 - Behavioral (halfa...
- halfsub\_11 - Behavioral (halfsub...
- halfsubtractor\_2 - Behavioral (h...
- mux2and1\_2 - Behavioral (m...
- nand\_2 - Behavioral (nand\_2.vh...
- nor\_2 - Behavioral (nor\_2.vhd)

No Processes Running

Processes: mux2and1\_2 - Behavioral

- Design Summary/Reports
- Design Utilities
- Create Schematic Symbol
- View Command Line Log...
- View HDL Instantiation Te...
- User Constraints
- Implement Design
- Synthesize - XST
- View RTL Schematic
- View Technology Sche...

Start Design Files Libraries

paralladderandsb\_stru.ngr:1

paralladderandsb\_stru (RTL1)

mux2and1\_2.vhd

mux2and1\_tb.vhd

mux2and1\_2.ngr:1

mux2and1\_2 (RTL1)

View by Category

Design Objects of mux2and1\_2

Instances

- cout\_and0001\_imp\_cout\_and...
- cout\_imp\_cout1

Pins

- i1
- s

Signals

- i1
- s

Properties of Instance: cout\_and0001\_imp\_cout\_and00011

Name	Value
Verilog Model	AND2B1
VHDL Model	AND2B1

Console Errors Warnings Find in Files Results View by Category

[324,400]

10:33 AM 4/12/2022

ISim (M53d) - [Default.wcfg]

File Edit View Simulation Window Layout Help

Source Files

- Simulation Objects for mux2and1\_tb
- Object Name Value
- i0 1
- i1 1
- s 1
- cout 1

Simulation Objects for mux2and1\_tb

Name	Value
i0	1
i1	0
s	1
cout	1

Waveform

2,061,138,228,464 ns

X1: 2,061,138,228,464 ns

Default.wcfg

Console

WARNING: A full ISim license cannot be checked out due to the issues listed above. Please use Xilinx License Configuration Manager to fix these issues in order to check out a full ISim license.

WARNING: ISim will run in Lite mode. Please refer to the ISim documentation for more information on the differences between the Lite and the Full version.

This is a Lite version of ISim.

Time resolution is 1 ps

Simulator is doing circuit initialization process.

Finished circuit initialization process.

ISim>

# run 10.00us

ISim>

# run 10.00us

ISim>

# run all

Sim Time: 10,124,839,600,000 ps

10:32 AM 4/12/2022







