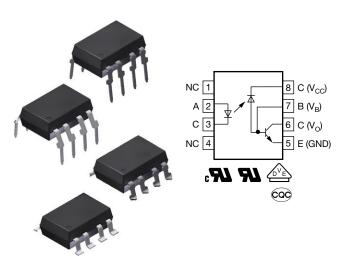


# High Speed Optocoupler, 1 MBd, Photodiode with Transistor Output



#### **DESCRIPTION**

The 6N135 and 6N136 are optocouplers with a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package.

Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

#### **FEATURES**

Isolation test voltages: 5300 V<sub>RMS</sub>

TTL compatible

• High bit rates: 1 Mbit/s

• High common-mode interference immunity

• Bandwidth 2 MHz

Open-collector output

• External base wiring possible

 Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

# Pb-free



ROHS COMPLIANT HALOGEN

FREE GREEN (5-2008)

#### AGENCY APPROVALS

- UL1577 file no. E52744, double protection
- DIN EN 60747-5-5 (VDE0884-5) available with option 1
- cUL components acceptance service no. 5A
- CQC GB8898-2011, GB4943.1-2011

ORDERING INFORMATION					
6 N 1 3 # PART NUMBER	- X 0 # # PACKAGE OPTION	TAPE AND REEL Option 7 Option 9  > 0.7 mm			
AGENCY CERTIFIED / PACKAGE	CTR (%)				
UL, CSA	≥7	≥ 19			
DIP-8	6N135	6N136			
DIP-8, 400 mil, option 6	-	6N136-X006			
SMD-8, option 7	6N135-X007T <sup>(1)</sup>	6N136-X007T <sup>(1)</sup>			
SMD-8, option 9	-	6N136-X009T <sup>(1)</sup>			
VDE, UL, CSA	≥7	≥ 19			
DIP-8	-	6N136-X001			
SMD-8, option 7	6N135-X017T <sup>(1)</sup>	6N136-X017T			
SMD-8, option 9	-	6N136-X019T			

#### Note

(1) Also available in tubes; do not add T to end



PARAMETER	CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		$V_R$	5	V
Forward current		I <sub>F</sub>	25	mA
Peak forward current	t = 1 ms, duty cycle 50 %	I <sub>FSM</sub>	50	mA
Maximum surge forward current	t ≤ 1 µs, 300 pulses/s		1	Α
Thermal resistance		R <sub>th</sub>	700	K/W
Power dissipation	T <sub>amb</sub> = 70 °C	P <sub>diss</sub>	45	mW
OUTPUT				
Supply voltage		V <sub>S</sub>	-0.5 to 15	V
Output voltage		V <sub>O</sub>	-0.5 to 15	V
Emitter base voltage		$V_{EBO}$	5	V
Output current		Io	8	mA
Maximum output current			16	mA
Base current		I <sub>B</sub>	5	mA
Thermal resistance			300	K/W
Power dissipation	T <sub>amb</sub> = 70 °C	P <sub>diss</sub>	100	mW
COUPLER	•			
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C
Ambient temperature range		T <sub>amb</sub>	-55 to +100	°C
Soldering temperature	max. ≤ 10 s, dip soldering ≥ 0.5 mm from case bottom	T <sub>sld</sub>	260	°C

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT									
Forward voltage	I <sub>F</sub> = 16 mA		$V_{F}$	-	1.33	1.9	V		
Breakdown voltage	$I_R = 10 \mu A$		$V_{BR}$	5	-	-	V		
Reverse current	$V_R = 5 V$		I <sub>R</sub>	-	0.5	10	μA		
Capacitance	$V_R = 0 V$ , $f = 1 MHz$		Co	-	30	-	pF		
Temperature coefficient, forward voltage	I <sub>F</sub> = 16 mA		$\Delta V_F / \Delta T_A$	-	-1.7	-	mV/°C		
OUTPUT	OUTPUT								
Logic low supply current	$I_F = 16$ mA, $V_O = open$ , $V_{CC} = 15$ V		I <sub>CCL</sub>	-	150	-	μA		
Logic high supply current	$I_F = 0$ mA, $V_O = open$ , $V_{CC} = 15 V$		I <sub>CCH</sub>	-	0.01	1	μA		
Output voltage, output low	$I_F = 16 \text{ mA}, I_O = 1.1 \text{ mA}, V_{CC} = 4.5 \text{ V}$	6N135	$V_{OL}$	-	0.1	0.4	V		
Output voltage, output low	$I_F = 16 \text{ mA}, I_O = 3.0 \text{ mA}, V_{CC} = 4.5 \text{ V}$	6N136	$V_{OL}$	-	0.1	0.4	V		
Output ourrent output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I <sub>OH</sub>	-	3	500	nA		
Output current, output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I <sub>OH</sub>	-	0.01	1	μA		
COUPLER	COUPLER								
Capacitance (input to output)	f = 1 MHz		C <sub>IO</sub>	-	0.6	-	pF		

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



CURRENT TR	<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
	1 16 m	6N135	CTR	7	16	-	%	
Current transfer	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	PART SYMBOL MIN. TYP. MAX.	%					
ratio	I <sub>F</sub> = 16 mA, V <sub>O</sub> = 0.5 V, V <sub>CC</sub> = 4.5 V	6N135	CTR	5	-	-	%	
		6N136	CTR	15	-	-	%	

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
High to low	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	t <sub>PHL</sub>	-	0.3	1.5	μs	
	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t <sub>PHL</sub>	-	0.2	0.8	μs	
Low to high	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 4.1 \text{ k}\Omega$	6N135	t <sub>PLH</sub>	-	0.3	1.5	μs	
Low to night	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	6N136	t <sub>PLH</sub>	-	0.2	0.8	μs	

<b>COMMON MODE TRANSIENT IMMUNITY</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UN							
High	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 4.1 \text{ k}\Omega$	6N135	CM <sub>H</sub>	-	1000	-	V/µs
	$I_F = 0 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 1.9 \text{ k}\Omega$	6N136	CM <sub>H</sub>	-	1000	-	V/µs
Low	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 4.1 \text{ k}\Omega$	6N135	CM <sub>L</sub>	-	1000	-	V/µs
LOW	$I_F = 16 \text{ mA}, V_{CM} = 10 V_{P-P}, V_{CC} = 5 V, R_L = 1.9 \text{ k}\Omega$	6N136	CM <sub>L</sub>	-	1000	-	V/µs

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>
Isolation resistance	T <sub>amb</sub> = 25 °C, V <sub>IO</sub> = 500 V	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	$T_{amb} = 100  ^{\circ}C,  V_{IO} = 500  V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power		P <sub>SO</sub>	500	mW
Input safety current		I <sub>SI</sub>	300	mA
Input safety temperature		T <sub>S</sub>	175	°C
Creepage distance	DIP-8		≥ 7	mm
Clearance distance	DIP-8		≥ 7	mm
Creepage distance	DIP-8, 400 mil, option 6		≥8	mm
Clearance distance	DIP-8, 400 mil, option 6		≥8	mm
Creepage distance	SMD-8, option 7		≥8	mm
Clearance distance	SMD-8, option 7		≥8	mm
Creepage distance	SMD-8, option 9		≥8	mm
Clearance distance	SMD-8, option 9		≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm

#### Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

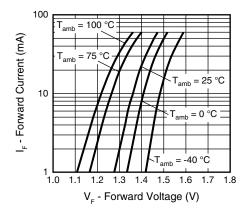


Fig. 1 - LED Forward Current vs. Forward Voltage

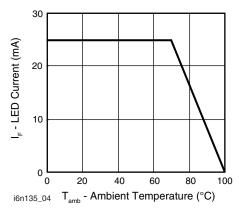


Fig. 2 - Permissible Forward LED Current vs. Temperature

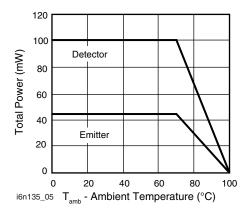


Fig. 3 - Permissible Power Dissipation vs. Temperature

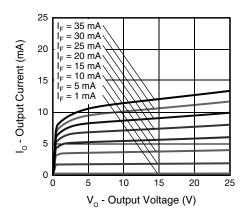


Fig. 4 - Output Current vs. Output Voltage

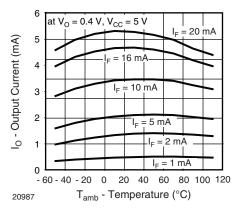


Fig. 5 - Output Current vs. Temperature

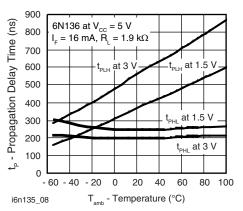


Fig. 6 - Propagation Delay vs. Ambient Temperature



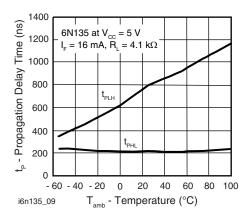


Fig. 7 - Propagation Delay vs. Ambient Temperature

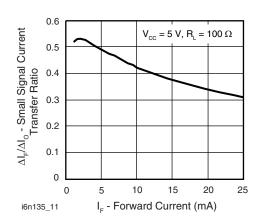


Fig. 9 - Small Signal Current Transfer Ratio vs. Quiescent Input Current

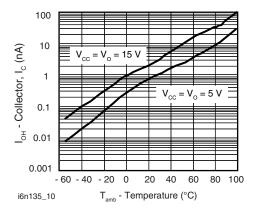


Fig. 8 - Logic High Output Current vs. Temperature

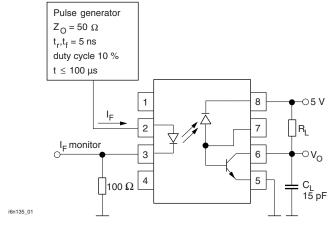
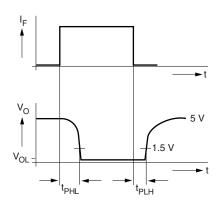


Fig. 10 - Switching Times





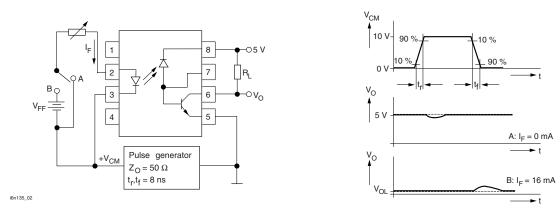
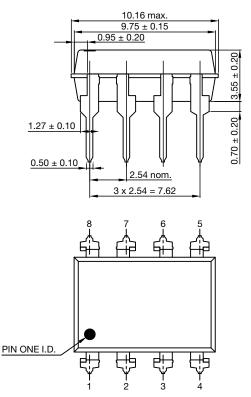
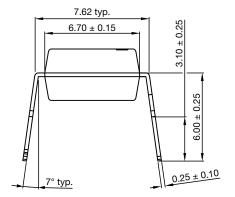


Fig. 11 - Common-Mode Interference Immunity

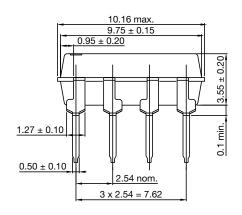
#### **PACKAGE DIMENSIONS** (in millimeters)

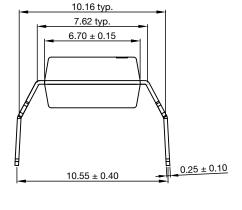
#### DIP-8, Standard

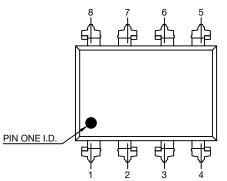




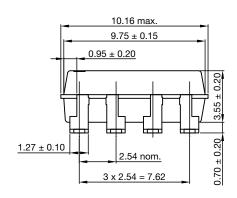
#### DIP-8, Option 6

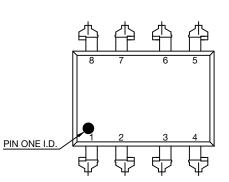


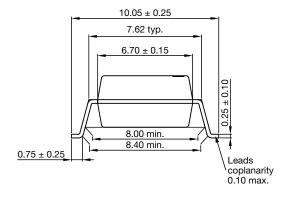


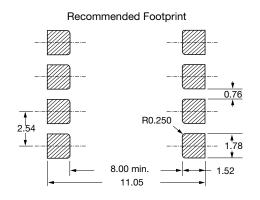


DIP-8, Option 7

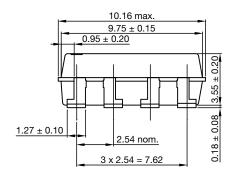


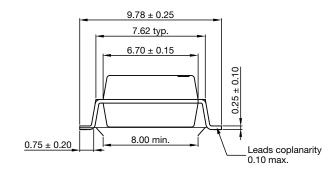




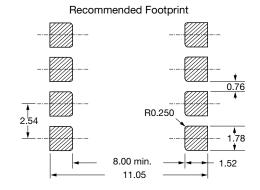


#### DIP-8, Option 9





# PIN ONE I.D. 2 3 4



#### **PACKAGE MARKING**

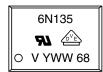


Fig. 12 - 6N135

# 6N136 **R1** (\*\*) O V YWW 68

Fig. 13 - 6N136

#### Notes

- The VDE logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.

#### **SOLDER PROFILES**

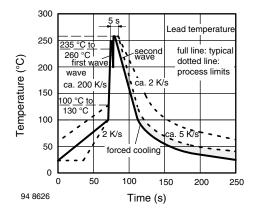


Fig. 14 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP-8 Devices

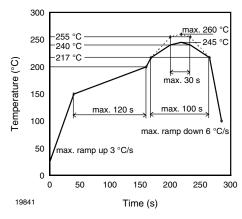


Fig. 15 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD-8 Devices



#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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Vishay

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