Flexi Soft Designer

SICK

Application name: SICK Flexi Soft main module FX3-CPU1

Device CRC Tool: 0xFB6FBD3D - Device CRC Device: 0xFB6FBD3D

Configuration date and time: 8/8/2021 6:06:00 PM



Content

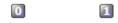
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1. Bill of material





Test pulses for the following output pins have been switched off: Q1, Q2, Q3.

C	QTY	Title	Tag name	Part number	Internal item number	Description
1		CPU1	CPU1[0]	1043784		SICK Flexi Soft main module FX3-CPU1
1		MPL0	System plug	1043700		Flexi Soft system plug
1		S300 / Safety laser scanner, type 3	S300.CPU1 [0].EFI1.1			Sampling laser scanner for access protection on machines, for mobile applications as well as for vertical access protection
1		XTIO	XTIO[1]	1044125		SICK Flexi Soft expansion module FX3-XTIO
1		Safety switch / Single channel	Safety switch.XTIO [1].I1			
1		E-Stop, ES21 / Single channel	EX-Stop.XTIO[1].I2			
1		Single channel NO / Single channel	LCase1/2.XTIO[1].I3			



QTY	Title	Tag name	Part number	Internal item number	Description
1	Single channel NO / Single channel	LCase2/2.XTIO[1].I4			
1	Safety switch / Single channel	Remote Safety switch.XTIO[1].I8			
1	PNP output / Single channel	ON DRV1.XTIO [1].Q1			
1	PNP output / Single channel	ON DRV2.XTIO [1].Q2			
1	PNP output / Single channel	SAFETY STOP ON.XTIO[1].Q3			
1	UE410-4RO3	UE410-4RO3	6026143		SICK relay output expansion UE410-4RO3

2. Diagnostics

Current operation time: 4.01:57:43, power cycle: 185 - Current: 1, Historical: 42

-	•	•		
Time stamp Description	Local time	Power-up cycles	Source	Code
4.01:40:28	8/10/2021 9:54:25 PM Communication at EFI 1 to de cyclic data.	185 evice address 7 interrupted: T	Main module imeout at reception of	0x000F000B [01 07 03 FFFF]
4 1.17:02:40	Extension module 1: cross-cir	120 cuit at output Q1, 2, 3, 4	Extension module 1	0x4704 [01 00 0F 00]
4 1.02:36:44	Extension module 1: cross-cir	104 cuit at output Q1, 2, 4	Extension module 1	0x4704 [01 00 0B 00]
22:55:03	Internal error: Cross-checking	96 for inputs	Extension module 1	0x4502 [01 55 04 00]
22:55:01	Internal error: Cross-checking	96 for inputs	Extension module 1	0x4502 [01 55 04 00]
22:54:37	Internal error: Cross-checking	96 for inputs	Extension module 1	0x4502 [01 AA 08 00]
% 20:14:48	Internal error in the Flexi Soft	80	Extension module 1	0xC30A [01 00 0F 00]
% 20:14:48	Internal error in the Flexi Soft malfunctioning.	80	Main module on module is	0x0029C006 [00 02 00 00]
4 17:51:38	The Flexi Soft system perform module.	60 ned a restart due to a power s	Main module supply dip at the main	0x002D4006 [00 00 00 00]
% 14:45:52	Internal error in the Flexi Soft	56 system.	Extension module 1	0xC30A [01 00 0F 00]
% 14:45:52	Internal error in the Flexi Soft malfunctioning.	56 system: Probably an extension	Main module on module is	0x0029C006 [00 02 00 00]
4 14:36:23	Force mode terminated.	46	Main module	0x00404006 [00 00 00 00]
4 14:34:12	Force mode started.	46	Main module	0x003F4006 [00 00 00 00]
% 14:33:44	Force mode terminated.	46	Main module	0x00404006 [00 00 00 00]
2 14:32:27	Force mode started.	46	Main module	0x003F4006 [00 00 00 00]
2 11:54:28	Force mode terminated.	42	Main module	0x00404006 [00 00 00 00]
4 11:52:52	Force mode started.	42	Main module	0x003F4006 [00 00 00 00]
4 11:05:07	Force mode terminated.	40	Main module	0x00404006 [00 00 00 00]
4 11:03:17	Force mode started.	40	Main module	0x003F4006 [00 00 00 00]
2 10:00:13	Force mode terminated.	39	Main module	0x00404006 [00 00 00 00]
				•



Time stamp Description	Local time	Power-up cycles	Source	Code
% 09:57:58	Force mode started.	39	Main module	0x003F4006 [00 00 00 00]
% 08:14:05	Configuration in the system plug is			
% 08:13:59	Unexpected EFI device with addres adapt the configuration of the Flexi		Main module er remove this device or	0x0019400A [01 07 00 00]
% 08:13:53	Configuration in the system plug is	37	Main module	0x000E4006 [00 00 00 01]
% 08:13:11	Internal error in the Flexi Soft syste	36	Main module	0x001B4005 [58E6 526D 5BF8 DB91]
4 07:17:20		28	Main module	0x0009400A
	Communication at EFI1 interrupted solving the fault a restart of the Flex			[01 422 00 00]
4 06:51:21	_	28	Main module	0x000F4013
	Configuration in the system plug is			[01 FFFF 00 00]
4 06:51:15	Herein and FEL design with a date of	28	Main module	0x0019400A
	Unexpected EFI device with address adapt the configuration of the Flexi	Soft system.		
4 06:51:09		28	Main module	0x000E4006
A- 06:50:56	Configuration in the system plug is	invalid. 27	Main module	[00 00 00 01] 0x001B4005
4 06:50:56	Internal error in the Flexi Soft syste	- -	Main module	[5DB5 A17E 5EAB 2882]
% 06:47:07	Internal error in the Flexi Soft syste	27 m.	Main module	0x001B4005 [5DB5 A17E 5EAB 2882]
% 05:11:53	Extension module 1: cross-circuit a	20 t output Q1, 2, 3, 4	Extension module 1	0x4704 [01 00 0F 00]
4 04:50:54		18	Main module	0x0019400A
	Unexpected EFI device with address adapt the configuration of the Flexi			
4 02:15:25		11	Main module	0x0019400A
	Unexpected EFI device with address adapt the configuration of the Flexi			
2:13:12	Force mode terminated.	10	Main module	0x00404006 [00 00 00 00]
% 02:11:32	Force mode started.	10	Main module	0x003F4006 [00 00 00 00]
2:01:45	Configuration in the system plug is	10	Main module	0x000F4013 [01 FFFF 00 00]
2 02:01:33		10	Main module	0x000E4006 [00 00 00 01]
A- 02:01:00	Configuration in the system plug is	9	Main module	0x000F4013
2 02:01:00	Configuration in the system plug is	-		[01 FFFF 00 00]
% 02:00:48	Configuration in the system plug is	9	Main module	0x000E4006 [00 00 00 01]
% 01:57:38	Configuration in the system plug is	8	Main module	0x000F4013
% 01:57:26		8	Main module	[01 FFFF 00 00] 0x000E4006
1 01:55:17	Configuration in the system plug is	7	Extension module 1	[00 00 00 01] 0x4601
•	Extension module 1: Cross circuit a	t input l'i		[01 00 01 00]

3. Summary

3.1. Module 0



	Device class:	Type code:	Serial number:	Software version	Hardware version:	Version/ Step:	Operational status:
0 p	CPU1	FX3- CPU130002	1427 0089	V 3.01.0	4.00	1.9.2.187 V 3.xx	c Online

3.2. Module 1

Device class:	Type code:	Serial number:	Software version	Hardware version:	Version/ Step:	Operational status:
XTIO	FX3-XTIO84002	1632 1341	V 3.10.0	1.11	1.9.2.187 V 3.xx	Online

3.3. EFI 1.1 S300 [H]



Device Device name Type code Serial number

Software version Operational status \$300 [H] \$300 LR \$30B-3011GB 19250478 15391838 02.11 Online

4. Configuration

4.1. Installed software components

Basic components (station)	1.9.2.187
Software component for GCC1 Network Modules	1.9.1.279
Software component for GS3S gateway	1.8.0.1
Software component for UE410-2RO3 relay module	1.9.2.187
Software component for UE410-4RO3 relay module	1.9.2.187
Software components for Flexi-Soft FX3-ANA0 expansion module	1.9.2.187
Software component for GCAN gateway	1.9.2.187
Software component for GDEV gateway	1.9.2.187
Software component for GENT gateway	1.9.2.187
Software component for GETC gateway	1.9.2.187
Software component for GMOD gateway	1.9.2.187
Software component for GPNT gateway	1.9.2.187
Software component for GPRO gateway	1.9.2.187
Software component for Drive Monitor FX3-MOC0 module	1.9.2.187
Software component for Drive Monitor FX3-MOC1 module	1.9.2.187
Software components for MOC3SA Motion Control module	1.9.2.187



Software component for ReLy OSSD4 relay module 1.9.2.187 Software component for STIO expansion module 1.9.2.187 Software component for UE10 relay module 1.9.2.187 Software component for UE12 relay module 1.9.2.187 Software component for XTDI extension module 192187 Software components for FX3-XTDS expansion module 1.9.2.187 Software component for XTIO extension module 1.9.2.187 Software component for CPU0 and CPU1 main modules 1.9.2.187

4.2. General information

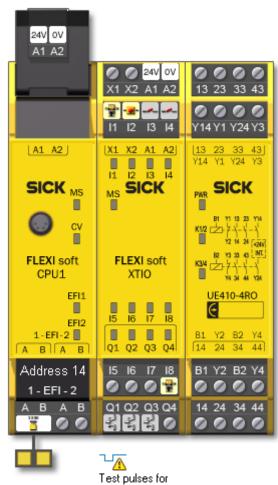
User group
Application name
Application description
Configuration CRC
Device CRC
Configuration state
Device state
Configuration date

0



Machine Operator SICK Flexi Soft main module FX3-CPU1

0xFB6FBD3D 0xFB6FBD3D Verified Verified 8/8/2021 6:06 PM



Test pulses for the following output pins have been switched off: Q1, Q2, Q3.



Module	Type code	Step	Address
CPU1	FX3-CPU130002	V 3.xx	0
XTIO	FX3-XTIO84002	V 3.xx	1
UE410-4RO3	UE410-4RO3	-	

4.3. CPU

4.3.1. CPU1 - General information

Type code	Serial number	Software version	Hardware version	Version/Step	Memory usage (UI/ Logic)	Address
FX3-CPU130002	1427 0089	V 3.01.0	4.00	1.9.2.187 V 3.xx	6.33% / 6.25%	0
FX3-MPL000001	1435 0415	-	-	1.9.2.187	-	-
S30B-3011GB	-	-	-	-	-	EFI1.1

4.3.1.1. CRC values

	Project CRC value	Device CRC value
Configuration CRC	0xFB6FBD3D	0xFB6FBD3D

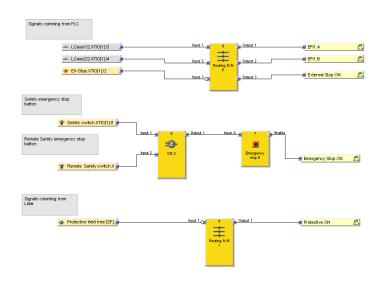
4.3.2. **CPU logic**

	Used	
Function blocks	16	
Execution time (ms)	4	

4.3.2.1. 0-Inputs

No	Name	Input	Output	Settings
0	Routing N:N	I.0 LCase1/2.XTIO[1].I3	O.0 Routing 1:N 0 -> I.0 -> Input	Input 1: Not Inverted
		I.1 LCase2/2.XTIO[1].I4	O.1 Routing 1:N 2 -> I.0 -> Input	Input 2: Not Inverted
		I.2 EX-Stop.XTIO[1].I2	O.2 Routing N:N 0 -> I.2 -> Input 3	Input 3: Inverted
5	Routing N:N	I.0 Protective field free [SF].CPU1[0].EFI1.1	O.0 Routing N:N 0 -> I.0 -> Input 1	Input 1: Inverted
6	OR	I.0 Safety switch.XTIO [1].I1	O.0 Emergency stop 0 -> I. 0 -> Input A	Input 1: Not Inverted
		I.1 Remote Safety switch.XTIO[1].I8		Input 2: Not Inverted
7	Emergency stop	I.0 OR 2 -> O.0 -> Output 1	O.0 Routing N:N 0 -> I.1 -> Input 2	Inputs: Single channel
			•	Discrepancy time: 30 ms Mode: Emergency stop

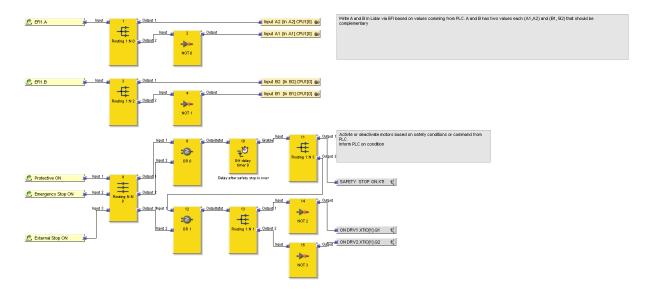




4.3.2.2. Outputs

			_	
No	Name	Input	Output	Settings
1	Routing 1:N	I.0 Routing N:N 5 -> O.0 ->		
		Output 1	[0].EFI1.1	
			O.1 NOT 0 -> I.0 -> Input	
2	NOT	O .	O.0 Input A1 [In A1].CPU1	
_		Output 2	[0].EFI1.1	
3	Routing 1:N	I.0 Routing N:N 5 -> O.1 ->		
		Output 2	[0].EFI1.1	
4	NOT	LO Douting 1:N 2 > O 1 >	O.1 NOT 1 -> I.0 -> Input O.0 Input B1 [In B1].CPU1	
4	NOT	Output 2	[0].EFI1.1	
8	Pouting N:N	I.0 Routing N:N 1 -> 0.0 ->		Input 1: Not Inverted
O	Routing N.N	Output 1	0.0 OK 0 -> 1.0 -> Input 1	input 1. Not inverted
		•	O.1 OR 0 -> I.1 -> Input 2	Input 2: Not Inverted
		0 -> Enable	o.r orto - i.i - inpat2	mpat 2. Not involted
			O.2 OR 1 -> I.1 -> Input 2	Input 3: Not Inverted
		Output 3	•	
9	OR	I.0 Routing N:N 0 -> O.0 ->	O.0 Off delay timer 0 -> I.0	Input 1: Not Inverted
		Output 1	-> Input	
		I.1 Routing N:N 0 -> O.1 ->		Input 2: Not Inverted
		Output 2		
10	Off delay	I.0 OR 0 -> O.0 -> Output 1	O.0 Routing 1:N 3 -> I.0 ->	Delay time: 2000 ms
	timer		Input	
11	Routing 1:N	I.0 Off delay timer 0 -> O.0		
		-> Enable	ON.XTIO[1].Q3	
10	OR	LO Douting 1:N 2 > O 1 >	O.1 OR 1 -> I.0 -> Input 1 O.0 Routing 1:N 1 -> I.0 ->	Input 1. Not Inverted
12	UK	Output 2	Input	input 1. Not inverted
		I.1 Routing N:N 0 -> O.2 ->	•	Input 2: Not Inverted
		Output 3		input 2. Not inverted
13	Routing 1·N	1.0 OR 1 -> 0.0 -> Output 1	O 0 NOT 2 -> I 0 -> Input	
		C. C. C. Cutput I	0.1 NOT 3 -> 1.0 -> Input	
14	NOT	I.0 Routing 1:N 1 -> O.0 ->	0.0 ON DRV1.XTIO[1].Q1	
		Output 1	- [-]	
15	NOT	I.0 Routing 1:N 1 -> O.1 ->	O.0 ON DRV2.XTIO[1].Q2	
		Output 2		

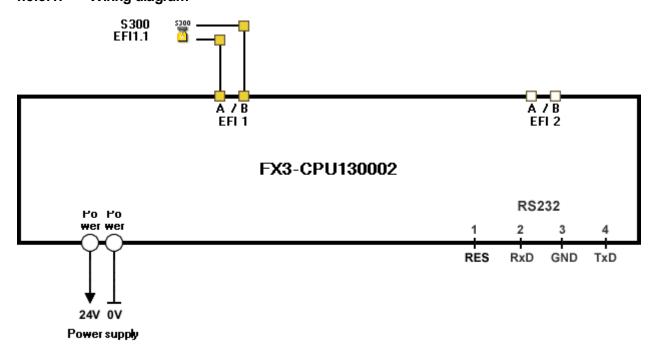




4.3.3. CPU1 - IO

			Tag name	test based on type	System integrity test based on serial number	System integrity test based on configuration date
24V 0V	6	A1 A2	CPU1[0] Power supply	-	-	-
	S300	EFI1.1	S300.CPU1[0].EFI1.1	-	-	-

4.3.3.1. Wiring diagram





4.4. I/O module

4.4.1. XTIO[1]

4.4.1.1. General information

Type code	Serial number	Software version	Hardware version	Version/Step Address
FX3-XTIO84002	1632 1341	V 3.10.0	1.11	1.9.2.187 V 3.xx 1

4.4.1.2. Inputs

		Mode	Title/tag name	ON- OFF	OFF- ON	Filter time [ms]	Dis. [ms]	Test period [ms]	Test gap [ms]	Max. off- on delay [ms]
2	24V 🛖	¹¹ 土	Safety switch (Single channel)	~	~	8	-	-	-	-
3	24V - <u>-</u> -	12	E-Stop, ES21 (Single channel) / EX-Stop	-	-	0	-	-	-	-
4	24V 🚤	13	Single channel NO (Single channel) / LCase1/2	-	-	0	-	-	-	-
5	24V 🚤	14	Single channel NO (Single channel) / LCase2/2	-	-	0	-	-	-	-
6	24V 🕌	18 1	Safety switch (Single channel) / Remote Safety	•	*	8	-	-	-	-

4.4.1.3. Outputs

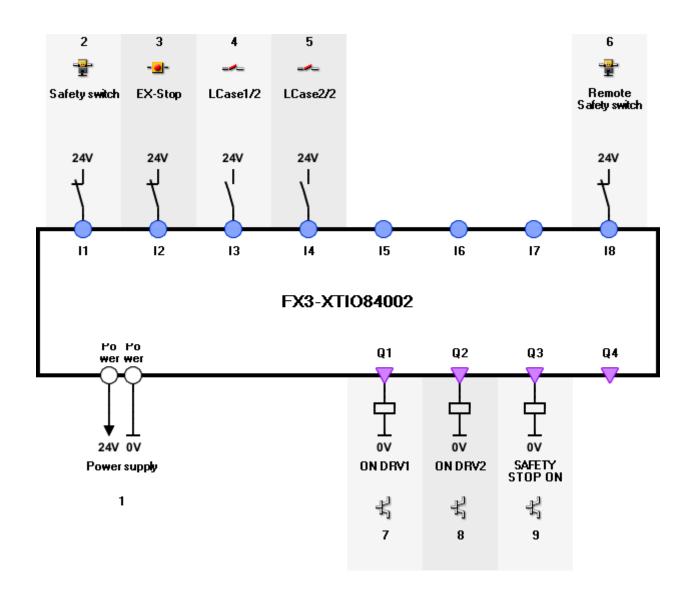
		Mode	Title/tag name	Test pulse	Increase d capaciti ve loads
7	볷	Q1 📥	PNP output (Single channel) / ON DRV1	Without	Disabled
8	롻	Q2 📥	PNP output (Single channel) / ON DRV2	Without	Disabled
9	4	Q3 📥	PNP output (Single channel) / SAFETY STOP ON	Without	Disabled

4.4.1.4. Power supply

			Title/tag name	
1	24V	A1	XTIO[1] Power supply	
	0V ==	A2		

4.4.1.5. Wiring diagram





5. Configuration draft S300 [H] from 8/10/2021, 22:10

S300 [H] Laser scanner



5.1. Version information

Version number CDS Software version DLL Created with CDS 3.7.2.122 3.7.2.122 3.7.2

5.2. General information



Type code Device name Serial number

Serial number (Scanner) Serial number (System plug) Software version (CPU A) Software version (CPU B)

Sensor head I/O module

S300 LR 19250478 19250478 15391838 00.00 00.00

S30B-3011GB

Long Range (3.0 m)

Safety Configuration ID (SCID)

Status

in testing

0xE1F3

5.3. System parameters

Name of the user Application name Rotation of the 7segment display AAP None

Rotated by 180°

5.4. Resolution/scanning range

Application variant

Resolution protective field

Basic response time

Maximum protective field range Angular resolution

Mobile 70 mm (leg detection)

80 ms 300 cm

0.5 °

5.5. Incremental encoder

Signal velocities

Inactive

5.6. Inputs

Control inputs (Permanently)

Input source CPU1 Input delay 10 ms

Sampling for the static control inputs

Complementary

 A
 Active

 B
 Active

 C
 Inactive

 D
 Inactive

 E
 Inactive

5.7. OSSDs

Object in the protective field switches OSSDs Local External device monitoring Inactive

5.8. Restart

Restart internal OSSDs Delay by 2 s



5.9. Universal I/O

	Uni I/O 1 (Pin5)	Uni I/O 2 (Pin6)	Uni I/O 3 (Pin13)	Uni I/O 4 (Pin14)	Uni I/O 5 (Pin15)	
Inputs						
External device monitoring	-	Х				
Reset	Х	-				
Outputs						
Contamination warning			х	-	-	
Contamination error			X	-	-	
Reset required			-	-	-	
Error			-	-	-	
Protective field			-	-	X	
Warning field 1			-	X	-	
Warning field 2			-	X	-	

5.10. Configuration of the measured data output

Baud rate 500 kBaud 5000ms 5000ms
Send mode Continuous data output

Trigger event Inactive

Measured data output Distance 1/O data output Current monitoring case

Message structure 1 message (I/O + measured data)

Measuring range(s)

Beginning [°]

End [°]

Type

Measuring range 1

-45

225

Every value

5.11. Cases

Number of monitoring cases 4

5.11.1. Monitoring case 1

Monitoring case name Short Case Park mode Inactive Allocated field set Short Fields Multiple sampling 4

Activation of the inputs

Control input A Low level
Control input B Low level
Velocity range Inactive



Switching sequence

Following case Any

5.11.2. Monitoring case 2

Monitoring case name

Park mode

Allocated field set

Multiple sampling

Long Case
Inactive

Long Fields

4

Activation of the inputs

Control input A Low level
Control input B High level
Velocity range Inactive

Switching sequence

Following case Any

5.11.3. Monitoring case 3

Monitoring case name No detection Park mode Inactive Allocated field set No detection Multiple sampling 2

Activation of the inputs

Control input A High level Control input B High level Velocity range Inactive

Switching sequence

Following case Any

5.11.4. Monitoring case 4

Monitoring case name Park mode Park mode Active

Activation of the inputs

Control input A High level Control input B Low level Velocity range Inactive

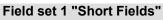
Switching sequence

Following case Any

5.12. Field sets

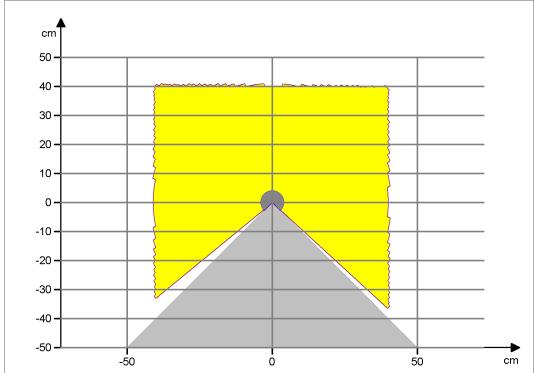
Number of field sets 3



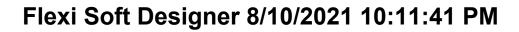


Protective field cm 30 20 10 0 -10 -20 -30 10 -40 -30 -20 -10 ò 20 30 40 cm

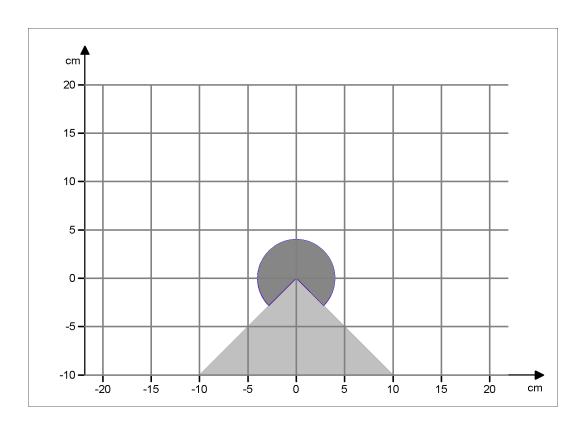


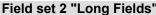


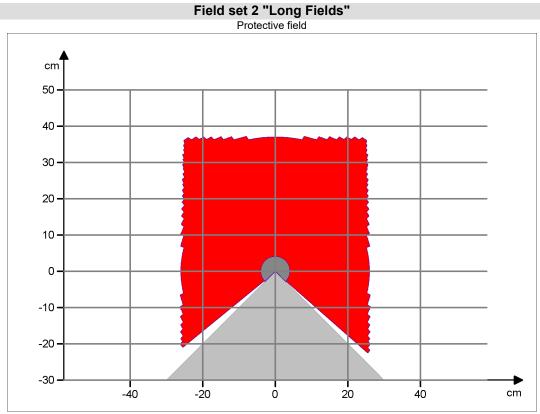
Warning field 2



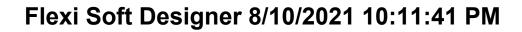




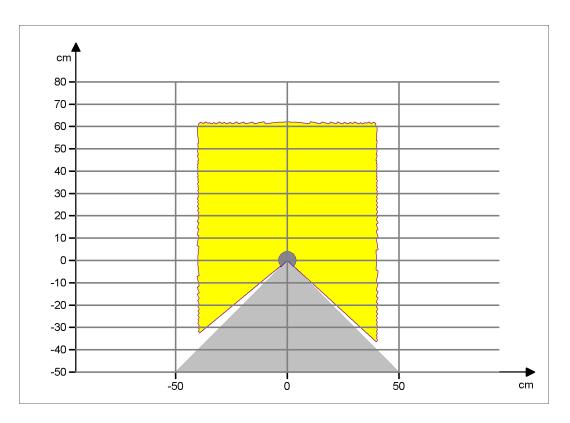


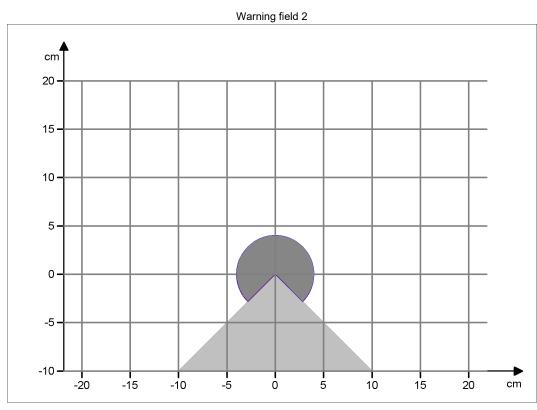


Warning field 1





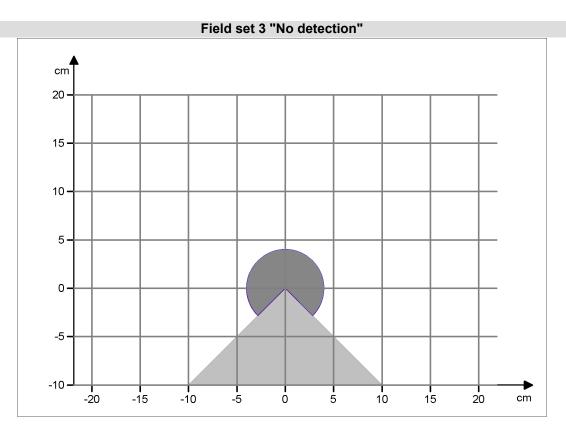


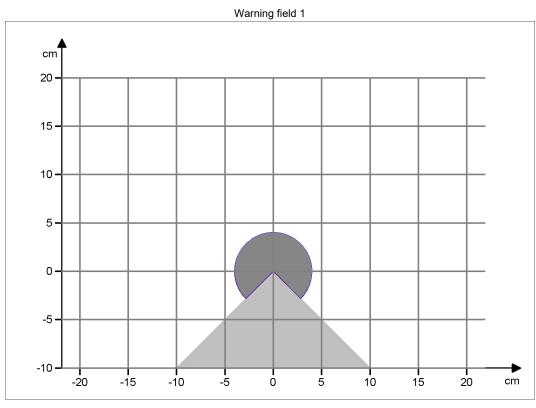


Field set 3 "No detection"

Protective field

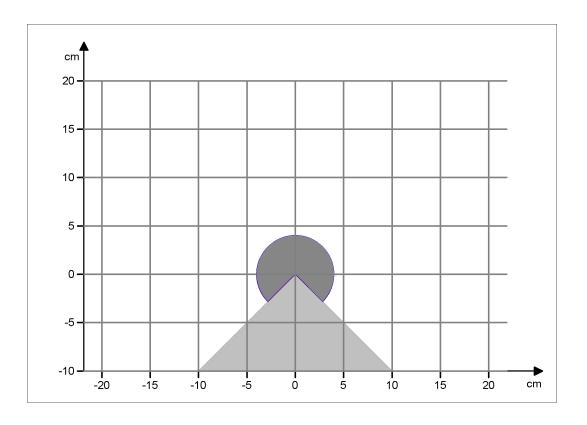






Warning field 2





6. I/O overview

6.1. I/O module

