

A

A

B

B

C

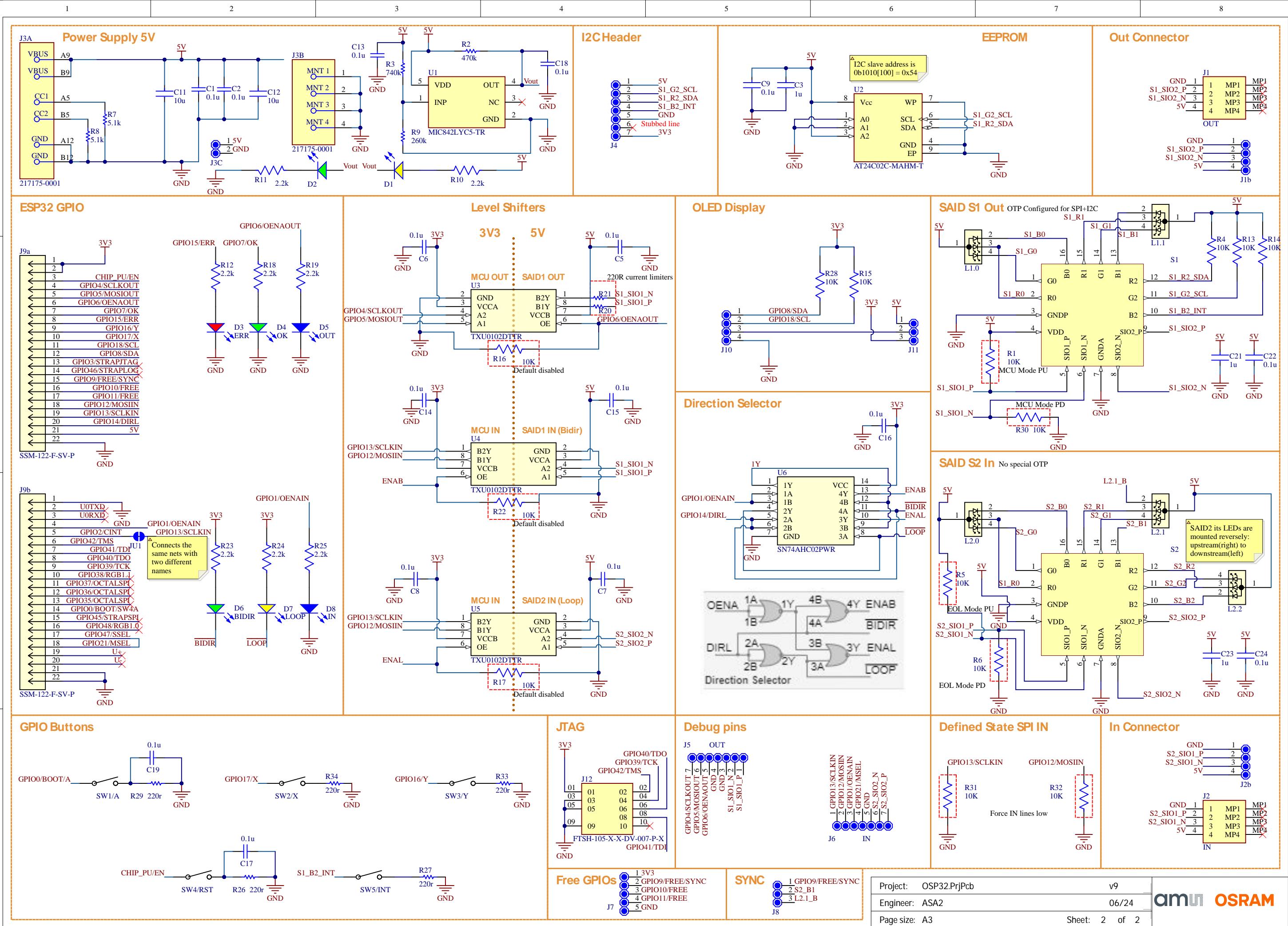
C

D

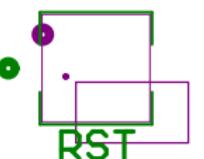
D



OSP32.PrjPcb



MCU	OEG	G	SAID	
C	D	OUT	C	D



5V
4V7
BIDIR
LOOP

OK
ERR

OUT
IN

L1.0
L1.1

SAID OUT

OSP32 v9

SAID IN

GND
P
N
5V
5V
N
P
GND

OLED Display



1

G
5V

A

X

Y

L2.2

L2.1

L2.0

MCU	IN	SAID			
C	D	OEMSG	G	C	D

am OSRAM

06/24 MPEN & ASA2

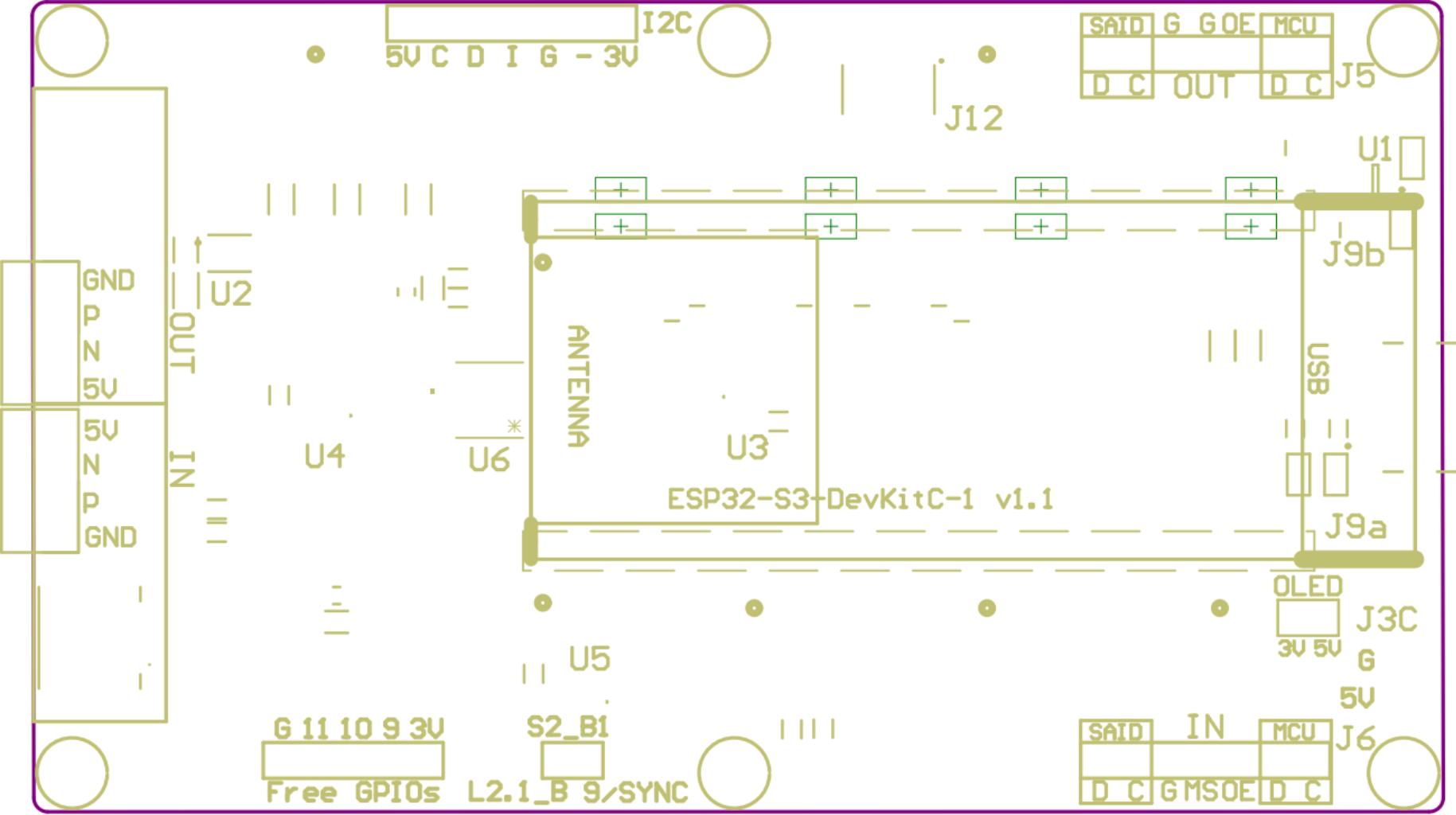
S2_B1

J8

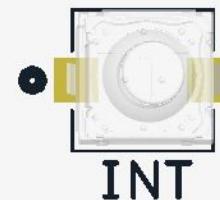
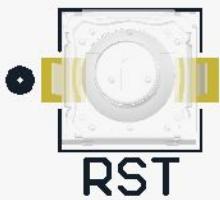
9/SYNC

L2.1_B

3V 9 10 11 G
Free GPIOs



MCU	OEG	G	SAID	
C	D	OUT	C	D



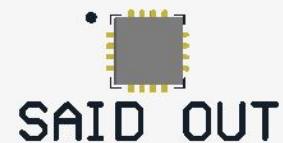
5V
4V7

BIDIR
LOOP

OK
ERR

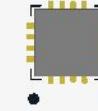
OUT
IN

L1.0
L1.1



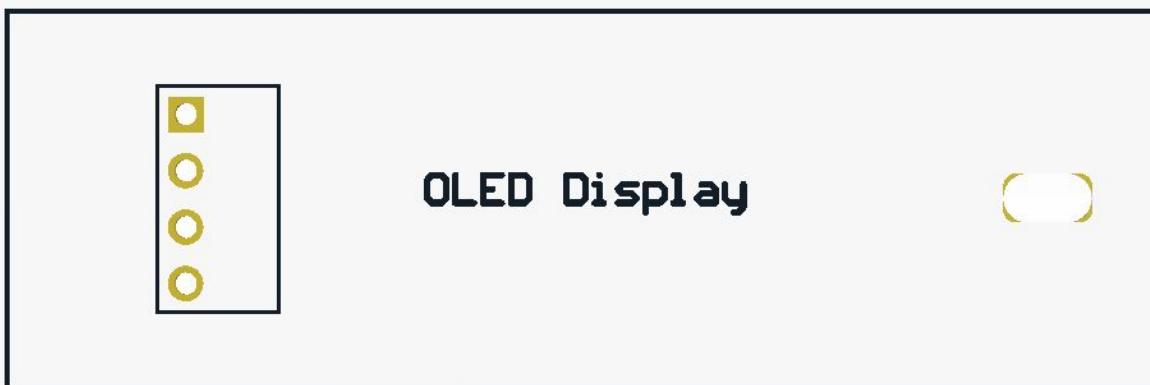
OSP32 v9

SAID IN



IN OUT

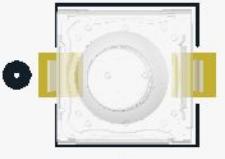
GND	<input type="checkbox"/>
P	<input type="checkbox"/>
N	<input type="checkbox"/>
5V	<input type="checkbox"/>
5V	<input type="checkbox"/>
N	<input type="checkbox"/>
P	<input type="checkbox"/>
GND	<input type="checkbox"/>



OLED Display



G
5V



L2.2

L2.1

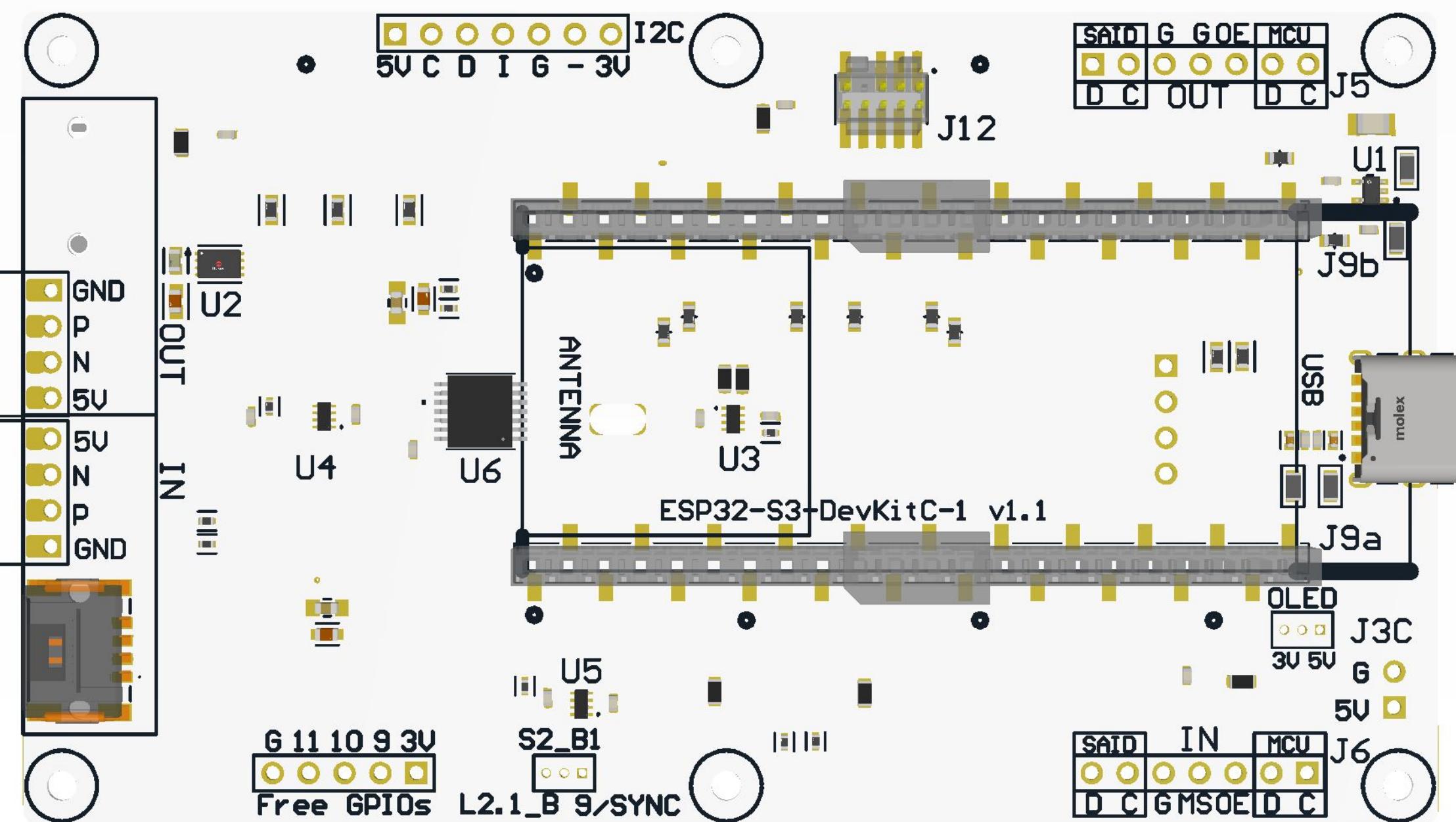
L2.0

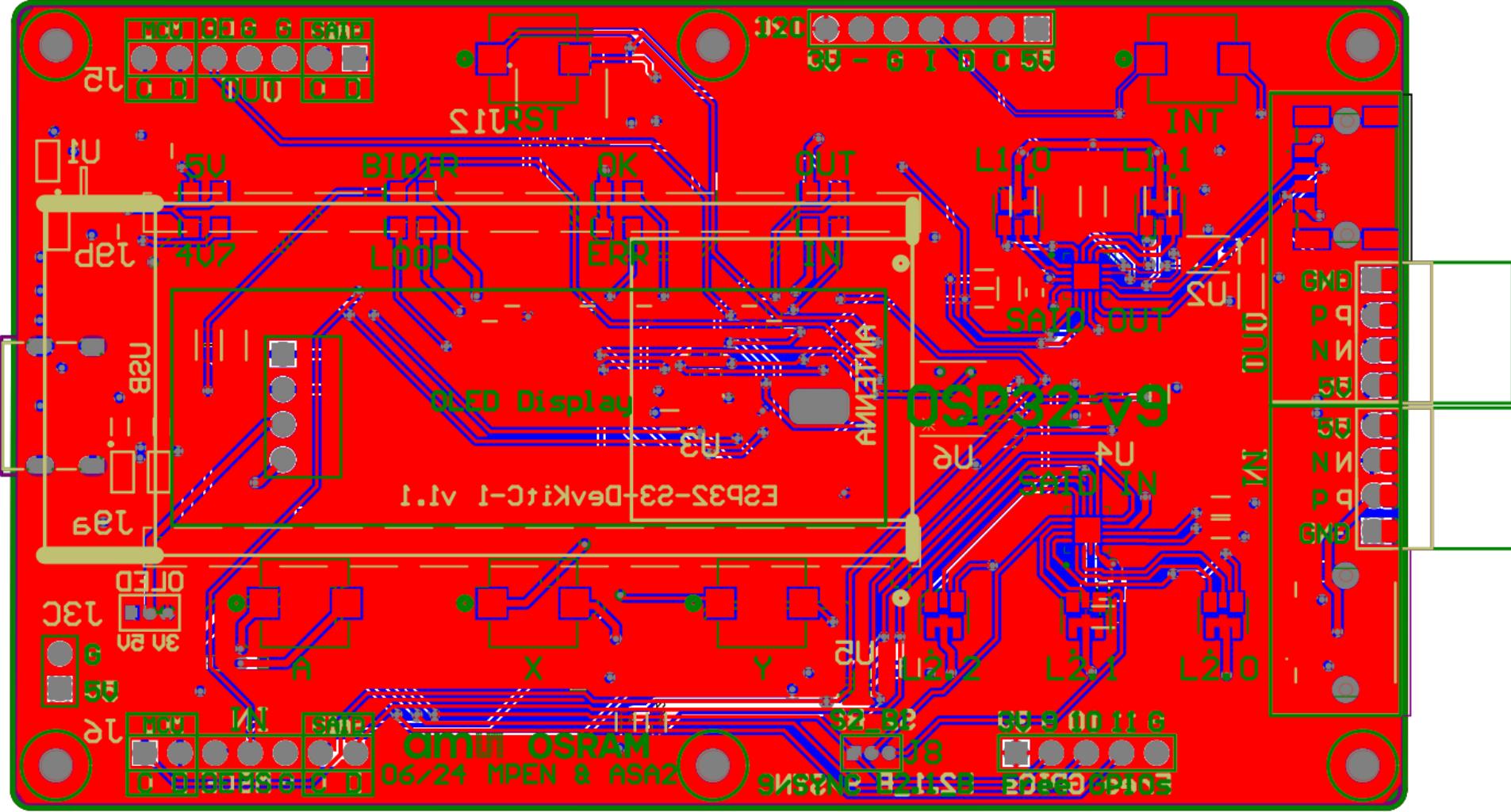
MCU	IN	SAID					
C	D	OE	M	S	G	C	D

am OSRAM
06/24 MPEN & ASA2

S2_B1
J8
9/SYNC L2.1_B

3V 9 10 11 G
Free GPIOs





MCU OE G G SAID
C D OUT C D

I2C 3V - G I D C 5V
RST

INT

5V
407
BIDIR
LOOP

OK
ERR
OUT IN

L1.0 L1.1

SAID OUT

OUT

OLED Display

OSP32 v9

SAID IN

IN

GND

G
5V
A

X Y

L2.2 L2.1 L2.0

GND

GND
P N

5V
5V

N P

GND

MCU IN SAID
C D OEM MSG C D

amitt OSRAM
06/24 MPEN & ASA2

S2_B1 J8 9/SYNC 12.1_B
3U 9 10 11 G
Free GPIOs

