

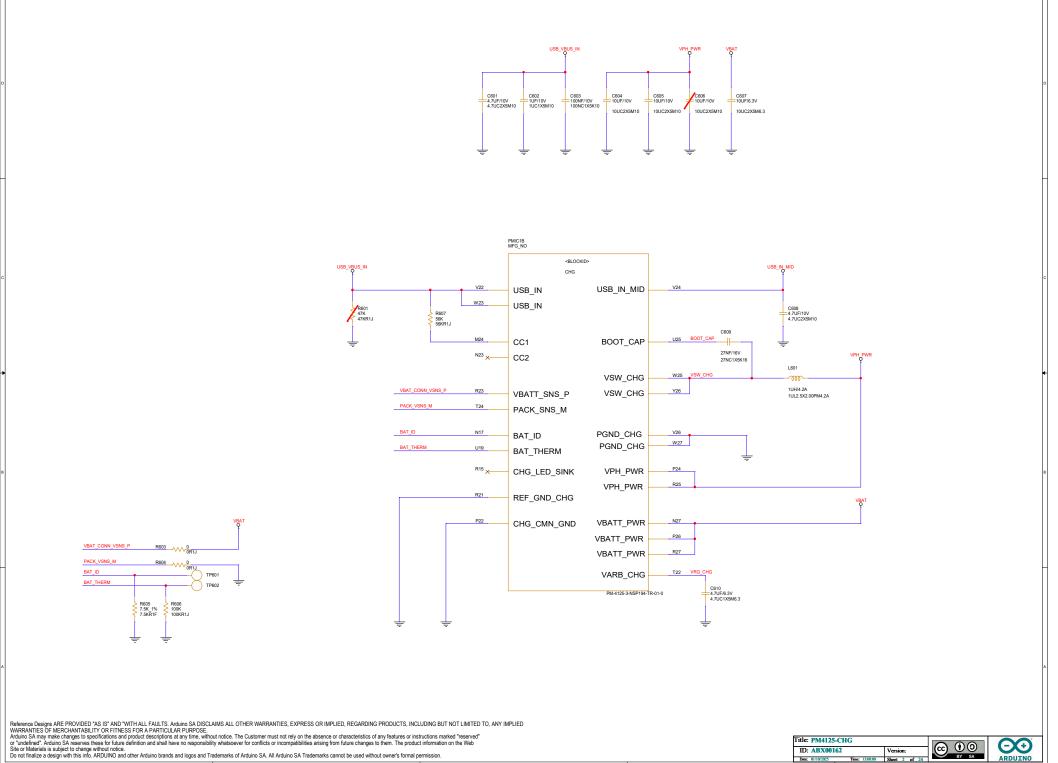
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Title: PM4125-CDC ID: ABX00162

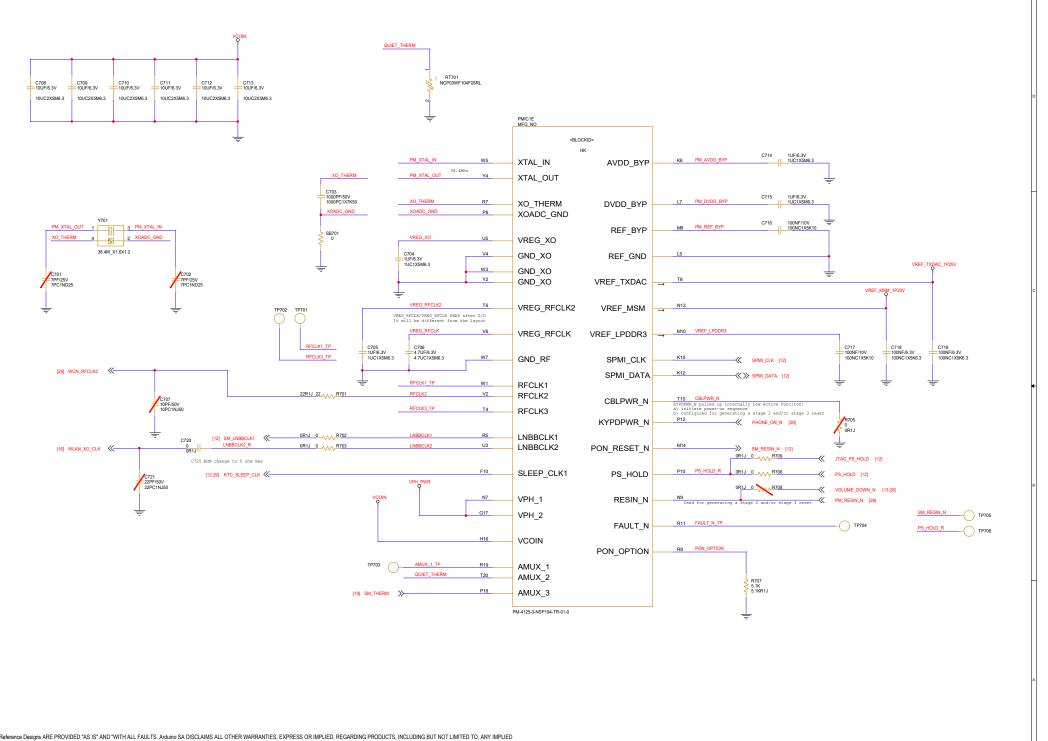






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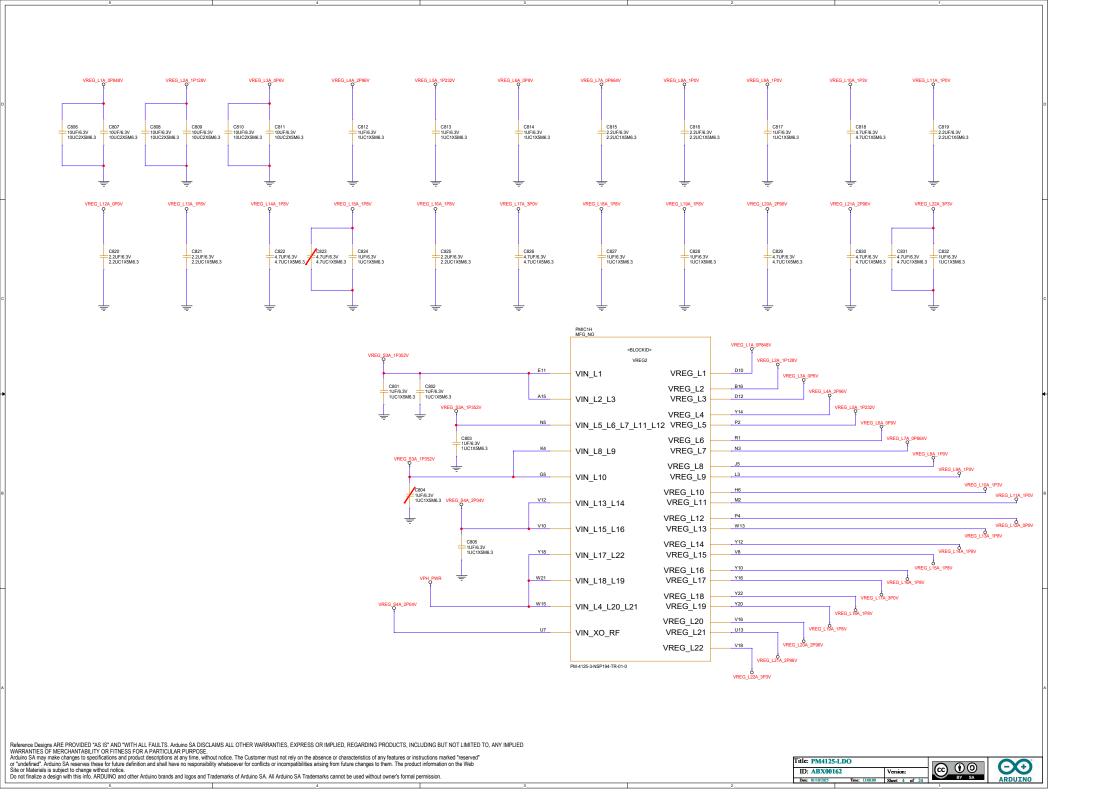
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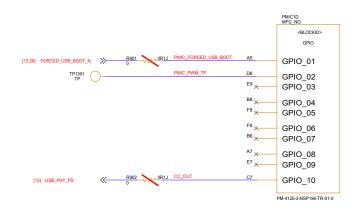
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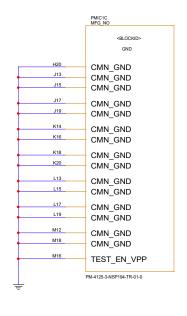
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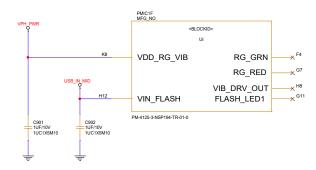
Title: PM4125-HK ID: ABX00162











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Title: PM4125-UI ID: ABX00162





PMIC1G MFG_NO <BLOCKID: VFB S1 VDD_S1 VREG_S1A_0P8V VSNS_S1A_APC_P [17] VSW_S1 470NH/4.7A VSNS_S1A_APC_M [17] VSW_S1 470NL2.00X1.60PM4.7A GND_S1 C19 GND_S1 VFB_NEG_S1 A13 VDD_S2 VFB_S2 B14 VDD_S2 C1002 = 10UF/10V 10UC2X5M10 L1002 VSNS_S2A_CX_P [18] VSW_S2 470NH/4.7A 470NL2.00X1.60PM4.7A VSW_S2 VSNS_S2A_CX_M [18] GND_S2 C11 VFB_NEG_S2 GND_S2 C1 VFB_S3 VDD_S3 VFB_S3 SB1001 SB2P VDD_S3 VREG_S3A_1P352V C1003 = 10UF/10V 10UC2X5M10 L1003 VSW_S3 470NH/4.7A 470NL2.00X1.60PM4.7A VSW_S3 C1005 10UF/6.3V 10UC2X5M6.3 C1007 C1008 =10UF/6.3V 10UF/6.3V 10UC2X5M6.3 10UC2X5M6.3 GND_S3 GND_S4 VFB_NEG_S3 ____SB1002 SB2P SB1003 VDD_S4 VFB_S4 SB2P VDD S4 VREG S4A 2P04V C1004 1UF/10V 1UC1X5M10 L1004 VSW S4 470NH/4.7A VSW_S4 470NL2.00X1.60PM4.7A GND_S4 C1011 C1012 10UF/6.3V 10UC2X5M6.3 10UC2X5M6.3 VVVVSB1004 GND_S4 VFB_NEG_S4 SB2P PM-4125-3-NSP194-TR-01-0

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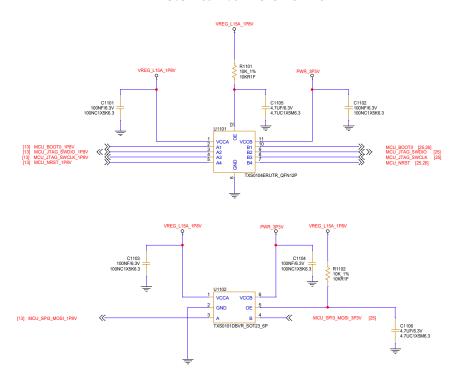
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Title: PM4125-VREG ID: ABX00162

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3.3V to 1.8V level shift



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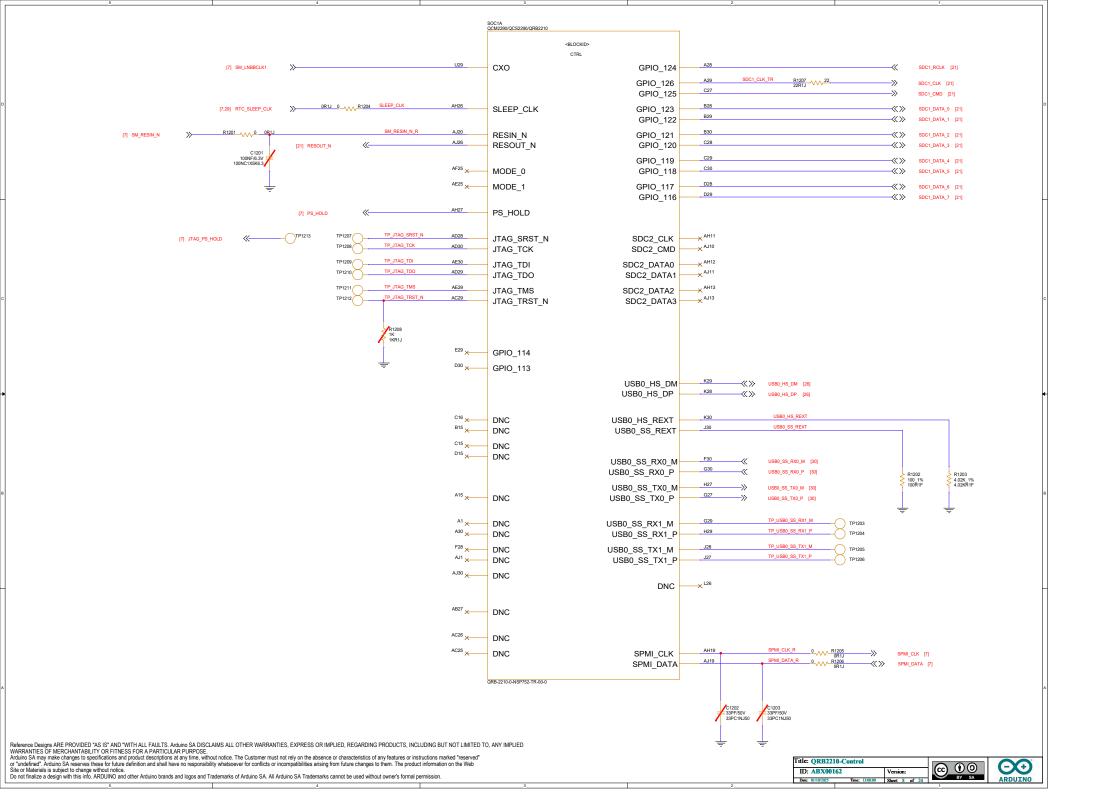
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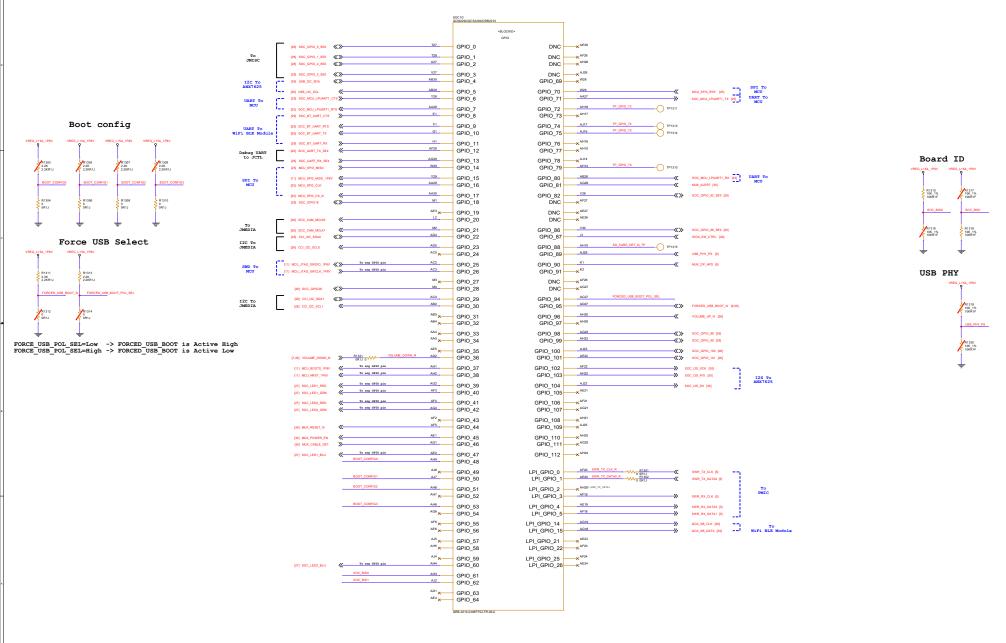
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Title: Level Shift ID: ABX00162







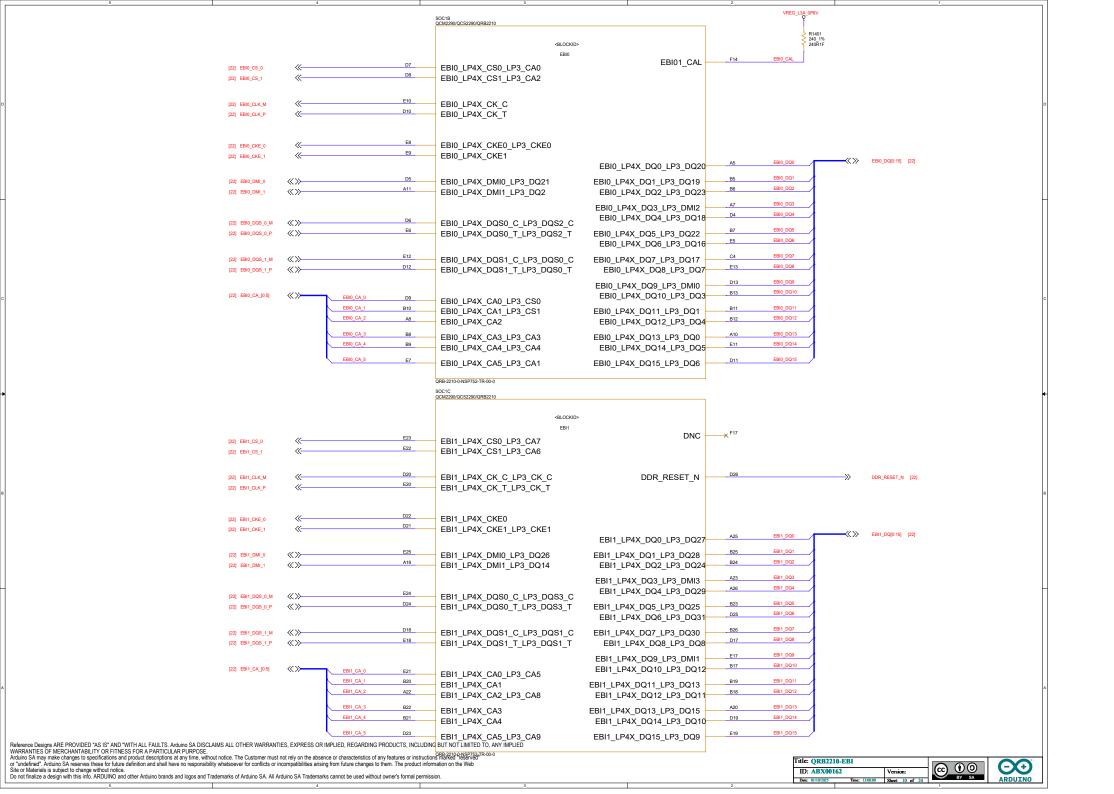


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R1501 0 0R1. →>> MIPI_DSI0_CLK_M_CONN [26] MIPI DSI0 CLK P R1502 0 0R1 ->> MIPI_DSI0_CLK_P_CONN [26] R1503 ______0 MIPI_DSI0_L0_M MIPI_DSI0_L0_P MIPI DSI0 L1 M R1505 0 0R1J MIPI_DSI0_L1_P R1506 0 MIPI DSI0 L1 P CONN [26] SOC1H QCM2290/QCS2290/QRB2210 MIPI_DSI0_L2_M R1507 0R1 MIPI_DSI0_L2_M_CONN [26] MIPI DSI0 L2 P R1508 VV 0R MIPI_DSI0_L2_P_CONN [26] <BLOCKID> MIPI_DSI0_L3_M R1509 0 0R1 →>> MIPI DSI0 L3 M CONN [26] MIPI MIPI_DSI0_L3_P R1510 VV 0R1 →>> MIPI DSI0 L3 P CONN [26] CSI0_NC_CLK_P [26] CSIO NC CLK P N2 [26] CSI0 A0 CLK M CSI0_A0_CLK_M → MIPI DSI0 CLK M [30] [26] CSI0_B0_LN0_P CSI0 B0 LN0 P → MIPI_DSI0_CLK_P [30] MIPI_DSI0_CLK_M N4 [26] CSI0_C0_LN0_M CSI0_C0_LN0_M DSI0 CLK M MIPI_DSI0_CLK_P DSI0_CLK_P [26] CSI0_A1_LN1_P CSI0 A1 LN1 P P3 MIPI_DSI0_L0_M [26] CSI0_B1_LN1_M CSI0_B1_LN1_M DSI0_LN0_M MIPI DSI0 L0 P DSI0 LN0 P CSI0 C1 LN2 P [26] CSI0_C1_LN2_P R2 MIPI DSI0 L1 M [26] CSI0_A2_LN2_M CSI0_A2_LN2_M DSI0_LN1_M MIPI_DSI0_L2_P [30] MIPI DSI0 L1 P DSI0 LN1 P MIPI_DSI0_L3_M [30] CSI0 B2 LN3 P [26] CSI0 B2 LN3 P →>> MIPI DSI0 L3 P [30] R4 MIPI_DSI0_L2_M CSI0_C2_LN3_M DSI0_LN2_M [26] CSI0 C2 LN3 M MIPI_DSI0_L2_P P29 DSI0_LN2_P T1 CSI1 NC CLK P [26] CSI1 NC CLK P MIPI_DSI0_L3_M T2 CSI1_A0_CLK_M DSI0 LN3 M [26] CSI1 A0 CLK M MIPI_DSI0_L3_P N27 DSI0_LN3_P [26] CSI1_B0_LN0_P CSI1 B0 LN0 P T4 [26] CSI1_C0_LN0_M CSI1_C0_LN0_M DSI0_REXT_PLL DSI0_REXT_PLL [26] CSI1_A1_LN1_P CSI1 A1 LN1 P [26] CSI1_B1_LN1_M CSI1 B1 LN1 M DSI1 CLK M TP TP1501 [26] CSI1_C1_LN2_P CSI1_C1_LN2_P DSI1_CLK_M DSI1_CLK_P_TP TP1502 DSI1 CLK P [26] CSI1_A2_LN2_M CSI1 A2 LN2 M R1522 1.4K_1% 1.4KR1F V3 [26] CSI1_B2_LN3_P CSI1_B2_LN3_P CSI1_C2_LN3_M [26] CSI1 C2 LN3 M W1 **★** DNC W2 × DNC DNC DNC DNC DNC AA1 DNC AB1 × DNC AA2 X DNC AA3 🛌 DNC QRB-2210-0-NSP752-TR-00-0

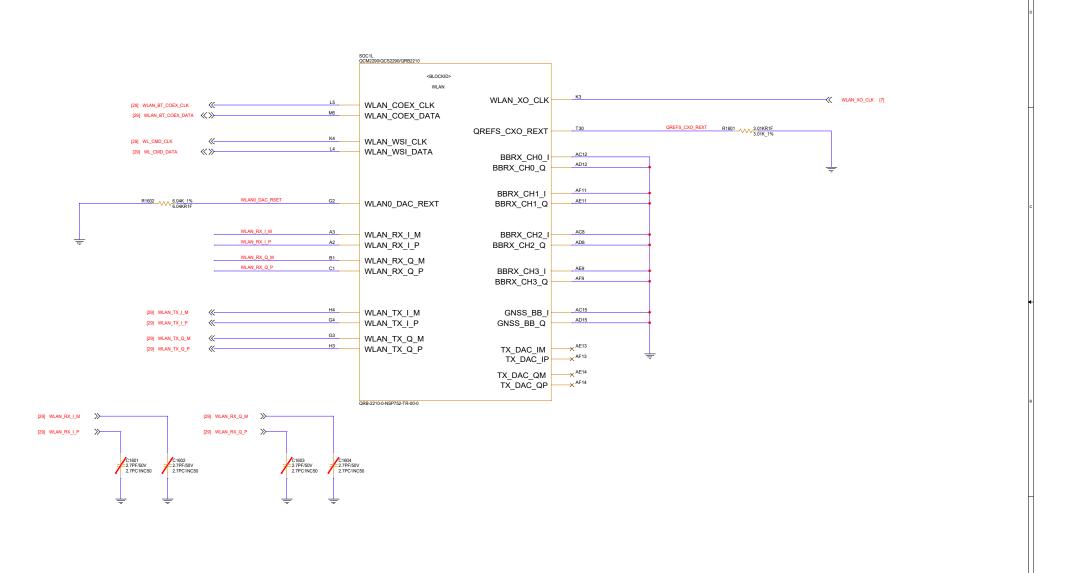
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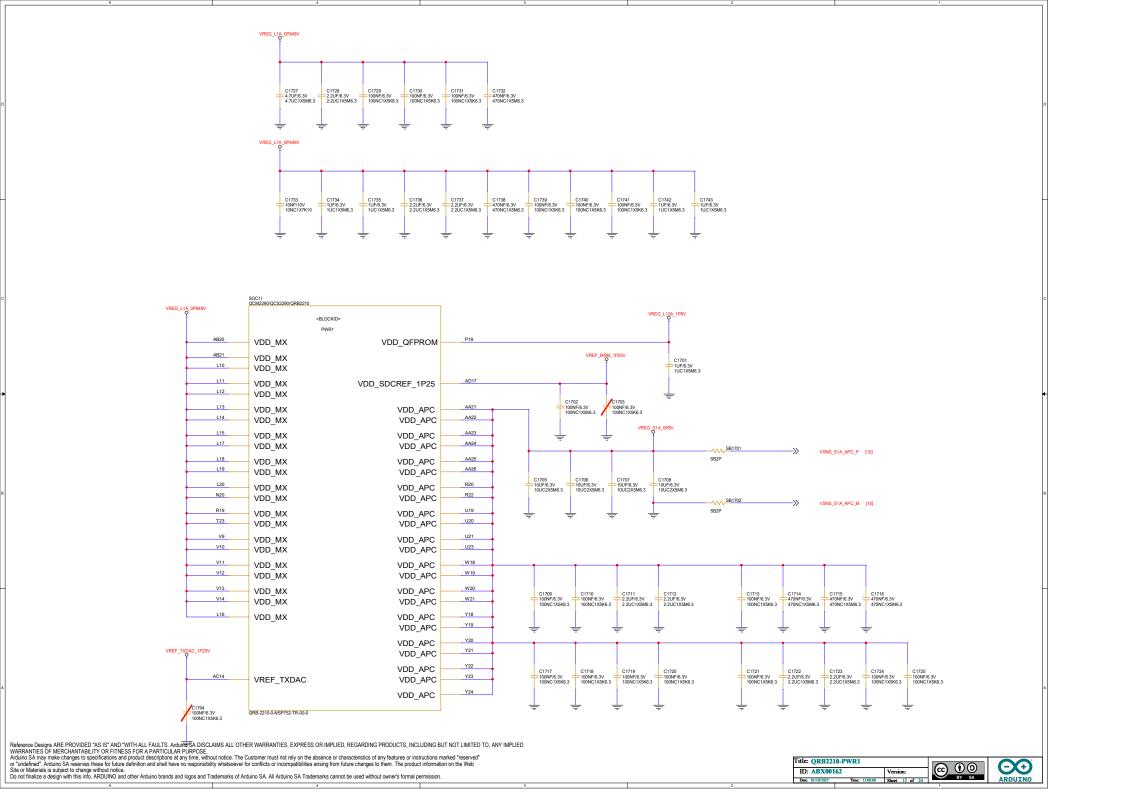
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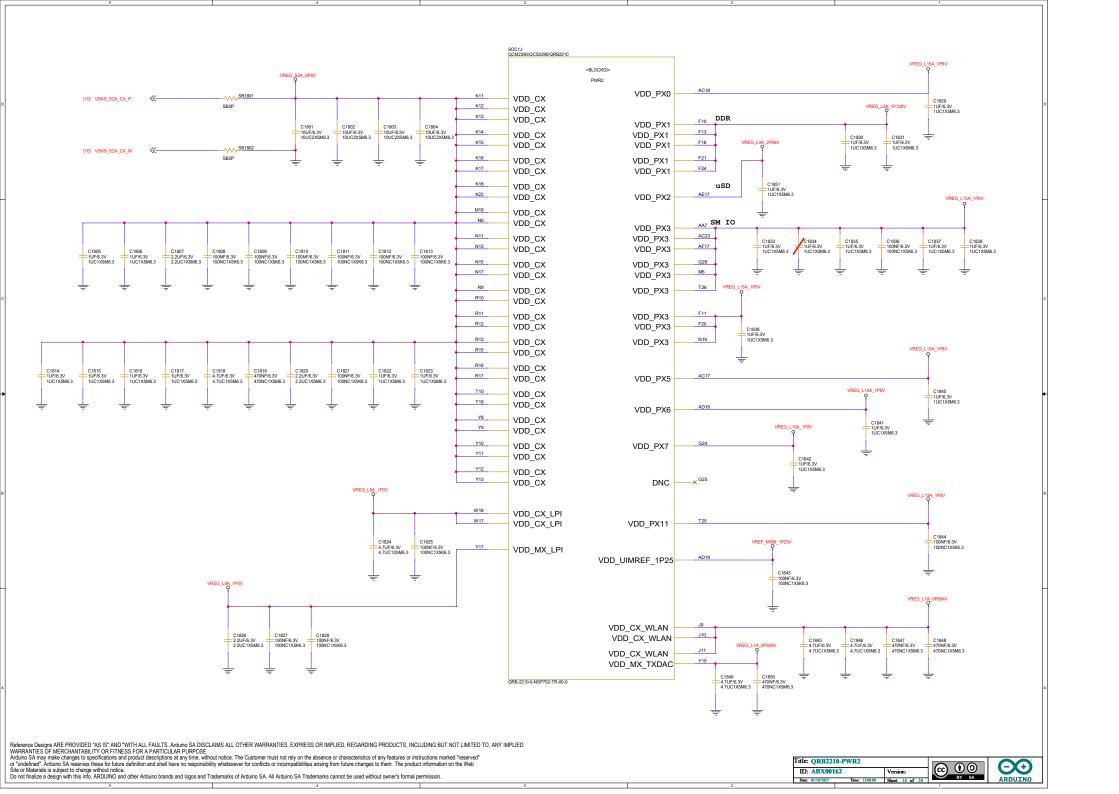
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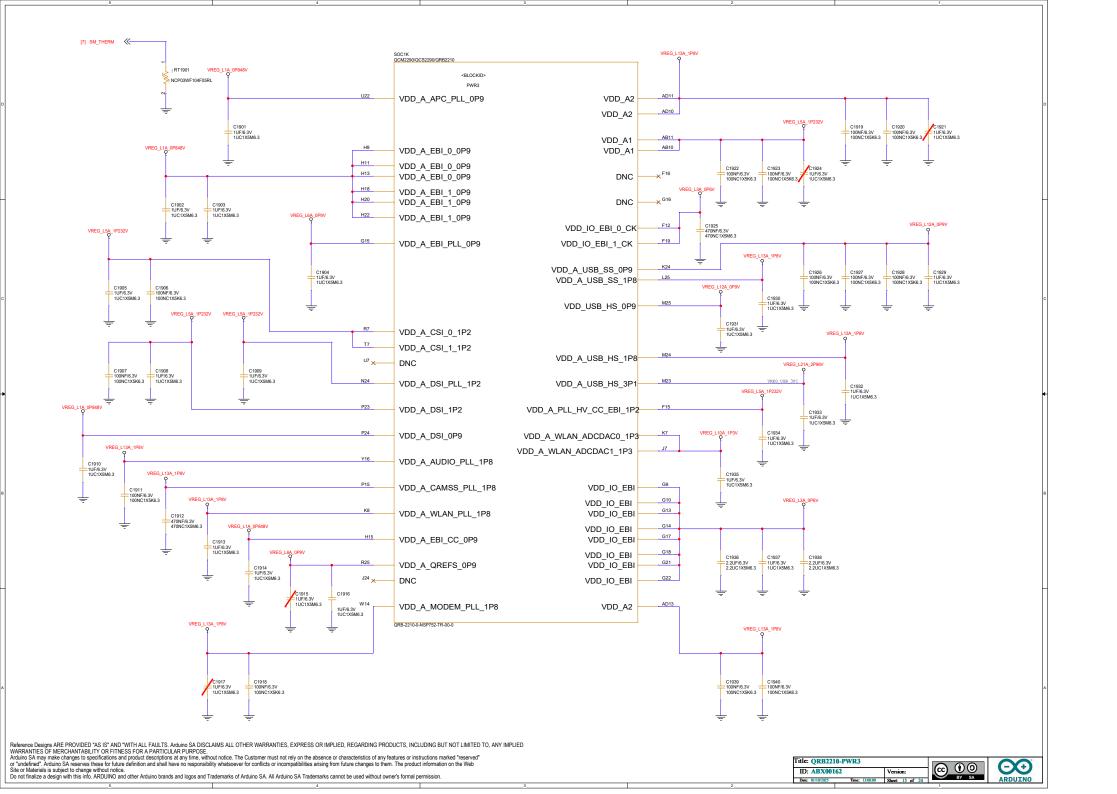
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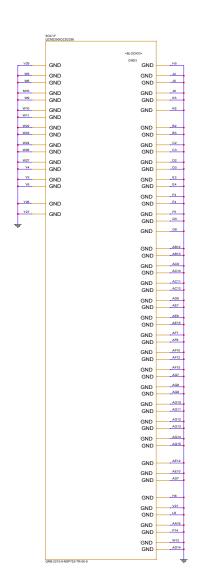


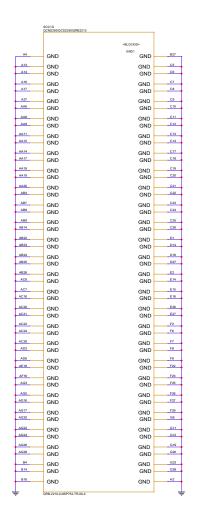


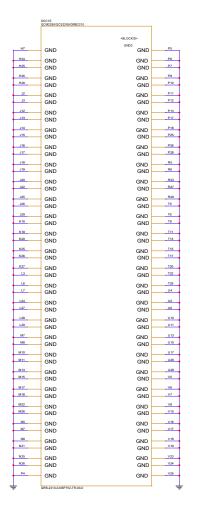












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