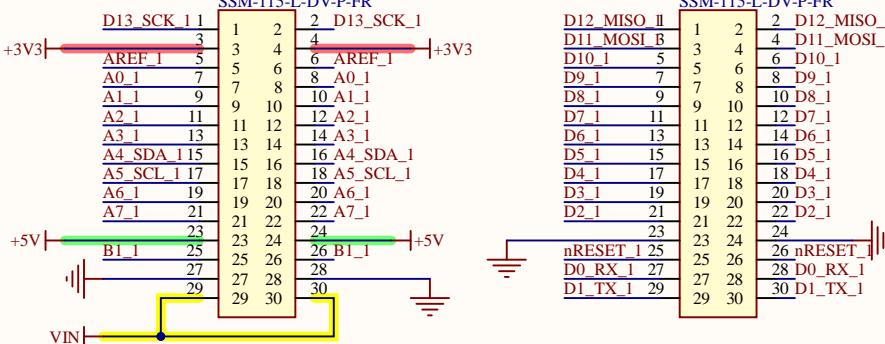
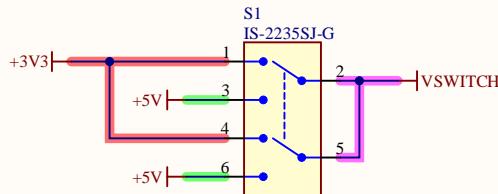


ARDUINO NANO TOP VIEW



POWER SWITCH
Set the switch accordingly to what is the IO voltage of the Nano



- Fiducial_1 ○ Fiducial_2
- Fiducial_3 ○ Fiducial_4

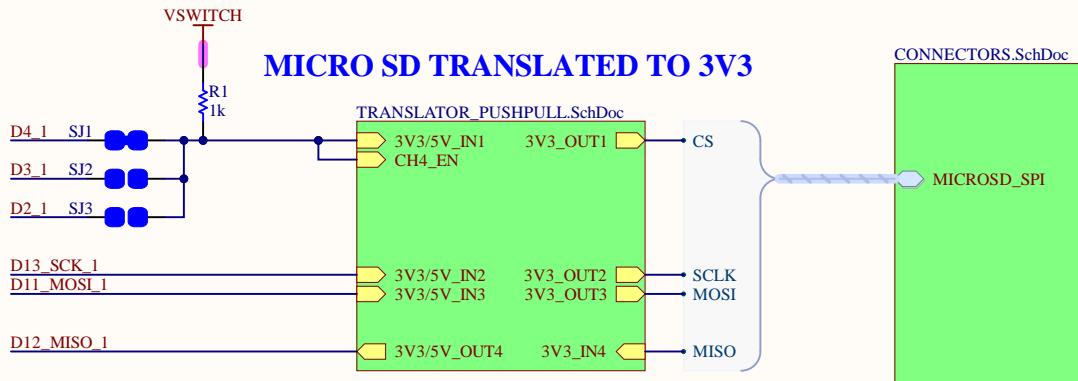
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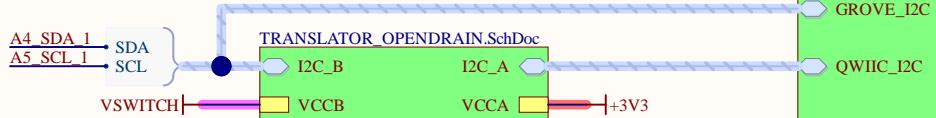
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VSWITCH

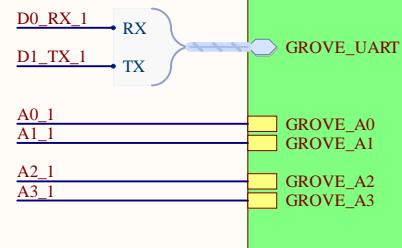
MICRO SD TRANSLATED TO 3V3



GROVE I2C IS NOT TRANSLATED QWIIC TRANSLATED TO 3V3



**GROVE UART and GROVE ANALOGS are not translated.
Their VDD pins are connected to VSWITCH.**



Title: TOP

ID: ASX00061

Version: V0.7

Date: 29/04/2025

Time: 12:55:01

Sheet 1 of 4

File: TOP.SchDoc

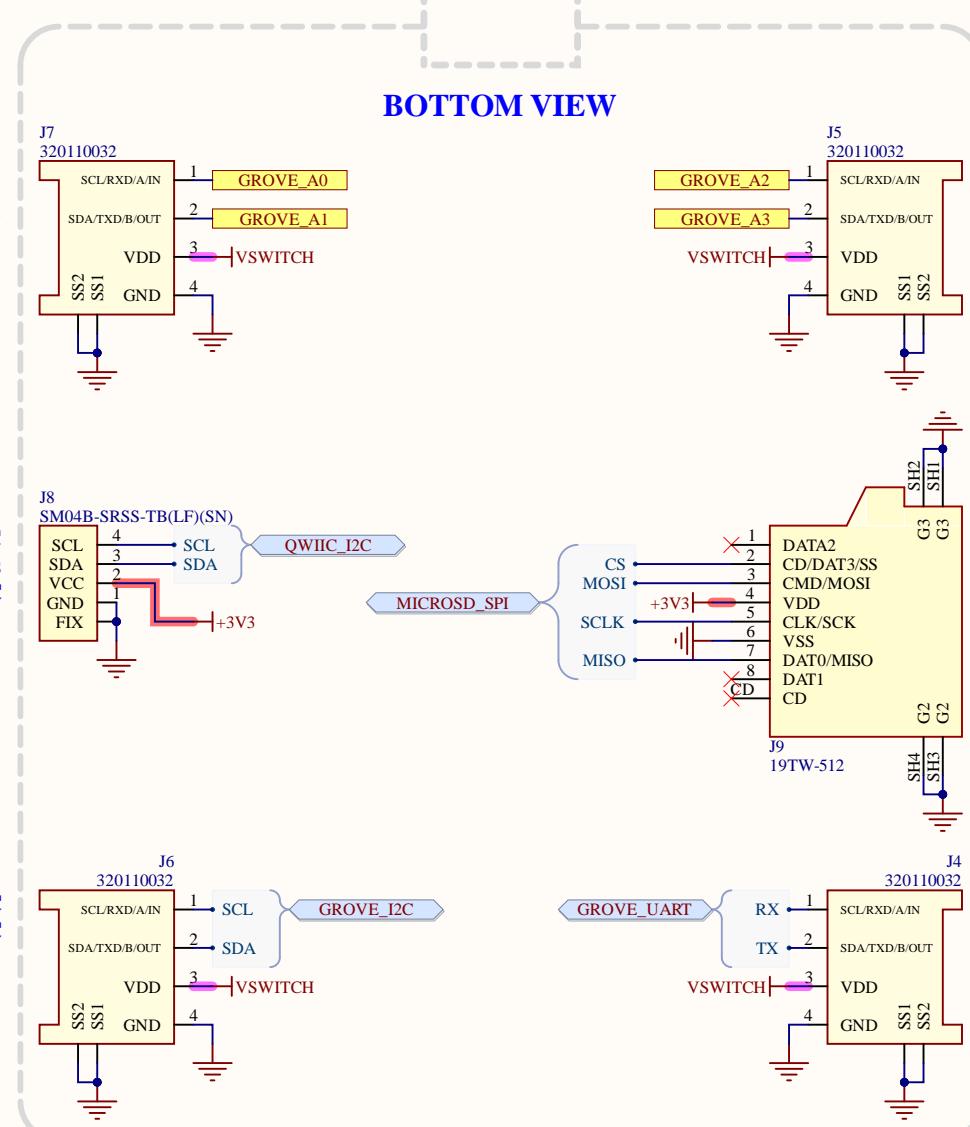
Author: Silvio Navaretti

RevAuthor: Silvio Navaretti



A

A

**GROVE
ANALOG A0, A1**

BOTTOM VIEW

B

B

QWIIC I2C
same as
GROVE I2C
and headers
A4, A5

GROVE I2C
same as QWIIC
and headers
A4, A5

**GROVE
ANALOG A2, A3**
**MICRO SD
CONNECTOR**
**GROVE
UART**

C

C

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Title: CONNECTORS
ID: ASX00061
Version: V0.7
Date: 29/04/2025
Time: 12:55:02
Sheet 2 of 4
File: CONNECTORS.SchDoc
Author: Silvio Navaretti
RevAuthor: Silvio Navaretti


A

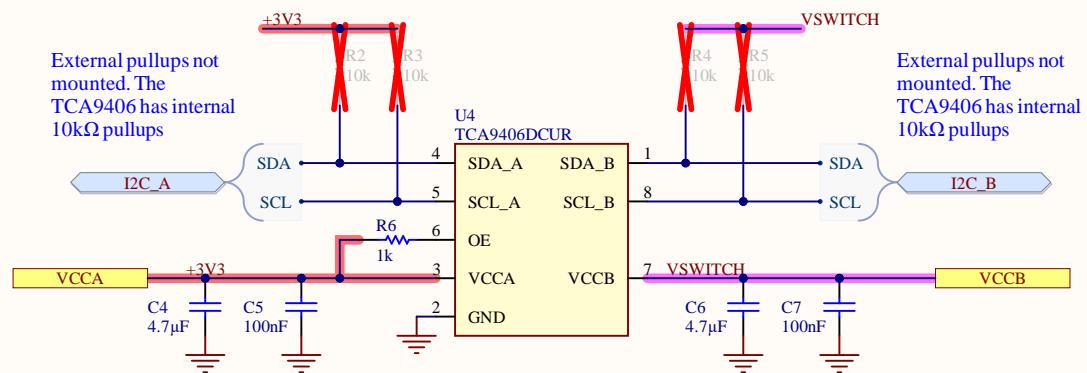
A

A SIDE
1.65V to 3.65V
ESD 2.5kV Human body model

B SIDE
2.3V to 5.5V
ESD 8kV Human body model

B

B



C

C

VCCA \leq VCCB must always be valid
TCA9406 has internal 10k pullups on both sides

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Title: **Translators OPEN DRAIN**

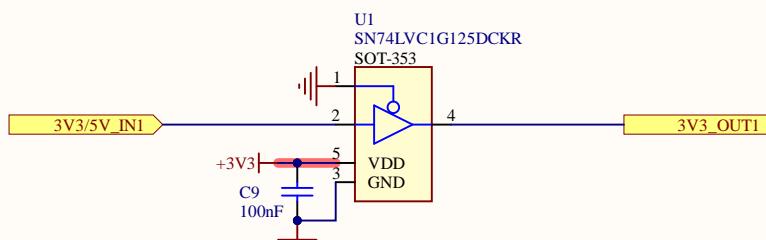
ID: ASX00061	Version: V0.7
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Date: 29/04/2025	Time: 12:55:03	Sheet 3 of 4
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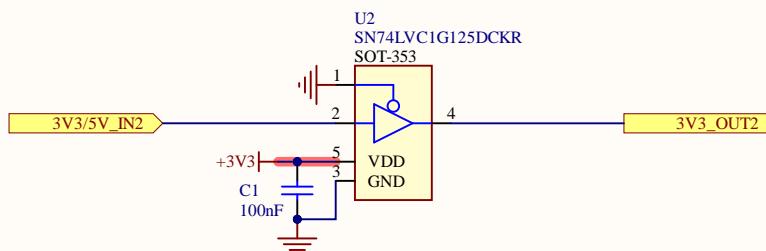
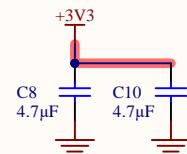
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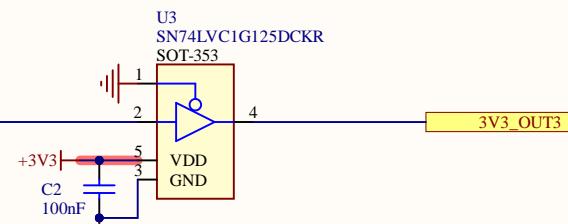
A



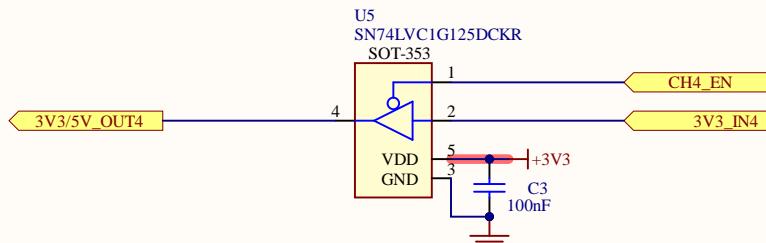
B



C



D



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Title: Translators PUSH PULL

ID: ASX00061

Version: V0.7

Date: 29/04/2025

Time: 12:55:03

Sheet 4 of 4

File: TRANSLATOR_PUSHPULL.SchDoc

Author: Silvio Navaretti



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