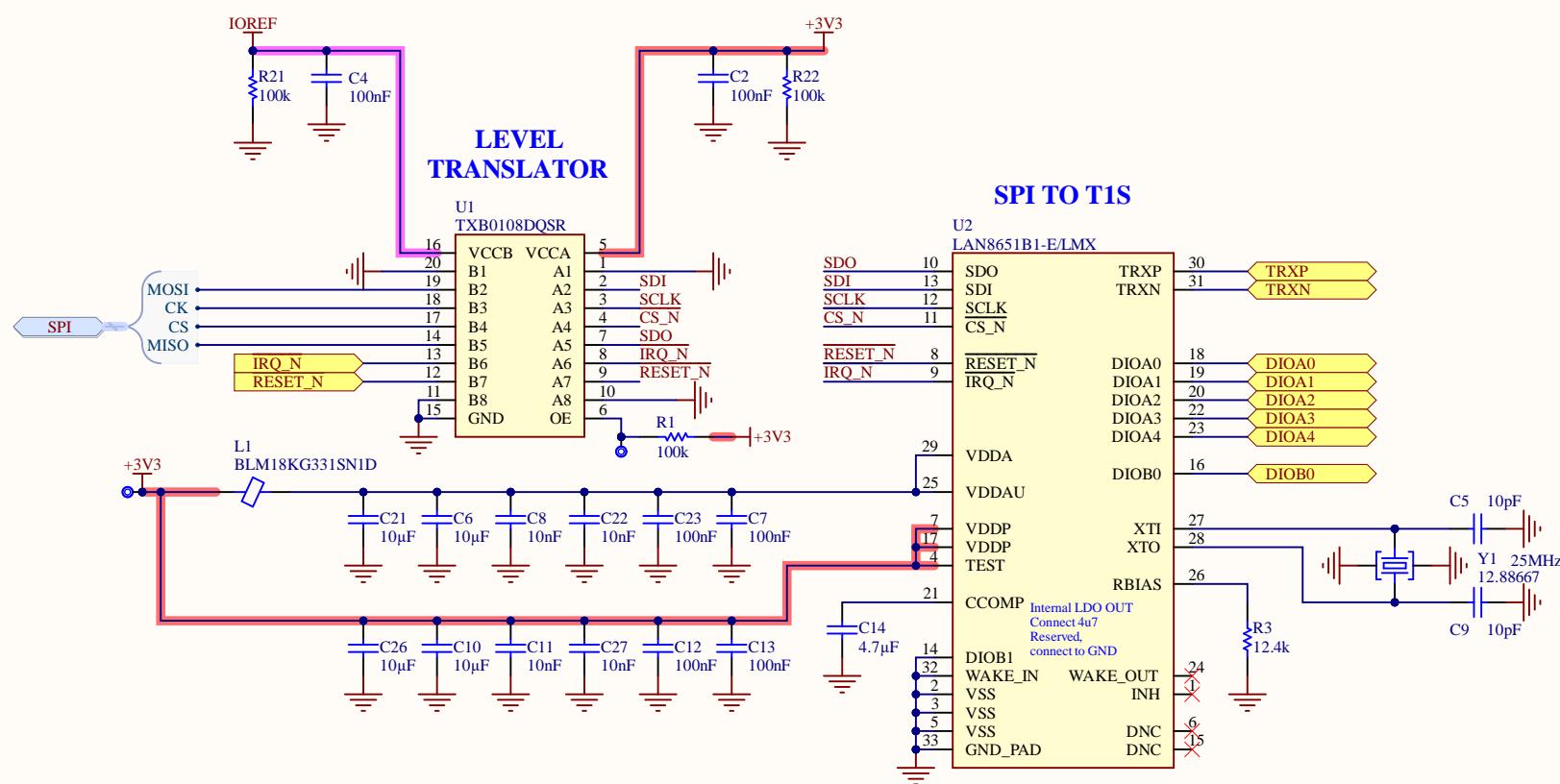


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Title: TOP			
ID: ASX00073	Version: V0.3		
Date: 6/2/2025	Time: 12:02:55 PM	Sheet 1 of 5	
File: TOP.SchDoc	Author: Daniela Catanzaro	RevAuthor: Silvio Navaretti	

A



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Title: T1S

ID: ASX00073

Version: V0.3

Date: 6/2/2025

Time: 12:02:57 PM

Sheet 2 of 5

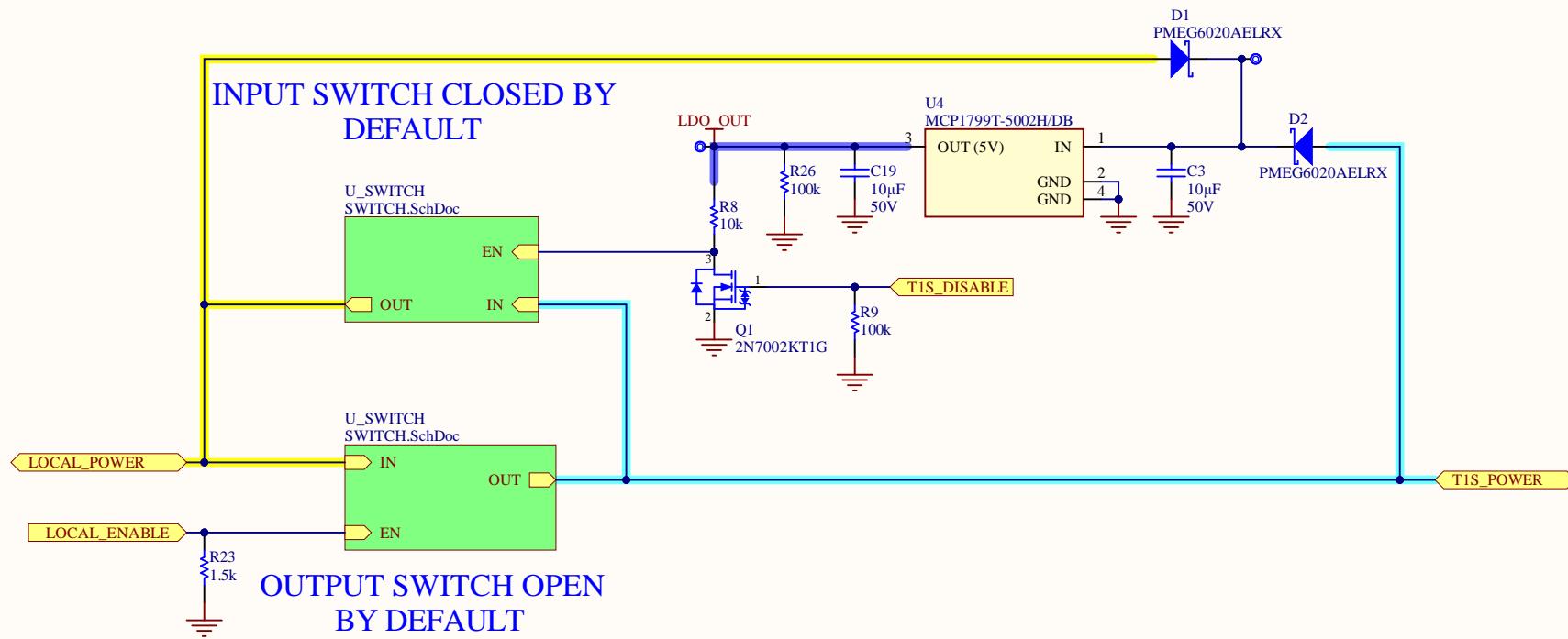
File: T1S.SchDoc

Author: Daniela Catanzaro



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Title: POWER SWITCHES

ID: ASX00073 Version: V0.3

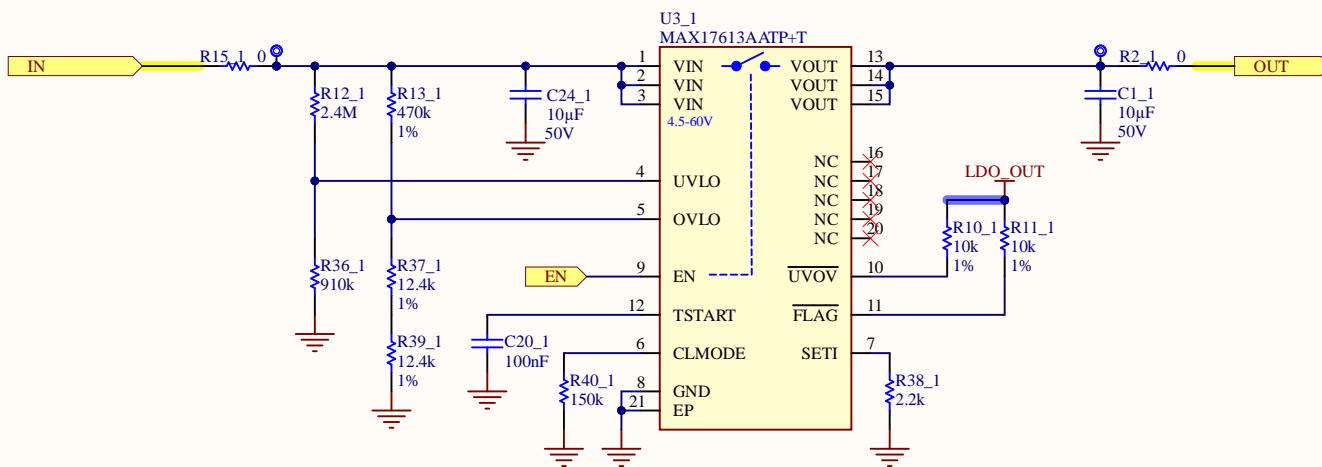
Date: 6/2/2025 Time: 12:02:58 PM Sheet 3 of 5

File: T1S_POWER.SchDoc



A

**OVLO SET AT 29.9V
UVLO SET UP 4.2V NOMINAL
OVERCURRENT PROTECTION AT 2A**



B

C

D

TSTART: unconnected for default 100ms start time
CLMODE: unconnected for auto retry mode

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Title: SWITCH

ID: ASX00073 Version: V0.3

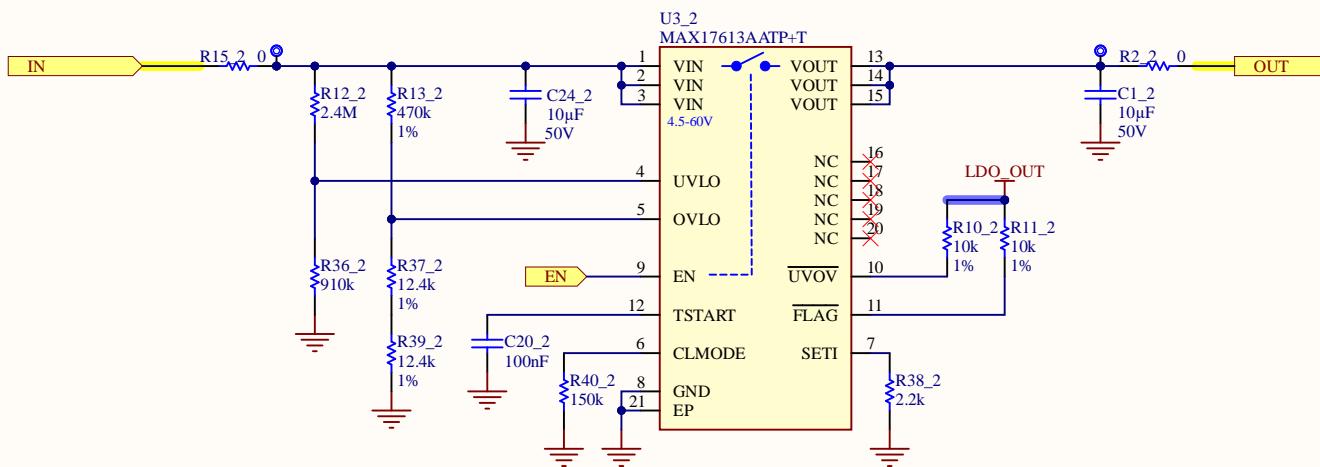
Date: 6/2/2025 Time: 12:03:00 PM Sheet 4 of 5

File: SWITCH.SchDoc Author: Daniela Catanzaro RevAuthor: Silvio Navaretti



A

**OVLO SET AT 29.9V
UVLO SET UP 4.2V NOMINAL
OVERCURRENT PROTECTION AT 2A**



TSTART: unconnected for default 100ms start time
CLMODE: unconnected for auto retry mode

B

A

C

B

D

C

D

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Title: SWITCH

ID: ASX00073	Version: V0.3
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Date: 6/2/2025	Time: 12:03:02 PM	Sheet 4 of 5
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File: SWITCH.SchDoc	Author: Daniela Catanzaro	RevAuthor: Silvio Navaretti
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A

A

B

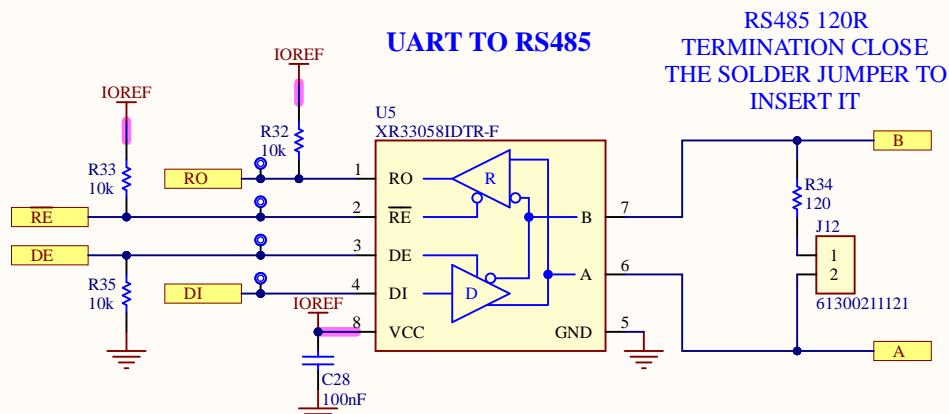
B

C

C

D

D



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Title: **UART-RS485 Transceiver**

ID: **ASX0073** Version: **V0.3**

Date: **6/2/2025** Time: **12:03:03 PM** Sheet **5 of 5**

File: **UART-RS485 Transceiver.SchDoc** Author: **Daniela Catanzaro** RevAuthor: **Silvio Navaretti**

