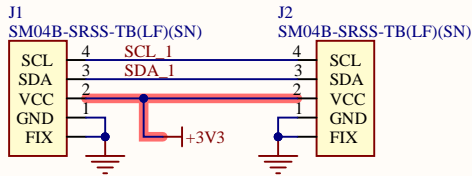
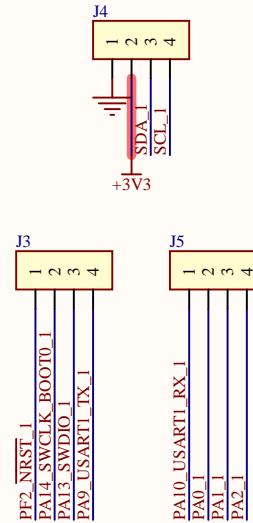
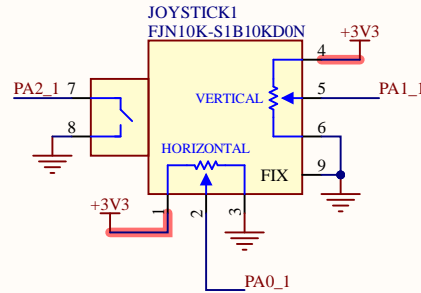
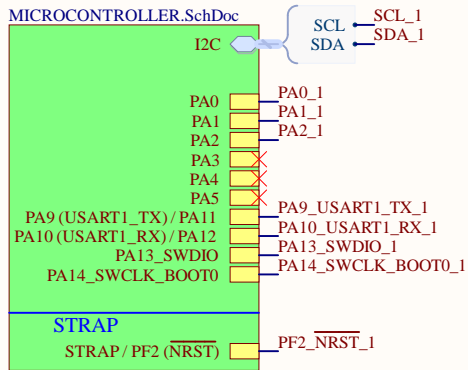


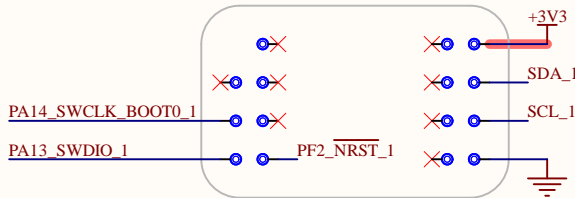
I2C CONNECTORS



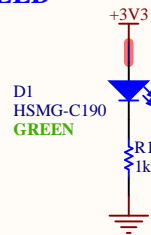
MICROCONTROLLER



TEST POINTS BOTTOM VIEW



POWER LED



QR1
QR CODE 4x4mm

Fiducial_1
Fiducial mark 1mm

Fiducial_2
Fiducial mark 1mm

Fiducial_3
Fiducial mark 1mm

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Title: **TOP**

ID: **ABX00135**

Date: **03/12/2025**

File: **TOP.SchDoc**

Version: **V0.3**

Time: **17:29:14**

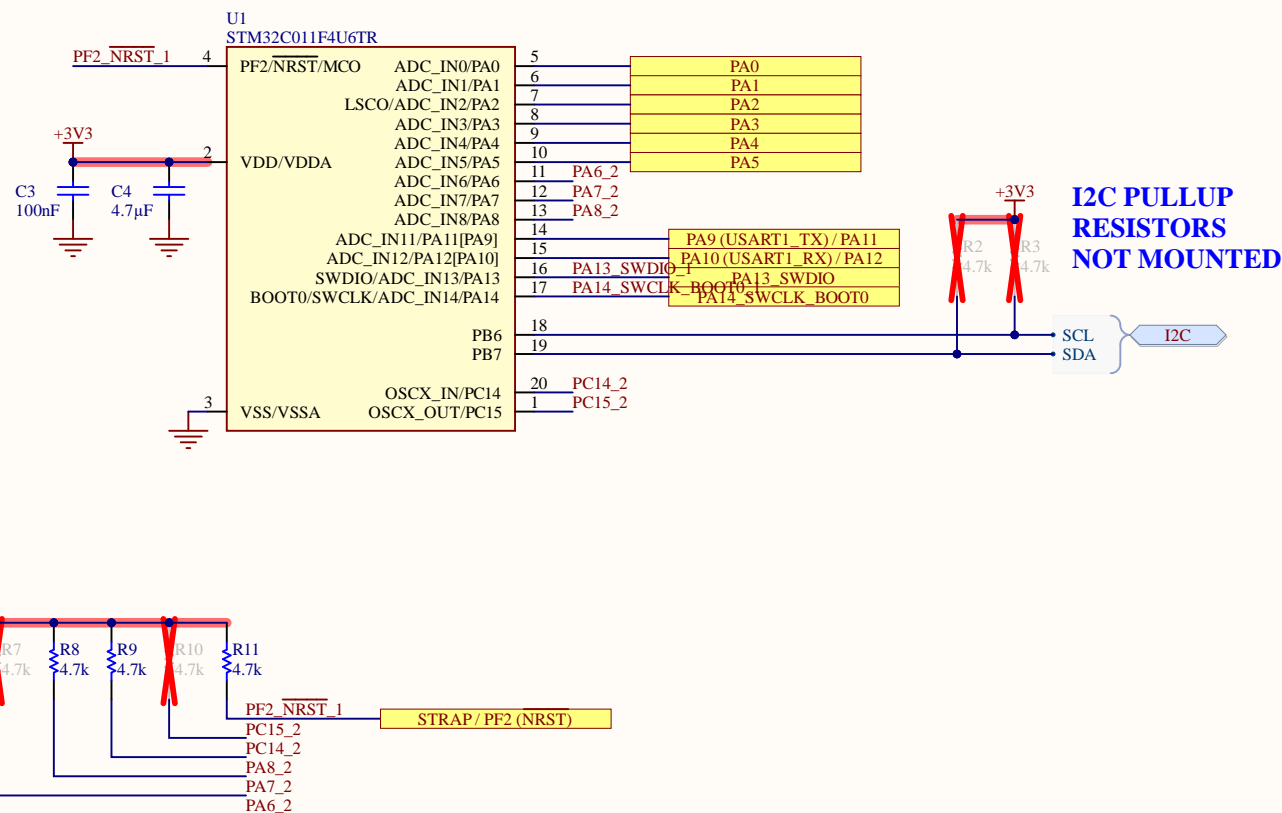
Sheet **1** of **2**

Author: **Silvio Navaretti**

RevAuthor: **Silvio Navaretti**



1. Pins PA9 and PA10 can be remapped in place of pins PA11 and PA12 (default mapping), using SYSCFG_CFGR1 register.
2. Upon reset, these pins are configured as SWD alternate functions, and the internal pull-up on PA13 pin and the internal pull-down on PA14 pin are activated.



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Title: **MICROCONTROLLER**

ID: ABX00135

Version: V0.3

Date: 03/12/2025 Time: 17:29:15

Sheet 2 of 2

File: MICROCONTROLLER.SchDoc

Author: **Silvio Navaretti**

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