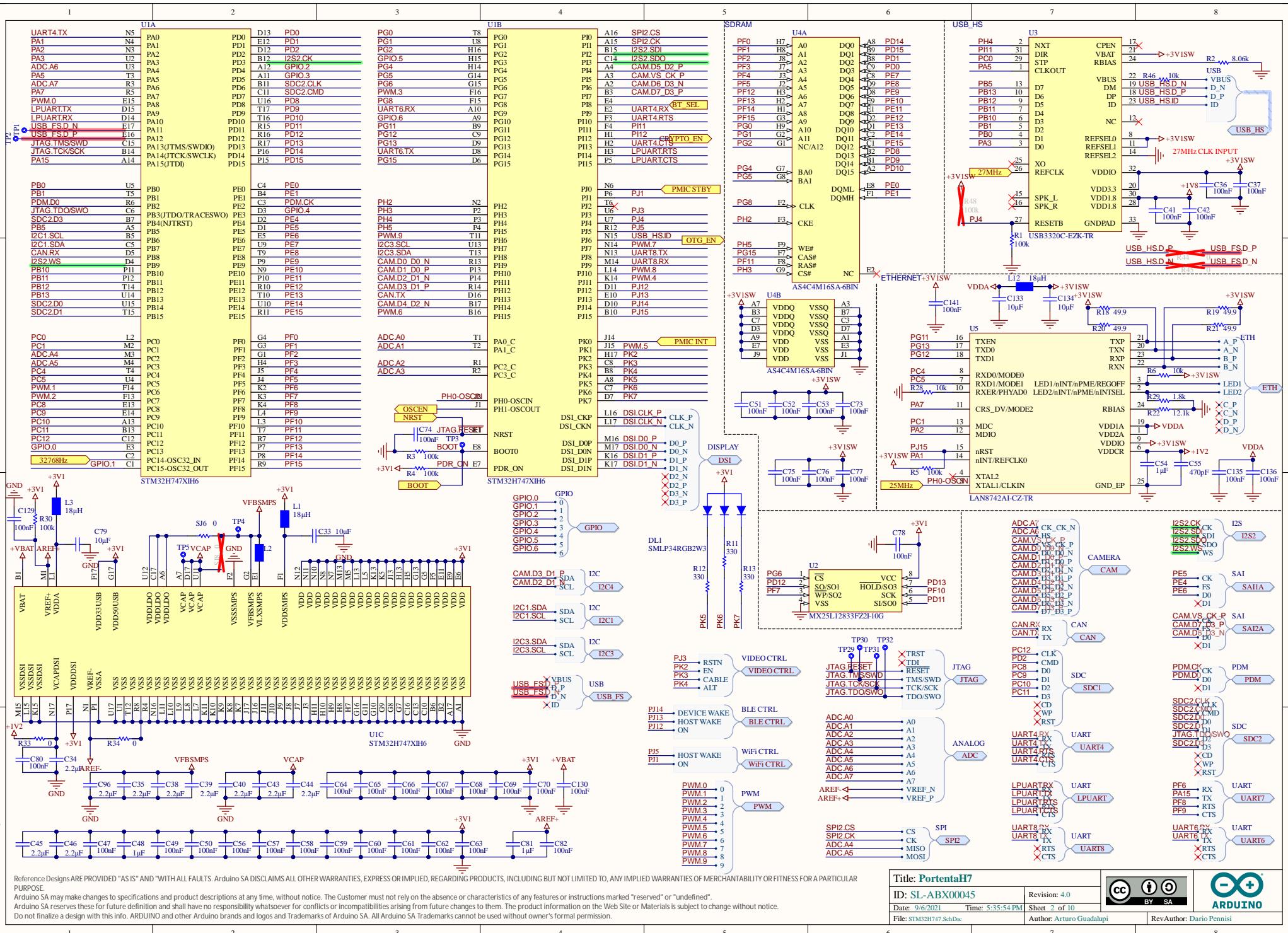


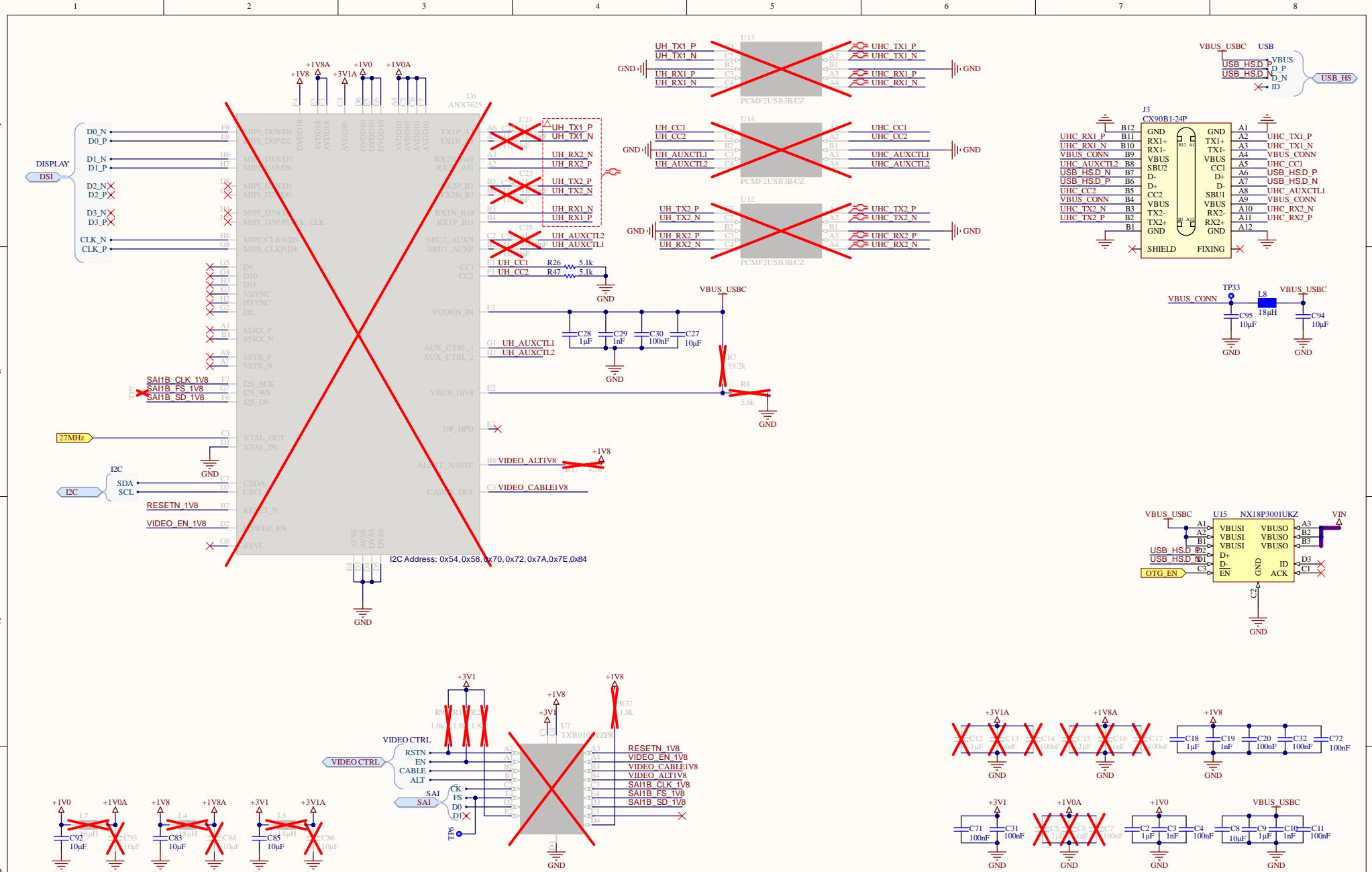
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Title: PortentaH7			
ID: SL-ABX00045	Revision: 4.0		
Date: 9/6/2021	Time: 5:35:53 PM	Sheet 1 of 10	
File: TopView.SchDoc	Author: Arturo Guadalupe	RevAuthor: Dario Pennisi	





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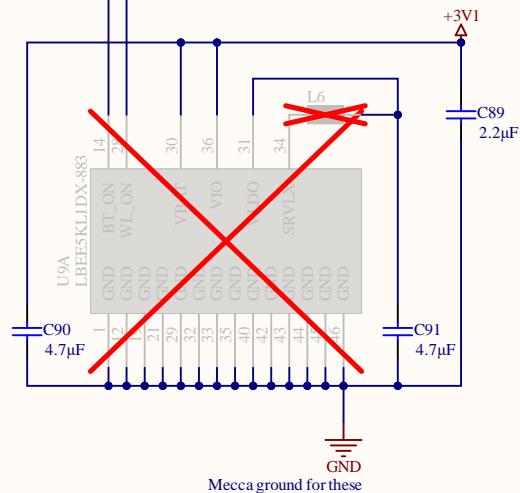
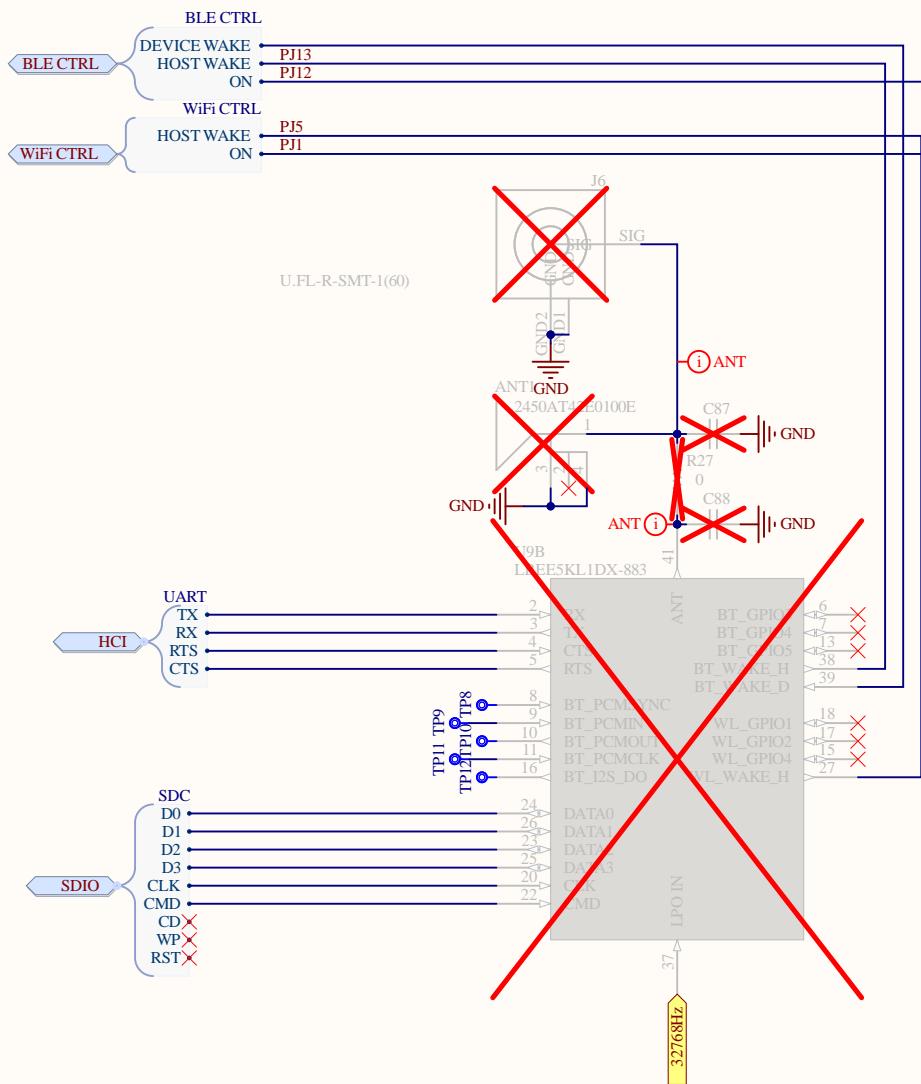
Title: PortentaH7

ED\_GARDEN17

ID: SL-ABX00045

Date: 9/6/2021





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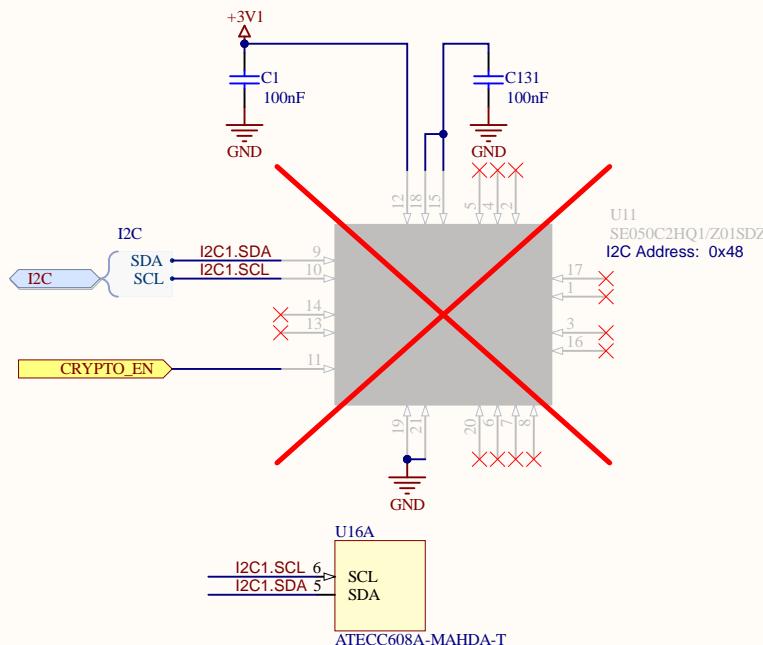
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ID: SL-ABX00045

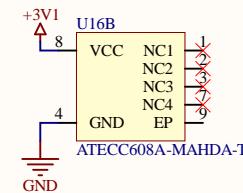
revision: 4.0



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Title: PortentaH7

ID: SL-ABX0045

Revision: 4.0

Date: 9/6/2021

Time: 5:35:58 PM

Sheet 5 of 10

File: Cryptochip.SchDoc

Author: ArturoGuadalupi



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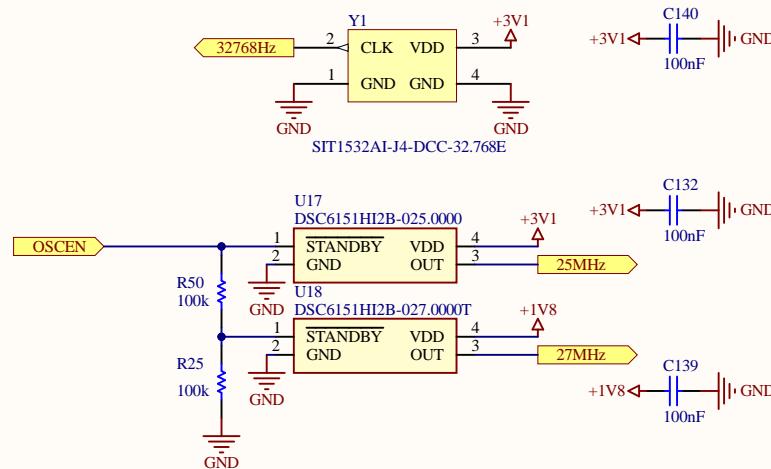
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Title: **PortentaH7**

ID: **SL-ABX0045**

Revision: **4.0**

Date: **9/6/2021** Time: **5:35:59 PM** Sheet **6** of **10**

File: **Oscillators.SchDoc**

Author: **Arturo Guadalupi**

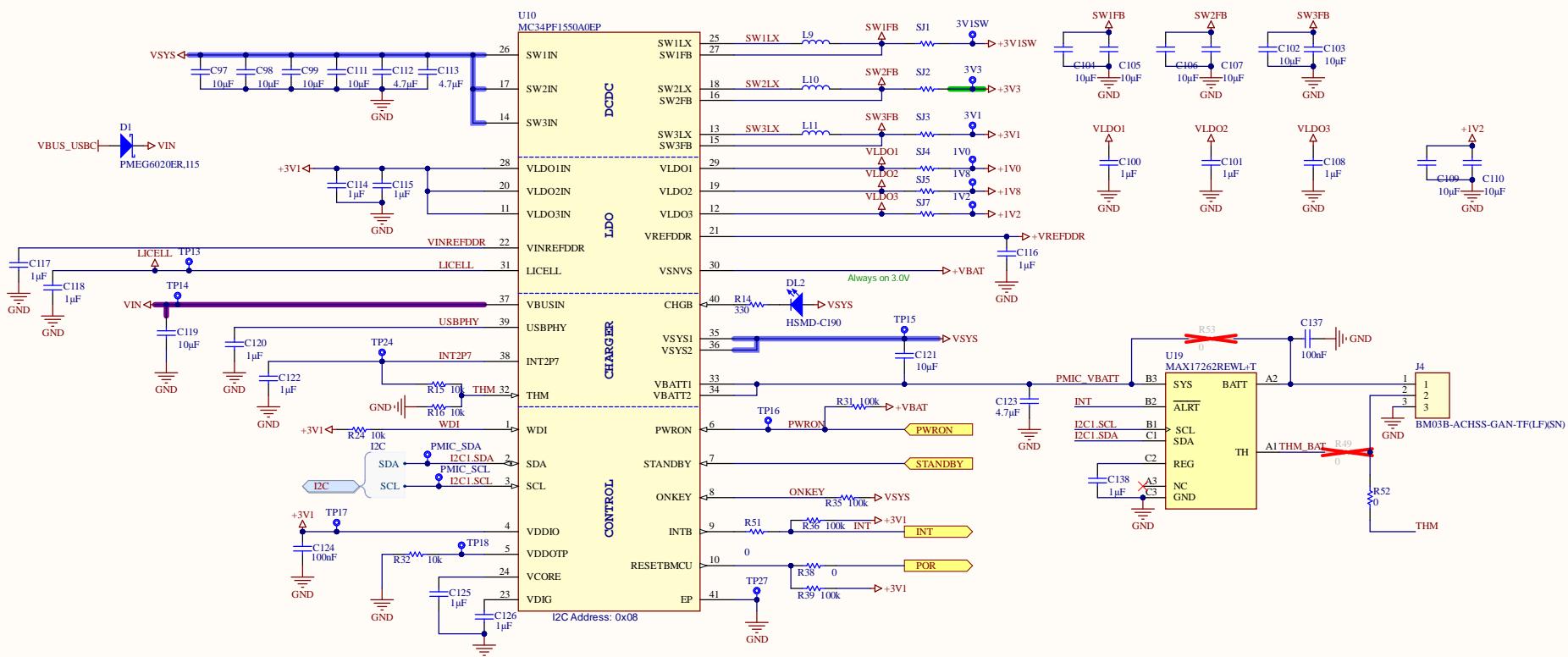
RevAuthor: **Dario Pennisi**



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## Title: PortentaH7

ID: SI-ABX0004

Date: 8/6/2021

Date: 9/6/2021 File: Power SchDoc



dalipi | RevAuthor: Dario Pennisi

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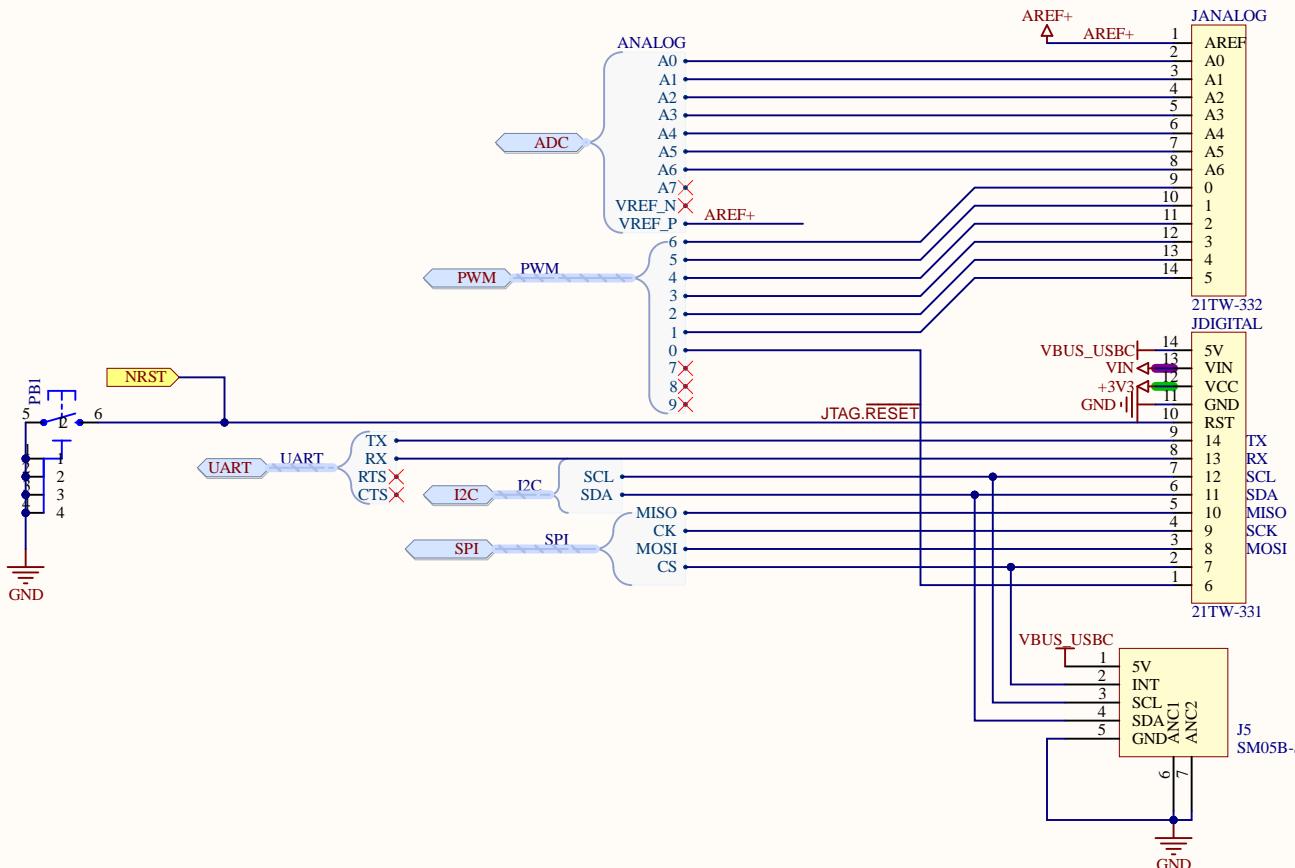
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Title: **PortentaH7**

ID: **SL-ABX0045**

Revision: **4.0**

Date: **9/6/2021**

Time: **5:36:01 PM**

Sheet **8** of **10**

File: **MKR.SchDoc**

Author: **Arturo Guadalupe**



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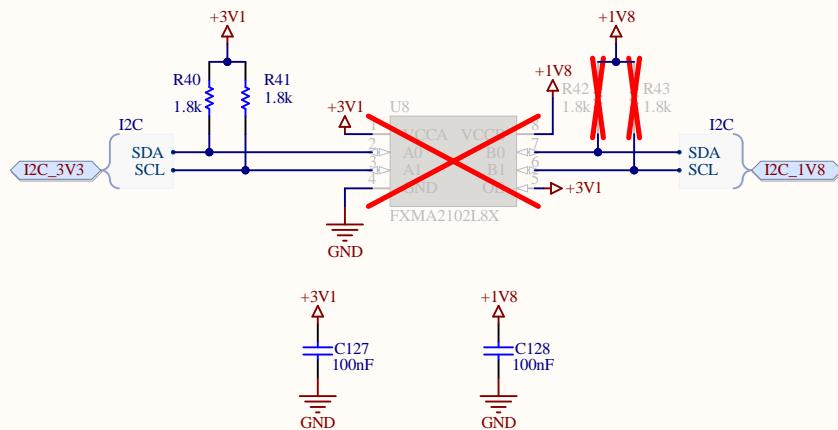
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Title: PortentaH7

ID: SL-ABX0045

Revision: 4.0

Date: 9/6/2021 Time: 5:36:02 PM Sheet 9 of 10

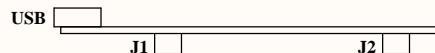
File: I2CLevelShifter.SchDoc

Author: Arturo Guadalupe

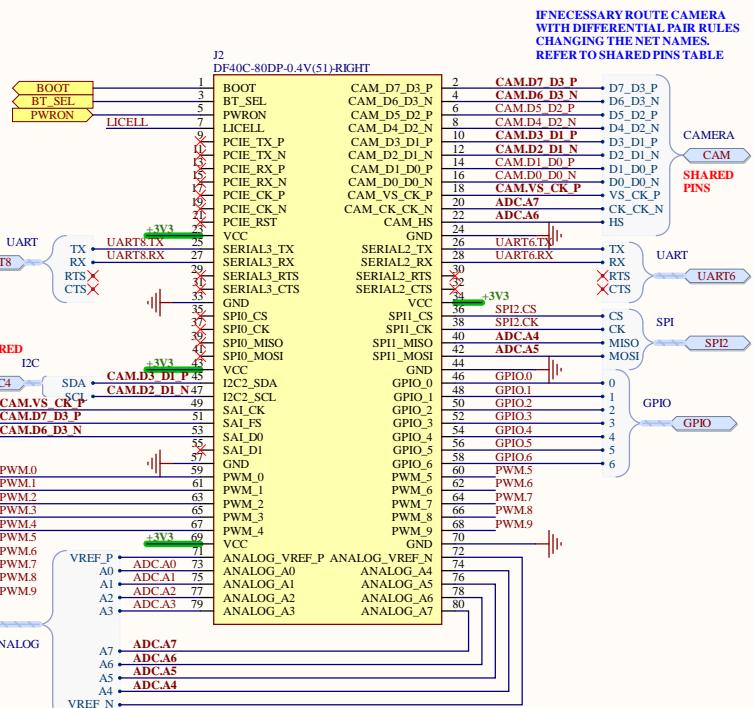
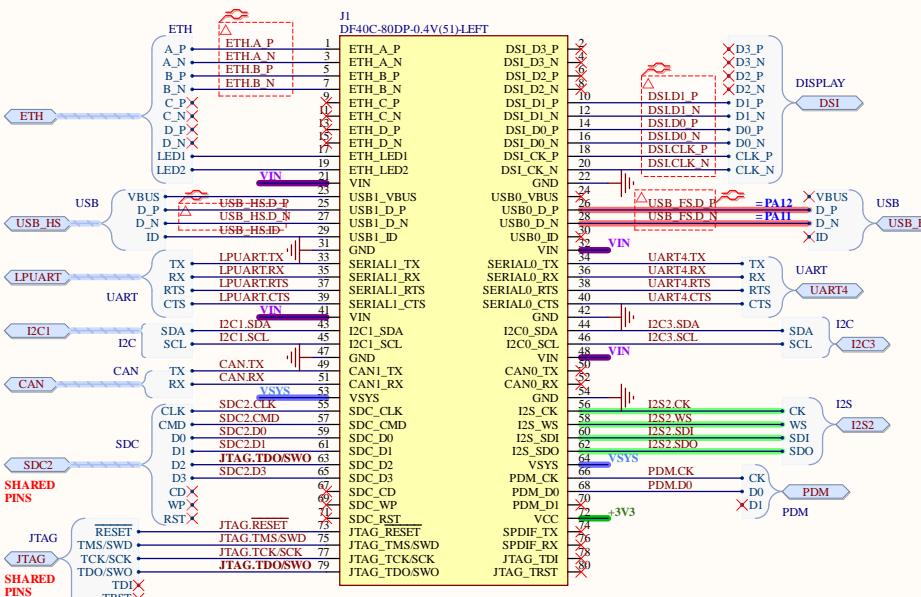
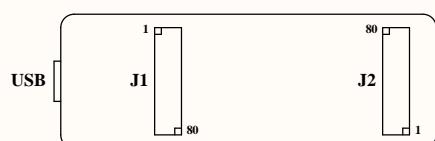
RevAuthor: Dario Pennisi



### **Board side view:**



#### Board BOTTOM view:



## SHARED PINS TABLE

NET	BUS 1	BUS 2
JTAG_TDO_SWO	JTAG	SDC2
CAM_D2_D1_N	CAM	I2C4
CAM_D3_D1_P	CAM	I2C4
CAM_VS_CLK_P	CAM	SAI2A
CAM_D6_D3_N	CAM	SAI2A
CAM_D7_D3_P	CAM	SAI2A
ADC_A6	CAM	ANALOG
ADC_A7	CAM	ANALOG
ADC_A4	ANALOG	SP12
ADC_A5	ANALOG	SP12

## POWER NETS TABLE

NET	TYPE	RANGES	DESCRIPTION
VIN -3V3 VSYS LICELL	<b>PORTENTA INPUT</b> <b>PORTENTA OUTPUT</b> <b>PORTENTA RESERVED OUTPUT</b> <b>PORTENTA INPUT</b>	4.1V to 6V. 1.1V to 3.3V in steps, max 1A. RESERVED, DO NOT USE 3.5V to 4.2V, max 600mA. Coin cell max 3.6V, max 46uA.	Default 3.3V, PMIC (U10) programmable output. Default 3.3V, PMIC (U10) programmable output. Default 4.2V, PMIC (U10) programmable output which is also the input voltage of the buck inside the PMIC itself. Max 4uA with PMIC (U10) in coin cell mode, max 46uA with PMIC in standby/suspend mode.

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## Title: HD Connectors

ID: SL ARX00045

ID: SL-ABA00045

File: HDG-001-SubB01

