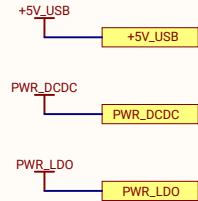
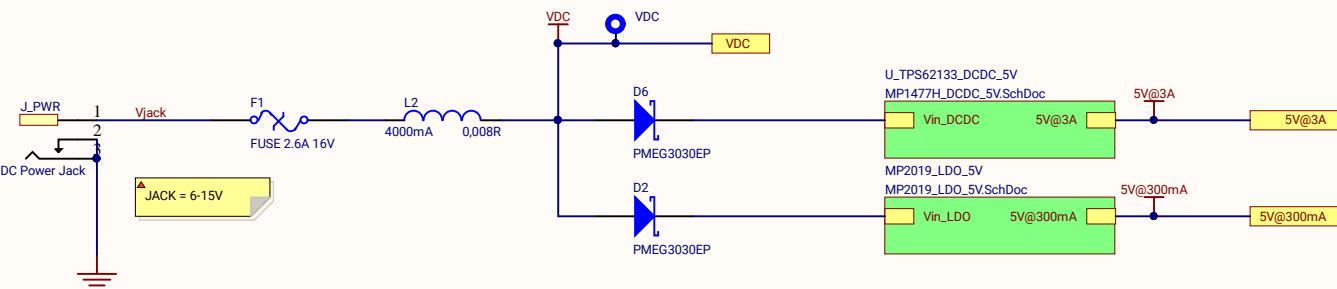
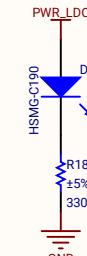
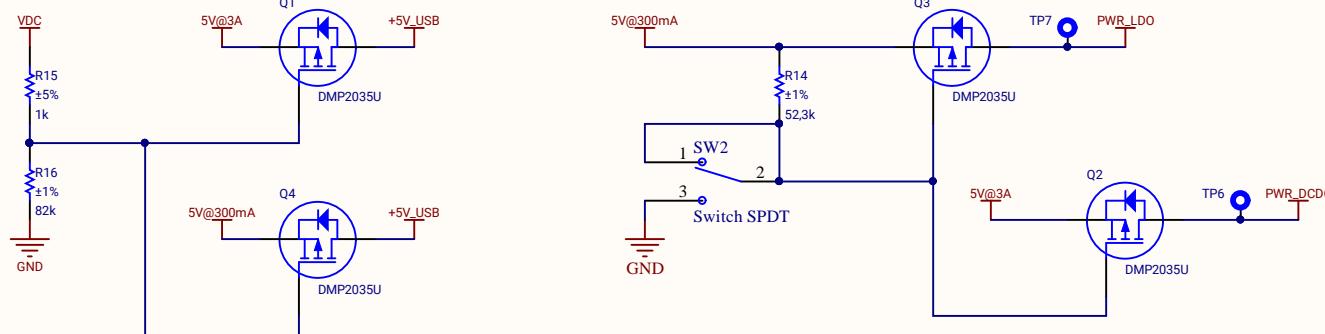


A



B


**GLOBAL SWITCH:**

Powered by Jack:

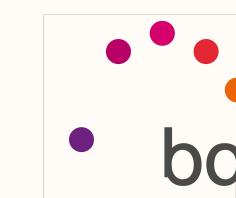
- SW2 ON => Q2 ON => PWR\_DCDC = 5V@3A
- Q3 ON => PWR\_LDO = 5V@300mA
- SW2 OFF => Q2 and Q3 OFF => NO PWR

Powered by +5V\_USB:

- SW2 ON => PWR\_DCDC and PWR\_LDO = 5V@500mA.
- SW2 OFF => NO PWR.

Powered by either Jack or +5V\_USB:

- Jack powers all PCB.



Title <b>ZUM Core Power</b>		
Project <b>ZUM_Core.PrdPcb</b>		
Author <b>Andrés Gullón</b>		
Size: <b>A4</b>	Number: <b>2</b>	Revision: <b>460</b>
Date: <b>01/06/2018</b>	Time: <b>10:04:44</b>	Sheet <b>2 of 9</b>

A

A

B

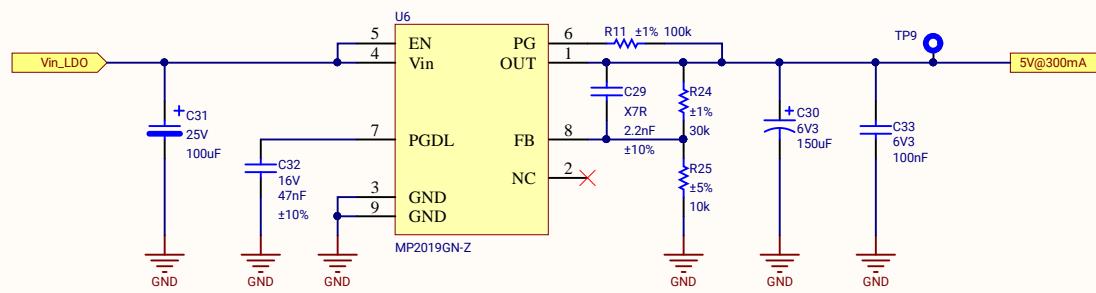
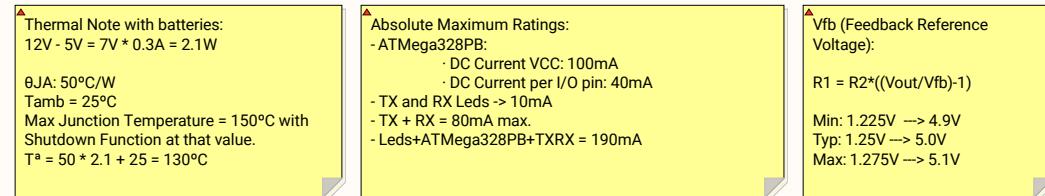
B

C

C

D

D



Title <b>MP2019_LDO_5V@300mA</b>		
Project <b>ZUM_Core.PrdPcb</b>		
Author <b>Andrés Gullón</b>		
Size: <b>A4</b>	Number: <b>3</b>	Revision: <b>466</b>
Date: <b>01/06/2018</b>	Time: <b>10:04:44</b>	Sheet <b>3</b> of <b>9</b>

A

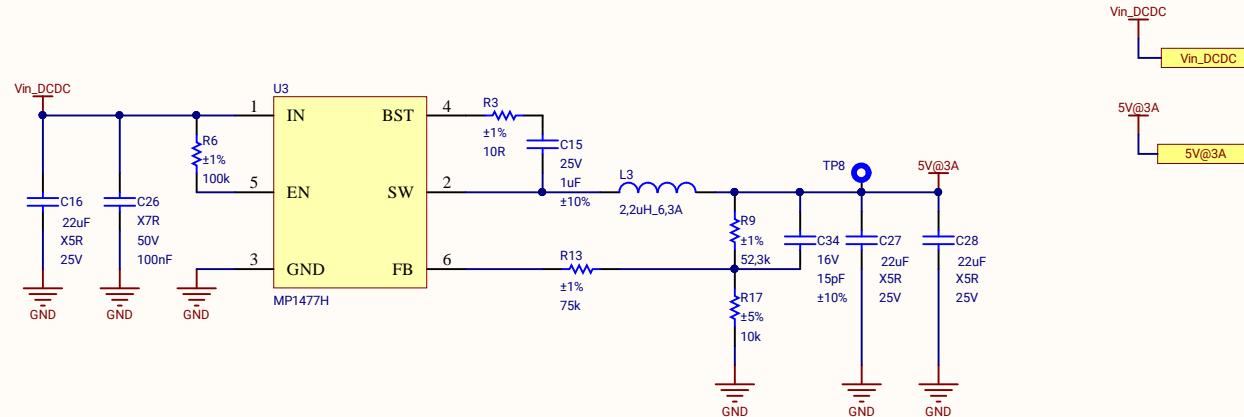
10

▲ Vref: R1 = ((Vout - Vref)/Vref)\*R2  
R1 = 52.3K 1%  
R2 = 10K 1%  
  
Vref:  
Min = 789mV → Vout = 4.92V  
Typ = 805mV → Vout = 5.02V

Input Voltage Range: 4.2V to 17V

B

10



C

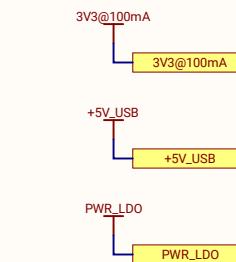
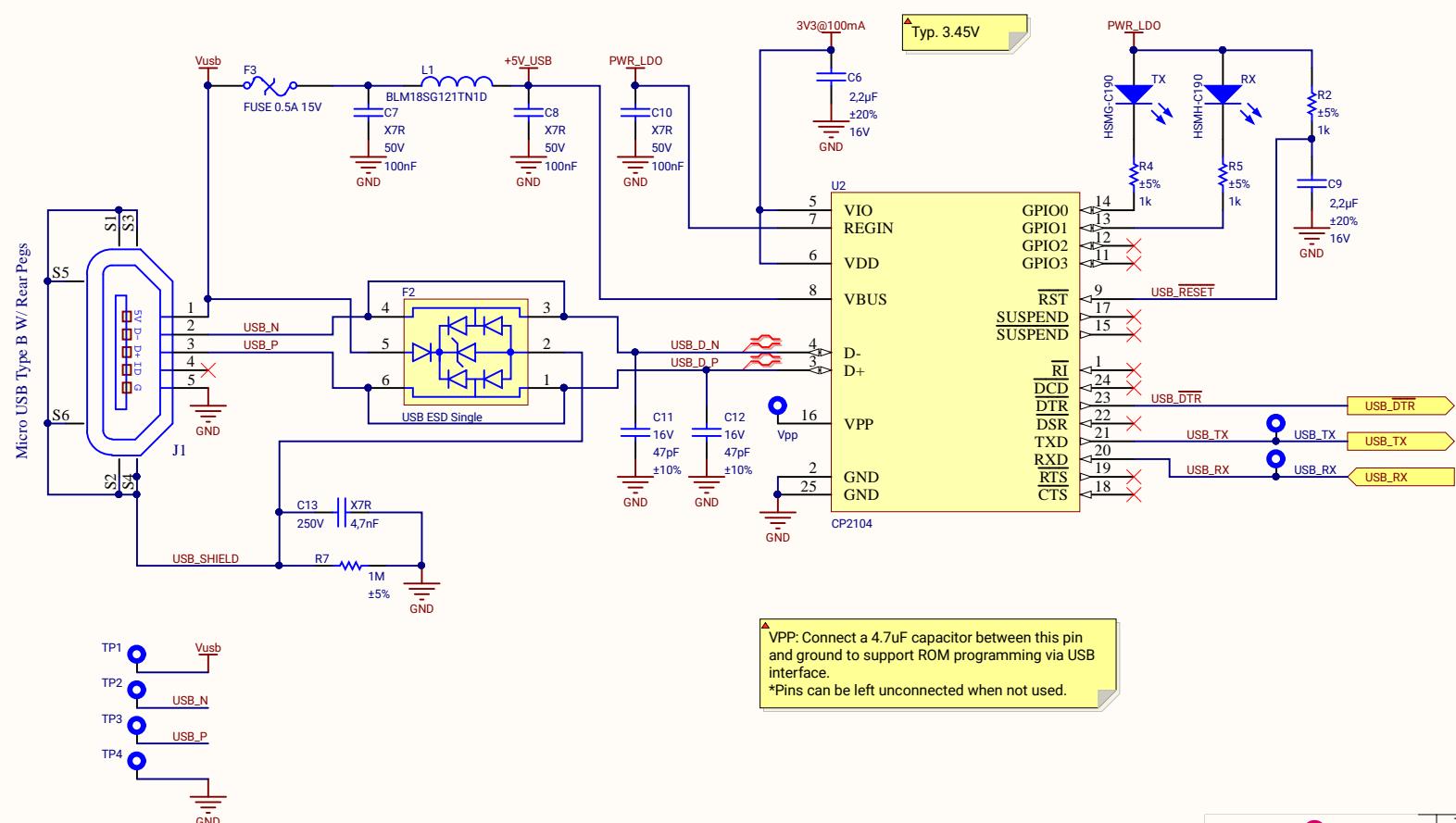
10

B

1



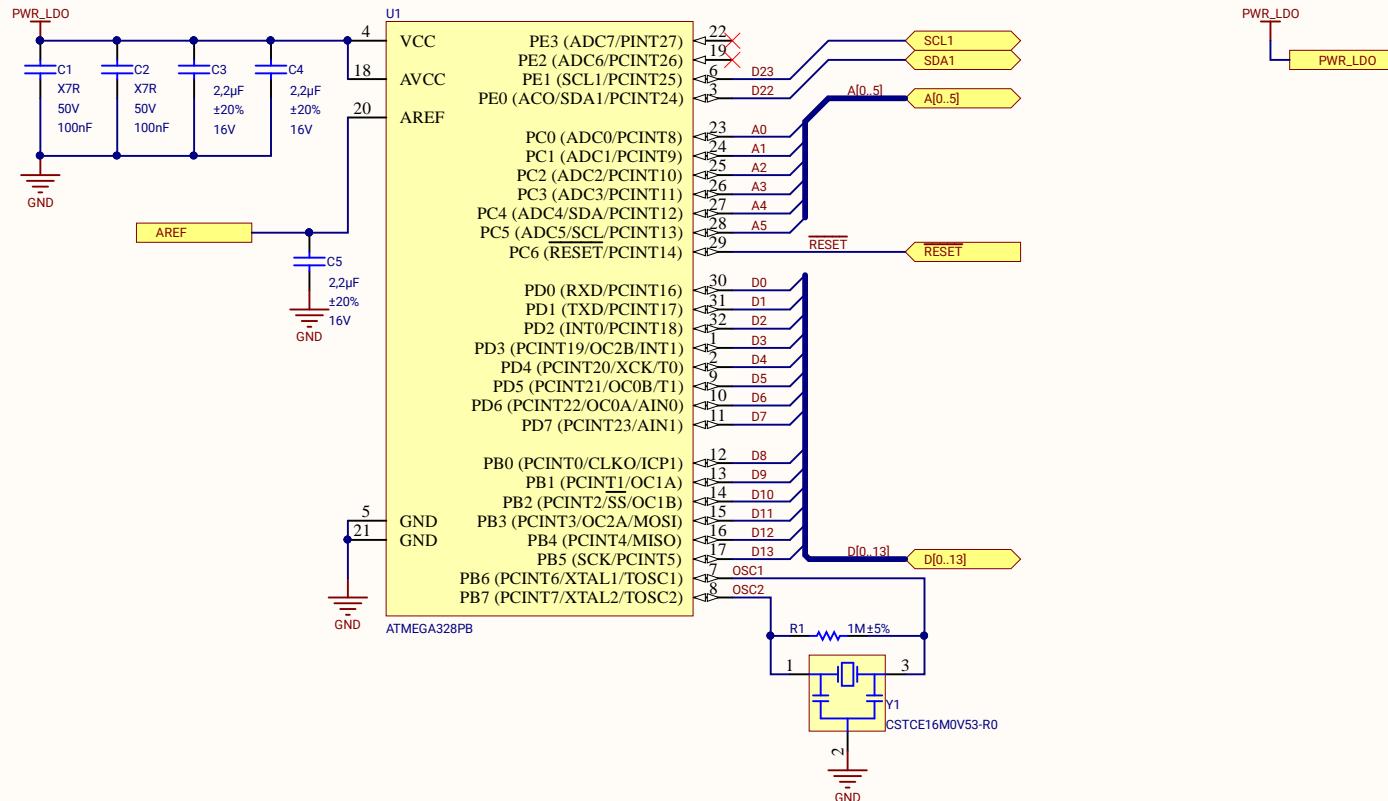
Title	MP1477H DCDC 5V@3A				
Project	ZUM_Core.PrjPcb				
Author	Andrés Gullón				
Size:	A4	Number:	4	Revision:	468
Date:	01/06/2018	Time:	10:04:45	Sheet	4 of 9



Title	CP2104 USB to Serial	
Project	ZUM_Core.PnjPcb	
Author	Andrés Gullón	
Size:	A4	Number: 5 Revision: 466
Date:	01/06/2018	Time: 10:04:45 Sheet 5 of 9

A

A



B

B

C

C

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D



Title <b>ATMega328-PB</b>		
Project <b>ZUM_Core.PrdPcb</b>		
Author <b>Andrés Gullón</b>		
Size: <b>A4</b>	Number: <b>6</b>	Revision: <b>460</b>
Date: <b>01/06/2018</b>	Time: <b>10:04:46</b>	Sheet <b>6 of 9</b>

A

A

B

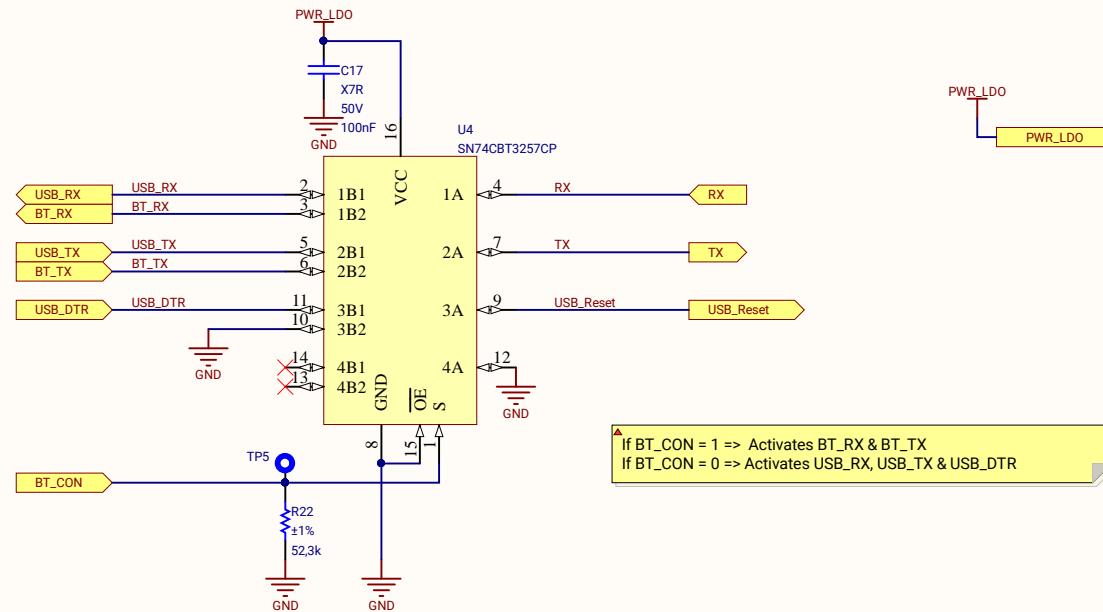
B

C

C

D

D



Title ZUM Core USB BT		
Project ZUM_Core.PrdPcb		
Author Andrés Gullón		
Size: A4	Number: 7	Revision: 460
Date: 01/06/2018	Time: 10:04:46	Sheet 7 of 9

A

10

B

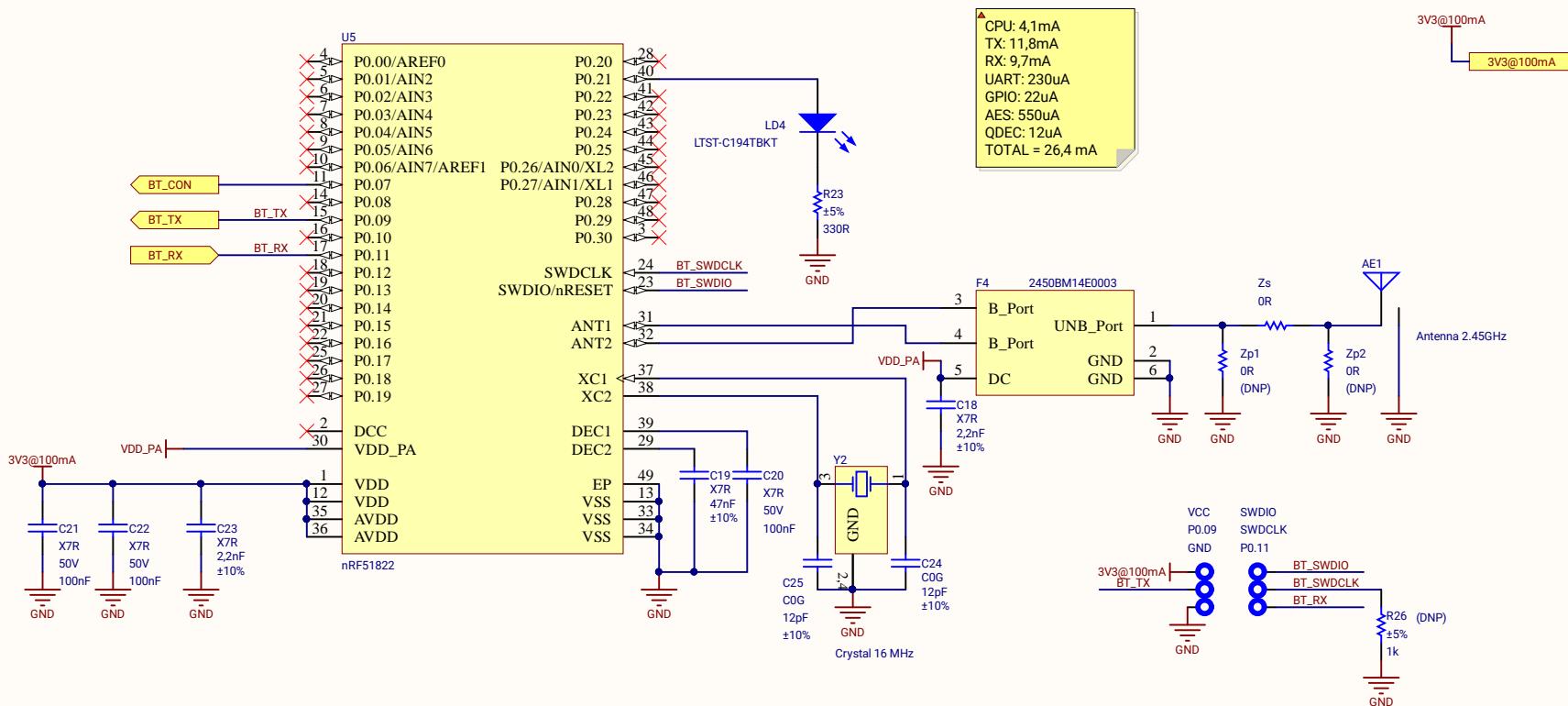
104

C

10

D

1



Title	Bluetooth				
Project	ZUM_Core.PrjPcb				
Author	Andrés Gullón				
Size:	A4	Number:	8	Revision:	463
Date:	01/06/2018	Time:	10:04:47	Sheet	8 of 9

A

A

B

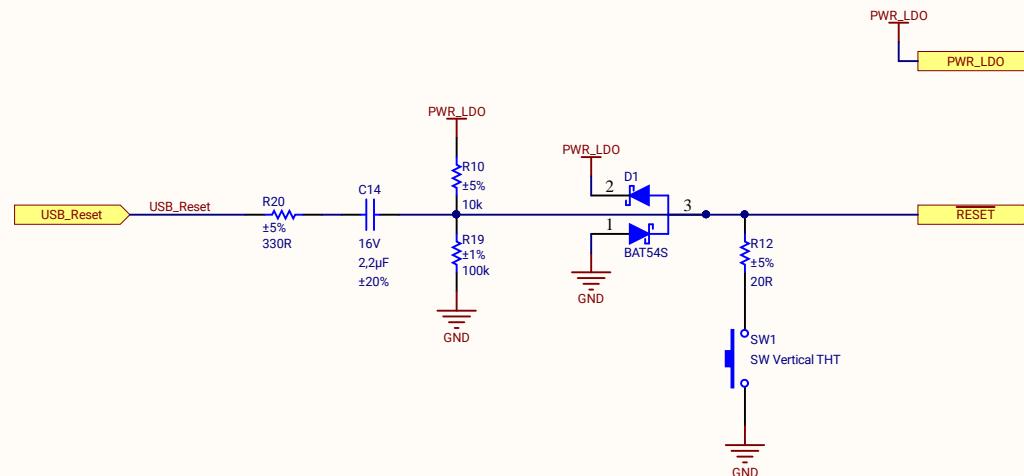
B

C

C

D

D



Title <b>ZUM Core Autoreset</b>		
Project <b>ZUM_Core.PrdPcb</b>		
Author <b>Andrés Gullón</b>		
Size: <b>A4</b>	Number: <b>9</b>	Revision: <b>460</b>
Date: <b>01/06/2018</b>	Time: <b>10:04:47</b>	Sheet <b>9 of 9</b>

