



[illegible]

floyd-warshall

Speedup Over Default

100

10

1

N\_N\_VL8

N\_N\_VL4

N\_N\_VL2

N\_VL2\_N

N\_VA\_N

N\_N\_VA

N\_VL4\_N

N\_VL8\_N

VL8\_N\_N

VL4\_N\_N

N\_IV\_N

VL2\_N\_N

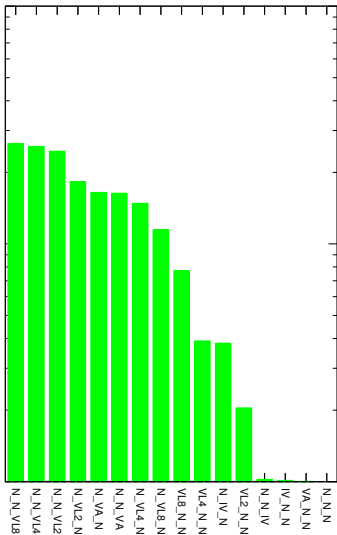
N\_N\_IV

IV\_N\_N

VA\_N\_N

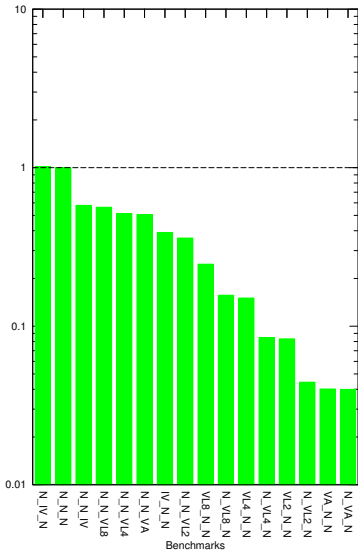
N\_N\_N

Benchmarks



gemm

Speedup Over Default





Speedup Over Default

10

1

0.1

N\_VL8

N\_VL2

N\_VL4

VL4\_N

IV\_N

VL8\_N

VL2\_N

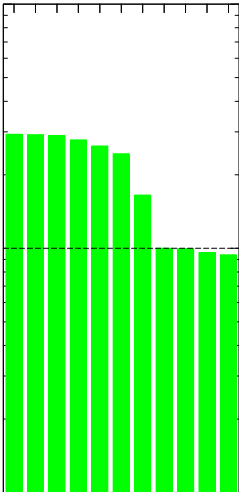
N\_N

N\_IV

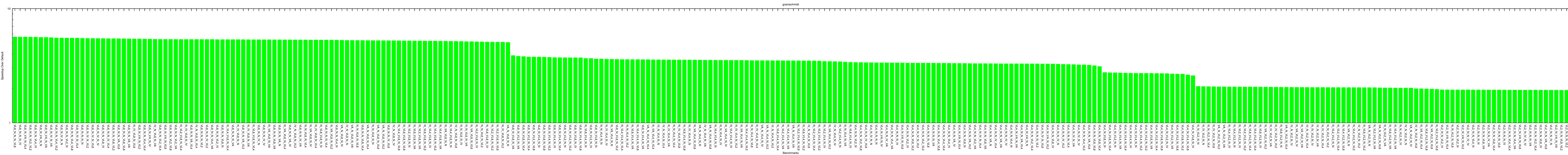
N\_VA

VA\_N

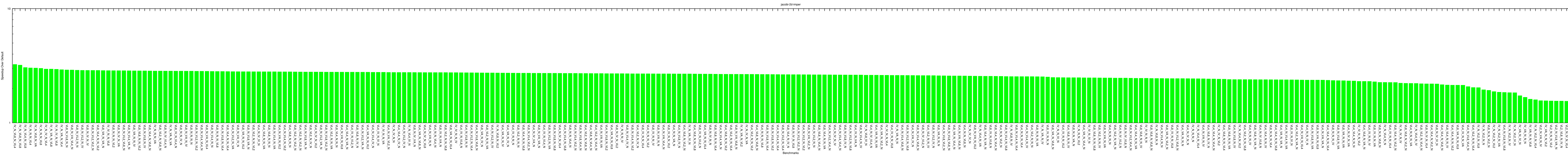
Benchmarks



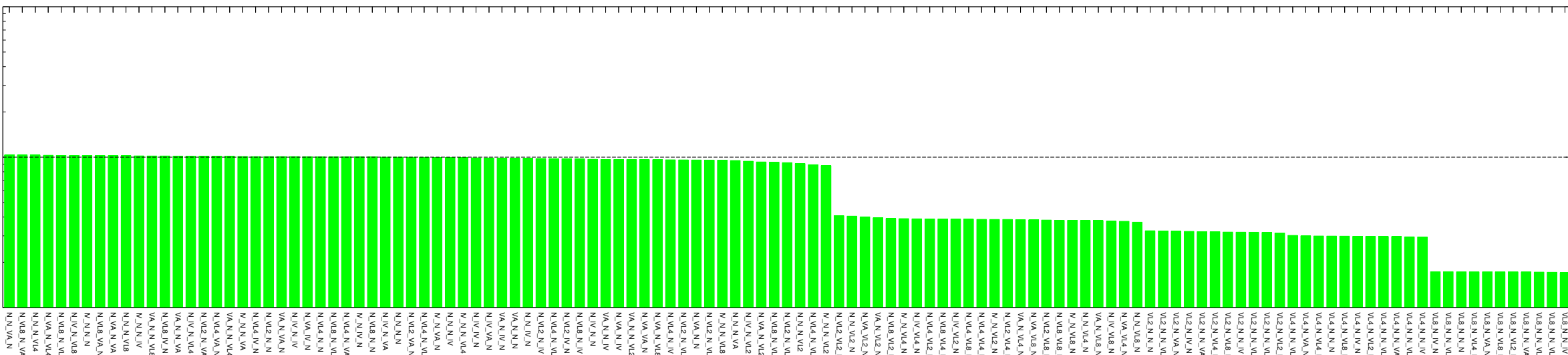


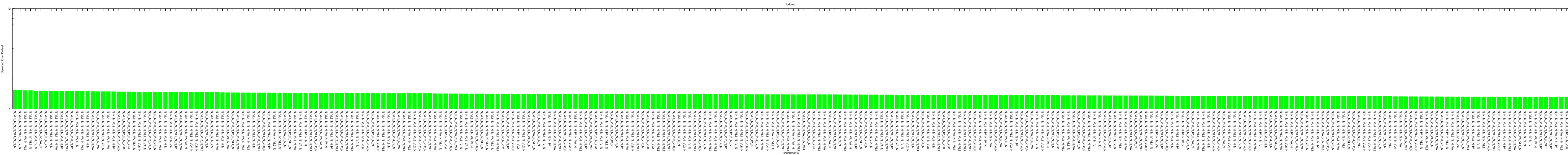


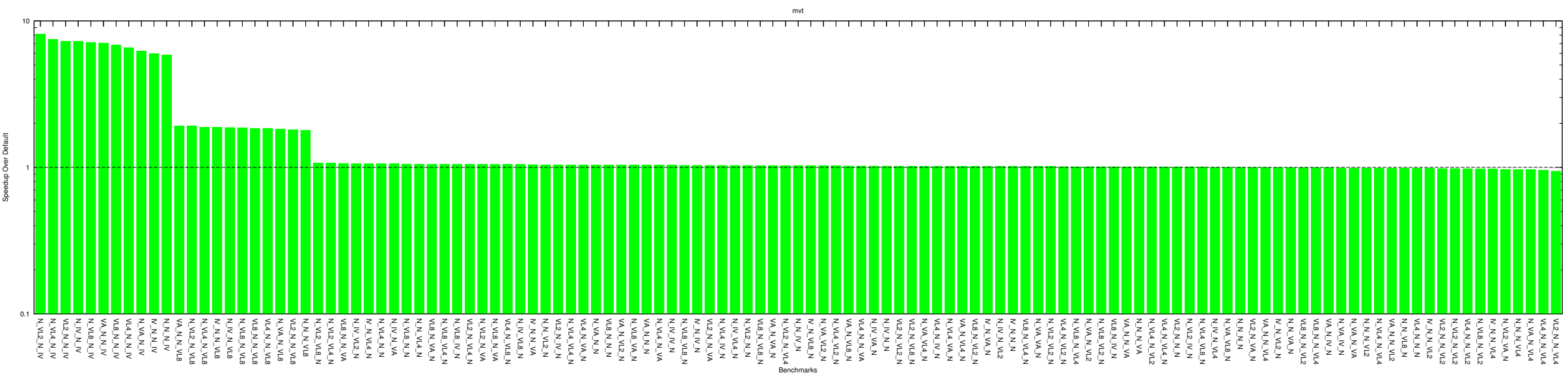
N\_VL8\_N  
N\_VL8\_IV  
N\_VL8\_N  
VL2\_N\_N  
VL2\_N\_VA  
VA\_N\_VL2  
VL2\_N\_VL4  
N\_VL8\_VL2  
N\_IV\_VL8  
IV\_N\_VL4  
N\_N\_VL8  
N\_VA\_VL8  
N\_N\_VL4  
VL4\_N\_VL4  
IV\_N\_IV  
VL2\_N\_VL8  
N\_IV\_VA  
VL4\_N\_N  
VL4\_N\_VL2  
IV\_N\_N  
VL8\_N\_VL8  
VL4\_N\_IV  
VL2\_N\_IV  
N\_VA\_VL4  
N\_IV\_VL2  
N\_VL8\_VA  
VL8\_N\_IV  
N\_VL4\_VL2  
IV\_N\_VA  
N\_VL8\_VL8  
VA\_N\_N  
N\_IV\_N  
N\_VL4\_VL4  
IV\_N\_VL2  
VA\_N\_VL4  
VL8\_N\_VL2  
N\_N\_IV  
VL4\_N\_VL8  
VA\_N\_IV  
N\_VA\_IV  
N\_N\_VL2  
N\_VL4\_N  
VL8\_N\_VA  
VL2\_N\_VL2  
N\_IV\_V  
VA\_N\_VL8  
N\_N\_N  
N\_VL8\_VL4  
N\_VA\_VA  
N\_VA\_VA  
N\_VL8\_VL4  
N\_N\_N  
VL4\_N\_VA  
N\_VL4\_VL8  
IV\_N\_VL8  
N\_IV\_VL4  
N\_N\_VA  
N\_VL2\_IV  
N\_VA\_N  
N\_VL4\_VA  
N\_VL2\_VL8  
N\_VL2\_VL4  
N\_VL4\_IV  
N\_VL2\_VA  
N\_VL2\_VL2

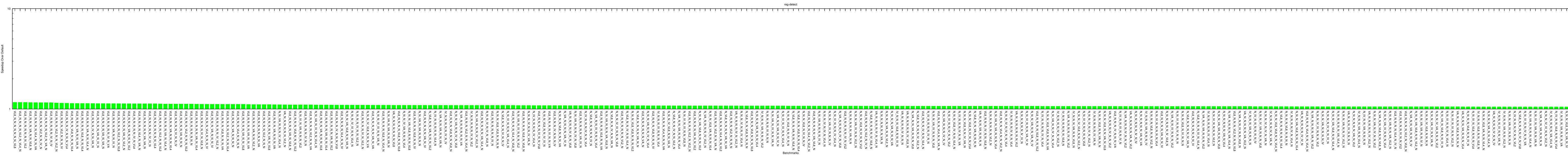


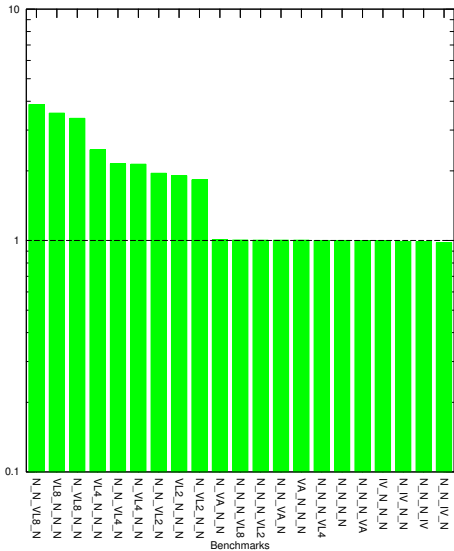
## Benchmark













symm

Speedup Over Default

10

1

0.1

VL8\_N\_N  
N\_VL8\_N  
VL4\_N\_N  
N\_VL4\_N  
VL2\_N\_N  
N\_VL2\_N  
N\_N\_N  
N\_N\_VL4  
IV\_N\_N  
VA\_N\_N  
N\_VA\_N  
N\_N\_VL2  
N\_N\_VA  
N\_IV\_N  
N\_N\_VL8  
N\_N\_IV

Benchmarks

