Core Flight System SP0-VxWorks6.9 Platform Support Package (PSP)

Software Design Document

Section 5.0

Contents

1 SP0-VxWorks6.9 Platform Support Package Mission Configuration Parameters **1**

2 SP0-VxWorks6.9 Platform Support Package Platform Configuration Parameters **1**

2.1 PSP Platform Configurations - SP0-VxWorks6.9 . . . 1

2.1.1 Detailed Description . . . 4

2.1.2 Macro Definition Documentation . . . 4

3 SP0-VxWorks6.9 Platform Support Package Application Programming Interfaces **8**

3.1 PSP Public APIs - Common . . . 8

3.1.1 Detailed Description . . . 14

3.1.2 Macro Definition Documentation . . . 14

3.1.3 Enumeration Type Documentation . . . 14

3.1.4 Function Documentation . . . 15

3.1.5 Variable Documentation . . . 59

3.2 PSP Public APIs - SP0-VxWorks6.9 Platform . . . 60

3.2.1 Detailed Description . . . 61

3.2.2 Macro Definition Documentation . . . 61

3.2.3 Typedef Documentation . . . 62

3.2.4 Function Documentation . . . 63

4 Data Structure Documentation **83**

4.1 CFE_PSP_Exception_ContextDataEntry_t Struct Reference . . . 83

4.1.1 Detailed Description . . . 83

4.2 CFE_PSP_Exception_LogData Struct Reference . . . 84

4.2.1 Detailed Description . . . 84

4.3 CFE_PSP_ExceptionStorage Struct Reference . . . 84

4.3.1 Detailed Description . . . 84

4.4 CFE_PSP_MemoryBlock_t Struct Reference . . . 84

4.4.1 Detailed Description . . . 85

4.5 CFE_PSP_MemTable_t Struct Reference . . . 85

4.5.1 Detailed Description . . . 85

4.6 CFE_PSP_ModuleApi_t Struct Reference . . . 85

4.6.1 Detailed Description . . . 86

4.7 CFE_PSP_OS_Task_and_priority_t Struct Reference . . . 86

4.7.1 Detailed Description . . . 86

4.8 CFE_PSP_ReservedMemoryBootRecord_t Struct Reference . . . 86

4.8.1 Detailed Description . . . 87

4.9 CFE_PSP_ReservedMemoryMap_t Struct Reference . . . 87
4.9.1 Detailed Description . . . 87
4.9.2 Field Documentation . . . 87
4.10 MEM_SCRUB_ERRSTATS_s Struct Reference . . . 87
4.10.1 Detailed Description . . . 88
4.11 MEM_SCRUB_STATUS_s Struct Reference . . . 88
4.11.1 Detailed Description . . . 89
4.12 PSP_VxWorks_Timebase_Global_t Struct Reference . . . 89
4.13 SP0_info_table_t Struct Reference . . . 89
4.13.1 Detailed Description . . . 90

5 File Documentation **90**

5.1 /home/tngo/GW_workspace/cert_testbed/psp/fsw/inc/cfe_psp.h File Reference . . . 90
5.1.1 Detailed Description . . . 94
5.2 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ntp_clock_vxworks/cfe_psp_ntp.c File Reference . . . 95
5.2.1 Detailed Description . . . 97
5.2.2 Macro Definition Documentation . . . 97
5.2.3 Function Documentation . . . 98
5.3 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/port_direct/cfe_psp_port_direct.c File Reference . . . 98
5.3.1 Detailed Description . . . 98
5.4 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ram_direct/cfe_psp_ram_direct.c File Reference . . . 98
5.4.1 Detailed Description . . . 99
5.5 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/timebase_vxworks/cfe_psp_timebase_vxworks.c File Reference . . . 99
5.5.1 Detailed Description . . . 100
5.6 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_api.h File Reference . . . 100
5.6.1 Detailed Description . . . 100
5.7 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_types.h File Reference . . . 101
5.7.1 Detailed Description . . . 101
5.8 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_memory.h File Reference . . . 102
5.8.1 Detailed Description . . . 102
5.9 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_module.h File Reference . . . 103
5.9.1 Detailed Description . . . 103
5.10 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_exceptionstorage.c File Reference 104

5.10.1 Detailed Description . . . 104
5.11 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memrange.c File Reference . . . 105
5.11.1 Detailed Description . . . 105
5.12 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memutils.c File Reference . . . 105
5.12.1 Detailed Description . . . 105
5.13 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_module.c File Reference . . . 106
5.13.1 Detailed Description . . . 106
5.13.2 Macro Definition Documentation . . . 106
5.13.3 Function Documentation . . . 106
5.14 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_version.c File Reference . . . 107
5.14.1 Detailed Description . . . 107
5.15 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/functional_test/target_config.c File Reference . . . 108
5.15.1 Detailed Description . . . 108
5.15.2 Variable Documentation . . . 108
5.16 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/cfe_psp_config.h File Reference . 110
5.16.1 Detailed Description . . . 113
5.17 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_cds_flash.h File Reference . . 113
5.17.1 Detailed Description . . . 113
5.18 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_mem_scrub.h File Reference 114
5.18.1 Detailed Description . . . 114
5.19 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_sp0_info.h File Reference . . 115
5.19.1 Detailed Description . . . 115
5.20 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_start.h File Reference . . . 116
5.20.1 Detailed Description . . . 117
5.20.2 Function Documentation . . . 117
5.21 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_time_sync.h File Reference . 118
5.21.1 Detailed Description . . . 118
5.22 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_verify.h File Reference . . . 119
5.22.1 Detailed Description . . . 119
5.22.2 Function Documentation . . . 120
5.23 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_version.h File Reference . . . 120
5.23.1 Detailed Description . . . 121
5.23.2 Macro Definition Documentation . . . 121
5.24 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_exception.c File Reference 121
5.24.1 Detailed Description . . . 122
5.24.2 Macro Definition Documentation . . . 123

5.24.3 Function Documentation . . . 123
5.24.4 Variable Documentation . . . 124
5.25 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_mem_scrub.c File Reference . . . 125
5.25.1 Detailed Description . . . 126
5.25.2 Function Documentation . . . 126
5.25.3 Variable Documentation . . . 127
5.26 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_memory.c File Reference 128
5.26.1 Detailed Description . . . 129
5.26.2 Function Documentation . . . 130
5.26.3 Variable Documentation . . . 131
5.27 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_sp0_info.c File Reference 132
5.27.1 Detailed Description . . . 133
5.27.2 Function Documentation . . . 133
5.28 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_start.c File Reference . . 133
5.28.1 Detailed Description . . . 135
5.28.2 Function Documentation . . . 135
5.28.3 Variable Documentation . . . 136
5.29 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_support.c File Reference 137
5.29.1 Detailed Description . . . 138
5.30 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_watchdog.c File Reference138
5.30.1 Detailed Description . . . 139

Index **140**

1 SP0-VxWorks6.9 Platform Support Package Mission Configuration Parameters

None.

2 SP0-VxWorks6.9 Platform Support Package Platform Configuration Parameters

2.1 PSP Platform Configurations - SP0-VxWorks6.9

Data Structures

- struct CFE_PSP_ReservedMemoryBootRecord_t

  Layout of the vxWorks boot record structure.
- struct CFE_PSP_Exception_ContextDataEntry_t

  Exception Context Data Entry.

- struct CFE_PSP_OS_Task_and_priority_t

  Task name and priority of tasks.

Macros

- #define OVERRIDE_OSAL_OS_APPLICATION_RUN true

  Override OSAL OS_Application_Run.
- #define VXWORKS_TASK_PRIORITIES

  The list of VxWorks tasks that PSP is tasked to adjust its priorites.
- #define CFE_PSP_MEM_TABLE_SIZE 10

  Memory Table Size.
- #define CFE_PSP_MAX_EXCEPTION_ENTRIES 4

  Maximum Exception Entries.
- #define CFE_PSP_MAXIMUM_TASK_LENGTH 30

  Maximum length of a task name created or spawn by PSP.
- #define CFE_PSP_MEMALIGN_MASK ((cpuaddr)0x1F)

  Memory Alignment Mask.

Typedefs

- typedef TASK_ID CFE_PSP_Exception_SysTaskId_t

  The data type used by the underlying OS to represent a thread ID.

VxWorks timebase configuration parameters

Description:

The SP0 uses the PowerPC decrementer register. The register is decremented at a speed of:

- SP0-s DDR2 Configuration: 50 MHz (1/20 = 0.05)
- SP0 DDR1 Configuration: 41.666 Mhz (1/24 = 0.041667)
  For SP0-s the ratio of Denominator/Numerator is 0.05, which is 50 MHz.
  Refer to Aitech 00-0092-01_17_SP0_Programmers_Guide sec. 5.9

Note:

This is expressed as a ratio in case it is not a whole number. The numerator unit of measure is nanoseconds per tick.

Warning

Numerator calculation has been validated only on SP0-s and SP0 with a DDR memory bus speed of 50 MHz and 41.666 MHz respectively.

- #define CFE_PSP_VX_TIMEBASE_PERIOD_NUMERATOR (uint32)(8000.0f / (float)getCoreClockSpeed())

  Numerator.
- #define CFE_PSP_VX_TIMEBASE_PERIOD_DENOMINATOR 1

  Denominator.

Watchdog Configuration Parameters

- #define CFE_PSP_WATCHDOG_MIN (0)

  Watchdog minimum (in milliseconds)
- #define CFE_PSP_WATCHDOG_MAX (0xFFFFFFFF)

  Watchdog maximum (in milliseconds)
- #define CFE_PSP_WATCHDOG_DEFAULT_MSEC 20000

  Default Watchdog Value in milliseconds.

CDS File Location on FLASH Configuration Parameters

- #define CFE_PSP_CFE_FLASH_FILEPATH "/ffx0/CDS"

  CDS FLASH Memory File Location.

Memory Scrubbing Configuration Parameters

- #define MEMSCRUB_DEFAULT_PRIORITY 254

  Memory Scrub Default Priority.
- #define MEMSCRUB_PRIORITY_UP_RANGE 255

  Memory Scrub Maximum Allowed Priority.
- #define MEMSCRUB_PRIORITY_DOWN_RANGE 120

  Memory Scrub Minimum Allowed Priority.
- #define MEMSCRUB_TASK_NAME "PSPMemScrub"

  Memory Scrub Task Name.

SP0 Info Configuration Parameters

- #define SP0_DATA_DUMP_FILEPATH "/ffx0/PSP_SP0_DUMP"

  SP0 Data Dump Filepath.

NTP Sync Configuration Parameters

- #define NTP_DAEMON_TASK_NAME "ipntpd"

  Task name of the NTP daemon task.
- #define CFE_MISSION_TIME_EPOCH_UNIX_DIFF 946728000

  EPOCH to Mission Time Difference.
- #define CFE_1HZ_TASK_NAME "TIME_1HZ_TASK"

  CFE Time Service Task Name.
- #define NTPSYNC_INITIAL_TIME_DELAY 500

  Time delay in msec before checking CFE Time Service status.
- #define NTPSYNC_MAX_ITERATION_TIME_DELAY 120

  Time delay maximum iterations.
- #define CFE_MISSION_TIME_SYNC_OS_ENABLE true

  Default NTP Sync Start/Stop on Startup.
- #define CFE_MISSION_TIME_SYNC_OS_SEC 30

  Default Synchronization Frequency.
- #define NTPSYNC_TASK_NAME "PSPNTPSync"

Default NTP Sync Task Name.

- #define NTPSYNC_DEFAULT_PRIORITY 60

  Default NTP Sync Task Priority.
- #define PSP_MEM_SCRUB_BSEM_NAME "PSP_BSEM_NAME"

  MEM SCRUB Binary semaphore name.
- #define PSP_CDS_SYNC_TO_FLASH_DEFAULT true

  CDS sync to FLASH.
- #define NTPSYNC_PRIORITY_UP_RANGE 255

  NTP Sync maximum allowed Task priority.
- #define NTPSYNC_PRIORITY_DOWN_RANGE 60

  NTP Sync maximum allowed Task priority.

2.1.1 Detailed Description

2.1.2 Macro Definition Documentation

2.1.2.1 #define CFE_1HZ_TASK_NAME "TIME_1HZ_TASK"

CFE Time Service Task Name.

Description:

This is the task name used by CFE Time Service to update the mission time.

Note:

This value is not checked against the CFE configuration, and it is up to the end user to verify it matches the CFE configuration.
Definition will be deleted once the NTP Sync App is ready to be released.

2.1.2.2 #define CFE_MISSION_TIME_EPOCH_UNIX_DIFF 946728000

EPOCH to Mission Time Difference.

Description:

Default value corresponding to the difference in seconds between CFE Mission Epoch and UNIX Epoch. It is left to the end user to calculate the correct value.

Note:

Value could be positive or negative depending if Mission Epoch is before or after UNIX Epoch. NTP Sync will not occur if NTP time is less than this value

2.1.2.3 #define CFE_MISSION_TIME_SYNC_OS_ENABLE true

Default NTP Sync Start/Stop on Startup.

Description:

Enable or disable the Automatic time sync with the OS

2.1.2.4 #define CFE_MISSION_TIME_SYNC_OS_SEC 30

Default Synchronization Frequency.

Description:

Default number of seconds between time synchronizations. CFE Time Service updates MET and STCF from VxWorks OS. When set to zero, CFE Time will be synchronized only once during start.

Limits

Positive integer up to 255. If this value is too low, it could starve other processes.

2.1.2.5 #define CFE_PSP_CFE_FLASH_FILEPATH "/ffx0/CDS"

CDS FLASH Memory File Location.

Note:

File will be overwritten every time CFS starts.

2.1.2.6 #define CFE_PSP_MAX_EXCEPTION_ENTRIES 4

Maximum Exception Entries.

Description:

This define sets the maximum number of exceptions that can be stored.

Limits:

Value $>$ 0
Must be a power of two

2.1.2.7 #define CFE_PSP_MAXIMUM_TASK_LENGTH 30

Maximum length of a task name created or spawn by PSP.

Description

This value will be used to verify task name length during build-time, and used to verity CFE_PSP_SetTaskPrio task name at run-time

2.1.2.8 #define CFE_PSP_MEM_TABLE_SIZE 10

Memory Table Size.

Descripion:

This sets the number of memory ranges that are defined in the memory range definition table.

Limits:

Value $>$ 0

2.1.2.9 #define CFE_PSP_MEMALIGN_MASK ((cpuaddr)0x1F)

Memory Alignment Mask.

Description:

The alignment to use for each reserved memory block.
This is a mask to be applied to each block base address
Chosen as the cache line size of the SP0 processor (32 bytes) such that the blocks will be cached more efficiently.

2.1.2.10 #define MEMSCRUB_DEFAULT_PRIORITY 254

Memory Scrub Default Priority.

Description:

Set the Active Memory Scrub Task Default Priority

Note: Must be set to lowest possible priority

2.1.2.11 #define MEMSCRUB_PRIORITY_DOWN_RANGE 120

Memory Scrub Minimum Allowed Priority.

Description:

Set the Active Memory Scrub Task Down Range Allowable Priority Task Priority can be changed using CFE_PSP_MEM_SCRUB_Set. Down Range priority should not be lower than your apps.

2.1.2.12 #define MEMSCRUB_PRIORITY_UP_RANGE 255

Memory Scrub Maximum Allowed Priority.

Description:

Set the Active Memory Scrub Task Up Range Allowable Priority Task Priority can be changed using CFE_PSP_MEM_SCRUB_Set. Up Range priority is capped by VxWorks OS.

2.1.2.13 #define MEMSCRUB_TASK_NAME "PSPMemScrub"

Memory Scrub Task Name.

Description:

Set the Active Memory Scrub Task Name

2.1.2.14 #define NTP_DAEMON_TASK_NAME "ipntpd"

Task name of the NTP daemon task.

Description:

The default task name in VxWorks is "ipntpd", but it may need to be changed

2.1.2.15 #define NTPSYNC_DEFAULT_PRIORITY 60

Default NTP Sync Task Priority.

Limits:

Value must be above NTP Daemon task and below Mem Scrub task

2.1.2.16 #define NTPSYNC_INITIAL_TIME_DELAY 500

Time delay in msec before checking CFE Time Service status.

Description:

NTP Sync starts before the CFE Time Service. This parameter introduces and non-blocking time delay before checking if the CFE Time Service has started. The goal is to start the NTP Sync as soon as possible after CFE Time Service starts. The time delay is defined in milliseconds and it will only occur during CFS booting.

2.1.2.17 #define NTPSYNC_MAX_ITERATION_TIME_DELAY 120

Time delay maximum iterations.

Description:

If the time delay introduced with NTPSYNC_INITIAL_TIME_DELAY is not enough the code will continue trying in a loop. This value sets the maximum number of times to run the time delay. For example, if NTPSYNC_INITIAL_TIME_DELAY $*$ NTPSYNC_MAX_ITERATION_TIME_DELAY is 500 ms $*$ 120 = 60 seconds maximum wait time.

2.1.2.18 #define NTPSYNC_PRIORITY_DOWN_RANGE 60

NTP Sync maximum allowed Task priority.

Description:

Set the active NTP Sync task down range allowable priority.

2.1.2.19 #define NTPSYNC_PRIORITY_UP_RANGE 255

NTP Sync maximum allowed Task priority.

Description:

Set the active NTP Sync task up range allowable priority.

2.1.2.20 #define OVERRIDE_OSAL_OS_APPLICATION_RUN true

Override OSAL OS_Application_Run.

Description:

OSAL default OS_Application_Run suspends the shell task on VxWorks. If that behaviour is not wanted, set this define to TRUE. The PSP default function implementation is empty.

2.1.2.21 #define PSP_CDS_SYNC_TO_FLASH_DEFAULT true

CDS sync to FLASH.

Description:

Default option used to determine to sync CDS to FLASH or not

2.1.2.22 #define SP0_DATA_DUMP_FILEPATH "/ffx0/PSP_SP0_DUMP"

SP0 Data Dump Filepath.

Description

This file is written only in the case when CFE_PSP_Panic is called.

2.1.2.23 #define VXWORKS_TASK_PRIORITIES

Value:

```
{"tLogTask", 0},\
                                                {"tShell0", 201},\
                                                {"tWdbTask", 203},\
                                                {"tVxdbgTask", 200},\
                                                {"tNet0", 25},\
                                                {"ipftps", 202},\
                                                {"ipcom_syslogd", 205},\
                                                {"ipcom_telnetd", 204},\
                                                {"ipcom_egd", 253},\
                                                {"FTCMP00", 253}
```

The list of VxWorks tasks that PSP is tasked to adjust its priorites.

Description:

PSP will adjust the priorities of each tasks according to the table.

Note:

Values are defined in cfe_psp_config.h header.
The priority reassignment will be moved to kernel in a future release.

3 SP0-VxWorks6.9 Platform Support Package Application Programming Interfaces

3.1 PSP Public APIs - Common

Data Structures

- struct CFE_PSP_Exception_LogData

  Exception Log Data Struct.
- struct CFE_PSP_ExceptionStorage

  Exception Storage Struct.
- struct CFE_PSP_MemTable_t

  Memory Table Type.
- struct CFE_PSP_MemoryBlock_t

Memory Block Type.

- struct CFE_PSP_ReservedMemoryMap_t

  Reserved Memory Map.

- struct CFE_PSP_ModuleApi_t

  Concrete version of the abstract API definition structure.

Macros

- #define CFE_PSP_SOFT_TIMEBASE_NAME "cFS-Master"

  The name of the software/RTOS timebase for general system timers.

- #define CFE_PSP_MODULE_NAME_MAX_LENGTH 30

  Maximum Module Name Length.

- #define CFE_PSP_MODULE_DECLARE_SIMPLE(name)

  CFE_PSP_MODULE_DECLARE_SIMPLE.

Typedefs

- typedef struct CFE_PSP_Exception_LogData CFE_PSP_Exception_LogData_t

  Exception Log Data Type.

- typedef struct CFE_PSP_ExceptionStorage CFE_PSP_ExceptionStorage_t

  Exception Storage Type.

- typedef void(∗ CFE_PSP_ModuleInitFunc_t)(uint32 PspModuleId)

  Protoype for a PSP module initialization function.

Enumerations

- enum CFE_PSP_ModuleType_t { CFE_PSP_MODULE_TYPE_INVALID = 0, CFE_PSP_MODULE_TYPE_SIMPLE }

  Enum Module Type.

Functions

- void CFE_PSP_Main (void)
- void CFE_PSP_GetTime (OS_time_t ∗LocalTime)
- void CFE_PSP_Restart (uint32 resetType)
- uint32 CFE_PSP_GetRestartType (uint32 ∗resetSubType)
- void CFE_PSP_FlushCaches (uint32 type, void ∗address, uint32 size)
- uint32 CFE_PSP_GetProcessorId (void)
- uint32 CFE_PSP_GetSpacecraftId (void)
- const char ∗ CFE_PSP_GetProcessorName (void)
- uint32 CFE_PSP_GetTimerTicksPerSecond (void)
- uint32 CFE_PSP_GetTimerLow32Rollover (void)
- void CFE_PSP_Get_Timebase (uint32 ∗Tbu, uint32 ∗Tbl)
- int32 CFE_PSP_GetCDSSize (uint32 ∗SizeOfCDS)
- int32 CFE_PSP_WriteToCDS (const void ∗PtrToDataToWrite, uint32 CDSOffset, uint32 NumBytes)

- int32 CFE_PSP_ReadFromCDS (void *PtrToDataFromRead, uint32 CDSOffset, uint32 NumBytes)
- int32 CFE_PSP_GetResetArea (cpuaddr *PtrToResetArea, uint32 *SizeOfResetArea)
- int32 CFE_PSP_GetUserReservedArea (cpuaddr *PtrToUserArea, uint32 *SizeOfUserArea)
- int32 CFE_PSP_GetVolatileDiskMem (cpuaddr *PtrToVolDisk, uint32 *SizeOfVolDisk)
- int32 CFE_PSP_GetKernelTextSegmentInfo (cpuaddr *PtrToKernelSegment, uint32 *SizeOfKernelSegment)
- int32 CFE_PSP_GetCFETextSegmentInfo (cpuaddr *PtrToCFESegment, uint32 *SizeOfCFESegment)
- void CFE_PSP_WatchdogInit (void)
- void CFE_PSP_WatchdogEnable (void)
- void CFE_PSP_WatchdogDisable (void)
- void CFE_PSP_WatchdogService (void)
- uint32 CFE_PSP_WatchdogGet (void)
- void CFE_PSP_WatchdogSet (uint32 watchDogValue_ms)
- bool CFE_PSP_WatchdogStatus (void)
- void CFE_PSP_Panic (int32 errorCode)
- int32 CFE_PSP_InitSSR (uint32 bus, uint32 device, char *DeviceName)
- void CFE_PSP_AttachExceptions (void)
- void CFE_PSP_SetDefaultExceptionEnvironment (void)
- uint32 CFE_PSP_Exception_GetCount (void)
- int32 CFE_PSP_Exception_GetSummary (uint32 *ContextLogId, osal_id_t *TaskId, char *ReasonBuf, uint32 ReasonSize)
- int32 CFE_PSP_Exception_CopyContext (uint32 ContextLogId, void *ContextBuf, uint32 ContextSize)
- int32 CFE_PSP_PortRead8 (cpuaddr PortAddress, uint8 *ByteValue)
- int32 CFE_PSP_PortWrite8 (cpuaddr PortAddress, uint8 ByteValue)
- int32 CFE_PSP_PortRead16 (cpuaddr PortAddress, uint16 *uint16Value)
- int32 CFE_PSP_PortWrite16 (cpuaddr PortAddress, uint16 uint16Value)
- int32 CFE_PSP_PortRead32 (cpuaddr PortAddress, uint32 *uint32Value)
- int32 CFE_PSP_PortWrite32 (cpuaddr PortAddress, uint32 uint32Value)
- int32 CFE_PSP_MemRead8 (cpuaddr MemoryAddress, uint8 *ByteValue)
- int32 CFE_PSP_MemWrite8 (cpuaddr MemoryAddress, uint8 ByteValue)
- int32 CFE_PSP_MemRead16 (cpuaddr MemoryAddress, uint16 *uint16Value)
- int32 CFE_PSP_MemWrite16 (cpuaddr MemoryAddress, uint16 uint16Value)
- int32 CFE_PSP_MemRead32 (cpuaddr MemoryAddress, uint32 *uint32Value)
- int32 CFE_PSP_MemWrite32 (cpuaddr MemoryAddress, uint32 uint32Value)
- int32 CFE_PSP_MemCpy (void *dest, const void *src, uint32 size)
- int32 CFE_PSP_MemSet (void *dest, uint8 value, uint32 size)
- int32 CFE_PSP_MemValidateRange (cpuaddr Address, size_t Size, uint32 MemoryType)
- uint32 CFE_PSP_MemRanges (void)
- int32 CFE_PSP_MemRangeSet (uint32 RangeNum, uint32 MemoryType, cpuaddr StartAddr, size_t Size, size_t WordSize, uint32 Attributes)
- int32 CFE_PSP_MemRangeGet (uint32 RangeNum, uint32 *MemoryType, cpuaddr *StartAddr, size_t *Size, size_t *WordSize, uint32 *Attributes)
- int32 CFE_PSP_EepromWrite8 (cpuaddr MemoryAddress, uint8 ByteValue)
- int32 CFE_PSP_EepromWrite16 (cpuaddr MemoryAddress, uint16 uint16Value)
- int32 CFE_PSP_EepromWrite32 (cpuaddr MemoryAddress, uint32 uint32Value)
- int32 CFE_PSP_EepromWriteEnable (uint32 Bank)
- int32 CFE_PSP_EepromWriteDisable (uint32 Bank)
- int32 CFE_PSP_EepromPowerUp (uint32 Bank)
- int32 CFE_PSP_EepromPowerDown (uint32 Bank)
- const char * CFE_PSP_GetVersionString (void)
- const char * CFE_PSP_GetVersionCodeName (void)
- void CFE_PSP_GetVersionNumber (uint8 VersionNumbers[4])

- uint32 CFE_PSP_GetBuildNumber (void)
- struct CFE_PSP_Exception_LogData * CFE_PSP_Exception_GetBuffer (uint32 seq)
- struct CFE_PSP_Exception_LogData * CFE_PSP_Exception_GetNextContextBuffer (void)
- void CFE_PSP_Exception_WriteComplete (void)
- void CFE_PSP_Exception_Reset (void)
- int32 CFE_PSP_ExceptionGetSummary_Impl (const struct CFE_PSP_Exception_LogData *Buffer, char *ReasonBuf, uint32 ReasonSize)
- void CFE_PSP_SetupReservedMemoryMap (void)
- int32 CFE_PSP_InitProcessorReservedMemory (uint32 RestartType)
- void CFE_PSP_DeleteProcessorReservedMemory (void)
- void CFE_PSP_ModuleInit (void)
- int32 CFE_PSP_Module_FindByName (const char *ModuleName, uint32 *PspModuleId)
- int32 CFE_PSP_Module_GetAPIEntry (uint32 PspModuleId, CFE_PSP_ModuleApi_t **API)

Variables

- CFE_PSP_ReservedMemoryMap_t CFE_PSP_ReservedMemoryMap

  Map to the reserved memory area(s) Contains a pointer to each of the separate memory blocks.
- CFE_StaticModuleLoadEntry_t CFE_PSP_BASE_MODULE_LIST []

  A list of fixed/base modules associated with the PSP.

Error and return codes

- #define CFE_PSP_SUCCESS (0)

  Success.
- #define CFE_PSP_ERROR (-1)

  Generic Error.
- #define CFE_PSP_INVALID_POINTER (-2)

  Invalid Pointer.
- #define CFE_PSP_ERROR_ADDRESS_MISALIGNED (-3)

  Misaligned Address.
- #define CFE_PSP_ERROR_TIMEOUT (-4)

  Timeout Error.
- #define CFE_PSP_INVALID_INT_NUM (-5)

  Invalid Integer Number.
- #define CFE_PSP_INVALID_MEM_ADDR (-21)

  Invalid Memory Address.
- #define CFE_PSP_INVALID_MEM_TYPE (-22)

  Invalid Memory Type.
- #define CFE_PSP_INVALID_MEM_RANGE (-23)

  Invalid Memory Range.
- #define CFE_PSP_INVALID_MEM_WORDSIZE (-24)

  Invalid Memory Word Size.
- #define CFE_PSP_INVALID_MEM_SIZE (-25)

  Invalid Memory Size.
- #define CFE_PSP_INVALID_MEM_ATTR (-26)

  Invalid Memory Attribute.
- #define CFE_PSP_ERROR_NOT_IMPLEMENTED (-27)

Not Implemented.

- #define CFE_PSP_INVALID_MODULE_NAME (-28)

  Invalid Module Name.
- #define CFE_PSP_INVALID_MODULE_ID (-29)

  Invalid Module ID.
- #define CFE_PSP_NO_EXCEPTION_DATA (-30)

  No Exception Data.
- #define CFE_PSP_ERROR_LEVEL_0 (-31)

  Generic Error, but returned data is valid.

Definitions for PSP PANIC types

- #define CFE_PSP_PANIC_STARTUP 1

  Startup.
- #define CFE_PSP_PANIC_VOLATILE_DISK 2

  Volatile Disk.
- #define CFE_PSP_PANIC_MEMORY_ALLOC 3

  Memory Allocation.
- #define CFE_PSP_PANIC_NONVOL_DISK 4

  Nonvolatile Disk.
- #define CFE_PSP_PANIC_STARTUP_SEM 5

  Startup Semaphore.
- #define CFE_PSP_PANIC_CORE_APP 6

  Core App.
- #define CFE_PSP_PANIC_GENERAL_FAILURE 7

  Generic Failure.

Macros for the file loader

- #define BUFF_SIZE 256

  Buffer Size.
- #define SIZE_BYTE 1

  Size Byte.
- #define SIZE_HALF 2

  Size Half.
- #define SIZE_WORD 3

  Size Word.

Define Memory Types

- #define CFE_PSP_MEM_RAM 1

  Memory RAM.
- #define CFE_PSP_MEM_EEPROM 2

  Memory EEPROM.
- #define CFE_PSP_MEM_ANY 3

  Memory ANY.
- #define CFE_PSP_MEM_INVALID 4

  Memory INVALID.

Define Memory Read/Write Attributes

- #define CFE_PSP_MEM_ATTR_WRITE 0x01

  Memory Attribute Write.
- #define CFE_PSP_MEM_ATTR_READ 0x02

  Memory Attribute Read.
- #define CFE_PSP_MEM_ATTR_READWRITE 0x03

  Memory Attribute ReadWrite.

Define the Memory Word Sizes

- #define CFE_PSP_MEM_SIZE_BYTE 0x01

  Memory Size Byte.
- #define CFE_PSP_MEM_SIZE_WORD 0x02

  Memory Size Word.
- #define CFE_PSP_MEM_SIZE_DWORD 0x04

  Memory Size DoubleWord.

Reset Types

- #define CFE_PSP_RST_TYPE_PROCESSOR 1
- #define CFE_PSP_RST_TYPE_POWERON 2
- #define CFE_PSP_RST_TYPE_MAX 3

Reset Sub-Types

- #define CFE_PSP_RST_SUBTYPE_POWER_CYCLE 1

  Reset caused by power having been removed and restored.
- #define CFE_PSP_RST_SUBTYPE_PUSH_BUTTON 2

  Reset caused by reset button on the board having been pressed.
- #define CFE_PSP_RST_SUBTYPE_HW_SPECIAL_COMMAND 3

  Reset was caused by a reset line having been stimulated by a hardware special command.
- #define CFE_PSP_RST_SUBTYPE_HW_WATCHDOG 4

  Reset was caused by a watchdog timer expiring.
- #define CFE_PSP_RST_SUBTYPE_RESET_COMMAND 5

  Reset was caused by cFE ES processing a Reset Command .
- #define CFE_PSP_RST_SUBTYPE_EXCEPTION 6

  Reset was caused by a Processor Exception.
- #define CFE_PSP_RST_SUBTYPE_UNDEFINED_RESET 7

  Reset was caused in an unknown manner.
- #define CFE_PSP_RST_SUBTYPE_HWDEBUG_RESET 8

  Reset was caused by a JTAG or BDM connection.
- #define CFE_PSP_RST_SUBTYPE_BANKSWITCH_RESET 9

  Reset reverted to a cFE POWERON due to a boot bank switch.
- #define CFE_PSP_RST_SUBTYPE_MAX 10

  Placeholder to indicate 1+ the maximum value that the PSP will ever use.

3.1.1 Detailed Description

3.1.2 Macro Definition Documentation

3.1.2.1 #define CFE_PSP_MODULE_DECLARE_SIMPLE(*name*)

Value:

```
static void         name##_Init(uint32 PspModuleId); \
    CFE_PSP_ModuleApi_t CFE_PSP_##name##_API = {         \
        .ModuleType     = CFE_PSP_MODULE_TYPE_SIMPLE,    \
        .OperationFlags = 0,                             \
        .Init           = name##_Init,                   \
    }
```

CFE_PSP_MODULE_DECLARE_SIMPLE.

Description:

Macro to simplify declaration of the IO Driver API structure according to the required naming convention. The "name" argument should match the name of the module object file

3.1.2.2 #define CFE_PSP_MODULE_NAME_MAX_LENGTH 30

Maximum Module Name Length.

Note: Currently not enfornced other than

in cfe_psp_module.c -> CFE_PSP_Module_FindByName

3.1.2.3 #define CFE_PSP_RST_TYPE_MAX 3

Placeholder to indicate 1+ the maximum value that the PSP will ever use.

3.1.2.4 #define CFE_PSP_RST_TYPE_POWERON 2

All memory has been cleared

3.1.2.5 #define CFE_PSP_RST_TYPE_PROCESSOR 1

Volatile disk, Critical Data Store and User Reserved memory could still be valid

3.1.2.6 #define CFE_PSP_SOFT_TIMEBASE_NAME "cFS-Master"

The name of the software/RTOS timebase for general system timers.

This name may be referred to by CFE TIME and/or SCH when setting up its own timers.

3.1.3 Enumeration Type Documentation

3.1.3.1 enum CFE_PSP_ModuleType_t

Enum Module Type.

Note:

May be extended in the future

Enumerator

CFE_PSP_MODULE_TYPE_INVALID Type Invalid.

CFE_PSP_MODULE_TYPE_SIMPLE Type Simple.

3.1.4 Function Documentation

3.1.4.1 void CFE_PSP_AttachExceptions (void)

Purpose Initialize exception handling

Description:

This function sets up the exception environment for a particular platform.

Assumptions, External Events, and Notes:

For VxWorks, this function initializes the EDR policy handling. The handler is called for every exception that other handlers do not handle.
Note that the floating point exceptions are handled by the default floating point exception handler, which does a graceful recovery from floating point exceptions in the file speExcLib.c.

Parameters

None	

Returns

None

Purpose Initialize exception handling

Description:

This function sets up the exception environment for a particular platform. It is called by CFE_ES_Main() in cfe_es_start.c

Assumptions, External Events, and Notes:

For VxWorks, this function initializes the EDR policy handling. The handler is called for every exception that other handlers do not handle. Note that the floating point exceptions are handled by the default floating point exception handler, which does a graceful recovery from floating point exceptions in the file speExcLib.c.

Parameters

None	

Returns

None

3.1.4.2 void CFE_PSP_DeleteProcessorReservedMemory (void)

Purpose Delete the processor's reserved memory

Description:

This function unlinks the memory segments within the CFE_PSP_ReservedMemoryMap global object.

Assumptions, External Events, and Notes:

This function is only relevant on systems where the objects are implemented as kernel shared memory segments. The segments will be marked for deletion but the local maps remain usable until the process ends.

Parameters

None	

Returns

None

Purpose Delete the processor's reserved memory

Description:

This function unlinks the memory segments within the CFE_PSP_ReservedMemoryMap global object.

Assumptions, External Events, and Notes:

This function is only relevant on systems where the objects are implemented as kernel shared memory segments. The segments will be marked for deletion but the local maps remain usable until the process ends.

Parameters

None	

Returns

None

3.1.4.3 int32 CFE_PSP_EepromPowerDown (uint32 *Bank*)

Purpose Power down the EEPROM

Description:

This function powers down the specified EEPROM bank.

Assumptions, External Events, and Notes:

This function is currently not implemented.

Parameters

in	*Bank*	- The EEPROM bank to power down

Returns

CFE_PSP_SUCCESS

3.1.4.4 int32 CFE_PSP_EepromPowerUp (uint32 *Bank*)

Purpose Power on the EEPROM

Description:

This function powers on the specified EEPROM bank.

Assumptions, External Events, and Notes:

This function is currently not implemented.

Parameters

in	*Bank*	- The EEPROM bank to power on

Returns

CFE_PSP_SUCCESS

3.1.4.5 int32 CFE_PSP_EepromWrite16 (cpuaddr *MemoryAddress,* uint16 *uint16Value*)

Purpose Write a 16-bit value to memory

Description:

This function writes a 16-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*uint16Value*	- A 16-bit value to be written

Returns

CFE_PSP_SUCCESS - Data wrote successfully
CFE_PSP_ERROR_ADDRESS_MISALIGNED - The Address is not aligned to 16-bit addressing scheme.

3.1.4.6 int32 CFE_PSP_EepromWrite32 (cpuaddr *MemoryAddress,* uint32 *uint32Value*)

Purpose Write a 32-bit value to memory

Description:

This function writes a 32-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*uint32Value*	- A 32-bit value to be written

Returns

CFE_PSP_SUCCESS - Data wrote successfully
CFE_PSP_ERROR_ADDRESS_MISALIGNED - The Address is not aligned to 16-bit addressing scheme.

3.1.4.7 int32 CFE_PSP_EepromWrite8 (cpuaddr *MemoryAddress*, uint8 *ByteValue*)

Purpose Write an 8-bit value to memory

Description:

This function writes an 8-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*ByteValue*	- An 8-bit value to be written

Returns

CFE_PSP_SUCCESS - Data wrote successfully
CFE_PSP_ERROR_ADDRESS_MISALIGNED - The Address is not aligned to 16-bit addressing scheme.

3.1.4.8 int32 CFE_PSP_EepromWriteDisable (uint32 *Bank*)

Purpose Disable EEPROM from write operations

Description:

This function disables the specified EEPROM bank from write operations.

Assumptions, External Events, and Notes:

This function is currently not implemented.

Parameters

in	*Bank*	- The EEPROM bank to disable

Returns

CFE_PSP_SUCCESS

3.1.4.9 int32 CFE_PSP_EepromWriteEnable (uint32 *Bank*)

Purpose Enable EEPROM for write operations

Description:

This function enables the specified EEPROM bank for write operations.

Assumptions, External Events, and Notes:

This function is currently not implemented.

Parameters

in	*Bank*	- The EEPROM bank to enable

Returns

CFE_PSP_SUCCESS

3.1.4.10 int32 CFE_PSP_Exception_CopyContext (uint32 *ContextLogId,* void * *ContextBuf,* uint32 *ContextSize*)

Purpose Translate a stored exception log entry into a summary string

Description:

This function takes a stored exception-log entry and converts it into a summary string.

Assumptions, External Events, and Notes:

None

Parameters

in	*ContextLogId*	- The stored exception log ID
out	*ContextBuf*	- Pointer to the variable that stores the copied data
in	*ContextSize*	- The maximum length of the buffer, ContextBuf

Returns

The actual size of the copied data
CFE_PSP_NO_EXCEPTION_DATA
CFE_PSP_INVALID_POINTER

3.1.4.11 struct CFE_PSP_Exception_LogData* CFE_PSP_Exception_GetBuffer (uint32 *seq*)

Purpose Get the next buffer for exception buffer corresponding to sequence

Description:

This function obtains a storage buffer corresponding to the given sequence number. The pointer to storage memory is directly returned.

Assumptions, External Events, and Notes:

It is not cleared or modified, and no checks are performed to determine if the sequence number is valid.

Parameters

in	*seq*	- Sequence number

Returns

Pointer to buffer.

3.1.4.12 uint32 CFE_PSP_Exception_GetCount (void)

Purpose Get the exception count

Description:

This function fetches the exception count.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

The exception count

3.1.4.13 struct CFE_PSP_Exception_LogData* CFE_PSP_Exception_GetNextContextBuffer (void)

Purpose Get the next buffer for exception context storage

Description:

This function is invoked by the low level exception handler (typically an ISR/signal) to obtain a buffer for context capture.

Assumptions, External Events, and Notes:

The buffer is cleared (memset zero) before returning to the caller.

Parameters

None	

Returns

Pointer to buffer - If successful
NULL - If storage is full

3.1.4.14 int32 CFE_PSP_Exception_GetSummary (uint32 * *ContextLogId,* osal_id_t * *TaskId,* char * *ReasonBuf,* uint32 *ReasonSize*)

Purpose Translate a stored exception log entry into a summary string

Description:

This function takes a stored exception-log entry and converts it into a summary string.

Assumptions, External Events, and Notes:

None

Parameters

out	*ContextLogId*	- Pointer to the variable that stores the returned log ID
out	*TaskId*	- Pointer to the variable that stores the returned OSAL task ID
out	*ReasonBuf*	- The buffer that stores the returned string
in	*ReasonSize*	- The maximum length of the buffer, ReasonBuf

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.15 void CFE_PSP_Exception_Reset (void)

Purpose Reset the exception storage buffer

Description:

This function resets the state of exception processing.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.16 void CFE_PSP_Exception_WriteComplete (void)

Purpose Wrap up the storage of exception data

Description:

This function is invoked by the low level exception handler (typically an ISR/signal) once the exception context capture is complete.

Assumptions, External Events, and Notes:

This should be invoked after a successful call to CFE_PSP_Exception_GetNextContextBuffer() to commit the information to the log.

Parameters

None	

Returns

None

3.1.4.17 int32 CFE_PSP_ExceptionGetSummary_Impl (const struct CFE_PSP_Exception_LogData * *Buffer,* char * *ReasonBuf,* uint32 *ReasonSize*)

Purpose Translate the exception context data into a string

Description:

This function translates the exception context data into a user-friendly "reason" string.

Assumptions, External Events, and Notes:

This is called in an application context to determine the cause of the exception.

Parameters

in	*Buffer*	- Pointer to the Buffer Context data previously stored by ISR/signal handler
out	*ReasonBuf*	- Buffer to store string
in	*ReasonSize*	- Size of string buffer

Returns

CFE_PSP_SUCCESS on success

3.1.4.18 void CFE_PSP_FlushCaches (uint32 *type,* void * *address,* uint32 *size*)

Purpose Flush memory caches

Description:

This function flushes the processor caches.

Assumptions, External Events, and Notes:

This function is in the PSP because it is sometimes implemented in hardware and sometimes taken care of by the OS.
This function is not implemented for the SP0-vxworks6.9 PSP since it is managed by the SP0 BSP/VxWorks OS.

Parameters

in	*type*	- Cache memory type
in	*address*	- Pointer to the cache memory address
in	*size*	- Cache memory size

Returns

None

Purpose Flush memory caches

Description:

This function flushes the processor caches. This function is in the PSP because it is sometimes implemented in hardware and sometimes taken care of by the OS.

Assumptions, External Events, and Notes:

This function is not implemented for the SP0-vxworks6.9 PSP since it is managed by the SP0 BSP/VxWorks OS.

Parameters

in	*type*	- Cache memory type
in	*address*	- Pointer to the cache memory address
in	*size*	- Cache memory size

Returns

None

3.1.4.19 void CFE_PSP_Get_Timebase (uint32 * *Tbu*, uint32 * *Tbl*)

Purpose Get the timebase values

Description:

This function provides the time values of the 32-bit upper and lower registers.

Assumptions, External Events, and Notes:

This function is in the BSP because it is sometimes implemented in hardware and sometimes taken care of by the OS.

Parameters

out	*Tbu*	- Pointer to the returned value of the 32-bit upper register
out	*Tbl*	- Pointer to the returned value of the 32-bit lower register

Returns

None

3.1.4.20 uint32 CFE_PSP_GetBuildNumber (void)

Purpose Obtain the PSP library numeric build number

Description:

The build number is a monotonically increasing number that (coarsely) reflects the number of commits/changes that have been merged since the epoch release.

Assumptions, External Events, and Notes:

During development cycles this number should increase after each subsequent merge/modification. Like other version information, this is a fixed number assigned at compile time.

Parameters

None	

Returns

The PSP library build number

Purpose Obtain the PSP library numeric build number

Description:

The build number is a monotonically increasing number that (coarsely) reflects the number of commits/changes that have been merged since the epoch release. During development cycles this number should increase after each subsequent merge/modification.
Like other version information, this is a fixed number assigned at compile time.

Assumptions, External Events, and Notes:

None

Returns

The OSAL library build number

3.1.4.21 int32 CFE_PSP_GetCDSSize (uint32 * *SizeOfCDS*)

Purpose Get the size of the Critical Data Store memory area

Description:

This function fetches the size of the OS Critical Data Store memory area.

Assumptions, External Events, and Notes:

None

Parameters

`out`	*SizeOfCDS*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the size of the Critical Data Store memory area

Description:

This function fetches the size of the OS Critical Data Store memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*SizeOfCDS*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.22 int32 CFE_PSP_GetCFETextSegmentInfo (cpuaddr * *PtrToCFESegment*, uint32 * *SizeOfCFESegment*)

Purpose Get the location and size of the cFE text segment

Description:

This function returns the location and size of the cFE text segment of the memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToCFE-Segment*	- Pointer to the variable that stores the returned memory address
out	*SizeOfCFE-Segment*	- Pointer to the variable that stores returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the location and size of the cFE text segment

Description:

This function returns the location and size of the cFE text segment of the memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToCFE-Segment*	- Pointer to the variable that stores the returned memory address
out	*SizeOfCFE-Segment*	- Pointer to the variable that stores returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.23 int32 CFE_PSP_GetKernelTextSegmentInfo (cpuaddr * *PtrToKernelSegment*, uint32 * *SizeOfKernelSegment*)

Purpose Get the location and size of the kernel text segment

Description:

This function returns the location and size of the kernel text segment of the memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToKernel-Segment*	- Pointer to the variable that stores the returned memory address
out	*SizeOfKernel-Segment*	- Pointer to the variable that stores returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the location and size of the kernel text segment

Description:

This function returns the location and size of the kernel text segment of the memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToKernel-Segment*	- Pointer to the variable that stores the returned memory address
out	*SizeOfKernel-Segment*	- Pointer to the variable that stores returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.24 uint32 CFE_PSP_GetProcessorId (void)

Purpose Get the CPU ID

Description:

This function returns the CPU ID as pre-defined by the cFE for specific board and BSP.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_CPU_ID

Purpose Get the CPU ID

Description:

This function returns the CPU ID as pre-defined by the cFE for specific board and BSP.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_CPU_ID

3.1.4.25 const char* CFE_PSP_GetProcessorName (void)

Purpose Get the processor name

Description:

This function returns the CPU name as pre-defined by the cFE.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_CPU_NAME

Purpose Get the processor name

Description:

This function returns the CPU name as pre-defined by the cFE.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_CPU_NAME

3.1.4.26 int32 CFE_PSP_GetResetArea (cpuaddr * *PtrToResetArea*, uint32 * *SizeOfResetArea*)

Purpose Get the location and size of the ES Reset memory area

Description:

This function returns the location and size of the ES Reset memory area. This area is preserved during a processor reset and is used to store the ER Log, System Log and reset related variables.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToResetArea*	- Pointer to the variable that stores the returned memory address
out	*SizeOfResetArea*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the location and size of the ES Reset memory area

Description:

This function returns the location and size of the ES Reset memory area. This area is preserved during a processor reset and is used to store the ER Log, System Log and reset related variables.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToResetArea*	- Pointer to the variable that stores the returned memory address
out	*SizeOfResetArea*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.27 uint32 CFE_PSP_GetRestartType (uint32 * *resetSubType*)

Purpose Get restart type

Description:

This function returns the last reset type.

Assumptions, External Events, and Notes:

If a pointer to a valid memory space is passed in, it returns the reset sub-type in that memory. Right now the reset types are application-specific.

Parameters

out	*resetSubType*	- Pointer to the variable that stores the returned reset sub-type

Returns

Last reset type

Purpose Get restart type

Description:

This function returns the last reset type. If a pointer to a valid memory space is passed in, it returns the reset sub-type in that memory. Right now the reset types are application-specific.

Assumptions, External Events, and Notes:

None

Parameters

out	*resetSubType*	- Pointer to the variable that stores the returned reset sub-type

Returns

Last reset type

3.1.4.28 uint32 CFE_PSP_GetSpacecraftId (void)

Purpose Get the spacecraft ID

Description:

This function returns the spacecraft ID as pre-defined by the cFE.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_SPACECRAFT_ID

Purpose Get the spacecraft ID

Description:

This function returns the spacecraft ID as pre-defined by the cFE.

Assumptions, External Events, and Notes:

The macro is defined in cfe_platform_cfg.h.

Parameters

None	

Returns

CFE_PSP_SPACECRAFT_ID

3.1.4.29 void CFE_PSP_GetTime (OS_time_t * *LocalTime*)

Purpose Get time

Description:

Sample/Read a monotonic platform clock with normalization
Outputs an OS_time_t value indicating the time elapsed since an epoch. The epoch is not defined, but typically represents the system boot time. The value increases continuously over time and cannot be reset by software.
This is similar to the CFE_PSP_Get_Timebase(), but additionally it normalizes the output value to an OS_time_t, thereby providing consistent units to the calling application. Any OSAL-provided routine accepts OS_time_t inputs may be used to convert this value into other standardized time units.

Assumptions, External Events, and Notes:

This should refer to the same time domain as CFE_PSP_Get_Timebase(), the primary difference being the format and units of the output value.

Parameters

`out`	*LocalTime*	- Pointer to the structure that stores the returned time value

Returns

None

3.1.4.30 uint32 CFE_PSP_GetTimerLow32Rollover (void)

Purpose Get the lower 32-bit roll-over time value

Description:

This function provides the number that the least significant 32-bit of the 64-bit timestamp returned by CFE_PSP_-Get_Timebase() rolls over.

Assumptions, External Events, and Notes:

If the lower 32-bits rolls at 1 second, then the CFE_PSP_TIMER_LOW32_ROLLOVER will be 1000000. If the lower 32-bits rolls at its maximum value (2^32) then CFE_PSP_TIMER_LOW32_ROLLOVER will be 0.

Parameters

None	

Returns

The lower 32-bit value of the roll-over time value

3.1.4.31 uint32 CFE_PSP_GetTimerTicksPerSecond (void)

Purpose Get the timer ticks per second

Description:

This function provides the number of ticks per second based on the memory bus clock speed. For example, an SP0s uses 400 MHz core clock speed. Memory bus speed is 1/8 of the core clock speed, or 50 MHz, thus 50 million ticks per second.

Assumptions, External Events, and Notes:

The timer resolution for accuracy should not be any slower than 1000000 ticks per second, or 1 microsecond per tick.

Parameters

None	

Returns

Number of timer ticks per second

3.1.4.32 int32 CFE_PSP_GetUserReservedArea (cpuaddr * *PtrToUserArea,* uint32 * *SizeOfUserArea*)

Purpose Get the location and size of the cFE user-reserved memory area

Description:

This function returns the location and size of the cFE user-reserved memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToUserArea*	- Pointer to the variable that stores the returned memory address
out	*SizeOfUserArea*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the location and size of the cFE user-reserved memory area

Description:

This function returns the location and size of the cFE user-reserved memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToUserArea*	- Pointer to the variable that stores the returned memory address
out	*SizeOfUserArea*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.33 const char* CFE_PSP_GetVersionCodeName (void)

Purpose Obtain the version code name

Description:

This retrieves the PSP code name.

Assumptions, External Events, and Notes:

This is a compatibility indicator for the overall cFS ecosystem. All modular components which are intended to interoperate should report the same code name.

Parameters

None	

Returns

Code name. This is a fixed string and cannot be NULL.

Purpose Obtain the version code name

Description:

This retrieves the PSP code name.
This is a compatibility indicator for the overall NASA CFS ecosystem.
All modular components which are intended to interoperate should report the same code name.

Assumptions, External Events, and Notes:

None

Returns

Code name. This is a fixed string and cannot be NULL.

3.1.4.34 void CFE_PSP_GetVersionNumber (uint8 *VersionNumbers[4]*)

Purpose Obtain the PSP numeric version numbers as uint8 values

Description:

This retrieves the numeric PSP version identifier as an array of 4 uint8 values.

Assumptions, External Events, and Notes:

The array of numeric values is in order of precedence:

- [0] = Major Number
- [1] = Minor Number
- [2] = Revision Number
- [3] = Mission Revision

  The "Mission Revision" (last output) also indicates whether this is an official release, a patched release, or a development version.
- 0 indicates an official release
- 1-254 local patch level (reserved for mission use)
- 255 indicates a development build

Parameters

out	*VersionNumbers*	A fixed-size array to be filled with the version numbers

Returns

None

Purpose Obtain the PSP numeric version numbers as uint8 values

Description:

This retrieves the numeric PSP version identifier as an array of 4 uint8 values.
The array of numeric values is in order of precedence: [0] = Major Number [1] = Minor Number [2] = Revision Number [3] = Mission Revision
The "Mission Revision" (last output) also indicates whether this is an official release, a patched release, or a development version. 0 indicates an official release 1-254 local patch level (reserved for mission use) 255 indicates a development build

Assumptions, External Events, and Notes:

None

Parameters

out	*VersionNumbers*	A fixed-size array to be filled with the version numbers

Returns

None

3.1.4.35 const char* CFE_PSP_GetVersionString (void)

Purpose Obtain the PSP version/baseline identifier string

Description:

This retrieves the PSP version identifier string without extra info.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

Version string. This is a fixed string and cannot be NULL.

Purpose Obtain the PSP version/baseline identifier string

Description:

This retrieves the PSP version identifier string without extra info.

Assumptions, External Events, and Notes:

None

Returns

Version string. This is a fixed string and cannot be NULL.

3.1.4.36 int32 CFE_PSP_GetVolatileDiskMem (cpuaddr * *PtrToVolDisk*, uint32 * *SizeOfVolDisk*)

Purpose Get the location and size of the cFE volatile memory area

Description:

This function returns the location and size of the cFE volatile memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToVolDisk*	- Pointer to the variable that stores the returned memory address
out	*SizeOfVolDisk*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Get the location and size of the cFE volatile memory area

Description:

This function returns the location and size of the cFE volatile memory area.

Assumptions, External Events, and Notes:

None

Parameters

out	*PtrToVolDisk*	- Pointer to the variable that stores the returned memory address
out	*SizeOfVolDisk*	- Pointer to the variable that stores the returned memory size

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.37 int32 CFE_PSP_InitProcessorReservedMemory (uint32 *RestartType*)

Purpose Initialize the processor's reserved memory

Description:

This function initializes all of the memory in the BSP that is preserved on a processor reset.

Assumptions, External Events, and Notes:

The memory includes the Critical Data Store, the ES Reset Area, the Volatile Disk Memory and the User Reserved Memory. Options include:

- CFE_PSP_RST_TYPE_PROCESSOR
- CFE_PSP_RST_TYPE_POWERON
- CFE_PSP_RST_TYPE_MAX

  This initializes based on the reset type. Typically, the information is preserved on a processor reset, and cleared/reinitialized on a power-on reset.

Parameters

in	*RestartType*	- The reset type

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Initialize the processor's reserved memory

Description:

This function initializes all of the memory in the BSP that is preserved on a processor reset. The memory includes the Critical Data Store, the ES Reset Area, the Volatile Disk Memory and the User Reserved Memory. Options include CFE_PSP_RST_TYPE_PROCESSOR, CFE_PSP_RST_TYPE_POWERON, CFE_PSP_RST_TYPE_MAX

Assumptions, External Events, and Notes:

This initializes based on the reset type. Typically, the information is preserved on a processor reset, and cleared/reinitialized on a power-on reset.

Parameters

in	*RestartType*	- The reset type

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.38 int32 CFE_PSP_InitSSR (uint32 *bus*, uint32 *device*, char * *DeviceName*)

Purpose Initialize the Solid State Recorder

Description:

This function configures and initializes the Solid State Recorder for a particular platform.

Assumptions, External Events, and Notes:

This function is not implemented for the SP0-vxworks6.9 PSP since SSR is not used.

Parameters

in	*bus*	- ATA controller number
in	*device*	- ATA drive number
in	*DeviceName*	- Name of the XBD device to create

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Initialize the Solid State Recorder

Description:

This function configures and initializes the Solid State Recorder for a particular platform.

Assumptions, External Events, and Notes:

This function is not implemented for the SP0-vxworks6.9 PSP since SSR is not used.

Parameters

in	*bus*	- ATA controller number
in	*device*	- ATA drive number
in	*DeviceName*	- Name of the XBD device to create

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.39 void CFE_PSP_Main (void)

Purpose Main entry-point

Description:

This function is the entry point that the real time OS calls to start cFS. This function will do any BSP/OS-specific setup, then call the entry point of cFS, which is this function.

Assumptions, External Events, and Notes:

cFE should not call this function. See the description.

Parameters

None	

Returns

None

Purpose Main entry-point

Description:

This function is the entry point that the real time OS calls to start cFS. This function will do any BSP/OS-specific setup, then call the entry point of cFS, which is this function.

Assumptions, External Events, and Notes:

cFE should not call this function. See the description.

Parameters

None	

Returns

None

3.1.4.40 int32 CFE_PSP_MemCpy (void * *dest*, const void * *src*, uint32 *size*)

Purpose Copy from one memory block to another memory block

Description:

Copies 'size' byte from memory address pointed by 'src' to memory address pointed by ' dst' For now we are using the standard c library call 'memcpy' but if we find we need to make it more efficient then we'll implement it in assembly.

Assumptions, External Events, and Notes:

None

Parameters

out	*dest*	- Pointer to an address to copy to
in	*src*	- Pointer address to copy from
in	*size*	- Number of bytes to copy

Returns

CFE_PSP_SUCCESS

Purpose Copy from one memory block to another memory block

Description:

Copies 'size' byte from memory address pointed by 'src' to memory address pointed by ' dst' For now we are using the standard c library call 'memcpy' but if we find we need to make it more efficient then we'll implement it in assembly.

Assumptions, External Events, and Notes:

None

Parameters

out	*dest*	- Pointer to an address to copy to
in	*src*	- Pointer address to copy from
in	*size*	- Number of bytes to copy

Returns

CFE_PSP_SUCCESS

3.1.4.41 int32 CFE_PSP_MemRangeGet (uint32 *RangeNum,* uint32 * *MemoryType,* cpuaddr * *StartAddr,* size_t * *Size,* size_t * *WordSize,* uint32 * *Attributes*)

Purpose Get an entry in the memory range table

Description:

This function retrieves an entry in the global CFE_PSP_MemoryTable.

Assumptions, External Events, and Notes:

Because the table is fixed size, the entries are set by using the integer index.

Parameters

in	*RangeNum*	- A 32-bit integer (starting with 0) specifying the MemoryTable entry.
out	*MemoryType*	- A pointer to the 32-bit integer where the Memory Type is stored. Any defined CFE_PSP_MEM_* enumeration can be specified
out	*StartAddr*	- A pointer to the 32-bit integer where the 32-bit starting address of the memory range is stored.
out	*Size*	- A pointer to the 32-bit integer where the 32-bit size of the memory range is stored.
out	*WordSize*	- A pointer to the 32-bit integer where the the minimum addressable size of the range: (CFE_PSP_MEM_SIZE_BYTE, CFE_PSP_MEM_SIZE_WORD, CFE_PSP_MEM_SIZE_DWORD) is stored.
out	*Attributes*	- A pointer to the 32-bit integer where the attributes of the memory range: (CFE_PSP_MEM_ATTR_WRITE, CFE_PSP_MEM_ATTR_READ, CFE_PSP_MEM_ATTR_READWRITE) are stored.

Returns

CFE_PSP_SUCCESS - Memory range returned successfuly
CFE_PSP_INVALID_POINTER - Parameter error
CFE_PSP_INVALID_MEM_RANGE - The index into the table is invalid

Purpose Get an entry in the memory range table

Description:

This function retrieves one of the records in the CFE_PSP_MemoryTable.

Assumptions, External Events, and Notes:

Because the table is fixed size, the entries are accessed by using the integer index.

Parameters

in	*RangeNum*	- A 32-bit integer (starting with 0) specifying the MemoryTable entry.
out	*MemoryType*	- A pointer to the 32-bit integer where the Memory Type is stored. Any defined CFE_PSP_MEM_* enumeration can be specified

out	*StartAddr*	- A pointer to the 32-bit integer where the 32-bit starting address of the memory range is stored.
out	*Size*	- A pointer to the 32-bit integer where the 32-bit size of the memory range is stored.
out	*WordSize*	- A pointer to the 32-bit integer where the the minimum addressable size of the range: (CFE_PSP_MEM_SIZE_BYTE, CFE_PSP_MEM_SIZE_WORD, CFE_PSP_MEM_SIZE_DWORD) is stored.
out	*Attributes*	- A pointer to the 32-bit integer where the attributes of the memory range: (CFE_PSP_MEM_ATTR_WRITE, CFE_PSP_MEM_ATTR_READ, CFE_PSP_MEM_ATTR_READWRITE) are stored.

Returns

CFE_PSP_SUCCESS - Memory range returned successfully
CFE_PSP_INVALID_POINTER - Parameter error
CFE_PSP_INVALID_MEM_RANGE - The index into the table is invalid

3.1.4.42 uint32 CFE_PSP_MemRanges (void)

Purpose Get the number of memory ranges

Description:

This function fetches the number of memory ranges from the global CFE_PSP_MemoryTable.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

The number of entries in the CFE_PSP_MemoryTable

3.1.4.43 int32 CFE_PSP_MemRangeSet (uint32 *RangeNum,* uint32 *MemoryType,* cpuaddr *StartAddr,* size_t *Size,* size_t *WordSize,* uint32 *Attributes*)

Purpose Set an entry in the memory range table

Description:

This function populates an entry in the global CFE_PSP_MemoryTable.

Assumptions, External Events, and Notes:

Because the table is fixed size, the entries are set by using the integer index. No validation is done with the address or size.

Parameters

in	*RangeNum*	- A 32-bit integer (starting with 0) specifying the MemoryTable entry.
in	*MemoryType*	- The memory type to validate, including but not limited to: CFE_PSP_MEM_RAM, CFE_PSP_MEM_EEPROM, or CFE_PSP_MEM_ANY. Any defined CFE_PSP_MEM_∗ enumeration can be specified
in	*StartAddr*	- A 32-bit starting address of the memory range
in	*Size*	- A 32-bit size of the memory range (Address+Size = End Address)
in	*WordSize*	- The minimum addressable size of the range: (CFE_PSP_MEM_SIZE_BYTE, CFE_PSP_MEM_SIZE_WORD, CFE_PSP_MEM_SIZE_DWORD)
in	*Attributes*	- The attributes of the Memory Range: (CFE_PSP_MEM_ATTR_WRITE, CFE_PSP_MEM_ATTR_READ, CFE_PSP_MEM_ATTR_READWRITE)

Returns

CFE_PSP_SUCCESS - Memory range set successfuly
CFE_PSP_INVALID_MEM_RANGE - The index into the table is invalid
CFE_PSP_INVALID_MEM_TYPE - Memory type associated with the range does not match the passed in type.
CFE_PSP_INVALID_MEM_WORDSIZE - The WordSize parameter is not one of the types.
CFE_PSP_INVALID_MEM_ATTR - The Attributes parameter is not one of the predefined types.

3.1.4.44 int32 CFE_PSP_MemRead16 (cpuaddr *MemoryAddress*, uint16 ∗ *uint16Value*)

Purpose Read an 16-bit value from memory

Description:

This function reads a 16-bit value from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to read from
out	*uint16Value*	- Pointer to the variable that stores the 16-bit value read

Returns

CFE_PSP_SUCCESS

3.1.4.45 int32 CFE_PSP_MemRead32 (cpuaddr *MemoryAddress*, uint32 ∗ *uint32Value*)

Purpose Read a 32-bit value from memory

Description:

This function reads a 32-bit value from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to read from
out	*uint32Value*	- Pointer to the variable that stores the 32-bit value read

Returns

CFE_PSP_SUCCESS

3.1.4.46 int32 CFE_PSP_MemRead8 (cpuaddr *MemoryAddress*, uint8 * *ByteValue*)

Purpose Read an 8-bit value from memory

Description:

This function reads an 8-bit value from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to read from
out	*ByteValue*	- Pointer to the variable that stores the 8-bit value read

Returns

CFE_PSP_SUCCESS

3.1.4.47 int32 CFE_PSP_MemSet (void * *dest*, uint8 *value*, uint32 *size*)

Purpose Initialize the specified memory block with the specified value

Description:

Copies 'size' number of byte of value 'value' to memory address pointed by 'dst' .For now we are using the standard c library call 'memset' but if we find we need to make it more efficient then we'll implement it in assembly.

Assumptions, External Events, and Notes:

None

Parameters

out	*dest*	- Pointer to destination address
in	*value*	- An 8-bit value to fill in the memory
in	*size*	- The number of values to write

Returns

CFE_PSP_SUCCESS

Purpose Initialize the specified memory block with the specified value

Description:

Copies 'size' number of byte of value 'value' to memory address pointed by 'dst' .For now we are using the standard c library call 'memset' but if we find we need to make it more efficient then we'll implement it in assembly.

Assumptions, External Events, and Notes:

None

Parameters

out	*dest*	- Pointer to destination address
in	*value*	- An 8-bit value to fill in the memory
in	*size*	- The number of values to write

Returns

CFE_PSP_SUCCESS

3.1.4.48 int32 CFE_PSP_MemValidateRange (cpuaddr *Address,* size_t *Size,* uint32 *MemoryType*)

Purpose Validate memory range and type

Description:

This function validates the memory range and type using the global CFE_PSP_MemoryTable.

Assumptions, External Events, and Notes:

None

Parameters

in	*Address*	- A 32-bit starting address of the memory range
in	*Size*	- A 32-bit size of the memory range (Address+Size = End Address)
in	*MemoryType*	- The memory type to validate, including but not limited to: CFE_PSP_MEM_RAM, CFE_PSP_MEM_EEPROM, or CFE_PSP_MEM_ANY. Any defined CFE_PSP_MEM_∗ enumeration can be specified

Returns

CFE_PSP_SUCCESS - Memory range and type information is valid and can be used.
CFE_PSP_INVALID_MEM_ADDR - Starting address is not valid
CFE_PSP_INVALID_MEM_TYPE - Memory type associated with the range does not match the passed in type.
CFE_PSP_INVALID_MEM_RANGE - The Memory range associated with the address is not large enough to contain Address+Size.

3.1.4.49 int32 CFE_PSP_MemWrite16 (cpuaddr *MemoryAddress,* uint16 *uint16Value*)

Purpose Write 16-bit value to memory

Description:

This function writes a 16-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*uint16Value*	- A 16-bit value to be written

Returns

CFE_PSP_SUCCESS

3.1.4.50 int32 CFE_PSP_MemWrite32 (cpuaddr *MemoryAddress*, uint32 *uint32Value*)

Purpose Write a 32-bit value to memory

Description:

This function writes a 32-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*uint32Value*	- A 32-bit value to be written

Returns

CFE_PSP_SUCCESS

3.1.4.51 int32 CFE_PSP_MemWrite8 (cpuaddr *MemoryAddress*, uint8 *ByteValue*)

Purpose Write an 8-bit value to memory

Description:

This function writes an 8-bit value to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*MemoryAddress*	- The memory address to write to
in	*ByteValue*	- An 8-bit value to be written

Returns

CFE_PSP_SUCCESS

3.1.4.52 int32 CFE_PSP_Module_FindByName (const char * *ModuleName*, uint32 * *PspModuleId*)

Purpose Obtain the module ID by name

Description:

This function retrieves the module ID of the given module name.

Assumptions, External Events, and Notes:

Although this is currently prototyped as a function scoped to the PSP, this prototype could be moved to the public area so the cFS could use this.

Parameters

in	*ModuleName*	- Name of the module to look up
out	*PspModuleId*	- Pointer to the variable that stores the returned module ID

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MODULE_NAME

Purpose Find a module by name

Description:

None

Assumptions, External Events, and Notes:

None

Parameters

in	*ModuleName*	- The name of the Module
in,out	*PspModuleId*	- The Module Id

Returns

CFE_PSP_INVALID_MODULE_NAME
CFE_PSP_SUCCESS

3.1.4.53 int32 CFE_PSP_Module_GetAPIEntry (uint32 *PspModuleId,* CFE_PSP_ModuleApi_t ** *API*)

Purpose Obtain the API for a specific module

Description:

This function retrieves the API structure for a given module ID.

Assumptions, External Events, and Notes:

None

Parameters

in	*PspModuleId*	- The ID of the module (configuration-dependent)
out	*API*	- Pointer to the variable that stores the returned API structure

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MODULE_ID

Purpose Obtain the API for a specific module

Description:

This function retrieves the API structure for a given module ID.

Assumptions, External Events, and Notes:

None

Parameters

in	*PspModuleId*	- The ID of the module (configuration-dependent)
out	*API*	- Pointer to the variable that stores the returned API structure

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MODULE_ID

3.1.4.54 void CFE_PSP_ModuleInit (void)

Purpose Initialize the included PSP modules

Description:

This function initializes the include PSP modules.

Assumptions, External Events, and Notes:

This function is an optional part of the PSP and some PSP implementations may not use it.
Note 1: This function should only be called during PSP initialization before the system is operational. It is not intended to be called from application code after cFE has started. The function is not necessarily be thread-safe and should be called before any child threads are created.
Note 2: This function does *not* return any status. If a failure occurs during initialization that would make normal operation impossible, then the module itself will call CFE_PSP_Panic() and this will not return. Otherwise, benign/recoverable failures are expected to be just that, and the calling code will not need to take any special action either way. In short, if this function returns, then it means the system is good enough to continue.

Parameters

None	

Returns

None

Purpose Initialize a list of Modules

Description:

Initalize all modules for PSP including user-selected modules

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.55 void CFE_PSP_Panic (int32 *errorCode*)

Purpose Abort cFE startup

Description:

This function provides the mechanism to abort the cFE startup process and returns back to the OS.

Assumptions, External Events, and Notes:

This function should not be called by the cFS applications.

Parameters

in	*errorCode*	- Error code that causes the exit

Returns

None

Purpose Abort cFE startup

Description:

This function provides the mechanism to abort the cFE startup process and returns back to the OS.

Assumptions, External Events, and Notes:

This function should not be called by the cFS applications.

Parameters

in	*errorCode*	- Error code that causes the exit

Returns

None

3.1.4.56 int32 CFE_PSP_PortRead16 (cpuaddr *PortAddress*, uint16 * *uint16Value*)

Purpose Read two bytes from memory

Description:

This function reads two bytes from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to read from
out	*uint16Value*	- Pointer to the variable that stores the two-byte value read

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_POINTER
#CFE_PSP_ERROR_ADD_MISALIGNED
CFE_PSP_INVALID_MEM_ADDR

3.1.4.57 int32 CFE_PSP_PortRead32 (cpuaddr *PortAddress*, uint32 * *uint32Value*)

Purpose Read four bytes from memory

Description:

This function reads four bytes from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to read from
out	*uint32Value*	- Pointer to the variable that stores the four-byte value read

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_POINTER
#CFE_PSP_ERROR_ADD_MISALIGNED

3.1.4.58 int32 CFE_PSP_PortRead8 (cpuaddr *PortAddress*, uint8 * *ByteValue*)

Purpose Read one byte from memory

Description:

This function reads one byte from the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to read from
out	*ByteValue*	- Pointer to the variable that stores the one-byte value read

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_POINTER
CFE_PSP_INVALID_MEM_ADDR

3.1.4.59 int32 CFE_PSP_PortWrite16 (cpuaddr *PortAddress*, uint16 *uint16Value*)

Purpose Write two bytes to memory

Description:

This function writes two bytes to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to write to
in	*uint16Value*	- Two-byte value to be written

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MEM_ADDR
#CFE_PSP_ERROR_ADD_MISALIGNED

3.1.4.60 int32 CFE_PSP_PortWrite32 (cpuaddr *PortAddress*, uint32 *uint32Value*)

Purpose Write four bytes to memory

Description:

This function writes four bytes to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to write to
in	*uint32Value*	- Four-byte value to be written

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MEM_ADDR
#CFE_PSP_ERROR_ADD_MISALIGNED

3.1.4.61 int32 CFE_PSP_PortWrite8 (cpuaddr *PortAddress*, uint8 *ByteValue*)

Purpose Write one byte to memory

Description:

This function writes one byte to the specified memory.

Assumptions, External Events, and Notes:

None

Parameters

in	*PortAddress*	- The port address to write to
in	*ByteValue*	- One-byte value to be written

Returns

CFE_PSP_SUCCESS
CFE_PSP_INVALID_MEM_ADDR

3.1.4.62 int32 CFE_PSP_ReadFromCDS (void * *PtrToDataFromRead*, uint32 *CDSOffset*, uint32 *NumBytes*)

Purpose Read from the Critical Data Store memory area

Description:

This function reads from the CDS memory area.

Assumptions, External Events, and Notes:

Inability to read from FLASH does not affect return code because the reserve memory is the golden copy while flash is just a backup

Parameters

out	*PtrToDataFrom-Read*	- Pointer to the data buffer that stores the read data

in	*CDSOffset*	- Memory offset from the beginning of the CDS block
in	*NumBytes*	- Number of bytes to be read

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Read from the Critical Data Store memory area

Description:

This function reads from the CDS memory area.

Assumptions, External Events, and Notes:

Inability to read from FLASH does not affect return code because the reserve memory is the golden copy while flash is just a backup

Parameters

out	*PtrToDataFrom-Read*	- Pointer to the data buffer that stores the read data
in	*CDSOffset*	- Memory offset from the beginning of the CDS block
in	*NumBytes*	- Number of bytes to be read

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.4.63 void CFE_PSP_Restart (uint32 *resetType*)

Purpose Re-start

Description:

This function is the entry point back to the BSP to restart the processor. cFE calls this function to restart the processor.

Assumptions, External Events, and Notes:

Depending on the resetType, the function will reboot with the following restart type:

- resetType == CFE_PSP_RST_TYPE_POWERON –> reboot(BOOT_CLEAR)
- resetType != CFE_PSP_RST_TYPE_POWERON –> reboot(BOOT_NORMAL)
  System restart types defined in sysLib.h:
- BOOT_NORMAL _"normal reboot with countdown, memory is not cleared"_
- BOOT_CLEAR _"clear memory"_
  The following reboot options are not used.
- BOOT_NO_AUTOBOOT _"no autoboot if set, memory is not cleared"_
- BOOT_QUICK_AUTOBOOT _"fast autoboot, memory is not cleared"_

Parameters

in	*resetType*	- Type of cFE reset

Returns

None

Purpose Re-start

Description:

This function is the entry point back to the BSP to restart the processor. cFE calls this function to restart the processor.
Depending on the resetType, the function will reboot with the following restart type:

- resetType = CFE_PSP_RST_TYPE_POWERON –> reboot(BOOT_CLEAR)
- resetType != CFE_PSP_RST_TYPE_POWERON –> reboot(BOOT_NORMAL)

Assumptions, External Events, and Notes:

system restart types defined in sysLib.h:

- BOOT_NORMAL _"normal reboot with countdown, memory is not cleared"_
- BOOT_CLEAR _"clear memory"_ The following reboot options are not used.
- BOOT_NO_AUTOBOOT _"no autoboot if set, memory is not cleared"_
- BOOT_QUICK_AUTOBOOT _"fast autoboot, memory is not cleared"_

Parameters

in	*resetType*	- Type of cFE reset

Returns

None

3.1.4.64 void CFE_PSP_SetDefaultExceptionEnvironment (void)

Purpose Initialize default exception handling

Description:

This function sets up a default exception environment for a particular platform.

Assumptions, External Events, and Notes:

For VxWorks, the exception environment is local to each task. Therefore, this must be called for each task that wants to do floating point and catch exceptions. Currently, this is automatically called from OS_TaskRegister() for every task.

Parameters

None	

Returns

None

Purpose Initialize default exception handling

Description:

This function sets up a default exception environment for a particular platform.

Assumptions, External Events, and Notes:

For VxWorks, the exception environment is local to each task. Therefore, this must be called for each task that wants to do floating point and catch exceptions. Currently, this is automatically called from OS_TaskRegister() for every task.

Parameters

None	

Returns

None

3.1.4.65 void CFE_PSP_SetupReservedMemoryMap (void)

Purpose Initialize the CFE_PSP_ReservedMemoryMap global object

Description:

This function initializes the CFE_PSP_ReservedMemoryMap global object.

Assumptions, External Events, and Notes:

This function must be called by the startup code before the map is accessed.

Parameters

None	

Returns

None

Purpose Initialize the CFE_PSP_ReservedMemoryMap global object

Description:

This function initializes the CFE_PSP_ReservedMemoryMap global object.

Assumptions, External Events, and Notes:

This function must be called by the startup code before the map is accessed.

Parameters

None	

Returns

None

3.1.4.66 void CFE_PSP_WatchdogDisable (void)

Purpose Disable the watchdog timer

Description:

This function disables the watchdog timer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

Purpose Disable the watchdog timer

Description:

This function disables the watchdog timer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.67 void CFE_PSP_WatchdogEnable (void)

Purpose Enable the watchdog timer

Description:

This function enables the watchdog timer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

Purpose Enable the watchdog timer

Description:

This function enables the watchdog timer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.68 uint32 CFE_PSP_WatchdogGet (void)

Purpose Get the watchdog time

Description:

This function fetches the watchdog time, in milliseconds.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

The watchdog time in milliseconds

Purpose Get the watchdog time

Description:

This function fetches the watchdog time, in milliseconds.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

The watchdog time in milliseconds

3.1.4.69 void CFE_PSP_WatchdogInit (void)

Purpose Initialize the watchdog timer

Description:

This function configures and intializes the watchdog timer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

Purpose Initialize the watchdog timer

Description:

This function configures and initializes the watchdog timer to its default setting.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.70 void CFE_PSP_WatchdogService (void)

Purpose Service the watchdog timer

Description:

This function services the watchdog timer according to the value set in CFE_PSP_WatchdogSet().

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

Purpose Service the watchdog timer

Description:

This function services the watchdog timer according to the value set in CFE_PSP_WatchdogSet().

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.1.4.71 void CFE_PSP_WatchdogSet (uint32 *watchDogValue_ms*)

Purpose Set the watchdog time

Description:

This function sets the current watchdog time, in milliseconds.

Assumptions, External Events, and Notes:

Although the WatchDog can be set to nano-seconds precision, the implementation only allows milliseconds precision.

Parameters

in	*watchDogValue_ms*	- watchdog time in milliseconds

Returns

None

Purpose Set the watchdog time

Description:

This function sets the current watchdog time, in milliseconds.

Assumptions, External Events, and Notes:

Although the WatchDog can be set to nano-seconds precision, the implementation only allows milliseconds precision.

Parameters

in	*watchDogValue_ms*	- watchdog time in milliseconds

Returns

None

3.1.4.72 bool CFE_PSP_WatchdogStatus (void)

Purpose Check if watchdog is enabled or disabled

Description:

This functions returns the status of the Watchdog

Assumptions, External Events, and Notes:

This function will return true of watchdog is enabled or false if watchdog is disabled

Parameters

None	

Returns

true - if Watchdog is currently enabled
false - if Watchdog is current disabled

Purpose Check if watchdog is enabled ro disabled

Description:

This functions returns the status of the Watchdog

Assumptions, External Events, and Notes:

This function will return true of watchdog is enabled or false if watchdog is disabled

Parameters

None	

Returns

true - if Watchdog is currently enabled
false - if Watchdog is current disabled

3.1.4.73 int32 CFE_PSP_WriteToCDS (const void * *PtrToDataToWrite*, uint32 *CDSOffset*, uint32 *NumBytes*)

Purpose Write to the Critical Data Store memory area

Description:

This function write the specified data to the specified memory area of the CDS.

Assumptions, External Events, and Notes:

Inability to write to FLASH does not affect return code because the reserve memory is the golden copy while flash is just a backup

Parameters

in	*PtrToDataToWrite*	- Pointer to the data buffer to be written
in	*CDSOffset*	- Memory offset from the beginning of the CDS block
in	*NumBytes*	- Number of bytes to be written

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Write to the Critical Data Store memory area

Description:

This function write the specified data to the specified memory area of the CDS.

Assumptions, External Events, and Notes:

Inability to write to FLASH does not affect return code because the reserve memory is the golden copy while flash is just a backup

Parameters

in	*PtrToDataToWrite*	- Pointer to the data buffer to be written
in	*CDSOffset*	- Memory offset from the beginning of the CDS block
in	*NumBytes*	- Number of bytes to be written

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.1.5 Variable Documentation

3.1.5.1 CFE_StaticModuleLoadEntry_t CFE_PSP_BASE_MODULE_LIST[ ]

A list of fixed/base modules associated with the PSP.

Description:

This list should be generated by the build system based on the user-selected PSP

3.1.5.2 CFE_PSP_ReservedMemoryMap_t CFE_PSP_ReservedMemoryMap

Map to the reserved memory area(s) Contains a pointer to each of the separate memory blocks.

Assumptions, External Events, and Notes:

None

Map to the reserved memory area(s) Contains a pointer to each of the separate memory blocks.

Description:

The sizes of each memory area is defined in os_processor.h for this architecture.

3.2 PSP Public APIs - SP0-VxWorks6.9 Platform

Data Structures

- struct MEM_SCRUB_STATUS_s

  Memory Scrubbing information struct.
- struct MEM_SCRUB_ERRSTATS_s

  Memory Error Statistics struct.
- struct SP0_info_table_t

  SP0 info structure.

Macros

- #define MEM_SCRUB_PRINT_SCOPE "PSP MEM SCRUB: "

  Default Memory Scrubbing pre-print string.
- #define MEM_SCRUB_TASK_START_ON_STARTUP true

  Start mem scrub on startup option.
- #define SP0_TEXT_BUFFER_MAX_SIZE 1000

  SP0_TEXT_BUFFER_MAX_SIZE.
- #define SP0_SAFEMODEUSERDATA_BUFFER_SIZE 256

  SP0_SAFEMODEUSERDATA_BUFFER_SIZE.
- #define SP0_PRINT_SCOPE "PSP SP0: "

  Default SP0 Info pre-print string.

Typedefs

- typedef struct MEM_SCRUB_STATUS_s MEM_SCRUB_STATUS_t

  Memory Scrubbing information struct.
- typedef struct MEM_SCRUB_ERRSTATS_s MEM_SCRUB_ERRSTATS_t

  Memory Error Statistics struct.

Functions

- void CFE_PSP_MEMORY_SYNC_Enable (void)
- void CFE_PSP_MEMORY_SYNC_Disable (void)
- int32 CFE_PSP_MEM_SCRUB_Set (uint32 newStartAddr, uint32 newEndAddr, osal_priority_t task_priority)
- bool CFE_PSP_MEM_SCRUB_isRunning (void)
- int32 CFE_PSP_MEM_SCRUB_Delete (void)
- void CFE_PSP_MEM_SCRUB_Status (MEM_SCRUB_STATUS_t *mss_Status, bool talk)
- int32 CFE_PSP_MEM_SCRUB_Init (void)
- int32 CFE_PSP_MEM_SCRUB_Enable (void)
- int32 CFE_PSP_MEM_SCRUB_Disable (void)

- void CFE_PSP_MEM_SCRUB_ErrStats (MEM_SCRUB_ERRSTATS_t *errStats, bool talkative)
- int32 PSP_SP0_GetInfo (void)
- static int32 PSP_SP0_PrintToBuffer (void)
- SP0_info_table_t PSP_SP0_GetInfoTable (bool print_to_console)
- int32 PSP_SP0_DumpData (void)
- int64_t PSP_SP0_GetDiskFreeSize (char *ram_disk_root_path)
- void CFE_PSP_ProcessPOSTResults (void)
- void CFE_PSP_LogSoftwareResetType (RESET_SRC_REG_ENUM resetSrc)
- void OS_Application_Startup (void)
- void OS_Application_Run (void)
- int32 CFE_PSP_SuspendConsoleShellTask (bool suspend)
- uint32 CFE_PSP_GetRestartType (uint32 *resetSubType)
- int32 CFE_PSP_SetTaskPrio (const char *tName, uint8 tgtPrio)
- int32 CFE_PSP_TIME_NTPSync_Task_Enable (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Disable (void)
- bool CFE_PSP_TIME_NTPSync_Task_isRunning (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Priority_Set (osal_priority_t opPriority)
- bool CFE_PSP_TIME_NTP_Daemon_isRunning (void)
- int32 ntp_clock_vxworks_Destroy (void)
- uint16 CFE_PSP_TIME_NTPSync_GetFreq (void)
- void CFE_PSP_TIME_NTPSync_SetFreq (uint16 uiNewFreqSec)
- int32 CFE_PSP_TIME_Set_OS_Time (const uint32 ts_sec, const uint32 ts_nsec)
- int32 CFE_PSP_TIME_Get_OS_Time (CFE_TIME_SysTime_t *myT)
- bool CFE_PSP_TIME_CFETimeService_isRunning (void)

  Check if CFS Time Service is up and running.
- int32 CFE_PSP_TIME_StartNTPDaemon (void)
- int32 CFE_PSP_TIME_StopNTPDaemon (void)

Variables

- const char * g_pMachineCheckCause_msg [10]

  List of MCHK Errors Messages.

3.2.1 Detailed Description

3.2.2 Macro Definition Documentation

3.2.2.1 #define MEM_SCRUB_PRINT_SCOPE "PSP MEM SCRUB: "

Default Memory Scrubbing pre-print string.

Description:

This string is printed before every print related to Memory Scrubbing API.

3.2.2.2 #define MEM_SCRUB_TASK_START_ON_STARTUP true

Start mem scrub on startup option.

Description:

This option can be set to indicate if PSP should start mem scrub task on startup. 0 = Do not start task during startup 1 = Start task during startup

3.2.2.3 #define SP0_PRINT_SCOPE "PSP SP0: "

Default SP0 Info pre-print string.

Description:

This string is printed before every print related to SP0 Info API.

3.2.2.4 #define SP0_SAFEMODEUSERDATA_BUFFER_SIZE 256

SP0_SAFEMODEUSERDATA_BUFFER_SIZE.

Description:

This is the maximum size of the safeModeUserData char array.

3.2.2.5 #define SP0_TEXT_BUFFER_MAX_SIZE 1000

SP0_TEXT_BUFFER_MAX_SIZE.

Description:

This is the maximum size of the SP0 char array table.

3.2.3 Typedef Documentation

3.2.3.1 typedef struct MEM_SCRUB_ERRSTATS_s MEM_SCRUB_ERRSTATS_t

Memory Error Statistics struct.

Description:

Returns a structure containing information about memory errors:

- uil2errTotal
- uil2errMult
- uil2errTagPar
- uil2errMBECC
- uil2errSBECC
- uil2errCfg
- uimchCause
- uimchkHook

Assumptions, External Events, and Notes:

From sysLib.c: "The machine check ISR will update these counters"

3.2.3.2 typedef struct MEM_SCRUB_STATUS_s MEM_SCRUB_STATUS_t

Memory Scrubbing information struct.

Description:

Memory scrubbing struct containing useful information:

- uiMemScrubStartAddr
- uiMemScrubEndAddr
- uiMemScrubCurrentPage
- uiMemScrubTotalPages
- opMemScrubTaskPriority
- bIsRunning

3.2.4 Function Documentation

3.2.4.1 uint32 CFE_PSP_GetRestartType (uint32 * *resetSubType*)

Purpose Get restart type

Description:

This function returns the last reset type.

Assumptions, External Events, and Notes:

If a pointer to a valid memory space is passed in, it returns the reset sub-type in that memory. Right now the reset types are application-specific. For the cFE, they are defined in the cfe_es.h file.

Parameters

out	*resetSubType*	- Pointer to the variable that stores the returned reset sub-type

Returns

Last reset type

Purpose Get restart type

Description:

This function returns the last reset type. If a pointer to a valid memory space is passed in, it returns the reset sub-type in that memory. Right now the reset types are application-specific.

Assumptions, External Events, and Notes:

None

Parameters

out	*resetSubType*	- Pointer to the variable that stores the returned reset sub-type

Returns

Last reset type

3.2.4.2 void CFE_PSP_LogSoftwareResetType (RESET_SRC_REG_ENUM *resetSrc*)

Purpose Logs software reset type

Description:

This function determines if started in safe mode and logs off software reset type.

Assumptions, External Events, and Notes:

RESET_SRC_REG_ENUM is defined in Aitech file scratchRegMap.h

Parameters

resetSrc	- Reset Type RESET_SRC_REG_ENUM

Returns

None

Purpose Determines if started in safe mode and logs off nominal resets.

Description:

None

Assumptions, External Events, and Notes:

RESET_SRC_REG_ENUM is defined in Aitech file scratchRegMap.h

Parameters

resetSrc	- Reset Type RESET_SRC_REG_ENUM

Returns

None

3.2.4.3 int32 CFE_PSP_MEM_SCRUB_Delete (void)

Purpose Stop the memory scrubbing task

Description:

This function deletes the Memory Scrubbing task. The task is deleted and the statistics are reset.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully deleted
CFE_PSP_ERROR - If unsuccessfully deleted

Purpose Stop the memory scrubbing task

Description:

This function resets all memory scrub related variables, then call CFE_PSP_MEM_SCRUB_Disable to delete the memory scrubbing task.

Assumptions, External Events, and Notes:

This function should only be used for shutdown/reset. To stop/delete memory scrub task for other situations, use SCRUB_Disable

Parameters

-	None

Returns

CFE_PSP_SUCCESS - If successfully deleted
CFE_PSP_ERROR - If unsuccessfully deleted

3.2.4.4 int32 CFE_PSP_MEM_SCRUB_Disable (void)

Purpose Disable the Memory Scrubbing task

Description:

This function disables the Memory Scrubbing task.

Assumptions, External Events, and Notes:

If the task is already running, delete it. If the task is not running, then do nothing.

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully disabled memory scrub task
CFE_PSP_ERROR - If unsuccessfully disabled memory scrub task

Purpose Disable the Memory Scrubbing task

Description:

This function disables the Memory Scrubbing task.

Assumptions, External Events, and Notes:

If the task is already running, delete it. If the task is not running, then do nothing.

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully disabled memory scrub task
CFE_PSP_ERROR - If unsuccessfully disabled memory scrub task

3.2.4.5 int32 CFE_PSP_MEM_SCRUB_Enable (void)

Purpose Enable the Memory Scrubbing task

Description:

This function enables the Memory Scrubbing task.

Assumptions, External Events, and Notes:

If the task is already running, do nothing. If the task is not running, then start it.

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully started memory scrubbing task
CFE_PSP_ERROR - If unsuccesffuly started memory scrubbing task

Purpose Enable the Memory Scrubbing task

Description:

This function enables (starts) the Memory Scrubbing task.

Assumptions, External Events, and Notes:

If the task is already running, do nothing. If the task is not running, then start it.

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully started memory scrubbing task
CFE_PSP_ERROR - If unsuccesffuly started memory scrubbing task

3.2.4.6 void CFE_PSP_MEM_SCRUB_ErrStats (MEM_SCRUB_ERRSTATS_t * *errStats,* bool *talkative*)

Purpose Get the memory error statistics

Description:

This function will fill the provided MEM_SCRUB_ERRSTATS_t pointer with memory error statistics

Assumptions, External Events, and Notes:

TBD what these individual values truely represent

Parameters

errStats	- Pointer to MEM_SCRUB_ERRSTATS_t structure
talkative	- Boolean to indicate if the ckCtrs should be called to print out statistics

Returns

None

Purpose Get the memory error statistics

Description:

This function will fill the provided MEM_SCRUB_ERRSTATS_t pointer with memory error statistics

Assumptions, External Events, and Notes:

TBD what these individual values truely represent. From sysLib.c: "The machine check ISR will update these counters"

Parameters

errStats	- Pointer to MEM_SCRUB_ERRSTATS_t structure
talkative	- Boolean to indicate if the ckCtrs should be called to print out statistics

Returns

None

3.2.4.7 int32 CFE_PSP_MEM_SCRUB_Init (void)

Purpose Initialize the Memory Scrubbing task

Description:

This function starts the Memory Scrubbing task as a child thread.

Assumptions, External Events, and Notes:

The scrubMemory function implemented by AiTech may never return an error.

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successful initialization
CFE_PSP_ERROR - If unsuccessful initialization

Purpose Initialize the Memory Scrubbing task

Description:

This function only initializes memory scrubbing-related variables, but does not actually handle starting the task

Assumptions, External Events, and Notes:

This function should only be called during startup. To handle starting memory scrub task post startup, use CFE_PSP_MEM_Scrub_Enable

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successful initialization
CFE_PSP_ERROR - If unsuccessful initialization

3.2.4.8 bool CFE_PSP_MEM_SCRUB_isRunning (void)

Purpose Check if the Memory Scrubbing task is running

Description:

This function provides the status whether the Memory Scrubbing task is running.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

true - If task is running
false - If task is not running

Purpose Check if the Memory Scrubbing task is running

Description:

This function provides the status whether the Memory Scrubbing task is running.

Assumptions, External Events, and Notes:

None

Parameters

-	None

Returns

true - If task is running
false - If task is not running

3.2.4.9 int32 CFE_PSP_MEM_SCRUB_Set (uint32 *newStartAddr,* uint32 *newEndAddr,* osal_priority_t *task_priority*)

Purpose Set the Memory Scrubbing parameters

Description:

This functions set the memory scrubbing parameters.

Assumptions, External Events, and Notes:

After calling this function, the new settings will be applied in the next call to the Activate Memory Scrubbing funtion. If newEndAddr is set to a value larger than the actual physical memory limit, the function will use the phyisical memory limit. Task priority can only be set between MEMSCRUB_PRIORITY_UP_RANGE and MEMSCRUB_PRIORITY_DOWN_RANGE defined in cfe_psp_config.h. Default is set to MEMSCRUB_DEFAULT_PRIORITY.

Parameters

in	*newStartAddr*	- Memory address to start from, usually zero
in	*newEndAddr*	- Memory address to end at, usually end of the physical RAM
in	*task_priority*	- The task priority

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Set the Memory Scrubbing parameters

Description:

This functions set the memory scrubbing parameters.

Assumptions, External Events, and Notes:

After calling this function, the new settings will be applied in the next call to the Activate Memory Scrubbing funtion. If newEndAddr is set to a value larger than the actual physical memory limit, the function will use the phyisical memory limit. Task priority can only be set between MEMSCRUB_PRIORITY_UP_RANGE and MEMSCRUB_PRIORITY_DOWN_RANGE defined in cfe_psp_config.h. Default is set to MEMSCRUB_DEFAULT_PRIORITY.
If the scrubMemory function is called in a task that has a timing restriction, the scrub range (i.e. endAddr - startAddr) should be adjusted to a small value but should be a multiple of the page size (4096 bytes).

Parameters

in	*newStartAddr*	- Memory address to start from, usually zero
in	*newEndAddr*	- Memory address to end at, usually end of the physical RAM
in	*task_priority*	- The task priority

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.10 void CFE_PSP_MEM_SCRUB_Status (MEM_SCRUB_STATUS_t * *mss_Status,* bool *talk*)

Purpose Print the Memory Scrubbing statistics

Description:

This function outputs to the console the following Memory Scrubbing statistics: Start memory address, End memory address, current memory page and total memory pages

Assumptions, External Events, and Notes:

Start memory address is usually 0. End memory address is usually set to the last value of RAM address. Note that a page is 4098 bytes.

Parameters

out	*mss_Status*	- Pointer to struct containing mem scrub info
in	*talk*	- Print out the status values

Returns

None

Purpose Print the Memory Scrubbing statistics

Description:

This function outputs to the console the following Memory Scrubbing statistics: Start memory address, End memory address, current memory page and total memory pages

Assumptions, External Events, and Notes:

Start memory address is usually 0. End memory address is usually set to the last value of RAM address. Note that a page is 4098 bytes.

Parameters

out	*mss_Status*	- Pointer to struct containing mem scrub info
in	*talk*	- Print out the status values

Returns

None

3.2.4.11 void CFE_PSP_MEMORY_SYNC_Disable (void)

Purpose Disable CDS sync to FLASH

Description:

This function will disable CDS syncing to FLASH

Assumptions, External Events, and Notes:

This will not cancel/stop any in-progress syncing

Parameters

None	

Returns

None

Purpose Disable CDS sync to FLASH

Description:

This function will disable CDS syncing to FLASH

Assumptions, External Events, and Notes:

This will not cancel/stop any in-progress syncing

Parameters

None	

Returns

None

3.2.4.12 void CFE_PSP_MEMORY_SYNC_Enable (void)

Purpose Enable CDS sync to FLASH

Description:

This function will enable CDS syncing to FLASH

Assumptions, External Events, and Notes:

Parameters

None	

Returns

None

Purpose Enable CDS sync to FLASH

Description:

This function will enable CDS syncing to FLASH

Assumptions, External Events, and Notes:

Parameters

None	

Returns

None

3.2.4.13 void CFE_PSP_ProcessPOSTResults (void)

Purpose Output POST results

Description:

This function prints the Power-On Self-Test (POST) results to the console.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

Purpose Print Power On Self Test (POST) results to the console

Description:

None

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.2.4.14 int32 CFE_PSP_SetTaskPrio (const char * *tName*, uint8 *tgtPrio*)

Purpose Set task priority

Description:

This function sets the new task priority for a given task name.

Assumptions, External Events, and Notes:

None

Parameters

in	*tName*	- Task name
in	*tgtPrio*	- New task priority

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Changes default task priority to a given priority

Description:

None

Assumptions, External Events, and Notes:

None

Parameters

in	*tName*	- Task name
in	*tgtPrio*	- New task priority

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.15 int32 CFE_PSP_SuspendConsoleShellTask (bool *suspend*)

Purpose Suspend/Resume the Console Shell Task

Description:

This function suspends/resumes the Console Shell task.

Assumptions, External Events, and Notes:

None

Parameters

in	*suspend*	- True to suspend task, False to resume task

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Application Run entry point from OSAL BSP.

Description:

SP0 Implementation Specific

Assumptions, External Events, and Notes:

This function is declared but empty so that we don't run the default OSAL equivalent function. The latter will actively suspend the console shell. Replication of the behaviour to suspend the shell is performed via API function CFE_PSP_SuspendConsoleShellTask().

Parameters

None	

Returns

None

Purpose Function Suspend/Resume the Console Shell Task.

Description:

None

Assumptions, External Events, and Notes:

None

Parameters

in	*suspend*	- True to suspend task, False to resume task

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.16 bool CFE_PSP_TIME_CFETimeService_isRunning (void)

Check if CFS Time Service is up and running.

Description:

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

true - CFE Time Service is ready
false - CFE Time Service is not ready

3.2.4.17 int32 CFE_PSP_TIME_Get_OS_Time (CFE_TIME_SysTime_t * *myT*)

Purpose Gets the current time from vxworks OS

Description:

This function gets the current vxworks OS time.

Assumptions, External Events, and Notes:

This function is used by the NTP Sync task to grab the current OS time. It uses CLOCK_REALTIME.
NTP Sync will not occur if NTP time is less than CFE_MISSION_TIME_EPOCH_UNIX_DIFF

Parameters

out	*myT*	- Pointer to the variable that stores the returned time value

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.18 bool CFE_PSP_TIME_NTP_Daemon_isRunning (void)

Purpose Check if the NTP Daemon is running

Description:

This function checks if the vxworks NTP client task is running

Assumptions, External Events, and Notes:

This function will not check if the task has successfully syncronized with an NTP server

Parameters

None	

Returns

True - If NTP client task is running
False - If NTP client task is not running

3.2.4.19 uint16 CFE_PSP_TIME_NTPSync_GetFreq (void)

Purpose Get the currently set sync frequency

Description:

This function returns the NTP time synchronization frequency, in seconds.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

Current frequency

3.2.4.20 void CFE_PSP_TIME_NTPSync_SetFreq (uint16 *uiNewFreqSec*)

Purpose Change the sync frequency

Description:

This function updates the NTP time synchronization frequency, in seconds.

Assumptions, External Events, and Notes:

None

Parameters

in	*uiNewFreqSec*	- The new frequency, in seconds

3.2.4.21 int32 CFE_PSP_TIME_NTPSync_Task_Disable (void)

Purpose Disables the CFE PSP Time Task synchronizing with the NTP server

Description:

This function disable the cFE PSP Time sync task with the NTP server.

Assumptions, External Events, and Notes:

Function will return CFE_PSP_SUCCESS if there is no NTP Sync task running

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully started NTP Sync task
CFE_PSP_ERROR - If unsuccessfully started NTP Sync task

3.2.4.22 int32 CFE_PSP_TIME_NTPSync_Task_Enable (void)

Purpose Initialize the CFE PSP Time Task synchronizing with the NTP server

Description:

This function intializes the cFE PSP Time sync task with the NTP server.

Assumptions, External Events, and Notes:

Function will return CFE_PSP_SUCCESS if there is already an NTP Sync task running and will NOT attempt to start another

Parameters

None	

Returns

CFE_PSP_SUCCESS - If successfully started NTP Sync task
CFE_PSP_ERROR - If unsuccessfully started NTP Sync task

3.2.4.23 bool CFE_PSP_TIME_NTPSync_Task_isRunning (void)

Purpose Check if the NTP Sync task is running

Description:

This function checks on whether or not the NTP Sync task is running

Assumptions, External Events, and Notes:

Will check via OS_TaskGetIdByName using NTPSYNC_TASK_NAME as the name of NTP sync task

Parameters

None	

Returns

true - If NTP Sync Task is running
false - If NTP Sync Task is not running

3.2.4.24 int32 CFE_PSP_TIME_NTPSync_Task_Priority_Set (osal_priority_t *opPriority*)

Purpose Set the NTP Sync task priority

Description:

This function sets the NTP Sync task priority

Assumptions, External Events, and Notes:

New priority must be between NTPSYNC_PRIORITY_DOWN_RANGE and NTPSYNC_PRIORITY_UP_RANGE. If the new priority is not within this range, the default priority will be assigned.

Parameters

in	*opPriority*	- The new task priority

Returns

CFE_PSP_SUCCESS - If successfully set new priority
CFE_PSP_ERROR - If unsuccessfully set new priority

3.2.4.25 int32 CFE_PSP_TIME_Set_OS_Time (const uint32 *ts_sec,* const uint32 *ts_nsec*)

Purpose Set the OS time

Description:

This function sets the vxworks OS time.

Assumptions, External Events, and Notes:

The changes do not occur if the NTP client is setup to synchronize with an NTP server. Set the OS CLOCK_REALTIME to a specified timestamp. Parameters are in UNIX time format, since Epoch 1/1/1970.

Parameters

in	*ts_sec*	- Time in seconds
in	*ts_nsec*	- Time in nanoseconds

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.26 int32 CFE_PSP_TIME_StartNTPDaemon (void)

Purpose Start the NTP client

Description:

This function starts the NTP client task, ipntpd, on vxworks.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

NTP client Task ID
CFE_PSP_ERROR

3.2.4.27 int32 CFE_PSP_TIME_StopNTPDaemon (void)

Purpose Stop the NTP client

Description:

This function stops the NTP client task, ipntpd, on vxworks.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.28 int32 ntp_clock_vxworks_Destroy (void)

Purpose Gracefully shutdown NTP Sync Module

Description:

Function will attempt to delete the NTP Sync task

Assumptions, External Events, and Notes:

When this function is called, no matter what it's return status is, the g_bEnableGetTimeFromOS_flag will be set to false. Intended only for use upon module initialization, not for 'normal' use during starting/stopping of NTP Sync task

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.29 void OS_Application_Run (void)

Purpose OSAL run entry point

Description:

This function serves as the PSP run entry point.

Assumptions, External Events, and Notes:

This is an SP0-specific implementation.
This function is declared but empty so that we don't run the default OSAL-equivalent function. The latter will actively suspend the console shell.

Parameters

None	

Returns

None

3.2.4.30 void OS_Application_Startup (void)

Purpose OSAL startup entry point

Description:

This function serves as the OSAL startup entry point.

Assumptions, External Events, and Notes:

This is an SP0-specific implementation so that we don't run the default OSAL-equivalent function.

Parameters

None	

Returns

None

Purpose Application startup entry point from OSAL BSP.

Description:

SP0 Implementation Specific

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

None

3.2.4.31 int32 PSP_SP0_DumpData (void)

Purpose Function dumps the collected data to file

Description:

Saves data dump to location defined by SP0_DATA_DUMP_FILEPATH

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Function dumps the collected data to file

Description:

Saves data dump to location defined by SP0_DATA_DUMP_FILEPATH

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.32 int64_t PSP_SP0_GetDiskFreeSize (char * *ram_disk_root_path*)

Purpose Get disk free disk space

Description:

Function uses the statfs64 to gather statistics about the file system. It works with both RAM and FLASH file systems such as "/ram0" and "/ffx0"

Assumptions, External Events, and Notes:

None

Parameters

in	*ram_disk_root_path*	

Returns

int64_t - Size of free space in disk in bytes
CFE_PSP_ERROR - If statfs returned error

Purpose Get disk free disk space in Mibytes

Description:

Function uses the statfs64 to gather statistics about the file system. It works with both RAM and FLASH file systems such as "/ram0" and "/ffx0"

Assumptions, External Events, and Notes:

None

Parameters

in	*ram_disk_root_path*	

Returns

int64_t - Size of free space in disk in bytes
CFE_PSP_ERROR - If statfs returned error

3.2.4.33 int32 PSP_SP0_GetInfo (void)

Purpose Collect SP0 Hardware and Firmware data

Description:

This function collects the SP0 hardware and firmware data and saves it in the sp0_info_table object, as well as a string in the sp0_data_dump object.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

Purpose Collect SP0 Hardware and Firmware data

Description:

This function collects the SP0 hardware and firmware data and saves it in the g_sp0_info_table object, as well as a string in the g_cSP0DataDump object.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

3.2.4.34 SP0_info_table_t PSP_SP0_GetInfoTable (bool *print_to_console*)

Purpose Get the structure containing the SP0 Hardware and Firmware data

Description:

This function returns and print the structure containing the SP0 Hardware and Firmware data.

Assumptions, External Events, and Notes:

None

Parameters

print_to_console	Print string buffer to console if True

Returns

SP0_info_table_t structure containing all the collect info from SP0

Purpose Get the structure containing the SP0 Hardware and Firmware data

Description:

This function returns and print the structure containing the SP0 Hardware and Firmware data.

Assumptions, External Events, and Notes:

None

Parameters

print_to_console	Print string buffer to console if True

Returns

SP0_info_table_t structure containing all the collect info from SP0

3.2.4.35 static int32 PSP_SP0_PrintToBuffer (void) [static]

Purpose Print the SP0 data to string buffer

Description:

Internal function to print the gathered data from SP0 to a string buffer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

4 Data Structure Documentation

4.1 CFE_PSP_Exception_ContextDataEntry_t Struct Reference

Exception Context Data Entry.

`#include <cfe_psp_config.h>`

Data Fields

- UINT32 timebase_upper
  Upper 32 bits of timebase as sampled by hook.
- UINT32 timebase_lower
  Lower 32 bits of timebase as sampled by hook.
- int vector
  vector number
- ESFPPC esf
  Exception stack frame.
- UINT64 force64BitAlign
  Force the spe register to 64 bit alignment.
- SPE_CONTEXT fp
  floating point registers

4.1.1 Detailed Description

Exception Context Data Entry.

4.2 CFE_PSP_Exception_LogData Struct Reference

Exception Log Data Struct.

`#include <cfe_psp_exceptionstorage_types.h>`

Data Fields

- uint32 context_id

  a unique ID assigned to this exception entry
- uint32 context_size

  actual size of the "context_info" data
- CFE_PSP_Exception_SysTaskId_t sys_task_id

  the BSP-specific task info (not osal abstracted id)
- CFE_PSP_Exception_ContextDataEntry_t context_info

  Context Info.

4.2.1 Detailed Description

Exception Log Data Struct.

4.3 CFE_PSP_ExceptionStorage Struct Reference

Exception Storage Struct.

`#include <cfe_psp_exceptionstorage_types.h>`

Data Fields

- volatile uint32 NumWritten

  Num Written.
- volatile uint32 NumRead

  Num Read.
- struct CFE_PSP_Exception_LogData Entries [CFE_PSP_MAX_EXCEPTION_ENTRIES]

  Entries.

4.3.1 Detailed Description

Exception Storage Struct.

4.4 CFE_PSP_MemoryBlock_t Struct Reference

Memory Block Type.

`#include <cfe_psp_memory.h>`

Data Fields

- void * BlockPtr
  Block Pointer.
- size_t BlockSize
  Block Size.

4.4.1 Detailed Description

Memory Block Type.

4.5 CFE_PSP_MemTable_t Struct Reference

Memory Table Type.

`#include <cfe_psp_memory.h>`

Data Fields

- uint32 MemoryType
  Memory Type.
- size_t WordSize
  Word Size.
- cpuaddr StartAddr
  Start Address.
- size_t Size
  Size.
- uint32 Attributes
  Attributes.

4.5.1 Detailed Description

Memory Table Type.

4.6 CFE_PSP_ModuleApi_t Struct Reference

Concrete version of the abstract API definition structure.

`#include <cfe_psp_module.h>`

Data Fields

- CFE_PSP_ModuleType_t ModuleType
  Module Type.
- uint32 OperationFlags
  OperationFlags.
- CFE_PSP_ModuleInitFunc_t Init
  Module Initialization Function.

4.6.1 Detailed Description

Concrete version of the abstract API definition structure.

Note:

More API calls may be added for other module types

4.7 CFE_PSP_OS_Task_and_priority_t Struct Reference

Task name and priority of tasks.

`#include <cfe_psp_config.h>`

Data Fields

- const char * VxWorksTaskName

  Pointer to the task name.
- uint8 VxWorksTaskPriority

  Task priority from 0 to 255.

4.7.1 Detailed Description

Task name and priority of tasks.

Description:

This structure will be used to build an array of VxWorks tasks. The task priority of each task name in the array will be modified according to the assigned priority.

4.8 CFE_PSP_ReservedMemoryBootRecord_t Struct Reference

Layout of the vxWorks boot record structure.

`#include <cfe_psp_config.h>`

Data Fields

- uint32 bsp_reset_type

  BSP Reset Type.
- uint32 spare1

  Spare 1.
- uint32 spare2

  Spare 2.
- uint32 spare3

  Spare 3.

4.8.1 Detailed Description

Layout of the vxWorks boot record structure.

Description:

This is statically placed at the beginning of system memory (sysMemTop) which should be reserved in the kernel.

4.9 CFE_PSP_ReservedMemoryMap_t Struct Reference

Reserved Memory Map.

`#include <cfe_psp_memory.h>`

Data Fields

- CFE_PSP_ReservedMemoryBootRecord_t * BootPtr
  Pointer to Reserved Memory Boot Record.
- CFE_PSP_ExceptionStorage_t * ExceptionStoragePtr
  Pointer to Exception Storage.
- CFE_PSP_MemoryBlock_t ResetMemory
  Reset Memory.
- CFE_PSP_MemoryBlock_t VolatileDiskMemory
  Voltatile Disk Memory.
- CFE_PSP_MemoryBlock_t CDSMemory
  CDS Memory.
- CFE_PSP_MemoryBlock_t UserReservedMemory
  User Reservded Memory.
- CFE_PSP_MemTable_t SysMemoryTable [CFE_PSP_MEM_TABLE_SIZE]
  The system memory table.

4.9.1 Detailed Description

Reserved Memory Map.

4.9.2 Field Documentation

4.9.2.1 CFE_PSP_MemTable_t CFE_PSP_ReservedMemoryMap_t::SysMemoryTable[CFE_PSP_MEM_TABLE_SIZE]

The system memory table.

Description:

This is the table used for CFE_PSP_MemRangeGet/Set and related ops that allow CFE applications to query the general system memory map.

4.10 MEM_SCRUB_ERRSTATS_s Struct Reference

Memory Error Statistics struct.

`#include <psp_mem_scrub.h>`

Data Fields

- uint32 **uil2errTotal**
- uint32 **uil2errMult**
- uint32 **uil2errTagPar**
- uint32 **uil2errMBECC**
- uint32 **uil2errSBECC**
- uint32 **uil2errCfg**
- uint32 **uimchCause**
- uint32 **uimchkHook**

4.10.1 Detailed Description

Memory Error Statistics struct.

Description:

Returns a structure containing information about memory errors:

- uil2errTotal
- uil2errMult
- uil2errTagPar
- uil2errMBECC
- uil2errSBECC
- uil2errCfg
- uimchCause
- uimchkHook

Assumptions, External Events, and Notes:

From sysLib.c: "The machine check ISR will update these counters"

4.11 MEM_SCRUB_STATUS_s Struct Reference

Memory Scrubbing information struct.

`#include <psp_mem_scrub.h>`

Data Fields

- uint32 **uiMemScrubStartAddr**
- uint32 **uiMemScrubEndAddr**
- uint32 **uiMemScrubCurrentPage**
- uint32 **uiMemScrubTotalPages**
- osal_priority_t **opMemScrubTaskPriority**

4.11.1 Detailed Description

Memory Scrubbing information struct.

Description:

Memory scrubbing struct containing useful information:

- uiMemScrubStartAddr
- uiMemScrubEndAddr
- uiMemScrubCurrentPage
- uiMemScrubTotalPages
- opMemScrubTaskPriority
- bIsRunning

4.12 PSP_VxWorks_Timebase_Global_t Struct Reference

Data Fields

- uint32 **TicksPerSecond**
- uint32 **OSTimeConvNumerator**
- uint32 **OSTimeConvDenominator**

4.13 SP0_info_table_t Struct Reference

SP0 info structure.

`#include <psp_sp0_info.h>`

Data Fields

- struct timespec lastUpdatedUTC

  UTC date time when the data was collected.
- char * systemModel

  Pointer to the string identifing the System Model.
- char * systemBspRev

  Pointer to the string identifing the system BSP Revision.
- uint32 systemPhysMemTop

  Top of the System Physical Memory.
- int systemProcNum

  Number of Processors.
- int systemSlotId

  Slod ID in the chassis.
- bool systemCpciSysCtrl

  Identifies if the SP0 is the cPCI main system controller.
- uint32 systemCoreClockSpeed

  System Core Clock Speed in MHz.
- uint8 systemLastResetReason

  Reason for last SP0 computer reset.

- uint8 active_boot

  Identifies the EEPROM to successfully booted the kernel.
- int systemClkRateGet

  System Clock Rate.
- int systemAuxClkRateGet

  System Aux Clock Rate.
- uint64 bitExecuted

  Identifies the POST Test Bit Executed.
- uint64 bitResult

  Identifies the POST Test Results.
- char safeModeUserData [SP0_SAFEMODEUSERDATA_BUFFER_SIZE]

  Safe Mode User Data.
- float systemStartupUsecTime

  Number of usec since startup.
- float temperatures [4]

  Array of 4 temperatures on the SP0 computer.
- float voltages [6]

  Array of 6 voltages powering the SP0.

4.13.1 Detailed Description

SP0 info structure.

Description:

The table includes values that changes only once during boot and others that changes at a regular interval.

Variables that changes at regular intervals are:

- systemStartupUsecTime
- temperatures
- voltages

5 File Documentation

5.1 /home/tngo/GW_workspace/cert_testbed/psp/fsw/inc/cfe_psp.h File Reference

Main PSP public API functions.

```
#include "common_types.h"
#include "osapi.h"
```

Macros

- #define CFE_PSP_SOFT_TIMEBASE_NAME "cFS-Master"

  The name of the software/RTOS timebase for general system timers.

Error and return codes

- #define CFE_PSP_SUCCESS (0)

  Success.
- #define CFE_PSP_ERROR (-1)

  Generic Error.
- #define CFE_PSP_INVALID_POINTER (-2)

  Invalid Pointer.
- #define CFE_PSP_ERROR_ADDRESS_MISALIGNED (-3)

  Misaligned Address.
- #define CFE_PSP_ERROR_TIMEOUT (-4)

  Timeout Error.
- #define CFE_PSP_INVALID_INT_NUM (-5)

  Invalid Integer Number.
- #define CFE_PSP_INVALID_MEM_ADDR (-21)

  Invalid Memory Address.
- #define CFE_PSP_INVALID_MEM_TYPE (-22)

  Invalid Memory Type.
- #define CFE_PSP_INVALID_MEM_RANGE (-23)

  Invalid Memory Range.
- #define CFE_PSP_INVALID_MEM_WORDSIZE (-24)

  Invalid Memory Word Size.
- #define CFE_PSP_INVALID_MEM_SIZE (-25)

  Invalid Memory Size.
- #define CFE_PSP_INVALID_MEM_ATTR (-26)

  Invalid Memory Attribute.
- #define CFE_PSP_ERROR_NOT_IMPLEMENTED (-27)

  Not Implemented.
- #define CFE_PSP_INVALID_MODULE_NAME (-28)

  Invalid Module Name.
- #define CFE_PSP_INVALID_MODULE_ID (-29)

  Invalid Module ID.
- #define CFE_PSP_NO_EXCEPTION_DATA (-30)

  No Exception Data.
- #define CFE_PSP_ERROR_LEVEL_0 (-31)

  Generic Error, but returned data is valid.

Definitions for PSP PANIC types

- #define CFE_PSP_PANIC_STARTUP 1

  Startup.
- #define CFE_PSP_PANIC_VOLATILE_DISK 2

  Volatile Disk.
- #define CFE_PSP_PANIC_MEMORY_ALLOC 3

  Memory Allocation.
- #define CFE_PSP_PANIC_NONVOL_DISK 4

  Nonvolatile Disk.

- #define CFE_PSP_PANIC_STARTUP_SEM 5

  Startup Semaphore.
- #define CFE_PSP_PANIC_CORE_APP 6

  Core App.
- #define CFE_PSP_PANIC_GENERAL_FAILURE 7

  Generic Failure.

Macros for the file loader

- #define BUFF_SIZE 256

  Buffer Size.
- #define SIZE_BYTE 1

  Size Byte.
- #define SIZE_HALF 2

  Size Half.
- #define SIZE_WORD 3

  Size Word.

Define Memory Types

- #define CFE_PSP_MEM_RAM 1

  Memory RAM.
- #define CFE_PSP_MEM_EEPROM 2

  Memory EEPROM.
- #define CFE_PSP_MEM_ANY 3

  Memory ANY.
- #define CFE_PSP_MEM_INVALID 4

  Memory INVALID.

Define Memory Read/Write Attributes

- #define CFE_PSP_MEM_ATTR_WRITE 0x01

  Memory Attribute Write.
- #define CFE_PSP_MEM_ATTR_READ 0x02

  Memory Attribute Read.
- #define CFE_PSP_MEM_ATTR_READWRITE 0x03

  Memory Attribute ReadWrite.

Define the Memory Word Sizes

- #define CFE_PSP_MEM_SIZE_BYTE 0x01

  Memory Size Byte.
- #define CFE_PSP_MEM_SIZE_WORD 0x02

  Memory Size Word.
- #define CFE_PSP_MEM_SIZE_DWORD 0x04

  Memory Size DoubleWord.

Reset Types

- #define CFE_PSP_RST_TYPE_PROCESSOR 1
- #define CFE_PSP_RST_TYPE_POWERON 2
- #define CFE_PSP_RST_TYPE_MAX 3

Reset Sub-Types

- #define CFE_PSP_RST_SUBTYPE_POWER_CYCLE 1

  Reset caused by power having been removed and restored.
- #define CFE_PSP_RST_SUBTYPE_PUSH_BUTTON 2

  Reset caused by reset button on the board having been pressed.
- #define CFE_PSP_RST_SUBTYPE_HW_SPECIAL_COMMAND 3

  Reset was caused by a reset line having been stimulated by a hardware special command.
- #define CFE_PSP_RST_SUBTYPE_HW_WATCHDOG 4

  Reset was caused by a watchdog timer expiring.
- #define CFE_PSP_RST_SUBTYPE_RESET_COMMAND 5

  Reset was caused by cFE ES processing a Reset Command .
- #define CFE_PSP_RST_SUBTYPE_EXCEPTION 6

  Reset was caused by a Processor Exception.
- #define CFE_PSP_RST_SUBTYPE_UNDEFINED_RESET 7

  Reset was caused in an unknown manner.
- #define CFE_PSP_RST_SUBTYPE_HWDEBUG_RESET 8

  Reset was caused by a JTAG or BDM connection.
- #define CFE_PSP_RST_SUBTYPE_BANKSWITCH_RESET 9

  Reset reverted to a cFE POWERON due to a boot bank switch.
- #define CFE_PSP_RST_SUBTYPE_MAX 10

  Placeholder to indicate 1+ the maximum value that the PSP will ever use.

Functions

- void CFE_PSP_Main (void)
- void CFE_PSP_GetTime (OS_time_t *LocalTime)
- void CFE_PSP_Restart (uint32 resetType)
- uint32 CFE_PSP_GetRestartType (uint32 *resetSubType)
- void CFE_PSP_FlushCaches (uint32 type, void *address, uint32 size)
- uint32 CFE_PSP_GetProcessorId (void)
- uint32 CFE_PSP_GetSpacecraftId (void)
- const char * CFE_PSP_GetProcessorName (void)
- uint32 CFE_PSP_GetTimerTicksPerSecond (void)
- uint32 CFE_PSP_GetTimerLow32Rollover (void)
- void CFE_PSP_Get_Timebase (uint32 *Tbu, uint32 *Tbl)
- int32 CFE_PSP_GetCDSSize (uint32 *SizeOfCDS)
- int32 CFE_PSP_WriteToCDS (const void *PtrToDataToWrite, uint32 CDSOffset, uint32 NumBytes)
- int32 CFE_PSP_ReadFromCDS (void *PtrToDataFromRead, uint32 CDSOffset, uint32 NumBytes)
- int32 CFE_PSP_GetResetArea (cpuaddr *PtrToResetArea, uint32 *SizeOfResetArea)
- int32 CFE_PSP_GetUserReservedArea (cpuaddr *PtrToUserArea, uint32 *SizeOfUserArea)
- int32 CFE_PSP_GetVolatileDiskMem (cpuaddr *PtrToVolDisk, uint32 *SizeOfVolDisk)
- int32 CFE_PSP_GetKernelTextSegmentInfo (cpuaddr *PtrToKernelSegment, uint32 *SizeOfKernelSegment)
- int32 CFE_PSP_GetCFETextSegmentInfo (cpuaddr *PtrToCFESegment, uint32 *SizeOfCFESegment)
- void CFE_PSP_WatchdogInit (void)
- void CFE_PSP_WatchdogEnable (void)
- void CFE_PSP_WatchdogDisable (void)
- void CFE_PSP_WatchdogService (void)
- uint32 CFE_PSP_WatchdogGet (void)
- void CFE_PSP_WatchdogSet (uint32 watchDogValue_ms)
- bool CFE_PSP_WatchdogStatus (void)

- void CFE_PSP_Panic (int32 errorCode)
- int32 CFE_PSP_InitSSR (uint32 bus, uint32 device, char ∗DeviceName)
- void CFE_PSP_AttachExceptions (void)
- void CFE_PSP_SetDefaultExceptionEnvironment (void)
- uint32 CFE_PSP_Exception_GetCount (void)
- int32 CFE_PSP_Exception_GetSummary (uint32 ∗ContextLogId, osal_id_t ∗TaskId, char ∗ReasonBuf, uint32 ReasonSize)
- int32 CFE_PSP_Exception_CopyContext (uint32 ContextLogId, void ∗ContextBuf, uint32 ContextSize)
- int32 CFE_PSP_PortRead8 (cpuaddr PortAddress, uint8 ∗ByteValue)
- int32 CFE_PSP_PortWrite8 (cpuaddr PortAddress, uint8 ByteValue)
- int32 CFE_PSP_PortRead16 (cpuaddr PortAddress, uint16 ∗uint16Value)
- int32 CFE_PSP_PortWrite16 (cpuaddr PortAddress, uint16 uint16Value)
- int32 CFE_PSP_PortRead32 (cpuaddr PortAddress, uint32 ∗uint32Value)
- int32 CFE_PSP_PortWrite32 (cpuaddr PortAddress, uint32 uint32Value)
- int32 CFE_PSP_MemRead8 (cpuaddr MemoryAddress, uint8 ∗ByteValue)
- int32 CFE_PSP_MemWrite8 (cpuaddr MemoryAddress, uint8 ByteValue)
- int32 CFE_PSP_MemRead16 (cpuaddr MemoryAddress, uint16 ∗uint16Value)
- int32 CFE_PSP_MemWrite16 (cpuaddr MemoryAddress, uint16 uint16Value)
- int32 CFE_PSP_MemRead32 (cpuaddr MemoryAddress, uint32 ∗uint32Value)
- int32 CFE_PSP_MemWrite32 (cpuaddr MemoryAddress, uint32 uint32Value)
- int32 CFE_PSP_MemCpy (void ∗dest, const void ∗src, uint32 size)
- int32 CFE_PSP_MemSet (void ∗dest, uint8 value, uint32 size)
- int32 CFE_PSP_MemValidateRange (cpuaddr Address, size_t Size, uint32 MemoryType)
- uint32 CFE_PSP_MemRanges (void)
- int32 CFE_PSP_MemRangeSet (uint32 RangeNum, uint32 MemoryType, cpuaddr StartAddr, size_t Size, size_t WordSize, uint32 Attributes)
- int32 CFE_PSP_MemRangeGet (uint32 RangeNum, uint32 ∗MemoryType, cpuaddr ∗StartAddr, size_t ∗Size, size_t ∗WordSize, uint32 ∗Attributes)
- int32 CFE_PSP_EepromWrite8 (cpuaddr MemoryAddress, uint8 ByteValue)
- int32 CFE_PSP_EepromWrite16 (cpuaddr MemoryAddress, uint16 uint16Value)
- int32 CFE_PSP_EepromWrite32 (cpuaddr MemoryAddress, uint32 uint32Value)
- int32 CFE_PSP_EepromWriteEnable (uint32 Bank)
- int32 CFE_PSP_EepromWriteDisable (uint32 Bank)
- int32 CFE_PSP_EepromPowerUp (uint32 Bank)
- int32 CFE_PSP_EepromPowerDown (uint32 Bank)
- const char ∗ CFE_PSP_GetVersionString (void)
- const char ∗ CFE_PSP_GetVersionCodeName (void)
- void CFE_PSP_GetVersionNumber (uint8 VersionNumbers[4])
- uint32 CFE_PSP_GetBuildNumber (void)

5.1.1 Detailed Description

Main PSP public API functions.

Description:

This file contains the cFE Platform Support Package(PSP) prototypes. The PSP functions serve as the "glue" between the RTOS and the cFS.
The functions fill gaps that are not really considered part of the OSAL, but are required for the cFE implementation. It is possible that some of these functions could migrate into the OSAL.

Limitations, Assumptions, External Events, and Notes:

None

5.2 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ntp_clock_vxworks/cfe_psp_ntp.c File Reference

API to control NTP Sync.

```
#include <vxWorks.h>
#include <ipcom_err.h>
#include <taskLib.h>
#include <timers.h>
#include "cfe_time_extern_typedefs.h"
#include "cfe_mission_cfg.h"
#include "cfe_psp.h"
#include "cfe_psp_config.h"
#include "cfe_psp_module.h"
#include "psp_time_sync.h"
```

Macros

- #define **IP_LITTLE_ENDIAN**
- #define NTPSYNC_PRINT_SCOPE "PSP NTP SYNC: "

  Default NTP Sync pre-print string.

NTP Sync Configuration - VxWorks

- #define CFE_MISSION_TIME_SYNC_TIME_ON_STARTUP true

  Default NTP Sync Start/Stop on Startup.
- #define CFE_MISSION_TIME_SYNC_OS_SEC 30

  Default Synchronization Frequency.
- #define NTPSYNC_TASK_NAME "PSPNTPSync"

  Default NTP Sync Task Name.
- #define NTPSYNC_DEFAULT_PRIORITY 60

  Default NTP Sync Task Priority.

Functions

- TASK_ID taskNameToId (char *name)

  VxWorks function to get ID of running task.
- IP_PUBLIC Ip_err ipcom_ipd_kill (const char *name)

  VxWorks function to kill a running daemon.
- IP_PUBLIC Ip_err ipcom_ipd_start (const char *name)

  VxWorks function to start a daemon.
- uint32 CFE_TIME_Micro2SubSecs (uint32)

  Convert micro seconds in subseconds.
- void CFE_TIME_SetTime (CFE_TIME_SysTime_t)

  Adjust CFE Time STCF so that local time match the new time.
- static void **CFE_PSP_TIME_NTPSync_Task** (void)
- CFE_PSP_MODULE_DECLARE_SIMPLE (ntp_clock_vxworks)

  Macro to define this file a PSP Module.
- int32 CFE_PSP_TIME_NTPSync_Task_Enable (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Disable (void)
- bool CFE_PSP_TIME_NTPSync_Task_isRunning (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Priority_Set (osal_priority_t opPriority)
- bool CFE_PSP_TIME_NTP_Daemon_isRunning (void)
- int32 ntp_clock_vxworks_Destroy (void)
- void **ntp_clock_vxworks_Init** (uint32 PspModuleId)
- uint16 CFE_PSP_TIME_NTPSync_GetFreq (void)
- void CFE_PSP_TIME_NTPSync_SetFreq (uint16 uiNewFreqSec)
- int32 CFE_PSP_TIME_Set_OS_Time (const uint32 ts_sec, const uint32 ts_nsec)
- int32 CFE_PSP_TIME_Get_OS_Time (CFE_TIME_SysTime_t *myT)
- bool CFE_PSP_TIME_CFETimeService_isRunning (void)

  Check if CFS Time Service is up and running.
- TASK_ID CFE_PSP_TIME_StartNTPDaemon (void)
- int32 CFE_PSP_TIME_StopNTPDaemon (void)

Variables

- static uint32 g_uiPSPNTPTask_id = 0

  Contains the NTP Sync Task ID If 0, task is not running.
- static osal_priority_t g_ucNTPSyncTaskPriority = NTPSYNC_DEFAULT_PRIORITY

  Current value of NTP Sync priority task.
- static bool g_bEnableGetTimeFromOS_flag = CFE_MISSION_TIME_SYNC_TIME_ON_STARTUP

  Boolean variable to control if to synchronize CFE Time Service with OS local time. True, synch will occur. False, timer will not be disabled, but sync will not execute.
- static uint16 g_usOSTimeSync_Sec = CFE_MISSION_TIME_SYNC_OS_SEC

  Change how often to sync CFE Time Service with OS Local Time. OS local time is synchronized to NTP server(s) automatically from within OS if enabled.

5.2.1 Detailed Description

API to control NTP Sync.

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Purpose:

This file contains the function declaration that synchronize the cFE Time services to the NTP server. Note that the NTP server must be built into the kernel.

Limitations, Assumptions, External Events, and Notes:

The way this module updates the local time is by calling the CFE Time Service function CFE_TIME_SetTime(). The function changes the STCF value.
GSFC developers do not recommend to use this method of updating CFE time, but rather to use the function CFE_TIME_ExternalTime(). The only way to use this function is by building an app that will periodically (1Hz) get NTP time and publish it via Software Bus.

5.2.2 Macro Definition Documentation

5.2.2.1 #define CFE_MISSION_TIME_SYNC_OS_SEC 30

Default Synchronization Frequency.

Description:

Default number of seconds between time synchronizations. CFE Time Service updates MET and STCF from VxWorks OS. When set to zero, CFE Time will be synchronized only once during start.

Limits

Positive integer up to 255. If this value is too low, it will starve the other processes.

5.2.2.2 #define CFE_MISSION_TIME_SYNC_TIME_ON_STARTUP true

Default NTP Sync Start/Stop on Startup.

Description:

Enable or disable the Automatic time sync with the OS

5.2.2.3 #define NTPSYNC_PRINT_SCOPE "PSP NTP SYNC: "

Default NTP Sync pre-print string.

Description:

This string is printed before every print related to NTP Sync API.

5.2.3 Function Documentation

5.2.3.1 uint32 CFE_TIME_Micro2SubSecs (uint32)

Convert micro seconds in subseconds.

Description:

Defined in CFE module time cfe_time.h

5.2.3.2 void CFE_TIME_SetTime (CFE_TIME_SysTime_t)

Adjust CFE Time STCF so that local time match the new time.

Description:

Defined in CFE module time cfe_time_utils.h

5.3 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/port_direct/cfe_psp_port_direct.c File Reference

```
#include "cfe_psp.h"
#include "cfe_psp_module.h"
```

Functions

- **CFE_PSP_MODULE_DECLARE_SIMPLE** (port_direct)
- void **port_direct_Init** (uint32 PspModuleId)
- int32 CFE_PSP_PortRead8 (cpuaddr PortAddress, uint8 *ByteValue)
- int32 CFE_PSP_PortWrite8 (cpuaddr PortAddress, uint8 ByteValue)
- int32 CFE_PSP_PortRead16 (cpuaddr PortAddress, uint16 *uint16Value)
- int32 CFE_PSP_PortWrite16 (cpuaddr PortAddress, uint16 uint16Value)
- int32 CFE_PSP_PortRead32 (cpuaddr PortAddress, uint32 *uint32Value)
- int32 CFE_PSP_PortWrite32 (cpuaddr PortAddress, uint32 uint32Value)

5.3.1 Detailed Description

A PSP module to satisfy the "PORT" API on systems which can access I/O ports directly via memory mapped addresses.

5.4 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ram_direct/cfe_psp_ram_direct.c File Reference

```
#include "cfe_psp.h"
#include "cfe_psp_module.h"
```

Functions

- **CFE_PSP_MODULE_DECLARE_SIMPLE** (ram_direct)
- void **ram_direct_Init** (uint32 PspModuleId)
- int32 CFE_PSP_MemRead8 (cpuaddr MemoryAddress, uint8 *ByteValue)
- int32 CFE_PSP_MemWrite8 (cpuaddr MemoryAddress, uint8 ByteValue)
- int32 CFE_PSP_MemRead16 (cpuaddr MemoryAddress, uint16 *uint16Value)
- int32 CFE_PSP_MemWrite16 (cpuaddr MemoryAddress, uint16 uint16Value)
- int32 CFE_PSP_MemRead32 (cpuaddr MemoryAddress, uint32 *uint32Value)
- int32 CFE_PSP_MemWrite32 (cpuaddr MemoryAddress, uint32 uint32Value)

5.4.1 Detailed Description

A PSP module to satisfy the "RAM" API on systems which can access physical memory directly.

5.5 /home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/timebase_vxworks/cfe_psp_timebase_vxworks.c File Reference

```
#include <stdio.h>
#include <stdlib.h>
#include <time.h>
#include <vxWorks.h>
#include <sysLib.h>
#include <vxLib.h>
#include <arch/ppc/vxPpcLib.h>
#include "osapi-clock.h"
#include "cfe_psp.h"
#include "cfe_psp_config.h"
#include "cfe_psp_module.h"
```

Data Structures

- struct PSP_VxWorks_Timebase_Global_t

Macros

- #define CFE_PSP_TIMER_LOW32_ROLLOVER 0

  The number that the least significant 32 bits of the 64 bit time stamp returned by CFE_PSP_Get_Timebase rolls over. If the lower 32 bits rolls at 1 second, then the CFE_PSP_TIMER_LOW32_ROLLOVER will be 1000000. if the lower 32 bits rolls at its maximum value (2^32) then CFE_PSP_TIMER_LOW32_ROLLOVER will be 0.

Functions

- **CFE_PSP_MODULE_DECLARE_SIMPLE** (timebase_vxworks)
- void **timebase_vxworks_Init** (uint32 PspModuleId)
- uint32 CFE_PSP_GetTimerTicksPerSecond (void)
- uint32 CFE_PSP_GetTimerLow32Rollover (void)
- void CFE_PSP_Get_Timebase (uint32 *Tbu, uint32 *Tbl)
- void CFE_PSP_GetTime (OS_time_t *LocalTime)

Variables

- PSP_VxWorks_Timebase_Global_t **PSP_VxWorks_Timebase_Global**

5.5.1 Detailed Description

A PSP module to implement the PSP time API via the VxWorks vxTimeBaseGet() routine. The VxWorks timebase is a 64 bit monotonically increasing counter implemented as a hardware register in PowerPC chips, and is described in the Power ISA specification v2.06b section 7.2.

The vxTimeBaseGet() function is provided by the VxWorks BSP and returns the value of this register as a pair of UINT32 values, containing the upper and lower 32 bit words.

Note that the tick rate of the clock is not specified by Power architecture and is system-dependent. This needs to have some platform-specific tuning to normalize the tick units.

5.6 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_api.h File Reference

Header file for the PSP exception storage functions.

```
#include "cfe_psp.h"
```

Functions

- struct CFE_PSP_Exception_LogData * CFE_PSP_Exception_GetBuffer (uint32 seq)
- struct CFE_PSP_Exception_LogData * CFE_PSP_Exception_GetNextContextBuffer (void)
- void CFE_PSP_Exception_WriteComplete (void)
- void CFE_PSP_Exception_Reset (void)
- int32 CFE_PSP_ExceptionGetSummary_Impl (const struct CFE_PSP_Exception_LogData *Buffer, char *ReasonBuf, uint32 ReasonSize)

5.6.1 Detailed Description

Header file for the PSP exception storage functions.

Copyright

Description:

This file provides a generic storage buffer ring for exceptions and functions to manipulate it.

Limitations, Assumptions, External Events, and Notes:

None

5.7 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_types.h File Reference

Provides a generic storage buffer ring for exceptions.

```
#include "cfe_psp.h"
#include "cfe_psp_config.h"
```

Data Structures

- struct CFE_PSP_Exception_LogData

  Exception Log Data Struct.
- struct CFE_PSP_ExceptionStorage

  Exception Storage Struct.

Typedefs

- typedef struct CFE_PSP_Exception_LogData CFE_PSP_Exception_LogData_t

  Exception Log Data Type.
- typedef struct CFE_PSP_ExceptionStorage CFE_PSP_ExceptionStorage_t

  Exception Storage Type.

5.7.1 Detailed Description

Provides a generic storage buffer ring for exceptions.

Copyright

Description:

The "MetaData" stores ephemeral exception information which only has meaning within the currently-running process.

This data is important for diagnosing the exception, but it is NOT saved to any persistent log because it will not be relevant once the process ends.

Limitations, Assumptions, External Events, and Notes:

None

5.8 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_memory.h File Reference

Header file for the Reserved Memory-related supporting functions.

```
#include "common_types.h"
#include "cfe_psp_config.h"
#include "cfe_psp_exceptionstorage_types.h"
```

Data Structures

- struct CFE_PSP_MemTable_t

  Memory Table Type.
- struct CFE_PSP_MemoryBlock_t

  Memory Block Type.
- struct CFE_PSP_ReservedMemoryMap_t

  Reserved Memory Map.

Functions

- void CFE_PSP_SetupReservedMemoryMap (void)
- int32 CFE_PSP_InitProcessorReservedMemory (uint32 RestartType)
- void CFE_PSP_DeleteProcessorReservedMemory (void)

Variables

- CFE_PSP_ReservedMemoryMap_t CFE_PSP_ReservedMemoryMap

  Map to the reserved memory area(s) Contains a pointer to each of the separate memory blocks.

5.8.1 Detailed Description

Header file for the Reserved Memory-related supporting functions.

Copyright

Description:

Header file containing the function declarations to initialize, manage, and delete Reserved Memory

Limitations, Assumptions, External Events, and Notes:

None

5.9 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_module.h File Reference

Header file for the PSP public module data types and functions.

```
#include "cfe_psp.h"
#include "target_config.h"
```

Data Structures

- struct CFE_PSP_ModuleApi_t

  Concrete version of the abstract API definition structure.

Macros

- #define CFE_PSP_MODULE_NAME_MAX_LENGTH 30

  Maximum Module Name Length.
- #define CFE_PSP_MODULE_DECLARE_SIMPLE(name)

  CFE_PSP_MODULE_DECLARE_SIMPLE.

Typedefs

- typedef void(∗ CFE_PSP_ModuleInitFunc_t)(uint32 PspModuleId)

  Protoype for a PSP module initialization function.

Enumerations

- enum CFE_PSP_ModuleType_t {
  CFE_PSP_MODULE_TYPE_INVALID = 0,
  CFE_PSP_MODULE_TYPE_SIMPLE }

  Enum Module Type.

Functions

- void CFE_PSP_ModuleInit (void)
- int32 CFE_PSP_Module_FindByName (const char ∗ModuleName, uint32 ∗PspModuleId)
- int32 CFE_PSP_Module_GetAPIEntry (uint32 PspModuleId, CFE_PSP_ModuleApi_t ∗∗API)

Variables

- CFE_StaticModuleLoadEntry_t CFE_PSP_BASE_MODULE_LIST []

  A list of fixed/base modules associated with the PSP.

5.9.1 Detailed Description

Header file for the PSP public module data types and functions.

Description:

Header file containing the function declarations to initialize and manage PSP Modules

Limitations, Assumptions, External Events, and Notes:

None

5.10 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_exceptionstorage.c File Reference

```
#include <stdio.h>
#include <string.h>
#include "common_types.h"
#include "osapi.h"
#include "cfe_psp.h"
#include "cfe_psp_config.h"
#include "cfe_psp_exceptionstorage_types.h"
#include "cfe_psp_exceptionstorage_api.h"
#include "cfe_psp_memory.h"
#include "target_config.h"
```

Macros

- #define CFE_PSP_MAX_EXCEPTION_ENTRY_MASK (CFE_PSP_MAX_EXCEPTION_ENTRIES - 1)

  CFE_PSP_MAX_EXCEPTION_ENTRY_MASK.
- #define CFE_PSP_EXCEPTION_ID_BASE ((OS_OBJECT_TYPE_USER + 0x101) << OS_OBJECT_TYPE_SHIFT)

  CFE_PSP_EXCEPTION_ID_BASE.

Functions

- void CFE_PSP_Exception_Reset (void)
- CFE_PSP_Exception_LogData_t * CFE_PSP_Exception_GetBuffer (uint32 seq)
- CFE_PSP_Exception_LogData_t * CFE_PSP_Exception_GetNextContextBuffer (void)
- void CFE_PSP_Exception_WriteComplete (void)
- uint32 CFE_PSP_Exception_GetCount (void)
- int32 CFE_PSP_Exception_GetSummary (uint32 *ContextLogId, osal_id_t *TaskId, char *ReasonBuf, uint32 ReasonSize)
- int32 CFE_PSP_Exception_CopyContext (uint32 ContextLogId, void *ContextBuf, uint32 ContextSize)

5.10.1 Detailed Description

```
MCP750 vxWorks 6.2 Version
```

Purpose: cFE PSP Exception related functions.

History: 2007/05/29 A. Cudmore | vxWorks 6.2 MCP750 version 2016/04/07 M.Grubb | Updated for PSP version 1.3

5.11 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memrange.c File Reference

```
#include "cfe_psp.h"
#include "cfe_psp_memory.h"
```

Functions

- int32 CFE_PSP_MemValidateRange (cpuaddr Address, size_t Size, uint32 MemoryType)
- uint32 CFE_PSP_MemRanges (void)
- int32 CFE_PSP_MemRangeSet (uint32 RangeNum, uint32 MemoryType, cpuaddr StartAddr, size_t Size, size_t WordSize, uint32 Attributes)
- int32 CFE_PSP_MemRangeGet (uint32 RangeNum, uint32 *MemoryType, cpuaddr *StartAddr, size_t *Size, size_t *WordSize, uint32 *Attributes)

5.11.1 Detailed Description

Author : Alan Cudmore

Purpose: This file contains the memory range functions for the cFE Platform Support Package. The memory range is a table of valid memory address ranges maintained by the cFE.

5.12 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memutils.c File Reference

```
#include <sys/types.h>
#include <unistd.h>
#include <string.h>
#include "cfe_psp.h"
```

Functions

- int32 CFE_PSP_MemCpy (void *dest, const void *src, uint32 size)
- int32 CFE_PSP_MemSet (void *dest, uint8 value, uint32 size)

5.12.1 Detailed Description

Author : Ezra Yeheskeli

Purpose: This file contains some of the cFE Platform Support Layer. It contains the processor architecture specific calls.

5.13 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_module.c File Reference

```
#include <stdio.h>
#include <string.h>
#include "osapi.h"
#include "cfe_psp_module.h"
```

Macros

- #define CFE_PSP_MODULE_BASE 0x01100000

  CFE PSP Module Base and Index.
- #define **CFE_PSP_MODULE_INDEX_MASK** 0xFFFF

Functions

- void CFE_PSP_ModuleInitList (CFE_StaticModuleLoadEntry_t ∗ListPtr)
- void CFE_PSP_ModuleInit (void)
- int32 CFE_PSP_Module_GetAPIEntry (uint32 PspModuleId, CFE_PSP_ModuleApi_t ∗∗API)
- int32 CFE_PSP_Module_FindByName (const char ∗ModuleName, uint32 ∗PspModuleId)

Variables

- static uint32 **CFE_PSP_ModuleCount** = 0

5.13.1 Detailed Description

Created on: Jul 25, 2014 Author: jphickey

5.13.2 Macro Definition Documentation

5.13.2.1 #define CFE_PSP_MODULE_BASE 0x01100000

CFE PSP Module Base and Index.

Description:

When using an OSAL that also supports "opaque object ids", choose values here that will fit in with the OSAL object ID values and not overlap anything.

5.13.3 Function Documentation

5.13.3.1 void CFE_PSP_ModuleInitList (CFE_StaticModuleLoadEntry_t ∗ *ListPtr*)

Purpose Initialize a list of Modules

Description:

Helper function to initialize a list of modules (not externally called)

Assumptions, External Events, and Notes:

The module list pointed by ListPtr is generated by cmake during build time with an added NULL at the end to guarantee that the while loop ends.

Parameters

out	*ListPtr*	- Pointer to the list of modules

Returns

None

5.14 /home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_version.c File Reference

API to obtain the values of the various version identifiers.

```
#include "cfe_psp.h"
#include "psp_version.h"
```

Functions

- const char * CFE_PSP_GetVersionString (void)
- const char * CFE_PSP_GetVersionCodeName (void)
- void CFE_PSP_GetVersionNumber (uint8 VersionNumbers[4])
- uint32 CFE_PSP_GetBuildNumber (void)

5.14.1 Detailed Description

API to obtain the values of the various version identifiers.

Description:

GSC-18128-1, "Core Flight Executive Version 6.7"

5.15 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/functional_test/target_config.c File Reference

```
#include "target_config.h"
#include "cfe_mission_cfg.h"
#include "cfe_platform_cfg.h"
#include "cfe_es.h"
#include "cfe_time.h"
#include "cfe_es_resetdata_typedef.h"
#include "cfe_version.h"
#include "osapi-version.h"
```

Macros

- #define **CFE_CPU_ID_VALUE** 2
- #define **CFE_CPU_NAME_VALUE** "unknown"
- #define **CFE_SPACECRAFT_ID_VALUE** 0x42
- #define **CFE_DEFAULT_MODULE_EXTENSION** ""
- #define **CFE_DEFAULT_CORE_FILENAME** ""

Variables

- const char CFE_MISSION_NAME [] = "PSP Functional Test"
- const char CFE_MISSION_CONFIG [] = "target"
- CFE_ConfigName_t CFE_CORE_MODULE_LIST []
- CFE_ConfigName_t CFE_STATIC_APP_LIST []
- CFE_ConfigKeyValue_t CFE_BUILD_ENV_TABLE []
- CFE_ConfigKeyValue_t CFE_MODULE_VERSION_TABLE []
- CFE_StaticModuleLoadEntry_t CFE_PSP_MODULE_LIST []
- Target_CfeConfigData GLOBAL_CFE_CONFIGDATA
- Target_ConfigData GLOBAL_CONFIGDATA

5.15.1 Detailed Description

Created on: Dec 3, 2013 Created by: joseph.p.hickey@nasa.gov

Defines constant configuration structures and pointers that link together the CFE core, PSP, OSAL. The content of these configuration structures can be used to avoid directly using #include to reference a function implemented in another library, which can greatly simplify include paths and create a more modular build.

5.15.2 Variable Documentation

5.15.2.1 CFE_ConfigKeyValue_t CFE_BUILD_ENV_TABLE[]

A NULL terminated key-value table containing certain environment information from the build system at the time CFE core was built.

This contains basic information such as the time of day, build host, and user.

5.15.2.2 CFE_ConfigName_t CFE_CORE_MODULE_LIST[ ]

A NULL terminated list of modules which are statically linked into CFE core, generated by the build system from MISSION_CORE_MODULES.

For module names which appear in this list, the code is directly linked into the core executable binary file, and therefore means several things:

- the module code is guaranteed to be present
- functions it provides may be used by CFE core apps
- it cannot be updated/changed without rebuilding CFE core.

5.15.2.3 const char CFE_MISSION_CONFIG[ ] = "target"

Configuration name used for build

5.15.2.4 const char CFE_MISSION_NAME[ ] = "PSP Functional Test"

Name of CFE mission

5.15.2.5 CFE_ConfigKeyValue_t CFE_MODULE_VERSION_TABLE[ ]

Version control (source code) versions of all modules

This NULL terminated list includes all modules known to the build system as determined by the version control system in use (e.g. git). It is generated by a post-build step to query version control and should change automatically every time code is checked in or out.

Notably this includes *all* modules known to the build system at the time CFE core was built, regardless of whether those modules are configured for runtime (dynamic) or static linkage.

For dynamic modules, this means the version info can become outdated if/when a single module is rebuilt/reloaded after the original CFE build. The keys in this table may be be checked against the CFE_STATIC_MODULE_LIST above to determine if static or dynamic linkage was used. In the case of dynamic linkage, then this table only represents the version of the module that was present at the time CFE was built, not necessarily the version on the target filesystem.

5.15.2.6 CFE_StaticModuleLoadEntry_t CFE_PSP_MODULE_LIST[ ]

A NULL terminated list of PSP modules included in this build of CFE core, generated by the build system from the target PSPMODULES.

These are always statically linked, and this table contains a pointer to its API structure, which in turn contains its entry point.

5.15.2.7 CFE_ConfigName_t CFE_STATIC_APP_LIST[ ]

A NULL terminated list of CFS apps which are also statically linked with this binary, generated by the build system from the target STATIC_APPLIST.

These apps can be started without dynamically loading any modules, however the entry point must be separately provided in order to avoid needing any support from the OS dynamic loader subsystem.

5.15.2.8 Target_CfeConfigData GLOBAL_CFE_CONFIGDATA

Initial value:

```
= {
```

```
    .System1HzISR = CFE_TIME_Local1HzISR,
    .SystemMain   = CFE_ES_Main,
    .SystemNotify = CFE_ES_ProcessAsyncEvent,


    .NonvolMountPoint  = CFE_PLATFORM_ES_NONVOL_DISK_MOUNT_STRING,
    .RamdiskMountPoint = CFE_PLATFORM_ES_RAM_DISK_MOUNT_STRING,
    .NonvolStartupFile = CFE_PLATFORM_ES_NONVOL_STARTUP_FILE,


    .CdsSize          = CFE_PLATFORM_ES_CDS_SIZE,
    .ResetAreaSize    = sizeof(CFE_ES_ResetData_t),
    .UserReservedSize = CFE_PLATFORM_ES_USER_RESERVED_SIZE,

    .RamDiskSectorSize   = CFE_PLATFORM_ES_RAM_DISK_SECTOR_SIZE,
    .RamDiskTotalSectors = CFE_PLATFORM_ES_RAM_DISK_NUM_SECTORS}
```

A structure that encapsulates all the CFE static configuration

5.15.2.9 Target_ConfigData GLOBAL_CONFIGDATA

Initial value:

```
= {
    .MissionName             = CFE_MISSION_NAME,
    .CfeVersion              = CFE_SRC_VERSION,
    .OsalVersion             = OS_VERSION,
    .Config                  = CFE_MISSION_CONFIG,
    .Default_CpuName         = CFE_CPU_NAME_VALUE,
    .Default_CpuId           = CFE_CPU_ID_VALUE,
    .Default_SpacecraftId    = CFE_SPACECRAFT_ID_VALUE,
    .Default_ModuleExtension = CFE_DEFAULT_MODULE_EXTENSION,
    .Default_CoreFilename    = CFE_DEFAULT_CORE_FILENAME,
    .CfeConfig               = &GLOBAL_CFE_CONFIGDATA,
    .PspModuleList           = CFE_PSP_MODULE_LIST,
    .BuildEnvironment        = CFE_BUILD_ENV_TABLE,
    .ModuleVersionList       = CFE_MODULE_VERSION_TABLE,
    .CoreModuleList          = CFE_CORE_MODULE_LIST,
    .StaticAppList           = CFE_STATIC_APP_LIST,
}
```

Instantiation of global system-wide configuration struct This contains build info plus pointers to the PSP and CFE configuration structures. Everything will be linked together in the final executable.

5.16 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/cfe_psp_config.h File Reference

Main PSP Configuration File for SP0.

```
#include <stdio.h>
#include <string.h>
#include <vxWorks.h>
#include <sysLib.h>
#include <excLib.h>
#include <taskLib.h>
#include <speLib.h>
#include <arch/ppc/esfPpc.h>
#include <sys950Lib.h>
#include "common_types.h"
```

Data Structures

- struct CFE_PSP_ReservedMemoryBootRecord_t

Layout of the vxWorks boot record structure.

- struct CFE_PSP_Exception_ContextDataEntry_t

  Exception Context Data Entry.
- struct CFE_PSP_OS_Task_and_priority_t

  Task name and priority of tasks.

Macros

- #define OVERRIDE_OSAL_OS_APPLICATION_RUN true

  Override OSAL OS_Application_Run.
- #define VXWORKS_TASK_PRIORITIES

  The list of VxWorks tasks that PSP is tasked to adjust its priorites.
- #define CFE_PSP_MEM_TABLE_SIZE 10

  Memory Table Size.
- #define CFE_PSP_MAX_EXCEPTION_ENTRIES 4

  Maximum Exception Entries.
- #define CFE_PSP_MAXIMUM_TASK_LENGTH 30

  Maximum length of a task name created or spawn by PSP.
- #define CFE_PSP_MEMALIGN_MASK ((cpuaddr)0x1F)

  Memory Alignment Mask.

VxWorks timebase configuration parameters

Description:

The SP0 uses the PowerPC decrementer register. The register is decremented at a speed of:

- *SP0-s DDR2 Configuration: 50 MHz (1/20 = 0.05)*
- *SP0 DDR1 Configuration: 41.666 Mhz (1/24 = 0.041667)*
  For SP0-s the ratio of Denominator/Numerator is 0.05, which is 50 MHz.
  Refer to Aitech 00-0092-01_17_SP0_Programmers_Guide sec. 5.9

Note:

This is expressed as a ratio in case it is not a whole number. The numerator unit of measure is nanoseconds per tick.

Warning

Numerator calculation has been validated only on SP0-s and SP0 with a DDR memory bus speed of 50 MHz and 41.666 MHz respectively.

- #define CFE_PSP_VX_TIMEBASE_PERIOD_NUMERATOR (uint32)(8000.0f / (float)getCoreClockSpeed())

  Numerator.
- #define CFE_PSP_VX_TIMEBASE_PERIOD_DENOMINATOR 1

  Denominator.

Watchdog Configuration Parameters

- #define CFE_PSP_WATCHDOG_MIN (0)

  Watchdog minimum (in milliseconds)
- #define CFE_PSP_WATCHDOG_MAX (0xFFFFFFFF)

  Watchdog maximum (in milliseconds)

- #define CFE_PSP_WATCHDOG_DEFAULT_MSEC 20000
  Default Watchdog Value in milliseconds.

CDS File Location on FLASH Configuration Parameters

- #define CFE_PSP_CFE_FLASH_FILEPATH "/ffx0/CDS"
  CDS FLASH Memory File Location.

Memory Scrubbing Configuration Parameters

- #define MEMSCRUB_DEFAULT_PRIORITY 254
  Memory Scrub Default Priority.
- #define MEMSCRUB_PRIORITY_UP_RANGE 255
  Memory Scrub Maximum Allowed Priority.
- #define MEMSCRUB_PRIORITY_DOWN_RANGE 120
  Memory Scrub Minimum Allowed Priority.
- #define MEMSCRUB_TASK_NAME "PSPMemScrub"
  Memory Scrub Task Name.

SP0 Info Configuration Parameters

- #define SP0_DATA_DUMP_FILEPATH "/ffx0/PSP_SP0_DUMP"
  SP0 Data Dump Filepath.

NTP Sync Configuration Parameters

- #define NTP_DAEMON_TASK_NAME "ipntpd"
  Task name of the NTP daemon task.
- #define CFE_MISSION_TIME_EPOCH_UNIX_DIFF 946728000
  EPOCH to Mission Time Difference.
- #define CFE_1HZ_TASK_NAME "TIME_1HZ_TASK"
  CFE Time Service Task Name.
- #define NTPSYNC_INITIAL_TIME_DELAY 500
  Time delay in msec before checking CFE Time Service status.
- #define NTPSYNC_MAX_ITERATION_TIME_DELAY 120
  Time delay maximum iterations.
- #define CFE_MISSION_TIME_SYNC_OS_ENABLE true
  Default NTP Sync Start/Stop on Startup.
- #define CFE_MISSION_TIME_SYNC_OS_SEC 30
  Default Synchronization Frequency.
- #define NTPSYNC_TASK_NAME "PSPNTPSync"
  Default NTP Sync Task Name.
- #define NTPSYNC_DEFAULT_PRIORITY 60
  Default NTP Sync Task Priority.
- #define PSP_MEM_SCRUB_BSEM_NAME "PSP_BSEM_NAME"
  MEM SCRUB Binary semaphore name.
- #define PSP_CDS_SYNC_TO_FLASH_DEFAULT true
  CDS sync to FLASH.
- #define NTPSYNC_PRIORITY_UP_RANGE 255
  NTP Sync maximum allowed Task priority.
- #define NTPSYNC_PRIORITY_DOWN_RANGE 60
  NTP Sync maximum allowed Task priority.

Typedefs

- typedef TASK_ID CFE_PSP_Exception_SysTaskId_t

  The data type used by the underlying OS to represent a thread ID.

5.16.1 Detailed Description

Main PSP Configuration File for SP0.

Copyright

Description:

This file includes most of the PSP configuration

Limitations, Assumptions, External Events, and Notes:

None

5.17 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_cds_flash.h File Reference

API header to save and restore CDS in FLASH memory.

Functions

- void CFE_PSP_MEMORY_SYNC_Enable (void)
- void CFE_PSP_MEMORY_SYNC_Disable (void)

5.17.1 Detailed Description

API header to save and restore CDS in FLASH memory.

Copyright

Description:

This file contains the function prototypes relating to CDS flash memory. API header to save and restore CDS in FLASH memory.

Limitations, Assumptions, External Events, and Notes:

None

5.18 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_mem_scrub.h File Reference

API header to control Memory Scrubbing.

```
#include "common_types.h"
#include "osapi.h"
```

Data Structures

- struct MEM_SCRUB_STATUS_s

  Memory Scrubbing information struct.
- struct MEM_SCRUB_ERRSTATS_s

  Memory Error Statistics struct.

Macros

- #define MEM_SCRUB_PRINT_SCOPE "PSP MEM SCRUB: "

  Default Memory Scrubbing pre-print string.
- #define MEM_SCRUB_TASK_START_ON_STARTUP true

  Start mem scrub on startup option.

Typedefs

- typedef struct MEM_SCRUB_STATUS_s MEM_SCRUB_STATUS_t

  Memory Scrubbing information struct.
- typedef struct MEM_SCRUB_ERRSTATS_s MEM_SCRUB_ERRSTATS_t

  Memory Error Statistics struct.

Functions

- int32 CFE_PSP_MEM_SCRUB_Set (uint32 newStartAddr, uint32 newEndAddr, osal_priority_t task_priority)
- bool CFE_PSP_MEM_SCRUB_isRunning (void)
- int32 CFE_PSP_MEM_SCRUB_Delete (void)
- void CFE_PSP_MEM_SCRUB_Status (MEM_SCRUB_STATUS_t *mss_Status, bool talk)
- int32 CFE_PSP_MEM_SCRUB_Init (void)
- int32 CFE_PSP_MEM_SCRUB_Enable (void)
- int32 CFE_PSP_MEM_SCRUB_Disable (void)
- void CFE_PSP_MEM_SCRUB_ErrStats (MEM_SCRUB_ERRSTATS_t *errStats, bool talkative)

5.18.1 Detailed Description

API header to control Memory Scrubbing.

Description:

This file contains the function prototypes relating to memory scrubbing. This is specific to the SP0-S processor running VxWorks 6.9 OS.

Limitations, Assumptions, External Events, and Notes:

None

5.19 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_sp0_info.h File Reference

API header for collecting SP0(s) hardware and software information.

```
#include <timers.h>
```

Data Structures

- struct SP0_info_table_t

  SP0 info structure.

Macros

- #define SP0_TEXT_BUFFER_MAX_SIZE 1000

  SP0_TEXT_BUFFER_MAX_SIZE.
- #define SP0_SAFEMODEUSERDATA_BUFFER_SIZE 256

  SP0_SAFEMODEUSERDATA_BUFFER_SIZE.
- #define SP0_PRINT_SCOPE "PSP SP0: "

  Default SP0 Info pre-print string.

Functions

- int32 PSP_SP0_GetInfo (void)
- static int32 PSP_SP0_PrintToBuffer (void)
- SP0_info_table_t PSP_SP0_GetInfoTable (bool print_to_console)
- int32 PSP_SP0_DumpData (void)
- int64_t PSP_SP0_GetDiskFreeSize (char ∗ram_disk_root_path)

5.19.1 Detailed Description

API header for collecting SP0(s) hardware and software information.

Description:

Functions here allow CFS to provide a method to probe SP0 hardware for information from POST, Temperatures, Voltages, Active Boot EEPROM, etc. In addition, this module has a function to save a dump_core text file before aborting CFS execution.

Limitations, Assumptions, External Events, and Notes:

None

5.20 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_start.h File Reference

Header file for the PSP function prototypes in cfe_psp_start.c.

```
#include <stdio.h>
#include <string.h>
#include <vxWorks.h>
#include <taskLib.h>
#include <scratchRegMap.h>
#include <aimonUtil.h>
#include "cfe_psp_config.h"
```

Macros

- #define **CHECK_BIT**(x, pos) ((x) & (1U << (pos)))

Functions

- void CFE_PSP_ProcessPOSTResults (void)
- void CFE_PSP_LogSoftwareResetType (RESET_SRC_REG_ENUM resetSrc)
- void OS_Application_Startup (void)
- void OS_Application_Run (void)
- int32 CFE_PSP_SuspendConsoleShellTask (bool suspend)
- uint32 CFE_PSP_GetRestartType (uint32 *resetSubType)
- int32 CFE_PSP_SetTaskPrio (const char *tName, uint8 tgtPrio)
- static RESET_SRC_REG_ENUM CFE_PSP_ProcessResetType (void)

  Get the reset type and subtype.
- static int32 CFE_PSP_SetSysTasksPrio (void)

  Change system task priorities.

Variables

- const char * g_pMachineCheckCause_msg [10]

  List of MCHK Errors Messages.

5.20.1 Detailed Description

Header file for the PSP function prototypes in cfe_psp_start.c.

Copyright

Description:

PSP Startup API for Aitech SP0. Functions implemented in this file are used to configure the SP0 target and the VxWorks OS, and gather information on how the system is setup.

Limitations, Assumptions, External Events, and Notes:

None

5.20.2 Function Documentation

5.20.2.1 static RESET_SRC_REG_ENUM CFE_PSP_ProcessResetType (void) [static]

Get the reset type and subtype.

Description:

This function determines the reset type and subtype.

Assumptions, External Events, and Notes:

Reset Types are defined in Aitech headers.
Function will save reset types to the respective global static variables:

- g_uiResetType
- g_uiResetSubtype
  Finally, function will print to console the reset type.
  Output defines are defined in Aitech file scratchRegMap.h

Parameters

None	

Returns

RESET_SRC_POR
RESET_SRC_WDT
RESET_SRC_FWDT
RESET_SRC_CPCI
RESET_SRC_SWR

5.20.2.2 static int32 CFE_PSP_SetSysTasksPrio (void) [static]

Change system task priorities.

Description:

This function changes the system task priorities so that they are lower than CFS system task priorities.

Assumptions, External Events, and Notes:

tNet0 priority should be adjusted to be right below what ever gets defined for CI/TO apps in your system if using the network interface CCSDS/UDP for CI/TO apps.

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.21 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_time_sync.h File Reference

API header to control NTP Sync.

```
#include "osapi.h"
```

Functions

- int32 CFE_PSP_TIME_NTPSync_Task_Enable (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Disable (void)
- bool CFE_PSP_TIME_NTPSync_Task_isRunning (void)
- int32 CFE_PSP_TIME_NTPSync_Task_Priority_Set (osal_priority_t opPriority)
- bool CFE_PSP_TIME_NTP_Daemon_isRunning (void)
- int32 ntp_clock_vxworks_Destroy (void)
- uint16 CFE_PSP_TIME_NTPSync_GetFreq (void)
- void CFE_PSP_TIME_NTPSync_SetFreq (uint16 uiNewFreqSec)
- int32 CFE_PSP_TIME_Set_OS_Time (const uint32 ts_sec, const uint32 ts_nsec)
- int32 CFE_PSP_TIME_Get_OS_Time (CFE_TIME_SysTime_t *myT)
- bool CFE_PSP_TIME_CFETimeService_isRunning (void)

  Check if CFS Time Service is up and running.
- int32 CFE_PSP_TIME_StartNTPDaemon (void)
- int32 CFE_PSP_TIME_StopNTPDaemon (void)

5.21.1 Detailed Description

API header to control NTP Sync.

Description:

This file contains the function prototypes that synchronize the cFE Time services to the NTP server. Note that the NTP server must be built into the kernel.

Limitations, Assumptions, External Events, and Notes:

The way this module updates the local time is by calling the CFE Time Service function CFE_TIME_SetTime(). The function changes the STCF value.
GSFC developers do not recommend to use this method of updating CFE time, but rather to use the function CFE_TIME_ExternalTime(). The only way to use this function is by building an app that will periodically (1Hz) get NTP time and publish it via Software Bus.

5.22 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_verify.h File Reference

Macros to run preprocessor checks on psp configuration.

```
#include "cfe_psp_config.h"
```

Functions

- CompileTimeAssert (sizeof(MEMSCRUB_TASK_NAME)<=CFE_PSP_MAXIMUM_TASK_LENGTH, MEMSCRUB_TASK_NAME_TOO_LONG)

  MEM SCRUB Task Name Verification.
- CompileTimeAssert (sizeof(NTPSYNC_TASK_NAME)<=CFE_PSP_MAXIMUM_TASK_LENGTH, NTPSYNC_TASK_NAME_TOO_LONG)

  MEM SCRUB Priority Verification.

5.22.1 Detailed Description

Macros to run preprocessor checks on psp configuration.

Description:

The file includes preprocessor statements to check the validity of the PSP configuration saved in cfe_psp_config.h

Limitations, Assumptions, External Events, and Notes:

None

5.22.2 Function Documentation

5.22.2.1 CompileTimeAssert (sizeof(MEMSCRUB_TASK_NAME)<= *CFE_PSP_MAXIMUM_TASK_LENGTH,* MEMSCRUB_TASK_NAME_TOO_LONG)

MEM SCRUB Task Name Verification.

Check that the MEM SCRUB Task name is no longer than the maximum allowed name length

5.22.2.2 CompileTimeAssert (sizeof(NTPSYNC_TASK_NAME)<= *CFE_PSP_MAXIMUM_TASK_LENGTH,* NTPSYNC_TASK_NAME_TOO_LONG)

MEM SCRUB Priority Verification.

SP0 File Path Verification CDS File Path Verification WatchDog Default Time Verification NTP SYNC Task Name Verification Check that the NTP SYNC Task name is no longer than the maximum allowed name length

5.23 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_version.h File Reference

API header to obtain the values of the various version identifiers.

Macros

- #define **_PSP_VERSION_**

Version Macro Definitions

- #define CFE_PSP_IMPL_BUILD_NUMBER 124
  Development Build Macro Definitions - Build Number.
- #define CFE_PSP_IMPL_BUILD_BASELINE "v1.5.0-rc1"
  Development Build Macro Definitions - Baseline.
- #define CFE_PSP_IMPL_MAJOR_VERSION 1
  ONLY APPLY for OFFICIAL releases. Major version number.
- #define CFE_PSP_IMPL_MINOR_VERSION 5
  ONLY APPLY for OFFICIAL releases. Minor version number.
- #define CFE_PSP_IMPL_REVISION 1
  ONLY APPLY for OFFICIAL releases. Revision number.
- #define CFE_PSP_IMPL_MISSION_REV 0
  ONLY APPLY for OFFICIAL releases. Revision version number. A value of "99" indicates an unreleased development version.
- #define CFE_PSP_IMPL_CODENAME "Caelum"
  ONLY APPLY for OFFICIAL releases. Codename.

Tools to construct version string

- #define CFE_PSP_IMPL_STR_HELPER(x) #x
  Helper function to concatenate strings from integer.
- #define CFE_PSP_IMPL_STR(x) CFE_PSP_IMPL_STR_HELPER(x)
  Helper function to concatenate strings from integer.
- #define CFE_PSP_IMPL_VERSION CFE_PSP_IMPL_BUILD_BASELINE "+dev" CFE_PSP_IMPL_STR(CFE_PSP_IMPL_BUILD_NUMBER)
  DEVELOPMENT Build Version Number.
- #define CFE_PSP_IMPL_VERSION_STRING
  DEVELOPMENT Build Version String.

5.23.1 Detailed Description

API header to obtain the values of the various version identifiers.

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Description:

Provide version identifiers for the cFE Platform Support Packages (PSP). See cfsversions for version and build number and description∗∗ GSC-18128-1, "Core Flight Executive Version 6.7"

Limitations, Assumptions, External Events, and Notes:

None

5.23.2 Macro Definition Documentation

5.23.2.1 #define CFE_PSP_IMPL_VERSION CFE_PSP_IMPL_BUILD_BASELINE "+dev" CFE_PSP_IMPL_STR(CFE_PSP_IMPL_BUILD_NUMBER)

DEVELOPMENT Build Version Number.

Baseline git tag + Number of commits since baseline.

See cfsversions for format differences between development and release versions.

5.23.2.2 #define CFE_PSP_IMPL_VERSION_STRING

Value:

```
" PSP Development Build " CFE_PSP_IMPL_VERSION /* Codename for current development */ \
    ", Last Official Release: psp v1.4.0"          /* For full support please use this version */
```

DEVELOPMENT Build Version String.

Reports the current development build's baseline, number, and name. Also includes a note about the latest official version.

See cfsversions for format differences between development and release versions.

5.24 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_exception.c File Reference

cFE PSP Exception related functions

```
#include <stdio.h>
#include <stddef.h>
#include <string.h>
#include <vxWorks.h>
#include <sysLib.h>
#include <excLib.h>
#include <taskLib.h>
#include <speLib.h>
#include <arch/ppc/vxPpcLib.h>
#include <arch/ppc/esfPpc.h>
#include <edrLib.h>
#include <private/edrLibP.h>
#include "common_types.h"
#include "target_config.h"
#include "osapi.h"
#include "cfe_psp.h"
#include "cfe_psp_config.h"
#include "cfe_psp_exceptionstorage_types.h"
#include "cfe_psp_exceptionstorage_api.h"
#include "cfe_psp_memory.h"
```

Macros

- #define PSP_EXCEP_PRINT_SCOPE "PSP EXC: "

  Default NTP Sync pre-print string.

Functions

- STATUS edrErrorPolicyHookRemove (void)

  Declared in Aitech BSP 'bootrom.map'.
- BOOL CFE_PSP_edrPolicyHandlerHook (int type, void *pInfo_param, BOOL debug)

  Makes the proper call to CFE_ES_ProcessCoreException.
- void CFE_PSP_AttachExceptions (void)
- void CFE_PSP_SetDefaultExceptionEnvironment (void)
- int32 CFE_PSP_ExceptionGetSummary_Impl (const CFE_PSP_Exception_LogData_t *Buffer, char *ReasonBuf, uint32 ReasonSize)

Variables

- static BOOL g_ucOverRideDefaultedrPolicyHandlerHook = true

  g_ucOverRideDefaultedrPolicyHandlerHook
- static EDR_POLICY_HANDLER_HOOK g_pDefaultedrPolicyHandlerHook = NULL

  g_pDefaultedrPolicyHandlerHook

5.24.1 Detailed Description

cFE PSP Exception related functions

Description:

This is the implementation of the PSP Exception API. Functions defined here handles exceptions occurring during the execution of CFS.

Limitations, Assumptions, External Events, and Notes:

The following was found in the `VxWorks 6.9 architecture supplement, pg 179, for PP-C85xx:`
"Do not confuse the hardware floating-point provided by the FPU with that provided by the SPE (see 6.3.10 Signal Processing Engine Support, p.190). If using the e500v2diab or e500v2gnu toolchains, you must use the speSave() speSave() and speRestore() routines to save and restore floating-point context."
The e500 core's SPE is a hardware double precision unit capable of both scalar and vector(SIMD) computation.

5.24.2 Macro Definition Documentation

5.24.2.1 #define PSP_EXCEP_PRINT_SCOPE "PSP EXC: "

Default NTP Sync pre-print string.

Description:

This string is printed before every print related to NTP Sync API.

5.24.3 Function Documentation

5.24.3.1 BOOL CFE_PSP_edrPolicyHandlerHook (int *type,* void * *pInfo_param,* BOOL *debug*)

Makes the proper call to CFE_ES_ProcessCoreException.

Description:

Assumptions, External Events, and Notes:

Assuming the VxWorks OS will call this function with the right parameters. Thus, there is no check on the validity of the input parameters. When speSave() is called, it captures the last floating point context, which may not be valid. If a floating point exception occurs you can be almost 100% sure that this will reflect the proper context. But if another type of exception occurred then this has the possibility of not being valid. Specifically if a task that is not enabled for floating point causes a non-floating point exception, then the meaning of the floating point context will not be valid. If the task is enabled for floating point, then it will be valid.

Parameters

in	*type*	- EDR_FACILITY_KERNEL - VxWorks kernel events EDR_FACILITY_INTERRUPT - interrupt handler events EDR_FACILITY_INIT - system startup events EDR_FACILITY_BOOT - system boot events EDR_FACILITY_REBOOT - system restart events EDR_FACILITY_RTP - RTP system events EDR_FACILITY_USER - user generated events
in	*pInfo_param*	- A pointer to an architecture-specific EXC_INFO structure, in case of exceptions, with CPU exception information. The exception information is saved by the default VxWorks exception handler. The structure is defined for each architecture in one of these files: target/h/arch/arch/excArchLib.h For example: target/h/arch/ppc/excPpcLib.h
in	*debug*	- This flag indicates whether the ED&R system is in debug (also known as lab) mode, or in field (or deployed) mode.

Returns

True - Do not stop offending task
False - Stop offending task

5.24.3.2 int32 CFE_PSP_ExceptionGetSummary_Impl (const CFE_PSP_Exception_LogData_t * *Buffer*, char * *ReasonBuf*, uint32 *ReasonSize*)

Purpose Translate the exception context data into a string

Description:

This function translates the exception context data into a user-friendly "reason" string.

Assumptions, External Events, and Notes:

This is called in an application context to determine the cause of the exception.

Parameters

in	*Buffer*	- Pointer to the Buffer Context data previously stored by ISR/signal handler
out	*ReasonBuf*	- Buffer to store string
in	*ReasonSize*	- Size of string buffer

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.24.4 Variable Documentation

5.24.4.1 EDR_POLICY_HANDLER_HOOK g_pDefaultedrPolicyHandlerHook = NULL [static]

g_pDefaultedrPolicyHandlerHook

Assumptions, External Events, and Notes:

The EDR_POLICY_HANDLER_HOOK is a function pointer defined in VxWorks header file edrLibP.h.

5.25 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_mem_scrub.c File Reference

API for Memory Scrubbing on SP0.

```
#include <vxWorks.h>
#include <mem_scrub.h>
#include "cfe_psp.h"
#include "psp_start.h"
#include "psp_mem_scrub.h"
#include "cfe_psp_config.h"
```

Functions

- static void CFE_PSP_MEM_SCRUB_Task (void)
- void **ckCtrs** (void)
- int32 CFE_PSP_MEM_SCRUB_Set (uint32 newStartAddr, uint32 newEndAddr, osal_priority_t task_priority)
- int32 CFE_PSP_MEM_SCRUB_Delete (void)
- void CFE_PSP_MEM_SCRUB_Status (MEM_SCRUB_STATUS_t *mss_Status, bool talk)
- int32 CFE_PSP_MEM_SCRUB_Init (void)
- bool CFE_PSP_MEM_SCRUB_isRunning (void)
- int32 CFE_PSP_MEM_SCRUB_Enable (void)
- int32 CFE_PSP_MEM_SCRUB_Disable (void)
- void CFE_PSP_MEM_SCRUB_ErrStats (MEM_SCRUB_ERRSTATS_t *errStats, bool talkative)

Variables

- uint32_t **l2errTotal**
- uint32_t **l2errMult**
- uint32_t **l2errTagPar**
- uint32_t **l2errMBECC**
- uint32_t **l2errSBECC**
- uint32_t **l2errCfg**
- uint32_t **mchCause**
- uint32_t **mchkHook**
- uint32 g_uiEndOfRam

  Contains the address of the end of RAM.
- static osal_priority_t g_uiMemScrubTaskPriority = MEMSCRUB_DEFAULT_PRIORITY

  Task Priority of Memory Scrubbing Task.
- static uint32 g_uiMemScrubTaskId = 0

  Contains the Active Memory Scrubbing Task ID.
- static uint32 g_uiMemScrubStartAddr = 0

  Contains the Active Memory Scrubbing Start Address.
- static uint32 g_uiMemScrubEndAddr = 0

  Contains the Active Memory Scrubbing End Address.
- static uint32 g_uiMemScrubCurrentPage = 0

  Contains the Active Memory Scrubbing Current Page.
- static uint32 g_uiMemScrubTotalPages = 0

  Contains the Active Memory Scrubbing Total Pages.

- static osal_id_t g_semUpdateMemAddr_id = 0

  Binary semaphore id used for mem scrub addresses changes.
- static bool g_bScrubAddrUpdates_flag = false

  Boolean flag to indicate scrub addresses have been udpated.

5.25.1 Detailed Description

API for Memory Scrubbing on SP0.

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Description:

Implementation of Memory Scrubbing task using Aitech internal functions

Limitations, Assumptions, External Events, and Notes:

None

5.25.2 Function Documentation

5.25.2.1 static void CFE_PSP_MEM_SCRUB_Task (void) [static]

Purpose Main function for the Memory Scrubbing task

Description:

This is the main function for the Memory Scrubbing task.

Assumptions, External Events, and Notes:

The scrubMemory function implemented by AiTech may never return an error. The function may never exit, the task is meant to be deleted using CFE_PSP_MEM_SCRUB_Delete. Additionally, changes to global Mem Scrub start and end address (by some other thread/task) will not have an affect on the thread running this scrub task. Global scrub memory start/end addresses should only be set via ...SCRUB_Set This function is function should not be part of mem scrub public API

Parameters

None	

Returns

None

5.25.3 Variable Documentation

5.25.3.1 bool g_bScrubAddrUpdates_flag = false [static]

Boolean flag to indicate scrub addresses have been udpated.

Description:

See above g_semUpdateMemAddr_id for more information. We use this flag for optimization sake; use this flag to check if scrub addresses need to be updated. See ...SCRUB_Task

5.25.3.2 osal_id_t g_semUpdateMemAddr_id = 0 [static]

Binary semaphore id used for mem scrub addresses changes.

Description:

It is possible for multiple threads to be altering global mem scrub start address variable and global mem scrub end address variable. In order to protect against disjoint mem address setting due to context switching between threads, we implement a semaphore to check before assigning new mem scrub start/end address values.

Assumptions, External Events, and Notes:

Cannot initialize static variable to non-constant expression. I believe compiler is interpreting OS_OBJECT_ID_UNDEFINED as non constant expression because of type cast in preprocessor macro. Instead, assign variable to OS_OBJECT_ID_UNDEFINED in CFE_PSP_MEM_SCRUB_Init. Since CFE_PSP_MEM_SCRUB_Init will be the first mem scrub-related function to be called and below variable is static, this will be safe.

5.25.3.3 uint32 g_uiEndOfRam

Contains the address of the end of RAM.

Description:

This variable is filled out once during boot and never changed again. Its value reflects the amount of RAM of the system. When moving cFS from SP0 to SP0-s, the value changes automatically. Value is also used for checking for out of range addresses.

5.25.3.4 uint32 g_uiMemScrubCurrentPage = 0 [static]

Contains the Active Memory Scrubbing Current Page.

Description:

Current page that the task is working on. This value gets reset whenever task restart.

5.25.3.5 uint32 g_uiMemScrubEndAddr = 0 [static]

Contains the Active Memory Scrubbing End Address.

Description:

End Address cannot be larger than the maximum RAM

5.25.3.6 uint32 g_uiMemScrubStartAddr = 0 [static]

Contains the Active Memory Scrubbing Start Address.

Description:

The start address can be anything in the address space.

5.25.3.7 uint32 g_uiMemScrubTaskId = 0 [static]

Contains the Active Memory Scrubbing Task ID.

Description:

If 0, task is not running

5.25.3.8 uint32 g_uiMemScrubTotalPages = 0 [static]

Contains the Active Memory Scrubbing Total Pages.

Description:

Total number of pages processed since the start of the task. This value gets reset whenever task restart.

5.26 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_memory.c File Reference

cFE PSP Memory related functions

```
#include <stdio.h>
#include <string.h>
#include <vxWorks.h>
#include <sysLib.h>
#include <moduleLib.h>
#include <sys/types.h>
#include <sys/stat.h>
#include <unistd.h>
#include <fcntl.h>
#include <userReservedMem.h>
#include "common_types.h"
#include "target_config.h"
#include "osapi.h"
#include "cfe_psp.h"
#include "psp_start.h"
#include "cfe_psp_memory.h"
```

Macros

- #define CFE_MODULE_NAME "cfe-core.o"

  Define cFE core loadable module name.

Functions

- unsigned int GetWrsKernelTextStart (void)

  External Kernel Function GetWrsKernelTextStart.
- unsigned int GetWrsKernelTextEnd (void)

  External Kernel Function GetWrsKernelTextEnd.
- static int32 CFE_PSP_ReadCDSFromFlash (uint32 *puiReadBytes)
- static int32 CFE_PSP_WriteCDSToFlash (uint32 *puiWroteBytes)
- static uint32 CFE_PSP_CalculateCRC (const void *DataPtr, uint32 DataLength, uint32 InputCRC)
- int32 CFE_PSP_GetCDSSize (uint32 *SizeOfCDS)
- int32 CFE_PSP_WriteToCDS (const void *PtrToDataToWrite, uint32 CDSOffset, uint32 NumBytes)
- int32 CFE_PSP_ReadFromCDS (void *PtrToDataFromRead, uint32 CDSOffset, uint32 NumBytes)
- int32 CFE_PSP_GetResetArea (cpuaddr *PtrToResetArea, uint32 *SizeOfResetArea)
- int32 CFE_PSP_GetUserReservedArea (cpuaddr *PtrToUserArea, uint32 *SizeOfUserArea)
- int32 CFE_PSP_GetVolatileDiskMem (cpuaddr *PtrToVolDisk, uint32 *SizeOfVolDisk)
- int32 CFE_PSP_InitProcessorReservedMemory (uint32 RestartType)
- void CFE_PSP_SetupReservedMemoryMap (void)
- void CFE_PSP_DeleteProcessorReservedMemory (void)
- int32 CFE_PSP_GetKernelTextSegmentInfo (cpuaddr *PtrToKernelSegment, uint32 *SizeOfKernelSegment)
- int32 CFE_PSP_GetCFETextSegmentInfo (cpuaddr *PtrToCFESegment, uint32 *SizeOfCFESegment)
- void CFE_PSP_MEMORY_SYNC_Enable (void)
- void CFE_PSP_MEMORY_SYNC_Disable (void)

Variables

- static char g_cCDSFilename [10] = CFE_PSP_CFE_FLASH_FILEPATH

  CDS File name in File System.
- static uint32 g_uiCDSCrc = 0

  Stored calculated CRC for the whole CDS reserved memory.
- static bool g_bCorruptedCDSFlash = false

  Flag to track corrupted CDS file in CDS flash memory.
- uint32 g_uiEndOfRam = 0

  Contains the address of the end of RAM.
- CFE_PSP_ReservedMemoryMap_t CFE_PSP_ReservedMemoryMap

  Pointer to the vxWorks USER_RESERVED_MEMORY area.
- static CFE_PSP_MemoryBlock_t g_ReservedMemBlock

  Pointer to the reserved memory block.
- static bool g_bCDSSyncFlash_flag = PSP_CDS_SYNC_TO_FLASH_DEFAULT

  Flag used to track sync CDS to FLASH status.

5.26.1 Detailed Description

cFE PSP Memory related functions

Copyright

Description:

This is the implementation of the cFE memory areas that have to be preserved, and the API that is designed to allow access to them. It also contains memory related routines to return the address of the kernel code used in the cFE checksum.

Limitations, Assumptions, External Events, and Notes:

None

5.26.2 Function Documentation

5.26.2.1 static uint32 CFE_PSP_CalculateCRC (const void * *DataPtr,* uint32 *DataLength,* uint32 *InputCRC*) [static]

Purpose Calculate 16 bits CRC from input data

Description:

None

Assumptions, External Events, and Notes:

InputCRC allows the user to calculate the CRC of non-contiguous blocks as a single value. Nominally, the user should set this value to zero.

CFE now includes a function to calculate the CRC. uint32 CFE_ES_CalculateCRC(void *pData, uint32 DataLength, uint32 InputCRC, uint32 TypeCRC); Only CFE_MISSION_ES_CRC_16 is implemented as the TypeCRC

Parameters

in	*DataPtr*	- Pointer to the input data buffer
in	*DataLength*	- Data buffer length
in	*InputCRC*	- A starting value for use in the CRC calculation.

Returns

Calculated CRC value

5.26.2.2 static int32 CFE_PSP_ReadCDSFromFlash (uint32 * *puiReadBytes*) [static]

Purpose Read the whole CDS data from Flash

Description:

This function read the whole CDS data on Flash to reserved memory on RAM.

Warning

It took about 117ms to read 131072 bytes (128KB) whole CDS area from Flash.

Assumptions, External Events, and Notes:

A failed read does not necessarily indicate corrupted FLASH memory

Parameters

out	*puiReadBytes*	- Number of read bytes

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.26.2.3 static int32 CFE_PSP_WriteCDSToFlash (uint32 * *puiWroteBytes*) [static]

Purpose Write the whole CDS data on Flash

Description:

This function write the whole CDS data from reserved memory on RAM to Flash.

Assumptions, External Events, and Notes:

It took about 117ms to write 131072 bytes (128KB) whole CDS data to Flash.

Parameters

out	*puiWroteBytes*	- Number of written bytes

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.26.3 Variable Documentation

5.26.3.1 char g_cCDSFilename[10] = CFE_PSP_CFE_FLASH_FILEPATH [static]

CDS File name in File System.

Description:

Fully qualified path of where the CDS file will be stored.

5.26.3.2 uint32 g_uiEndOfRam = 0

Contains the address of the end of RAM.

Description:

This variable is filled out once during boot and never changed again. Its value reflects the amount of RAM of the system. When moving cFS from SP0 to SP0-s, the value changes automatically. Value is also used for checking for out of range addresses.

5.27 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_sp0_info.c File Reference

API for collecting SP0(s) hardware and software information.

```
#include <fcntl.h>
#include <stdio.h>
#include <ioLib.h>
#include <vxWorks.h>
#include <float.h>
#include <stat.h>
#include <timers.h>
#include <aimonUtil.h>
#include <sys950Lib.h>
#include <sysApi.h>
#include <scratchRegMap.h>
#include <bflashCt.h>
#include <tempSensor.h>
#include "cfe_psp.h"
#include "cfe_psp_config.h"
#include "psp_sp0_info.h"
```

Macros

- #define SP0_UPGRADE_MAX_VOLTAGE_SENSORS 6

  Max number of Voltage and Temperature sensors per target generation.
- #define **SP0_ORIGINAL_MAX_VOLTAGE_SENSORS** 0
- #define **SP0_UPGRADE_MAX_TEMP_SENSORS** 4
- #define **SP0_ORIGINAL_MAX_TEMP_SENSORS** 3

Functions

- int32 PSP_SP0_GetInfo (void)
- static int32 PSP_SP0_PrintToBuffer (void)
- SP0_info_table_t PSP_SP0_GetInfoTable (bool print_to_console)
- int32 PSP_SP0_DumpData (void)
- int64_t PSP_SP0_GetDiskFreeSize (char ∗ram_disk_root_path)

Variables

- static SP0_info_table_t g_sp0_info_table

  SP0 Data Table.

SP0 Information String Buffer

- static char g_cSP0DataDump [SP0_TEXT_BUFFER_MAX_SIZE]

  SP0 String Buffer.
- static int g_iSP0DataDumpLength

  Actual length of the string buffer.

5.27.1 Detailed Description

API for collecting SP0(s) hardware and software information.

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Description:

Functions here allow CFS to provide a method to probe SP0 hardware for information from POST, Temperatures, Voltages, Active Boot EEPROM, etc. In addition, this module has a function to save a dump_core text file before aborting CFS execution.

Limitations, Assumptions, External Events, and Notes:

None

5.27.2 Function Documentation

5.27.2.1 static int32 PSP_SP0_PrintToBuffer (void) [static]

Purpose Print the SP0 data to string buffer

Description:

Internal function to print the gathered data from SP0 to a string buffer.

Assumptions, External Events, and Notes:

None

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.28 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_start.c File Reference

cFE PSP main entry point

```
#include <stdio.h>
#include <string.h>
#include <vxWorks.h>
#include <taskLib.h>
#include <scratchRegMap.h>
#include <aimonUtil.h>
#include "common_types.h"
#include "target_config.h"
#include "osapi.h"
#include "cfe_es.h"
#include "cfe_psp.h"
#include "cfe_psp_memory.h"
#include "cfe_psp_module.h"
#include "cfe_psp_config.h"
#include "psp_start.h"
#include "psp_mem_scrub.h"
#include "psp_sp0_info.h"
#include "psp_verify.h"
```

Macros

PSP Configuration

Description:

The preferred way to obtain the CFE tunable values at runtime is via the dynamically generated configuration object. This allows a single build of the PSP to be completely CFE-independent.

- #define CFE_PSP_MAIN_FUNCTION (∗GLOBAL_CONFIGDATA.CfeConfig->SystemMain)

  PSP Main function pointer.
- #define CFE_PSP_NONVOL_STARTUP_FILE (GLOBAL_CONFIGDATA.CfeConfig->NonvolStartupFile)

  PSP Non Volatile startup file.

Functions

- int OS_BSPMain (void)

  OSAL OS_BSPMain Entry Point.
- void CFE_PSP_Main (void)
- void CFE_PSP_ProcessPOSTResults (void)
- static RESET_SRC_REG_ENUM CFE_PSP_ProcessResetType (void)
- void CFE_PSP_LogSoftwareResetType (RESET_SRC_REG_ENUM resetSrc)
- void OS_Application_Startup (void)
- int32 CFE_PSP_SuspendConsoleShellTask (bool suspend)
- uint32 CFE_PSP_GetRestartType (uint32 ∗resetSubType)
- int32 CFE_PSP_SetTaskPrio (const char ∗tName, uint8 tgtPrio)
- static int32 CFE_PSP_SetSysTasksPrio (void)
- int32 CFE_PSP_InitSSR (uint32 bus, uint32 device, char ∗DeviceName)

Variables

- static uint32 g_uiResetType = 0

  Reset Type.
- static uint32 g_uiResetSubtype = 0

  Reset Sub Type.
- static USER_SAFE_MODE_DATA_STRUCT g_safeModeUserData

  Safe Mode User Data.
- static TASK_ID g_uiShellTaskID = 0

  Console Shell Task ID.
- const char * g_pMachineCheckCause_msg []

  List of MCHK Errors Messages.
- static CFE_PSP_OS_Task_and_priority_t g_VxWorksTaskList []

  The list of VxWorks task to change the task priority to before finishing initialization.

5.28.1 Detailed Description

cFE PSP main entry point

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Description:

PSP Startup API for Aitech SP0. Functions implemented in this file are used to configure the SP0 target and the VxWorks OS, and gather information on how the system is setup.

Limitations, Assumptions, External Events, and Notes:

None

5.28.2 Function Documentation

5.28.2.1 static RESET_SRC_REG_ENUM CFE_PSP_ProcessResetType (void) [static]

Purpose Determines the reset type and subtype

Description:

Reset Types are defined in Aitech headers Function will save reset types to the respective global static variables:

- g_uiResetType
- g_uiResetSubtype Finally, function will print to console the reset type

Assumptions, External Events, and Notes:

Output defines are defined in Aitech file scratchRegMap.h

Parameters

None	

Returns

RESET_SRC_POR
RESET_SRC_WDT
RESET_SRC_FWDT
RESET_SRC_CPCI
RESET_SRC_SWR

5.28.2.2 static int32 CFE_PSP_SetSysTasksPrio (void) [static]

Purpose Changes system task priorities so that they are lower than CFS system task priorities

Description:

None

Assumptions, External Events, and Notes:

tNet0 priority should be adjusted to be right below what ever gets defined for CI/TO apps in your system if using the network interface CCSDS/UDP for CI/TO apps.

Parameters

None	

Returns

CFE_PSP_SUCCESS
CFE_PSP_ERROR

5.28.3 Variable Documentation

5.28.3.1 const char* g_pMachineCheckCause_msg[]

Initial value:

```
= {
    "L1 instruction cache error",
    "L1 data cache error error: reset",
    "L1 data cache push error error: reset",
    "L2 multiple errors",
    "L2 tag parity error",
    "L2 multi-bit error",
    "L2 single bit error",
    "L2 configuration error",
    "DDR multi-bit error: reset",
    "Other machine check error"
}
```

List of MCHK Errors Messages.

5.28.3.2 CFE_PSP_OS_Task_and_priority_t g_VxWorksTaskList[] [static]

Initial value:

```
=
{
    VXWORKS_TASK_PRIORITIES
}
```

The list of VxWorks task to change the task priority to before finishing initialization.

Note:

Values are defined in cfe_psp_config.h header.
The priority reassignment will be moved to kernel in a future release.

5.29 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_support.c File Reference

Contains glue routines between the cFE and the OS Board Support Package (BSP)

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <vxWorks.h>
#include <cacheLib.h>
#include <rebootLib.h>
#include "common_types.h"
#include "target_config.h"
#include "osapi.h"
#include "cfe_psp.h"
#include "cfe_psp_memory.h"
#include "psp_mem_scrub.h"
#include "psp_sp0_info.h"
```

Macros

Vehicle and Processor IDs

- #define CFE_PSP_CPU_ID (GLOBAL_CONFIGDATA.Default_CpuId)
  CPU ID.
- #define CFE_PSP_CPU_NAME (GLOBAL_CONFIGDATA.Default_CpuName)
  CPU NAME.
- #define CFE_PSP_SPACECRAFT_ID (GLOBAL_CONFIGDATA.Default_SpacecraftId)
  SPACECRAFT ID.

Functions

- void CFE_PSP_Restart (uint32 resetType)
- void CFE_PSP_Panic (int32 errorCode)
- void CFE_PSP_FlushCaches (uint32 type, void ∗address, uint32 size)
- uint32 CFE_PSP_GetProcessorId (void)
- uint32 CFE_PSP_GetSpacecraftId (void)
- const char ∗ CFE_PSP_GetProcessorName (void)

5.29.1 Detailed Description

Contains glue routines between the cFE and the OS Board Support Package (BSP)

Copyright

Description:

The functions here allow the cFE to interface functions that are board and OS specific and usually don't fit well in the OS abstraction layer.

Limitations, Assumptions, External Events, and Notes:

None

5.30 /home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_watchdog.c File Reference

API to support Watchdog.

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <sysApi.h>
#include "common_types.h"
#include "osapi.h"
#include "cfe_psp.h"
#include "cfe_psp_config.h"
```

Functions

- void CFE_PSP_WatchdogInit (void)
- void CFE_PSP_WatchdogEnable (void)
- void CFE_PSP_WatchdogDisable (void)
- void CFE_PSP_WatchdogService (void)
- uint32 CFE_PSP_WatchdogGet (void)
- void CFE_PSP_WatchdogSet (uint32 watchDogValue_ms)
- bool CFE_PSP_WatchdogStatus (void)

Variables

- static uint32 g_uiCFE_PSP_WatchdogValue_ms = CFE_PSP_WATCHDOG_DEFAULT_MSEC

  Watchdog current millisecond value.
- static bool g_bWatchdogStatus = false

  Watchdog current status.

5.30.1 Detailed Description

API to support Watchdog.

Copyright

Copyright (c) 2019-2021 United States Government as represented by the Administrator of the National Aeronautics and Space Administration. All Rights Reserved. Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied. See the License for the specific language governing permissions and limitations under the License.

Description:

API to enable/disable, and control FPGA watchdog

Limitations, Assumptions, External Events, and Notes:

The FPGA watchdog timer has a counter with a tick precision of about 48 nano-seconds

Index

/home/tngo/GW_workspace/cert_testbed/psp/fsw/inc/cfe_psp.h, 90
/home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ntp_clock_vxworks/cfe_psp_ntp.c, 95
/home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/port_direct/cfe_psp_port_direct.c, 98
/home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/ram_direct/cfe_psp_ram_direct.c, 98
/home/tngo/GW_workspace/cert_testbed/psp/fsw/modules/timebase_vxworks/cfe_psp_timebase_vxworks.c, 99
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_api.h, 100
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_exceptionstorage_types.h, 101
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_memory.h, 102
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/inc/cfe_psp_module.h, 103
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_exceptionstorage.c, 104
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memrange.c, 105
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_memutils.c, 105
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_module.c, 106
/home/tngo/GW_workspace/cert_testbed/psp/fsw/shared/src/cfe_psp_version.c, 107
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/functional_test/target_config.c, 108
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/cfe_psp_config.h, 110
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_cds_flash.h, 113
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_mem_scrub.h, 114
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_sp0_info.h, 115
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_start.h, 116
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_time_sync.h, 118
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_verify.h, 119
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/inc/psp_version.h, 120
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_exception.c, 121
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_mem_scrub.c, 125
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_memory.c, 128
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_sp0_info.c, 132
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_start.c, 133
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_support.c, 137
/home/tngo/GW_workspace/cert_testbed/psp/fsw/sp0-vxworks6.9/src/cfe_psp_watchdog.c, 138
CFE_PSP_MODULE_TYPE_INVALID
PSP Public APIs - Common, 15
CFE_PSP_MODULE_TYPE_SIMPLE
PSP Public APIs - Common, 15
CFE_MISSION_CONFIG
target_config.c, 109
CFE_MISSION_NAME
target_config.c, 109
CFE_PSP_AttachExceptions
PSP Public APIs - Common, 15
CFE_PSP_CalculateCRC
cfe_psp_memory.c, 130
CFE_PSP_DeleteProcessorReservedMemory
PSP Public APIs - Common, 15
CFE_PSP_EepromPowerDown
PSP Public APIs - Common, 16
CFE_PSP_EepromPowerUp
PSP Public APIs - Common, 16
CFE_PSP_EepromWrite16
PSP Public APIs - Common, 17
CFE_PSP_EepromWrite32
PSP Public APIs - Common, 17
CFE_PSP_EepromWrite8
PSP Public APIs - Common, 18
CFE_PSP_EepromWriteDisable
PSP Public APIs - Common, 18
CFE_PSP_EepromWriteEnable
PSP Public APIs - Common, 18
CFE_PSP_Exception_ContextDataEntry_t, 83
CFE_PSP_Exception_CopyContext
PSP Public APIs - Common, 19
CFE_PSP_Exception_GetBuffer
PSP Public APIs - Common, 19
CFE_PSP_Exception_GetCount
PSP Public APIs - Common, 20
CFE_PSP_Exception_GetNextContextBuffer
PSP Public APIs - Common, 20
CFE_PSP_Exception_GetSummary
PSP Public APIs - Common, 20
CFE_PSP_Exception_LogData, 84

CFE_PSP_Exception_Reset
PSP Public APIs - Common, 21
CFE_PSP_Exception_WriteComplete
PSP Public APIs - Common, 21
CFE_PSP_ExceptionGetSummary_Impl
cfe_psp_exception.c, 124
PSP Public APIs - Common, 22
CFE_PSP_ExceptionStorage, 84
CFE_PSP_FlushCaches
PSP Public APIs - Common, 22
CFE_PSP_Get_Timebase
PSP Public APIs - Common, 23
CFE_PSP_GetBuildNumber
PSP Public APIs - Common, 23
CFE_PSP_GetCDSSize
PSP Public APIs - Common, 24
CFE_PSP_GetCFETextSegmentInfo
PSP Public APIs - Common, 25
CFE_PSP_GetKernelTextSegmentInfo
PSP Public APIs - Common, 25
CFE_PSP_GetProcessorId
PSP Public APIs - Common, 26
CFE_PSP_GetProcessorName
PSP Public APIs - Common, 27
CFE_PSP_GetResetArea
PSP Public APIs - Common, 28
CFE_PSP_GetRestartType
PSP Public APIs - Common, 28
PSP Public APIs - SP0-VxWorks6.9 Platform, 63
CFE_PSP_GetSpacecraftId
PSP Public APIs - Common, 29
CFE_PSP_GetTime
PSP Public APIs - Common, 30
CFE_PSP_GetTimerLow32Rollover
PSP Public APIs - Common, 30
CFE_PSP_GetTimerTicksPerSecond
PSP Public APIs - Common, 31
CFE_PSP_GetUserReservedArea
PSP Public APIs - Common, 31
CFE_PSP_GetVersionCodeName
PSP Public APIs - Common, 32
CFE_PSP_GetVersionNumber
PSP Public APIs - Common, 33
CFE_PSP_GetVersionString
PSP Public APIs - Common, 34
CFE_PSP_GetVolatileDiskMem
PSP Public APIs - Common, 34
CFE_PSP_InitProcessorReservedMemory
PSP Public APIs - Common, 35
CFE_PSP_InitSSR
PSP Public APIs - Common, 36
CFE_PSP_LogSoftwareResetType
PSP Public APIs - SP0-VxWorks6.9 Platform, 63
CFE_PSP_Main
PSP Public APIs - Common, 37
CFE_PSP_MemCpy
PSP Public APIs - Common, 38
CFE_PSP_MemRangeGet
PSP Public APIs - Common, 38
CFE_PSP_MemRangeSet
PSP Public APIs - Common, 40
CFE_PSP_MemRanges
PSP Public APIs - Common, 40
CFE_PSP_MemRead16
PSP Public APIs - Common, 41
CFE_PSP_MemRead32
PSP Public APIs - Common, 41
CFE_PSP_MemRead8
PSP Public APIs - Common, 42
CFE_PSP_MemSet
PSP Public APIs - Common, 42
CFE_PSP_MemTable_t, 85
CFE_PSP_MemValidateRange
PSP Public APIs - Common, 43
CFE_PSP_MemWrite16
PSP Public APIs - Common, 43
CFE_PSP_MemWrite32
PSP Public APIs - Common, 44
CFE_PSP_MemWrite8
PSP Public APIs - Common, 44
CFE_PSP_MemoryBlock_t, 84
CFE_PSP_Module_FindByName
PSP Public APIs - Common, 44
CFE_PSP_Module_GetAPIEntry
PSP Public APIs - Common, 45
CFE_PSP_ModuleApi_t, 85
CFE_PSP_ModuleInit
PSP Public APIs - Common, 46
CFE_PSP_ModuleInitList
cfe_psp_module.c, 106
CFE_PSP_ModuleType_t
PSP Public APIs - Common, 14
CFE_PSP_OS_Task_and_priority_t, 86
CFE_PSP_Panic
PSP Public APIs - Common, 47
CFE_PSP_PortRead16
PSP Public APIs - Common, 48
CFE_PSP_PortRead32
PSP Public APIs - Common, 48
CFE_PSP_PortRead8
PSP Public APIs - Common, 48
CFE_PSP_PortWrite16
PSP Public APIs - Common, 49
CFE_PSP_PortWrite32
PSP Public APIs - Common, 49
CFE_PSP_PortWrite8
PSP Public APIs - Common, 50
CFE_PSP_ProcessPOSTResults

PSP Public APIs - SP0-VxWorks6.9 Platform, 71
CFE_PSP_ProcessResetType
cfe_psp_start.c, 135
psp_start.h, 117
CFE_PSP_ReadCDSFromFlash
cfe_psp_memory.c, 130
CFE_PSP_ReadFromCDS
PSP Public APIs - Common, 50
CFE_PSP_ReservedMemoryBootRecord_t, 86
CFE_PSP_ReservedMemoryMap
PSP Public APIs - Common, 59
CFE_PSP_ReservedMemoryMap_t, 87
SysMemoryTable, 87
CFE_PSP_Restart
PSP Public APIs - Common, 51
CFE_PSP_SetDefaultExceptionEnvironment
PSP Public APIs - Common, 52
CFE_PSP_SetSysTasksPrio
cfe_psp_start.c, 136
psp_start.h, 117
CFE_PSP_SetTaskPrio
PSP Public APIs - SP0-VxWorks6.9 Platform, 72
CFE_PSP_SetupReservedMemoryMap
PSP Public APIs - Common, 53
CFE_PSP_SuspendConsoleShellTask
PSP Public APIs - SP0-VxWorks6.9 Platform, 73
CFE_PSP_WatchdogDisable
PSP Public APIs - Common, 54
CFE_PSP_WatchdogEnable
PSP Public APIs - Common, 54
CFE_PSP_WatchdogGet
PSP Public APIs - Common, 55
CFE_PSP_WatchdogInit
PSP Public APIs - Common, 56
CFE_PSP_WatchdogService
PSP Public APIs - Common, 56
CFE_PSP_WatchdogSet
PSP Public APIs - Common, 57
CFE_PSP_WatchdogStatus
PSP Public APIs - Common, 58
CFE_PSP_WriteCDSToFlash
cfe_psp_memory.c, 131
CFE_PSP_WriteToCDS
PSP Public APIs - Common, 58
CFE_PSP_edrPolicyHandlerHook
cfe_psp_exception.c, 123
CFE_TIME_Micro2SubSecs
cfe_psp_ntp.c, 98
CFE_TIME_SetTime
cfe_psp_ntp.c, 98
cfe_psp_exception.c
CFE_PSP_ExceptionGetSummary_Impl, 124
CFE_PSP_edrPolicyHandlerHook, 123
g_pDefaultedrPolicyHandlerHook, 124
cfe_psp_mem_scrub.c
g_bScrubAddrUpdates_flag, 127
g_semUpdateMemAddr_id, 127
g_uiEndOfRam, 127
g_uiMemScrubCurrentPage, 127
g_uiMemScrubEndAddr, 127
g_uiMemScrubStartAddr, 127
g_uiMemScrubTaskId, 128
g_uiMemScrubTotalPages, 128
cfe_psp_memory.c
CFE_PSP_CalculateCRC, 130
CFE_PSP_ReadCDSFromFlash, 130
CFE_PSP_WriteCDSToFlash, 131
g_cCDSFilename, 131
g_uiEndOfRam, 131
cfe_psp_module.c
CFE_PSP_ModuleInitList, 106
cfe_psp_ntp.c
CFE_TIME_Micro2SubSecs, 98
CFE_TIME_SetTime, 98
cfe_psp_sp0_info.c
PSP_SP0_PrintToBuffer, 133
cfe_psp_start.c
CFE_PSP_ProcessResetType, 135
CFE_PSP_SetSysTasksPrio, 136
g_VxWorksTaskList, 136
g_pMachineCheckCause_msg, 136
CompileTimeAssert
psp_verify.h, 120

g_VxWorksTaskList
cfe_psp_start.c, 136
g_bScrubAddrUpdates_flag
cfe_psp_mem_scrub.c, 127
g_cCDSFilename
cfe_psp_memory.c, 131
g_pDefaultedrPolicyHandlerHook
cfe_psp_exception.c, 124
g_pMachineCheckCause_msg
cfe_psp_start.c, 136
g_semUpdateMemAddr_id
cfe_psp_mem_scrub.c, 127
g_uiEndOfRam
cfe_psp_mem_scrub.c, 127
cfe_psp_memory.c, 131
g_uiMemScrubCurrentPage
cfe_psp_mem_scrub.c, 127
g_uiMemScrubEndAddr
cfe_psp_mem_scrub.c, 127
g_uiMemScrubStartAddr
cfe_psp_mem_scrub.c, 127
g_uiMemScrubTaskId
cfe_psp_mem_scrub.c, 128
g_uiMemScrubTotalPages

cfe_psp_mem_scrub.c, 128
GLOBAL_CONFIGDATA
target_config.c, 110

MEM_SCRUB_ERRSTATS_s, 87
MEM_SCRUB_STATUS_s, 88

ntp_clock_vxworks_Destroy
PSP Public APIs - SP0-VxWorks6.9 Platform, 78

OS_Application_Run
PSP Public APIs - SP0-VxWorks6.9 Platform, 78
OS_Application_Startup
PSP Public APIs - SP0-VxWorks6.9 Platform, 79

PSP Public APIs - Common
CFE_PSP_MODULE_TYPE_INVALID, 15
CFE_PSP_MODULE_TYPE_SIMPLE, 15
PSP Platform Configurations - SP0-VxWorks6.9, 1
PSP Public APIs - Common, 8
CFE_PSP_AttachExceptions, 15
CFE_PSP_DeleteProcessorReservedMemory, 15
CFE_PSP_EepromPowerDown, 16
CFE_PSP_EepromPowerUp, 16
CFE_PSP_EepromWrite16, 17
CFE_PSP_EepromWrite32, 17
CFE_PSP_EepromWrite8, 18
CFE_PSP_EepromWriteDisable, 18
CFE_PSP_EepromWriteEnable, 18
CFE_PSP_Exception_CopyContext, 19
CFE_PSP_Exception_GetBuffer, 19
CFE_PSP_Exception_GetCount, 20
CFE_PSP_Exception_GetSummary, 20
CFE_PSP_Exception_Reset, 21
CFE_PSP_Exception_WriteComplete, 21
CFE_PSP_ExceptionGetSummary_Impl, 22
CFE_PSP_FlushCaches, 22
CFE_PSP_Get_Timebase, 23
CFE_PSP_GetBuildNumber, 23
CFE_PSP_GetCDSSize, 24
CFE_PSP_GetCFETextSegmentInfo, 25
CFE_PSP_GetKernelTextSegmentInfo, 25
CFE_PSP_GetProcessorId, 26
CFE_PSP_GetProcessorName, 27
CFE_PSP_GetResetArea, 28
CFE_PSP_GetRestartType, 28
CFE_PSP_GetSpacecraftId, 29
CFE_PSP_GetTime, 30
CFE_PSP_GetTimerLow32Rollover, 30
CFE_PSP_GetTimerTicksPerSecond, 31
CFE_PSP_GetUserReservedArea, 31
CFE_PSP_GetVersionCodeName, 32
CFE_PSP_GetVersionNumber, 33
CFE_PSP_GetVersionString, 34
CFE_PSP_GetVolatileDiskMem, 34
CFE_PSP_InitProcessorReservedMemory, 35
CFE_PSP_InitSSR, 36
CFE_PSP_Main, 37
CFE_PSP_MemCpy, 38
CFE_PSP_MemRangeGet, 38
CFE_PSP_MemRangeSet, 40
CFE_PSP_MemRanges, 40
CFE_PSP_MemRead16, 41
CFE_PSP_MemRead32, 41
CFE_PSP_MemRead8, 42
CFE_PSP_MemSet, 42
CFE_PSP_MemValidateRange, 43
CFE_PSP_MemWrite16, 43
CFE_PSP_MemWrite32, 44
CFE_PSP_MemWrite8, 44
CFE_PSP_Module_FindByName, 44
CFE_PSP_ModuleInit, 46
CFE_PSP_ModuleType_t, 14
CFE_PSP_Panic, 47
CFE_PSP_PortRead16, 48
CFE_PSP_PortRead32, 48
CFE_PSP_PortRead8, 48
CFE_PSP_PortWrite16, 49
CFE_PSP_PortWrite32, 49
CFE_PSP_PortWrite8, 50
CFE_PSP_ReadFromCDS, 50
CFE_PSP_ReservedMemoryMap, 59
CFE_PSP_Restart, 51
CFE_PSP_SetDefaultExceptionEnvironment, 52
CFE_PSP_SetupReservedMemoryMap, 53
CFE_PSP_WatchdogDisable, 54
CFE_PSP_WatchdogEnable, 54
CFE_PSP_WatchdogGet, 55
CFE_PSP_WatchdogInit, 56
CFE_PSP_WatchdogService, 56
CFE_PSP_WatchdogSet, 57
CFE_PSP_WatchdogStatus, 58
CFE_PSP_WriteToCDS, 58
PSP Public APIs - SP0-VxWorks6.9 Platform, 60
ntp_clock_vxworks_Destroy, 78
OS_Application_Run, 78
OS_Application_Startup, 79
PSP_SP0_DumpData, 80
PSP_SP0_GetInfo, 81
PSP_SP0_GetInfoTable, 82
PSP_SP0_PrintToBuffer, 82
PSP_SP0_DumpData
PSP Public APIs - SP0-VxWorks6.9 Platform, 80
PSP_SP0_GetDiskFreeSize
PSP Public APIs - SP0-VxWorks6.9 Platform, 80
PSP_SP0_GetInfo
PSP Public APIs - SP0-VxWorks6.9 Platform, 81
PSP_SP0_GetInfoTable
PSP Public APIs - SP0-VxWorks6.9 Platform, 82

PSP_SP0_PrintToBuffer
cfe_psp_sp0_info.c, 133
PSP Public APIs - SP0-VxWorks6.9 Platform, 82
PSP_VxWorks_Timebase_Global_t, 89
psp_start.h
CFE_PSP_ProcessResetType, 117
CFE_PSP_SetSysTasksPrio, 117
psp_verify.h
CompileTimeAssert, 120

SP0_PRINT_SCOPE
PSP Public APIs - SP0-VxWorks6.9 Platform, 61
SP0_info_table_t, 89
SysMemoryTable
CFE_PSP_ReservedMemoryMap_t, 87

target_config.c
CFE_MISSION_NAME, 109
GLOBAL_CONFIGDATA, 110