

# An Introduction to TTCAN (Time Triggered Controller Area Network)

Rahul Shah & Xuanming Dong  
EE290O Class Discussion  
March 7, 2002

# Brief History

- Developed by Bosch
- Standardized in ISO 11898
- First deployed in 1986
- Used in:
  - Automotive applications
  - Industrial automation
  - Embedded systems

# CAN standards

- CAN version 1.0
- Standard CAN (version 2.0A)
- Extended CAN (version 2.0B)
- Time-Triggered CAN

# CAN vs. LAN

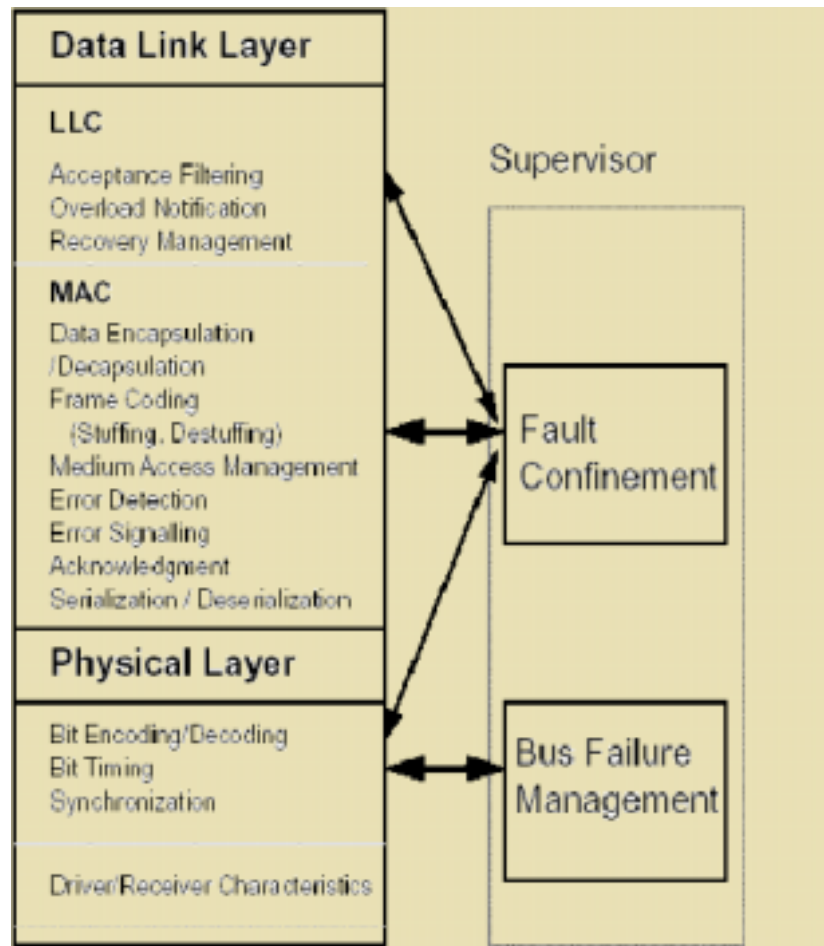
- Content addressing vs. node addressing
- Strict priority based system
- In case of collision, packet with higher priority gets through
- Better fault tolerance
- Engineered for known traffic patterns

# Controller Area Network (CAN)

# Layered Structure of a CAN Node

Application Layer
Object Layer <ul style="list-style-type: none"><li>–Message Filtering</li><li>–Message and Status Handling</li></ul>
Transfer Layer <ul style="list-style-type: none"><li>–Fault Confinement</li><li>–Error Detection and Signaling</li><li>–Message Validation</li><li>–Acknowledgement</li><li>–Arbitration</li><li>–Message Framing</li><li>–Transfer Rate and Timing</li></ul>
Physical Layer <ul style="list-style-type: none"><li>–Signal Level and Bit Representation</li><li>–Transmission Medium</li></ul>

# CAN Protocol Stack



# CAN Bit Rates

Bus length (m)	Max. bit rate (b/s)
40	1M
100	500k
200	250k
500	125k
6 km	10k



# CAN Bus

- Broadcast bus connected in wired-AND
- Non-return to zero with bit stuffing
- Dominant bit – 0
- Recessive bit – 1
- Bit wise arbitration
- Tagged priority for every message

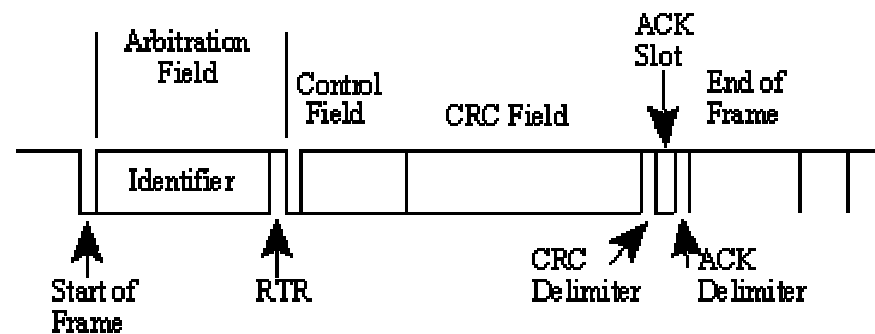
- Data Frame

The diagram illustrates the CAN 2.0B Message Frame structure. The frame is composed of several fields: Arbitration field, Control, Data Field, CRC field, ACK, EOF, Int, and Bus Idle. The Arbitration field contains the 11 bit Identifier and 18 bit Identifier. The Control field contains the DLC (Data Length Code) and Data (0-8 Bytes). The CRC field contains 15 bits. The ACK field contains the Delimiter. The EOF field contains the Delimiter. The Int field contains the Slot. The Bus Idle field is the final state of the bus. The diagram also shows the timing of the frame, with markers for SOF (Start of Frame), SRR (Start of Reception), IDE (Identifier Extension), RTR (Remote Transmission Request), r0 (first data byte), r1 (second data byte), and Slot (end of frame).

# Message Types

- Remote Frame

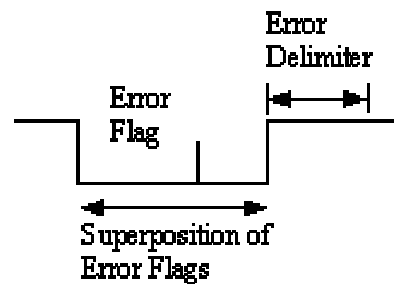
*“Hello everyone, can somebody please produce the data labeled X?”*



# Message Types

- Error Frame

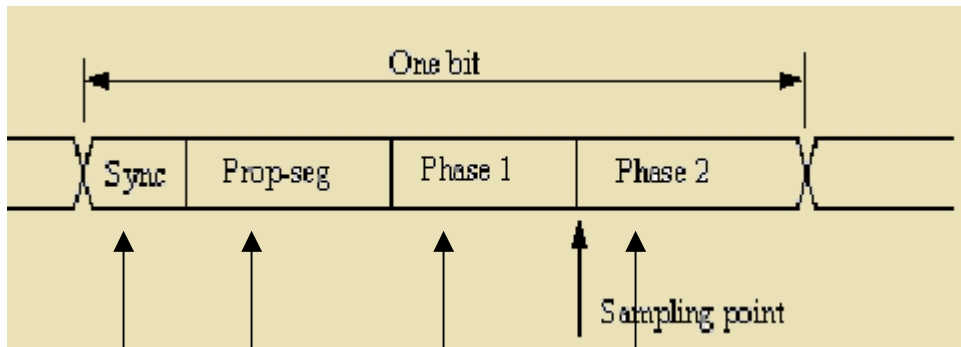
*(everyone, aloud) “OH DEAR, LET'S TRY AGAIN”*



- Overload Frame

*“I'm a very busy little guy, could you please wait for a moment?”*

# Bit Timing



Compensates for edge phase errors  
– used during resynchronization

Compensates for physical delay on the bus

An edge is expected in this segment

# Error Detection Mechanisms

- Bit monitoring
- Bit stuffing
- Frame check
- Acknowledgement check
- Cyclic redundancy check (CRC)

# Fault Confinement

- Unit may be in three states:
  - Error active
  - Error passive
  - Bus off
- Two registers are maintained in every unit:
  - Transmit error count
  - Receive error count

# CAN Advantages

- Mature standard
- Hardware implementation of the protocol
- Simple transmission medium
- Excellent error handling
- Fine fault confinement



# Time Triggered Controller Area Network (TTCAN)

# Motivation of TTCAN

- Under the bitwise arbitration of CAN, the access may be delayed, if some other message is already in the process of transmission or if another message with higher priority also competes for the bus.
- Even the message with the highest priority may experience a small latency.
- The lower the priority of a message is, the higher the latency jitter for the media access

# The Goals of TTCAN

- Reduce latency jitters
- Guarantee a deterministic communication pattern on the bus
- Use the physical bandwidth of a CAN network much more efficiently

# TTCAN: Two Level Extension

- Extension level 1
  - Guarantees the time triggered operation of CAN based on the reference message of a time master
  - Fault tolerance of that functionality is established by redundant time masters (potential time masters)
- Extension level 2
  - A globally synchronized time base is established
  - A continuous drift correction among the CAN controllers is realized

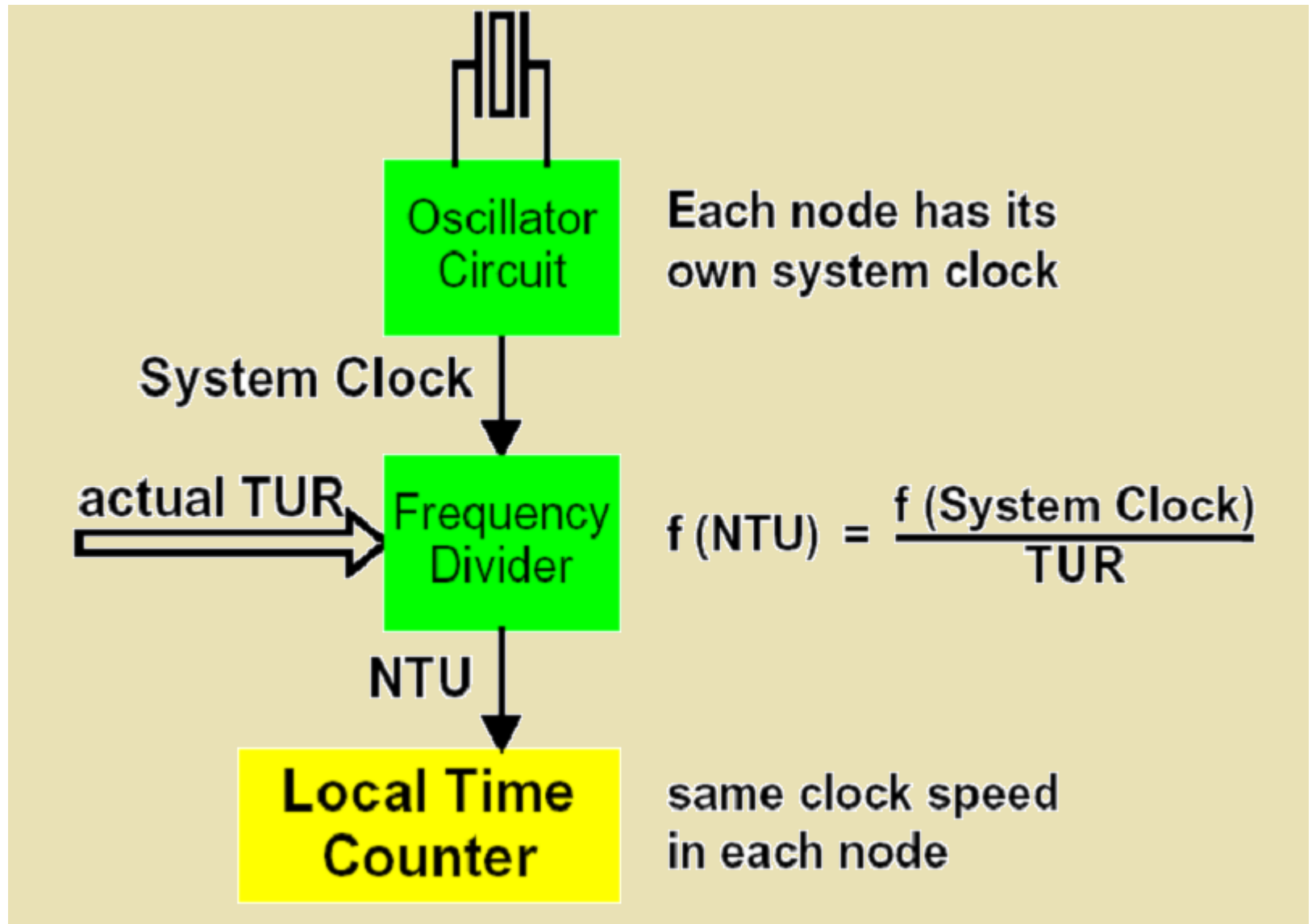
# Overview of TTCAN Mechanisms

- Time bases, provided either by an internal or by an external clock
  - Local\_Time, Cycle\_Time, and Global\_Time
- Three different types of time windows
  - Exclusive time window, arbitrating time window, and free time window
- Time-triggered and periodic communications clocked by a time master's reference message
- The system matrix specifies the sequence of messages transmitted in each basic cycle
- At least one event-trigger should be freely programmable by the control unit

# Local Time

- Local\_Time, a 16 bit integer value that is incremented each Network Time Unit (NTU)
- The length of the NTU is the same for all nodes
- It is generated locally, based on the local system clock period  $t_{\text{sys}}$  and the local Time Unit Ratio (TUR),  $\text{NTU} = \text{TUR} * t_{\text{sys}}$
- Different system clocks in the nodes require different (non-integer) TUR values
  - TUR is a non-integer value and may be adapted to compensate for clock drift or to synchronize to an external time base.

# Local Time

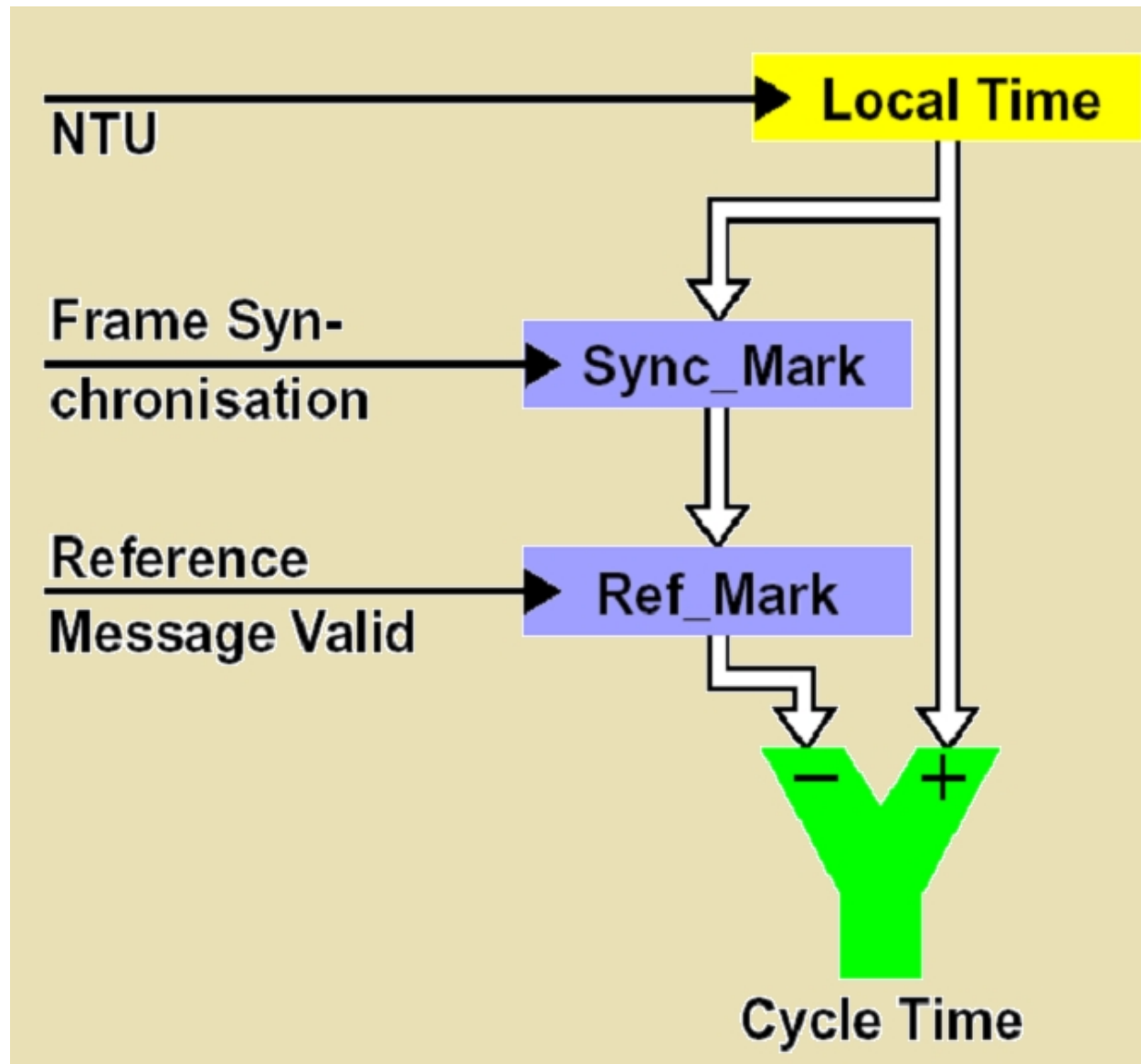


# Cycle Time

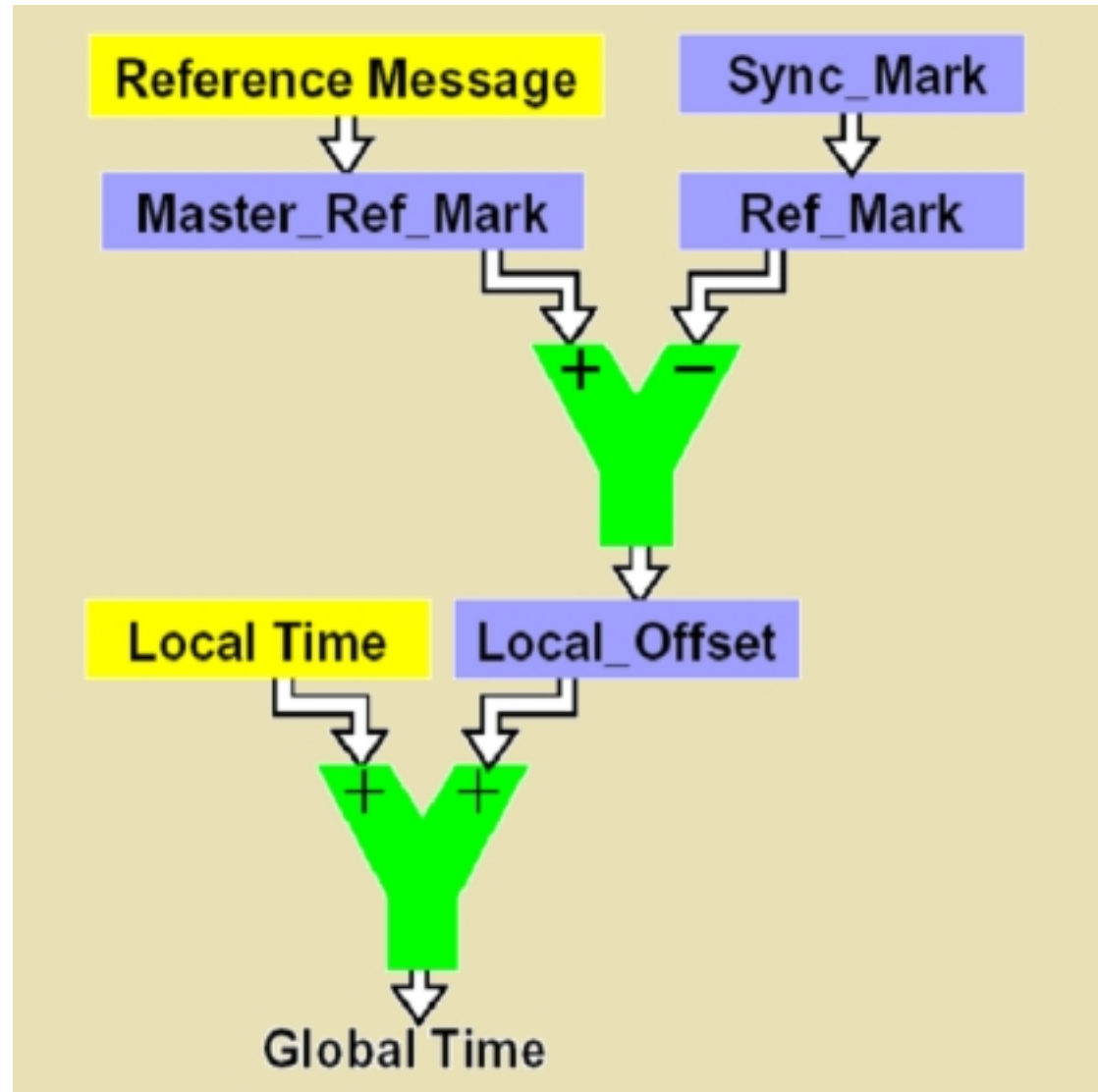
- Each valid Reference Message starts a new basic cycle and causes a reset of each node's Cycle\_Time.
- The value of Local\_Time is captured as Sync\_Mark at the start of frame (SOF) bit of each message. When a valid Reference Message is received, this message's Sync\_Mark becomes the new Ref\_Mark
- Cycle\_Time is the actual difference between Local\_Time and Ref\_Mark, restarting at the beginning of each basic cycle when Ref\_Mark is reloaded
- Even in a software implementation of TTCAN, the capturing of Local\_Time into Sync\_Mark at each SOF must be done in hardware



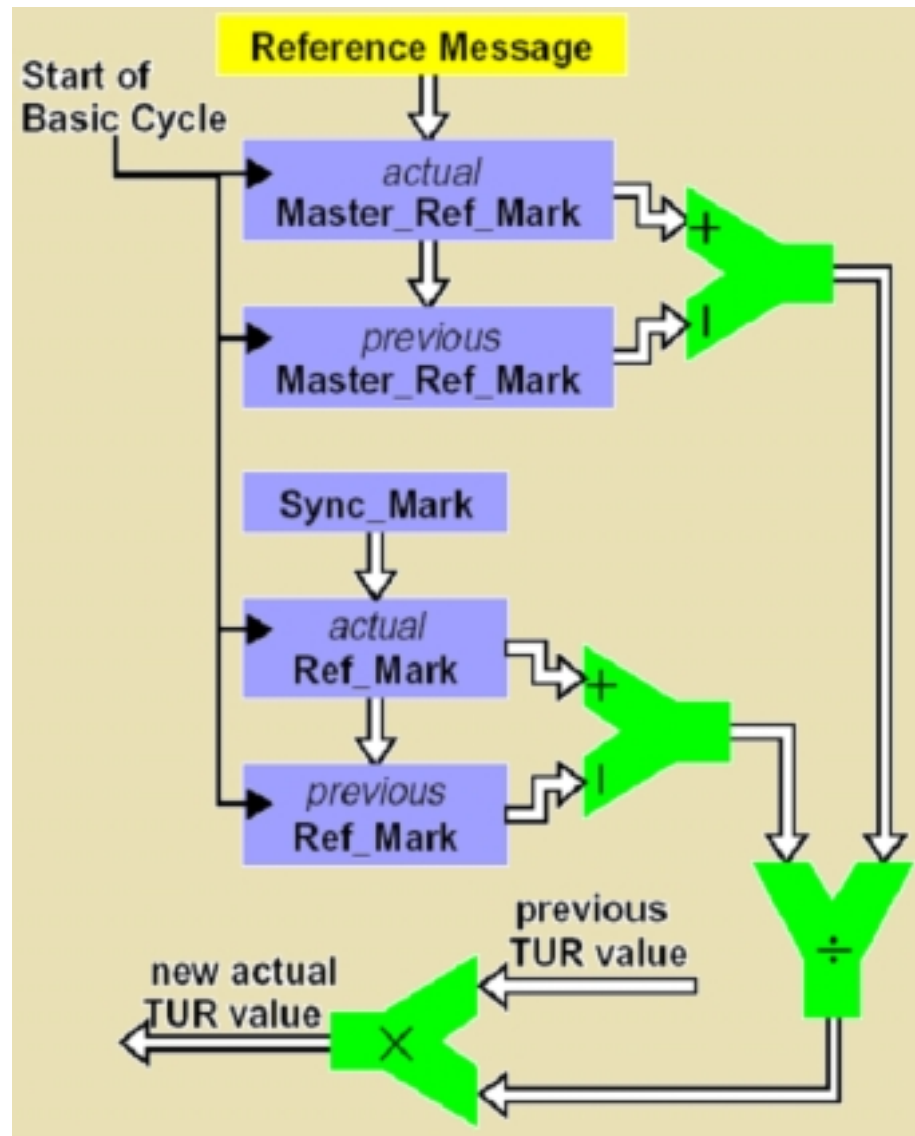
# Cycle Time



# Global Time



# Drift Compensation



# Time Windows

- Exclusive window
  - Exclusively reserved for one message, without competition for the CAN network access
  - The automatic retransmission of messages that could not be transmitted successfully is disabled, guaranteeing that messages in exclusive time windows are not delayed
- Arbitrating window
  - During which messages can compete for the bus by the bitwise arbitrating mechanism of CAN
- Free window
  - Reserved for further extensions of the network

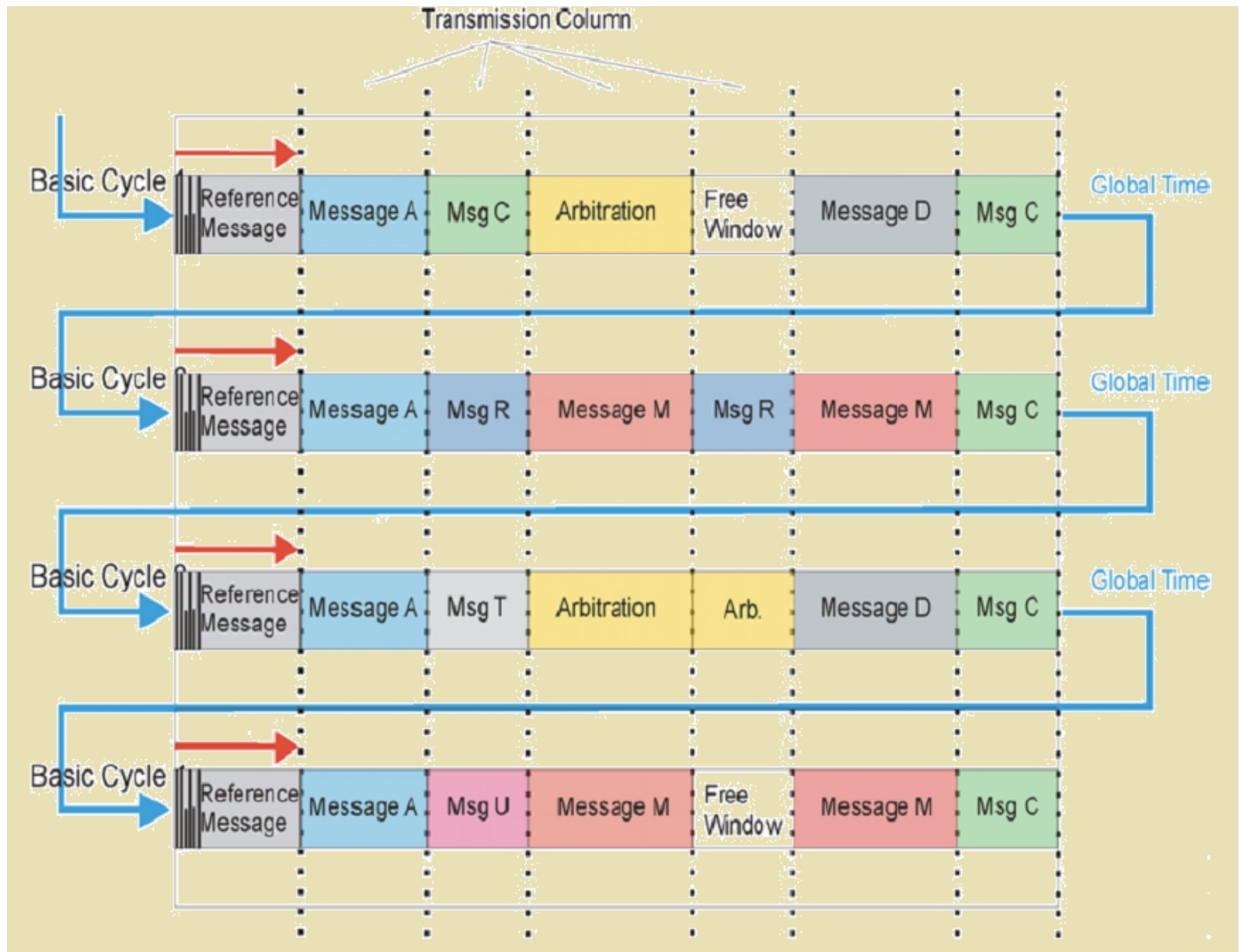
# The Reference Message

- The reference message can be easily recognized by its identifier
- In extension level 1
  - The reference message only holds some control information of one byte, the rest of a CAN message can be used for data transfer
- In extension level 2
  - The reference message holds additional control information, e.g. the global time information of the current TTCAN time master
  - The reference message of level 2 covers 4 bytes while downwards compatibility is guaranteed. The remaining 4 bytes are open for data communication as well

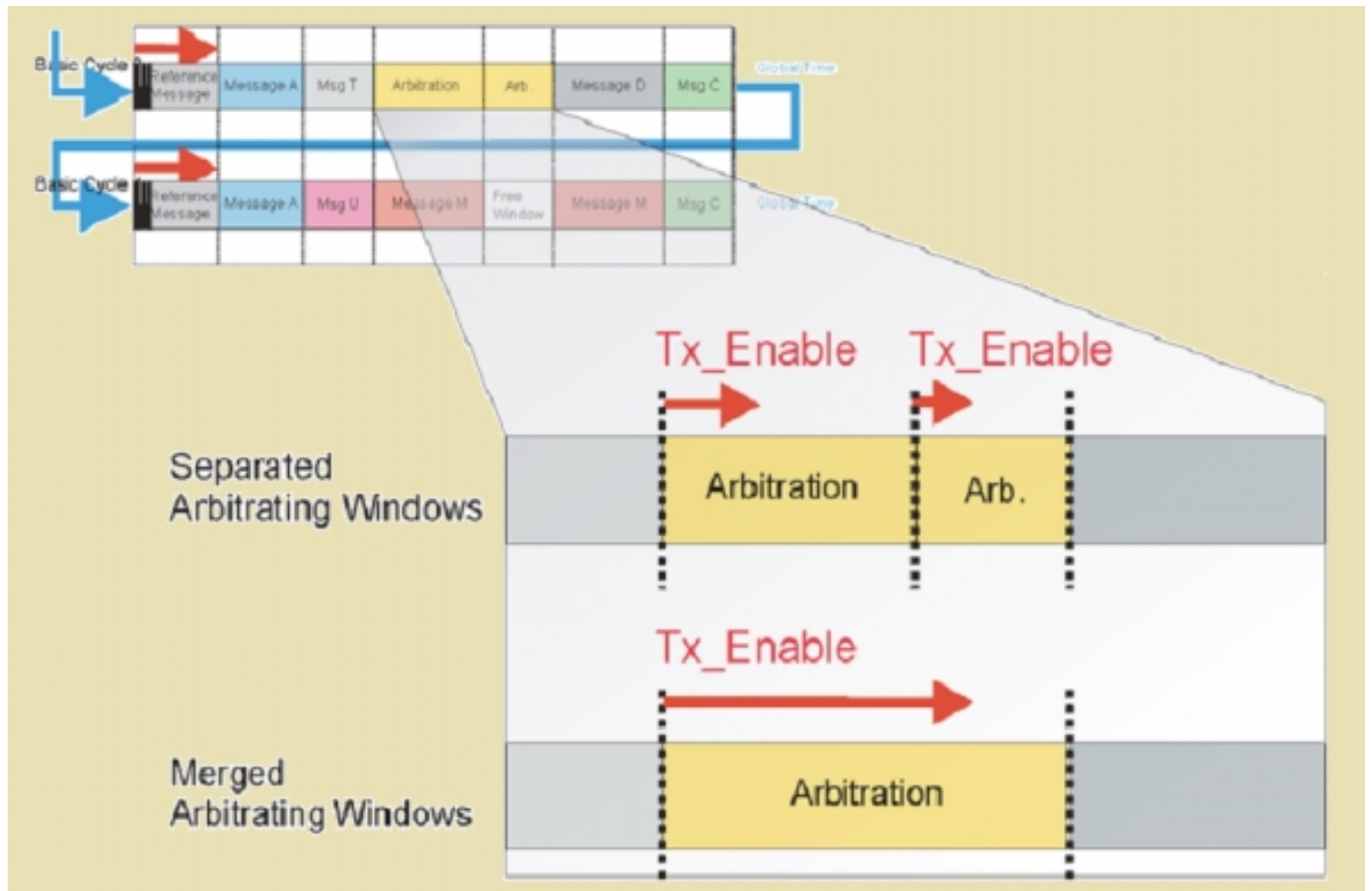
# The System Matrix

- In TTCAN not all basic cycles necessarily have to be the same
- Different basic cycles are distinguished by the cycle count
  - A cycle count is incremented each cycle up to the maximum value after which it is restarted again
- System matrix is obtained by combining all those different cycles
  - It represents the communication overview of a TTCAN network
- System matrix allows another useful exception
  - Ignore the columns in the case of two or more arbitrating time windows in series

# Example of a TTCAN System Matrix



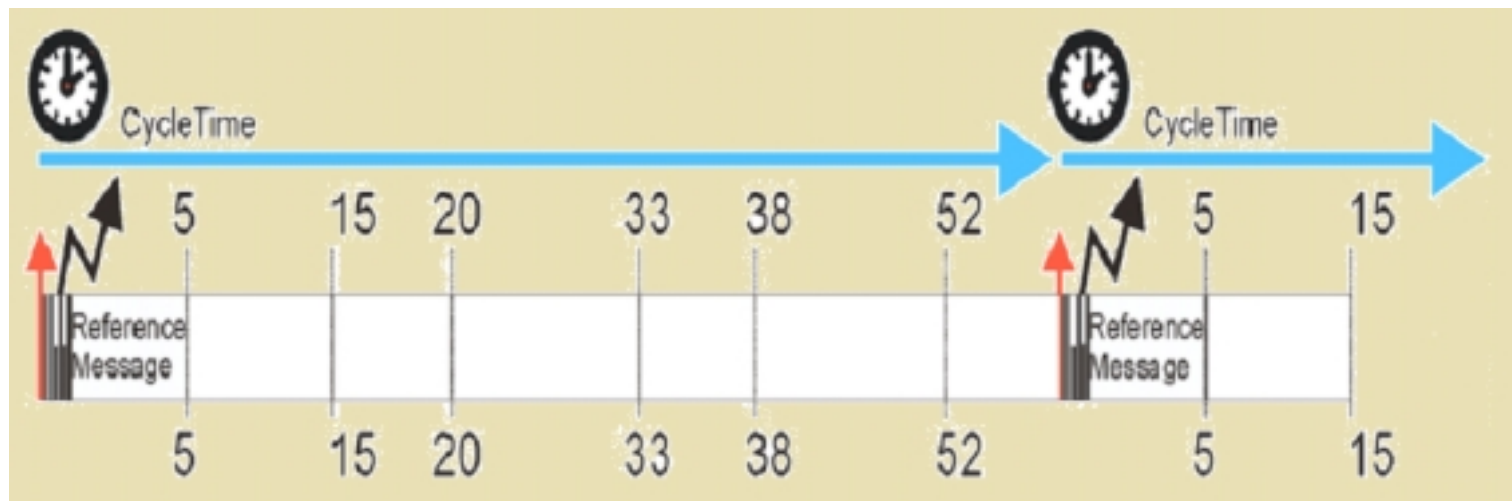
# Merged Arbitrating Windows





# Cycle Time and Time Marks

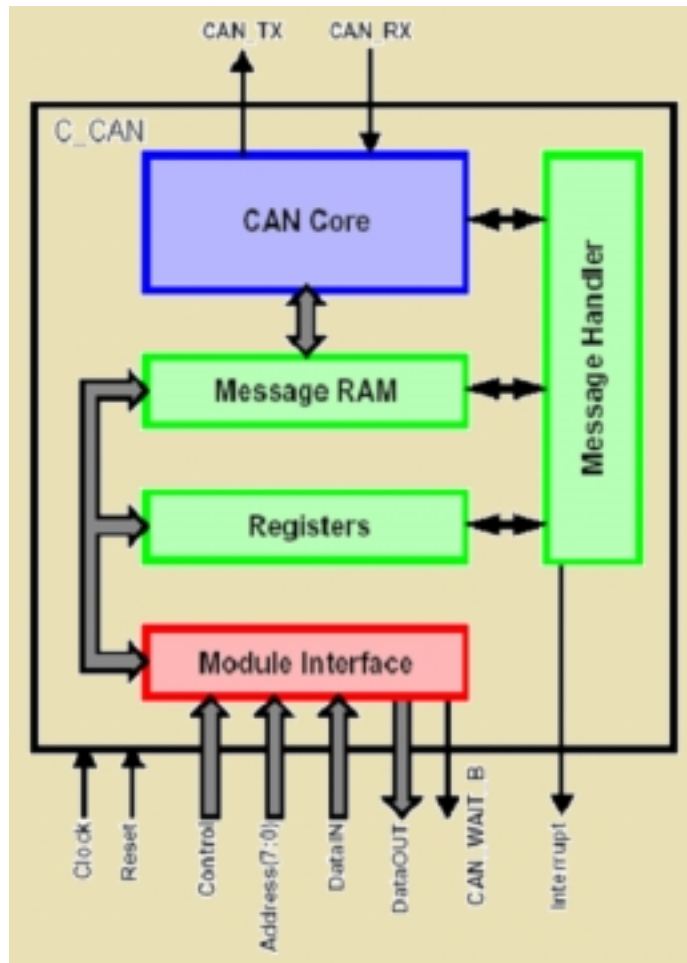
- A time mark furthermore consists of the base mark and the repeat count information.
  - The base mark determines the number of the first basic cycle after the beginning of the matrix cycle in which the message must be sent/received.
  - The repeat count determines the number of basic cycles between two successive transmissions/receptions of the message.



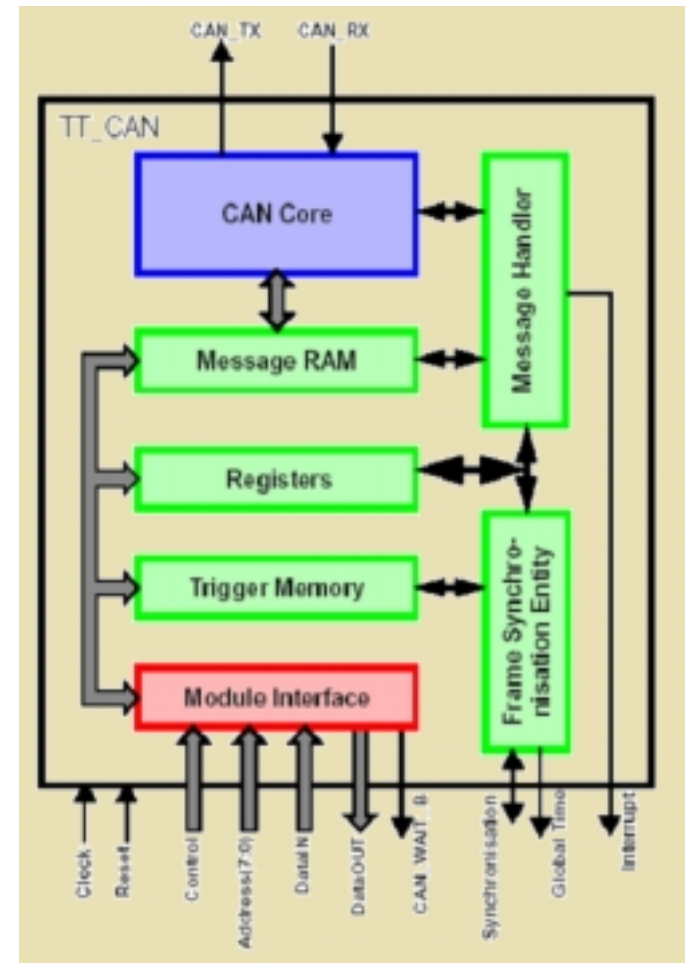
# TTCAN Implementation

- The TTCAN is expanded by two functional blocks: the Trigger Memory and the Frame Synchronization Entity (FSE)
- The Trigger Memory stores the time marks of the system matrix that are linked to the messages in the Message RAM; the data is provided to the Frame Synchronization Entity
- The Frame Synchronization Entity is the state machine that controls the time triggered communication. It synchronizes itself to the reference messages on the CAN bus, controls the cycle time, and generates Time Triggers. It is divided into six blocks:
  - TBB: Time Base Builder
  - CTC: Cycle Time Controller
  - TSO: Time Schedule Organizer
  - MSA: Master State Administrator
  - AOM: Application Operation Monitor
  - GTU: Global Time Unit

# TTCAN Implementation



C\_CAN



TT\_CAN