

# SiVi-AD3250

SMIC 110nm (1P6M)



## MAIN FEATURES

- Designed on SMIC 110nm
- ADC Conversion rate is 27MHz from a fully integrated low noise PLL
- Analog input range is from 0~0.5V to 0~1.0V single-ended.
- Each channel provides 10-bit data outputs.
- Each channel shall provide a 6-bit DAC for gain adjustment and a 6-bit DAC for offset adjustment
- Each channel shall provide mid-level clamping capability. The mid-level voltages shall have a 4-bit adjustable range
- Operational temperature range from -40°C to 125°C

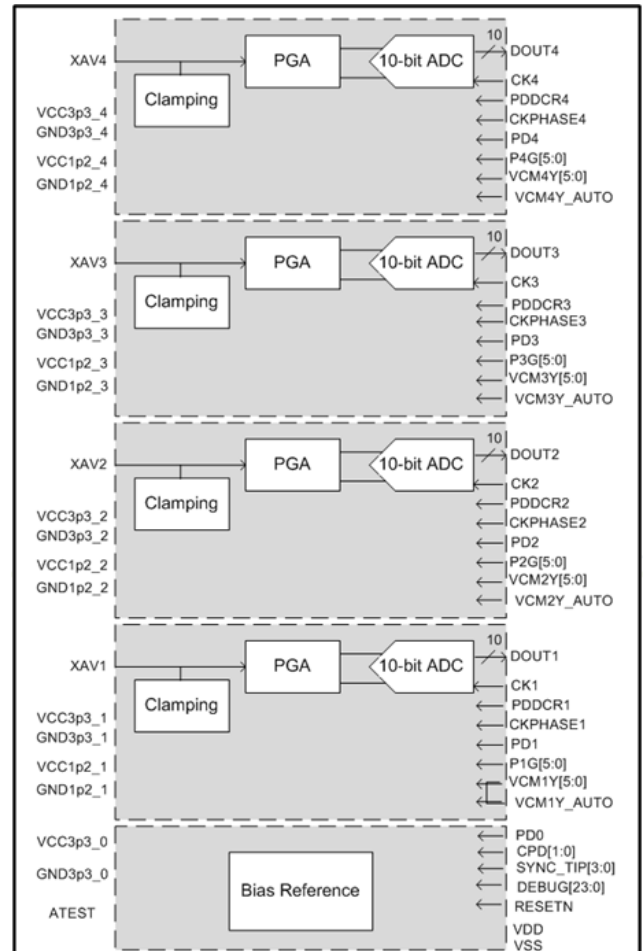
## IP DESCRIPTION

Silicon Vision SiVi-AD3250 is a fully integrated quad 10-bit video ADC for CVBS video input signals. It includes front end analog clamping and programmable gain amplifier.

SiVi-AD3250 is silicon proven in 110nm SMIC process technologies with MIM capacitors

## APPLICATIONS

- Plasma display panels
- LCDTV/HDTV
- Scan Converters



Block Diagram for SiVi-AD3250



**Silicon Vision Tech. Ltd.**  
14 Manchester Square, London  
W1U 3PP, United Kingdom  
[info@si-vision.com](mailto:info@si-vision.com)