

SIVI-SDADC1602

Toshiba 40nm LP (1P4M)



MAIN FEATURES

- Designed on Toshiba 40nm-LP
- 16 bit low power sigma delta ADC
- Operates from a 3.3V supply voltage
- 0 to 2.4V input differential dynamic range
- Low current consumption
- Small silicon area <40000um²
- Operational temperature range from -40°C to 125°C

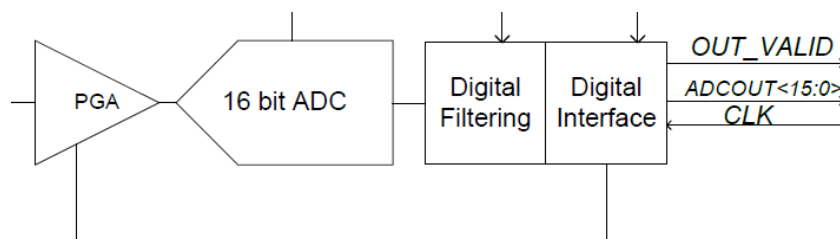
IP DESCRIPTION

Silicon Vision SiVi-SDADC1602 offers a high performance and cost-effective solution for monitoring ADCs. The IP uses a sampling clock of 8MHz with a throughput of 15.625Ksps. Silicon Vision SiVi-SDADC1602 consumes less than 800μA which makes it suitable for low power state of the art SoCs.

The IP was implemented on Toshiba 40nm LP

APPLICATIONS

- Accurate Temperature Sensing
- Monitoring Analogue Voltages or Currents from External Sensing Devices



Block Diagram for SiVi-SDADC1602



Silicon Vision Tech. Ltd.
14 Manchester Square, London
W1U 3PP, United Kingdom
info@si-vision.com