

SIVI-SV18001

TSMC/SMIC 180nm (1P5M)



MAIN FEATURES

- Designed on SMIC/TSMC 180nm
- 10-bit, Y/CVBS video decoder up to 36Mps
- C-Y/CVBS video signal digitization
- Automatic gain and offset calibration
- 1.0 Vp-p differential input range
- Low Power Consumption 150mW
- Fully integrated anti-alias filter
- Operational temperature range from -40°C to 125°C

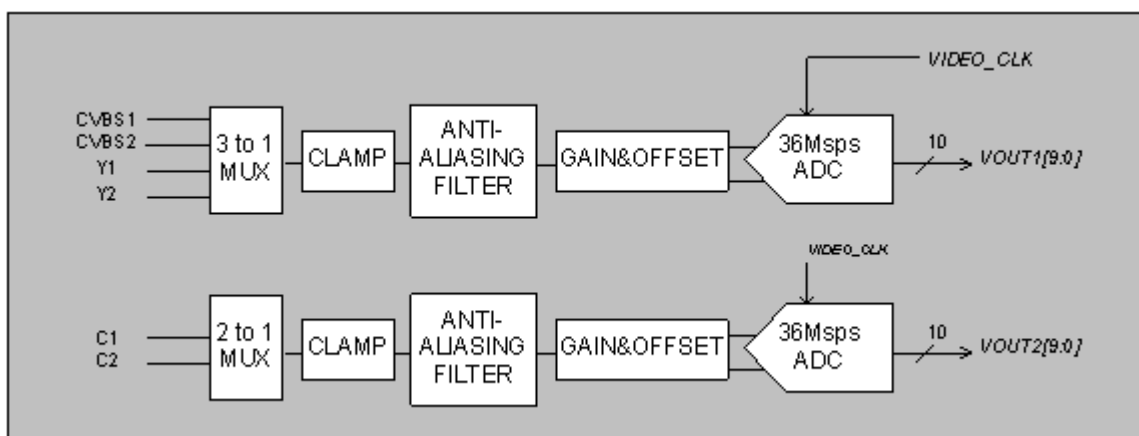
IP DESCRIPTION

Silicon Vision SiVi-SV18001 is a fully integrated fully programmable C-Y/CVBS video decoder. The chip composes of two channels 10-bit 36Mps ADC with 4 inputs multiplexer, clamping function, gain and offset programming and anti-aliasing filter for composite video signaling interface.

The IP block is silicon proven in 180nm TSMC and SMIC process technologies.

APPLICATIONS

- Advanced TVs
- Plasma Display Panels
- C-Y/CVBS signal processing
- LCD monitors and projectors
- Scan Converters
- Advanced Video Gaming



Block Diagram for SiVi-SV18001



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