

SiVi-FPLL600M180

TSMC/SMIC 180nm (1P5M)



MAIN FEATURES

- Designed on SMIC/TSMC 180nm
- Fully Integrated PLL for Hsync Synchronization
- On-chip integrated loop filter based on digital implementation
- Generates frequencies up to 150MHz
- Very low jitter with low power consumption
- Supports HDTV resolution up to 1080p mode
- 32 inter-pixel sampling positions
- Low current operation
- Operational temperature range from -40°C to 125°C

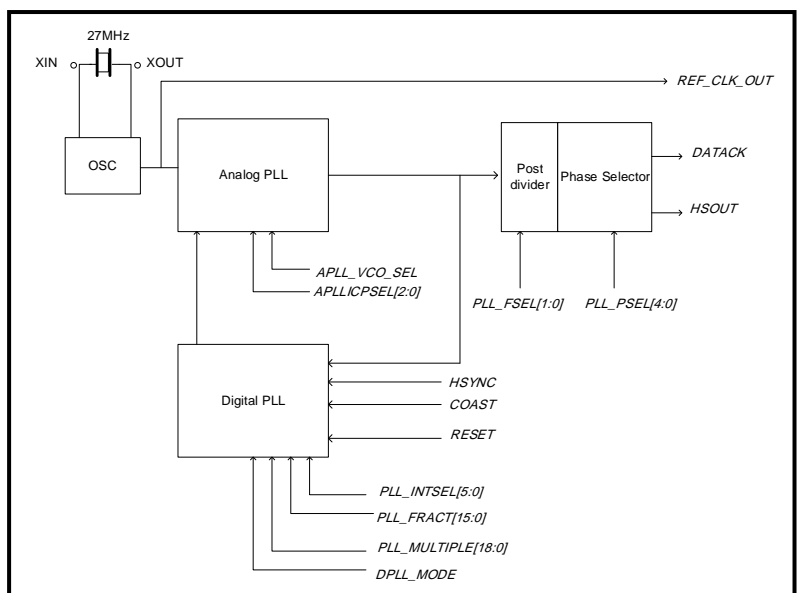
IP DESCRIPTION

Silicon Vision SiVi-FPLL600M180 is a fully integrated fully programmable PLL used in HDTV analog video front end. The PLL can generate frequencies up to 150MHz with 32 inter-pixel sampling positions with very low jitter that can support HDTV operation up to resolutions of 1080p. Thanks to the digital implementation of the loop, the PLL is fully integrated including the loop filter

SiVi-SVFPLL600M180 is silicon proven in 180nm TSMC and SMIC process technologies

APPLICATIONS

- Advanced TVs
- Plasma display panels
- LCDTV/HDTV
- C-Y/CVBS signal processing
- LCD monitors and projectors
- Scan converters



Block Diagram for SiVi-FPLL600M180



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