

SIVI-SDADC1202

TSMC 130nm (1P4M)



MAIN FEATURES

- Designed on TSMC 180nm
- 12 bit low power sigma delta ADC
- Operates from a 3.3V
- 0 to 6.6V input differential dynamic range
- Sampling frequency up to 32MHz
- Throughput up to 128Ksps
- Low current consumption <5.5mA
- Operational temperature range from -40°C to 125°C

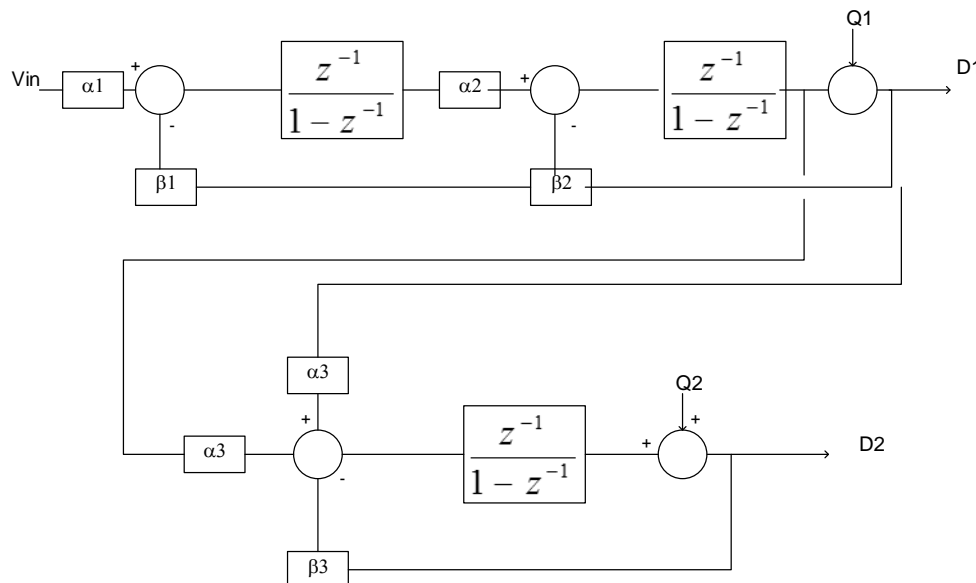
IP DESCRIPTION

Silicon Vision SiVi-SDADC1202 offers a high performance and cost-effective solution for ADCs that can be used monitoring analogue voltages. The IP uses a sampling clock of 32MHz to give a throughput of 128Ksps with a 12 bit resolution. Silicon Vision SiVi-SDADC1202 consumes less than 5.5mA at maximum amplitude that can go up to 6.6Vpp differential

SiVi-SDADC1202 is implemented and silicon verified on TSMC 180nm LP

APPLICATIONS

- Analog Front End Signal reception
- Monitoring Analogue Voltages or Currents from External Sensing Devices



Block Diagram for SiVi-SDADC1202



Silicon Vision Tech. Ltd.
14 Manchester Square, London
W1U 3PP, United Kingdom
info@si-vision.com