

SiVi-13AD205B

TSMC 90nm-G (1P5M)



MAIN FEATURES

- Designed on TSMC 90nm-G
- 13-bit, 205 MS/S
- Integrated S/H circuit and reference generator
- Integrated high linearity input buffer
- 1.0 V_{p-p} differential input range
- Low Power Consumption 65 mW
- Power down mode and Automatic fast startup
- DNL = +/- 1 LSB , INL = +/- 1 LSB
- Operational temperature range from -40°C to 125°C

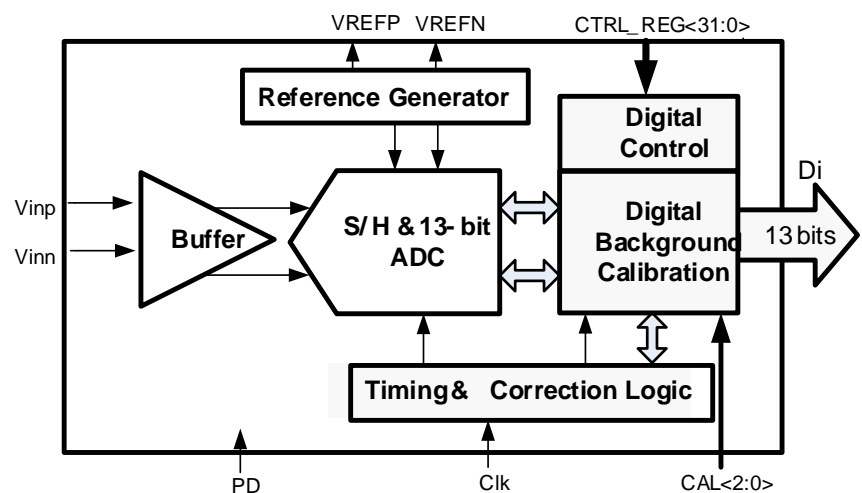
IP DESCRIPTION

Silicon Vision SiVi-13AD205B is a high accuracy, high speed Analog to Digital Converter (ADC) IP core that offers 13-bit accuracy at a sampling rate of up to 205 MS/s. An internal digital background calibration is included to ensure superior linearity to reduce startup calibration significantly. It includes high performance S/H circuit, digital error correction, reference generator and wide band high linearity input buffer

SiVi-13AD205B is silicon proven in 90nm TSMC-G process technologies.

APPLICATIONS

- Analog front-end for wireless communications such as Wi-Fi 802.11a/b/g, Wi-Fi 802.11n and WiMAX 802.16x
- Analog front-end for wire bond communication
- Analog front-end for flat panel display and HDTV
- Base station receivers



Block Diagram for SiVi-13AD205B



Silicon Vision
79 Almultaqa Alaraby,
Cairo, Egypt, P.O. Box 11361
Tel: +202-22669160
info@si-vision.com