

SIVI-SV18003

TSMC/SMIC 180nm (1P5M)



MAIN FEATURES

- Designed on SMIC/TSMC 180nm
- Fully integrated SiF Audio decoder
- 36Msps conversion rate with 10-bit ADCs
- On-Chip programmable gain amplifier
- Low Power Consumption 60mW
- Operational temperature range from -40°C to 125°C

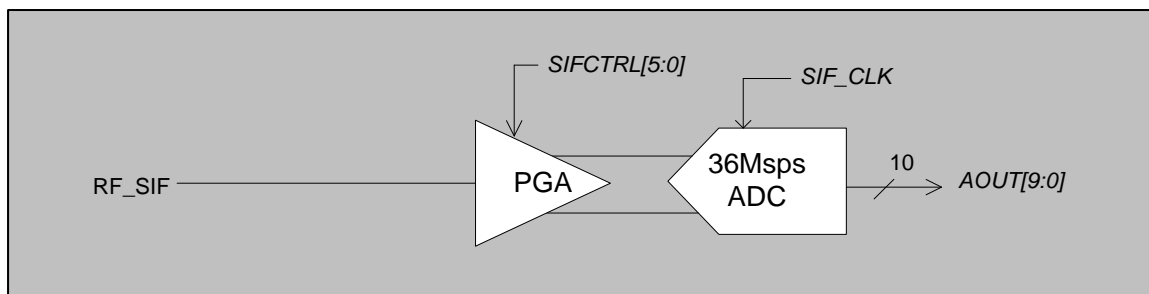
IP DESCRIPTION

Silicon Vision SiVi-SV18003 is a fully integrated SiF audio analog front end decoder. The chip composes a single channel 10-bit 36Msps ADC with a programmable gain amplifier in front of it for RF SiF audio signal digitization.

SiVi-SV18003 is silicon proven in 180nm TSMC and SMIC process technologies

APPLICATIONS

- Audio panels
- LCDTV/HDTV
- Portable and Handheld TVs



Block Diagram for SiVi-SV18003



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