SIVI-PADC1003

SMIC 130nm-G (1P5M)

MAIN FEATURES

- Designed on SMIC 130nm-G
- 10-bit, 50 MS/S
- Integrated S/H circuit and reference generator
- 1.2 Vp-p differential input range
- Low current consumption 34mA
- Architecture is based on opamp sharing for optimal power usage
- Power down mode and Automatic fast startup
- DNL = +/- 1 LSB , INL = +/- 1 LSB
- Operational temperature range from -40°C to 125°C

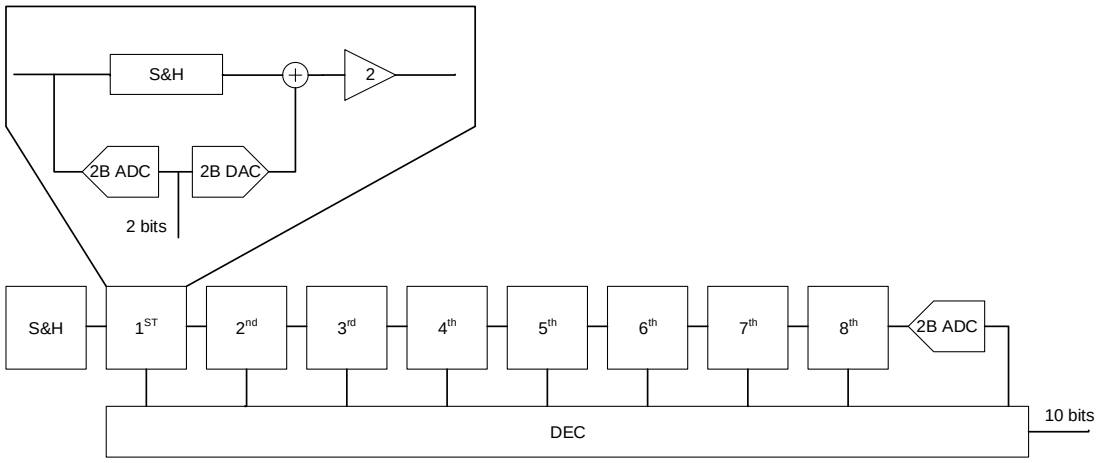
IP DESCRIPTION

Silicon Vision SiVi-PADC1003 is a high accuracy, high speed Analog to Digital Converter (ADC) IP core that offers 10-bit accuracy at a sampling rate of up to 50 MS/s. This IP is characterized by its low power consumption and small silicon area as it makes use of OPAMP sharing technique.

SiVi-PADC1003 is silicon proven in 130nm SMIC-G process technologies.

APPLICATIONS

- Video Analog Front Ends
- Analog front-end for wire bond communication
- Analog front-end for flat panel display and HDTV
- Base station receivers



Block Diagram for SIVI-PADC1003

Silicon Vision Tech. Ltd.
14 Manchester Square, London
W1U 3PP, United Kingdom
info@si-vision.com