SIVI-SAADC1301

TSMC 180nm-G (1P5M)

MAIN FEATURES

- Designed on TSMC 180nm-G
- 13-bit, 2 MS/S SAR ADC
- 4.0 Vp-p differential input range
- Built in voltage attenuator
- Low current consumption 4.5mA
- Supply Voltage 1.9V
- DNL = +/- 0.5 LSB , INL = +/- 0.5 LSB
- Operational temperature range from -40°C to 125°C

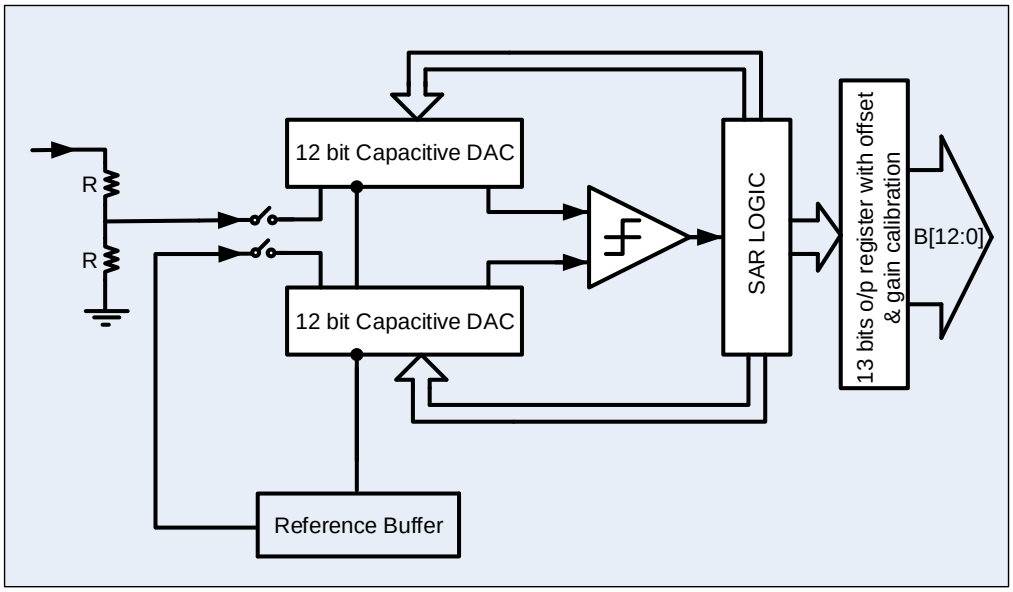
IP DESCRIPTION

Silicon Vision SiVi-SAADC1301 is a high accuracy, low power SAR Analog to Digital Converter (ADC) IP core that offers 13-bit accuracy at a sampling rate of up to 2.0 MS/s. This IP is characterized by its low power consumption and small silicon area which make it very attractive to state of the art SoCs. An integrated voltage attenuator is used to increase the input dynamic range up to 4.0Vpp differential.

SiVi-SAADC1301 is silicon proven in 180nm TSMC-G process technologies.

APPLICATIONS

- On Chip Temperature sensing
- Battery Monitoring
- Supply Monitoring
- Buck Controllers



Block Diagram for SiVi-SAADC1301

Silicon Vision
79 Almultaqa Alaraby,
Cairo, Egypt, P.O. Box 11361
Tel: +202-22669160
info@si-vision.com