

SIVI-LDO1802

TSMC 180nm-G (1P5M)

MAIN FEATURES

- Designed on TSMC 180nm Generic Process
- Supply a load current up to 2mA
- Input voltage ranges from 2.0V to 3.6V
- Output Voltage is 1.8V
- Ultra-Low Quiescent current
- Operating Temperature -40C° to 125C°

IP DESCRIPTION

SiVi-LDO1802 is a voltage regulator with ultra-low current consumption profile. It is suitable for providing regulated supplies in low power applications and SoCs. The regulator is fully integrated and does not need any external component. It can supply current up to 2mA.

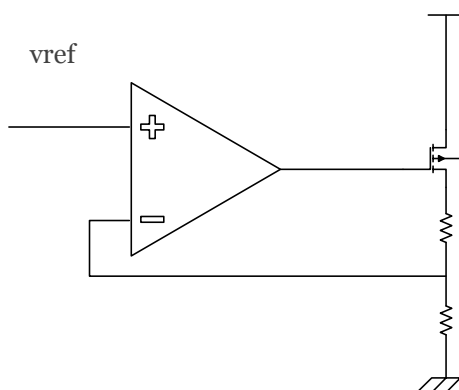
SiVi-LDO1802 is silicon proven on TSMC – 180nm Generic technology.

ELECTRICAL SPECIFICATIONS

Spec / Result		Min	Typ	Max	Unit
Supply Voltage		2		3.6	V
Temperature Range		-40	27	125	°C
Output Voltage			1.8		V
Output Voltage Accuracy		-1		1	%
Load Current				2.0	mA
PSRR	@1kHz		-35		dB
	@1MHz		-10	-7.0	
Quiescent Current				0.5	uA
Spot noise @100kHz				100	nV/√Hz

PIN DESCRIPTION

Pin Name	Direction	Description
vdd	Input	Analog supply rail
vss	Input	Analog ground rail
LDO_pd	Input	Power down signal for the LDO
vout	Output	Output voltage for the LDO



SiVi-LDO1802 Block Diagram