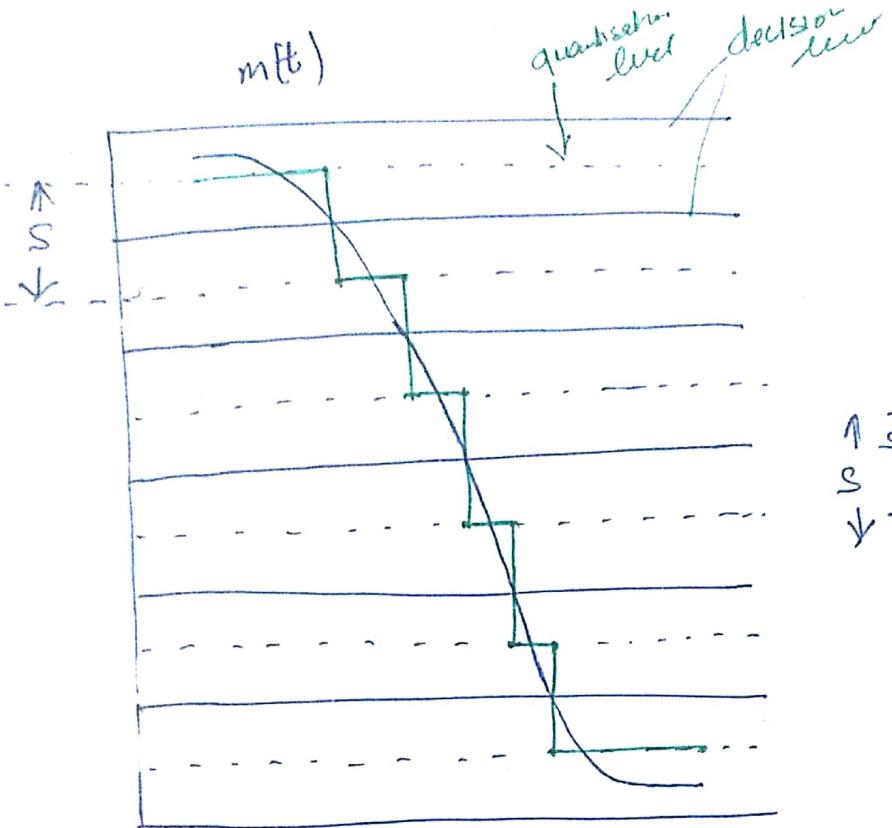


to counter balance the distortion produced by pulse stretcher network

Quantisation

- it is the process which makes a digital communication system to have better noise performance than analog communication system.
- in the process of quantisation, a sig $m_q(t)$ which is an approx. to $m(t)$ is generated.

consider a baseband sig $m(t)$ having b/w lower limit V_L and upper limit V_H . Now the baseband sig is applied to a quantiser which gives a staircase waveform $m_q(t)$. The $m_q(t)$ is having an advantage that it is free from additive noise. The baseband sig $m(t)$ is divided into M no. of quant'sation levels and separation b/w two successive levels is step size S .



Max quantization error
 $\pm S/2$

total sig range = $V_H - V_L$
 No of quantisation levels M
 step size = S

$$MS = V_H - V_L$$

$$M = \frac{V_H - V_L}{S}$$

Quantisation error

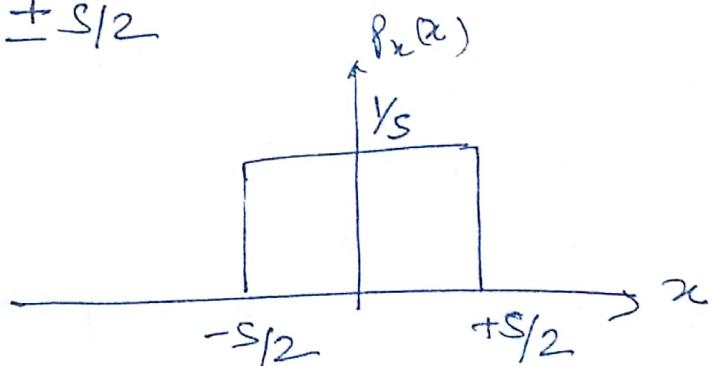
from the quantiser response, it is clear that at any instance of time, $m(t)$ and $m_q(t)$ are not exactly equal.

the difference b/w $m(t)$ & $m_q(t)$ is called as quantisation error

at the max. quantisation error can be $\pm \frac{S}{2}$

Quantisation noise Power

quantisation noise is uniformly distributed b/w $\pm \frac{S}{2}$



$$P_{nq} = E[e_{qf}^2(t)] = \int_{-\infty}^{\infty} x^2 P_x(x) dx$$

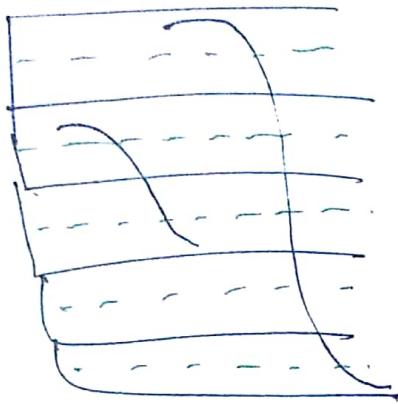
$$= \frac{1}{S} \int_{-S/2}^{S/2} x^2 dx$$

$$= \frac{1}{S} \left(\frac{2x^3}{3} \right) \Big|_{-S/2}^{S/2} = \frac{1}{3S} \left(\frac{S^3}{8} + \frac{S^3}{8} \right)$$

$$= \frac{2S^2}{24}$$

$$\boxed{P_{nq} = \frac{S^2}{12}}$$

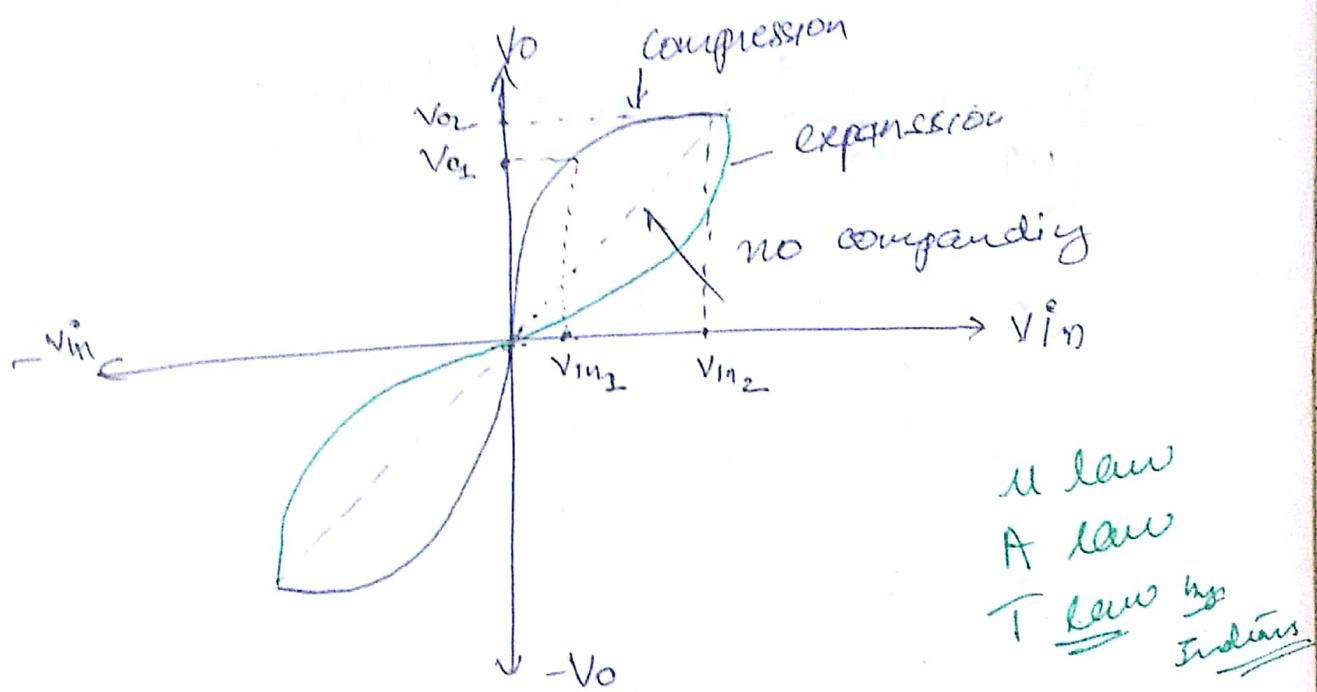
Companding



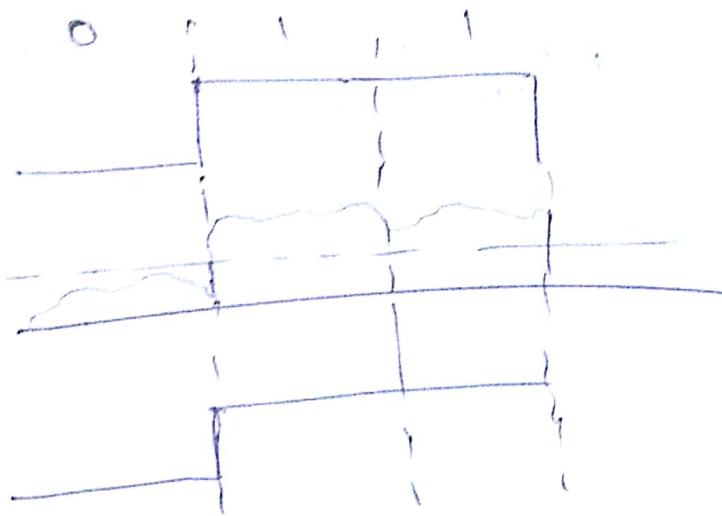
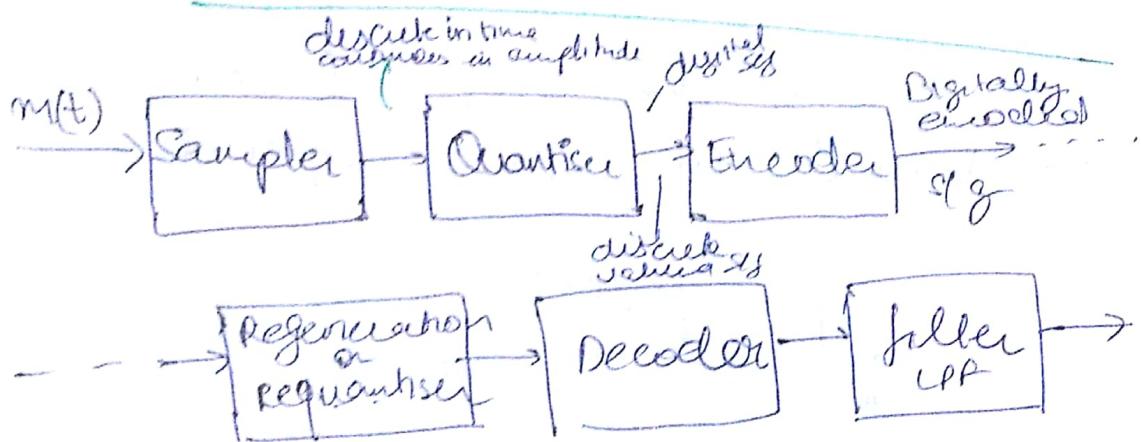
- the quantisation error is dependent on step size. Considering s/g to Quantisation noise ration (S/N_Q) when uniform quantiser is employed, lower amplitude s/gs will have a poorer S/N_Q
- when compared to larger amplitude s/g this is due to the fact that denominator of S/N_Q remains same for lower as well as for larger amplitude s/g
- but the numerator is low for low amplitude s/g.
So to have an uniform S/N_Q , the stepsize is to be adjusted in accordance with s/g that is step size should be small for small amplitude and large for large amplitude

To perform this the sig is passed through
a non linear func called compressor

At the Rxng end the original level of
sig is maintained using the expander



Pulse Code Modulation



Working

→ the modulating sig $m(t)$ is first passed through a sampler to convert the sig in discrete in time and continuous in valued discrete in time and continuous in valued sig. The quantizer converts it into discrete in time and discrete in valued sig (digital sig).

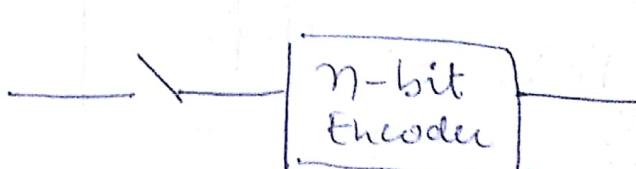
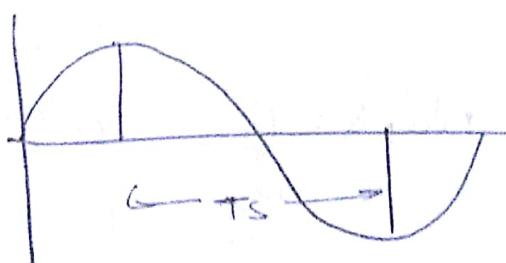
- The encoder assigns N no. of bits to each sample and finally digitally encoded PCM sig is transmitted over channel.
- at the receiving end, the sig is passed through Regenerator or Regenerator which removes the additive noise present in sig. and generates fresh digital pulses. The decoder converts the digitally-encoded sig into corresponding analog value and LPF is used to reconstruct the original sig.

Band width of PCM sig

00
 01
 10
 11

$$2^n \geq M$$

M - no of levels



$T_{sec} \rightarrow n$ bits

$$1 \text{ sec} \longleftrightarrow \frac{n}{T_s} \text{ bits/sec}$$

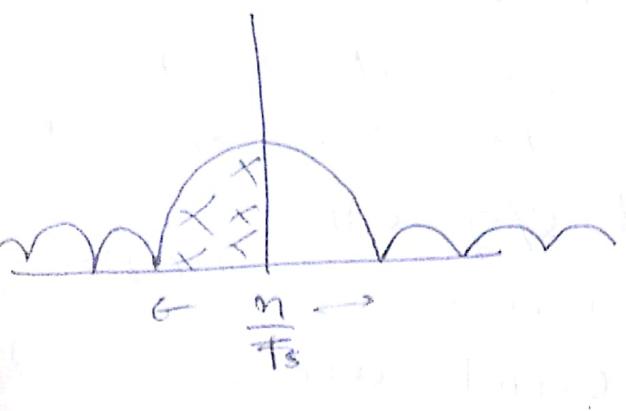
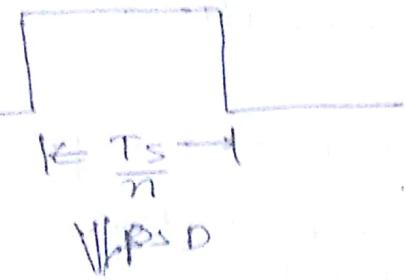
$$R_b = n f_s \text{ bits/sec}$$

Bit rate | Data rate | ~~sig~~ Rate | Pulse rate

N bits $\rightarrow T_s \text{ sec}$

Max pulse width = $T_s \text{ sec}$

Signal to Quantization
Noise Ratio in PAM



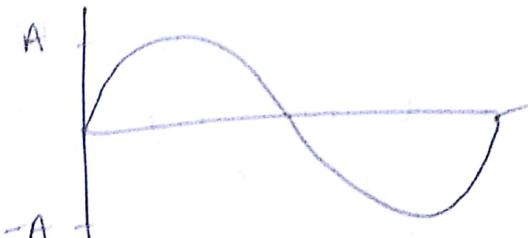
$$(\text{BW})_{\text{PCM}} \geq \frac{n}{2T_s}$$

$$(\text{BW})_{\text{PSK}} \geq \frac{nfs}{2}$$

$$\text{BW}_{\text{min}} = \frac{nfs}{2}$$

$$\boxed{\text{BW}_{\text{min}} = n \cdot f_m}$$

→ consider a sinusoidal modulating sig $m(t)$ varying b/w $\pm A$



$$P_s = \frac{A^2}{2}$$

$$V_H - V_L \approx 2A$$

$$MS = V_H - V_L$$

$$S = \frac{2A}{n}$$

$$P_{\text{NG}} = \frac{S^2}{12} = \frac{(2A)^2}{12} \\ = \frac{4A^2}{12n^2}$$

$$S/N_{\text{eqR}} = \frac{P_s}{P_{\text{NG}}} = \frac{A^2}{\frac{2}{12n^2}}$$

$$= \frac{3}{2} n^2$$

$$M = 2^{\frac{n}{2}}$$

$$S/N_{\text{eqR}} = \frac{3}{2} 2^{2n}$$

$$(SN_q R)_{dB} = 10 \log_{10} \frac{S}{N} + P_0$$

$$10 \log_{10} 2^n$$

Differential Pulse C.M

disadvantage of PCM

① as the no of bits increases the system performance improves but at the same time system complexity also increases

② since BW is directly proportional to no of bits so it also increases proportionally.

$$(SN_q R)_{dB} = 1.761 + 602n \text{ dB}$$

$n \rightarrow$ no of bits

$$(SN_q R)_{dB} \approx 1.8 + 6n \text{ dB}$$

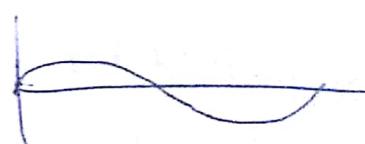
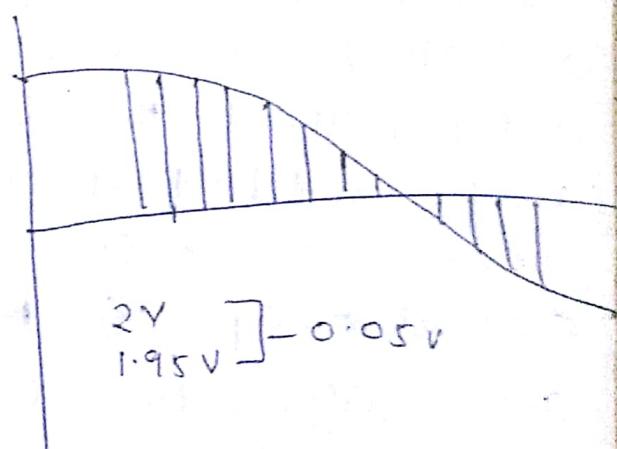
if n is high

$$(SN_q R) \approx 6n \text{ dB}$$

$$\begin{cases} n=6; SN_q R = 37.8 \text{ dB} \\ n=7; SN_q R = 43.8 \text{ dB} \\ n=8; SN_q R = 49.8 \text{ dB} \end{cases}$$

[in PCM system, for each bit increase, the $SN_q R$ increases by 6 dB]

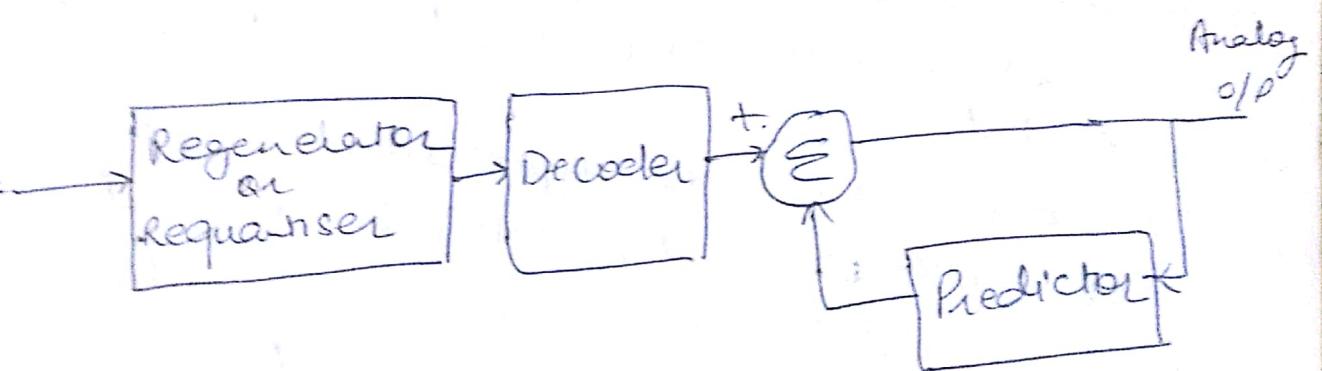
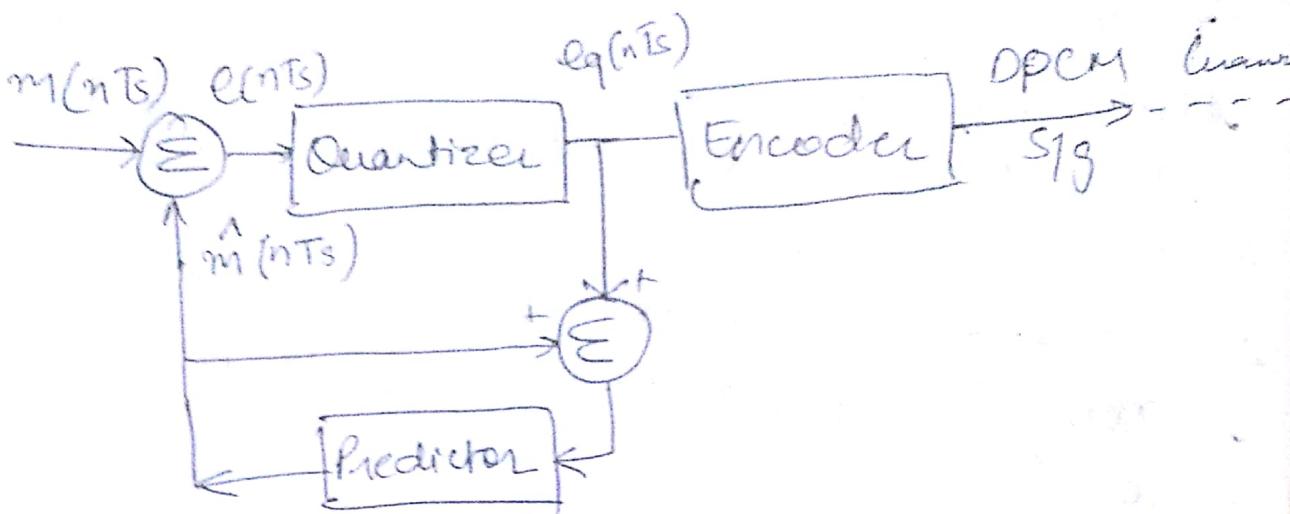
$SN_q R \Rightarrow$ dynamic range



When a sig is sampled at a rate higher than the Nyquist rate, the resulting sig is found to exhibit a high correlation b/w adjacent samples i.e. the sig does not change rapidly from one sample to next, so instead of transmitting samples at each sampling instant difference b/w sample and its predictive value is transmitted. The difference can be added at the receiver to get the original sample sig (att.). Such differential scheme has an advantage when being transmitted by using PCM that the sig formed by all these above differences will be confined to smaller voltage range than the original sig. Hence no. of quantization levels required will be less and fewer bits will be needed to encode the message.

Generation and Detection of DPCM sig

- the sampled i/p & its predicted value is feed to an adder which generates the difference b/w actual sample value and its predictive value.
- The difference value is quantised, encoded and transmitted as DPCM sig.
- the quantised value is feed to an adder to which another i/p is predicted value. The adder generates the sum of quantised value and predicted value.



and is given as i/p to predictor, so that it can predict next sample value
 → at the Rxing end the DPCM sig is passed through regenerator which removes the additive noise from the sig and generates fresh pulses. The decoder ckt converts digitally encoded sig into corresponding analog value.

Delta Modulation

This analog value is added with the o/p of receiving & predictor to generate the analog o/p

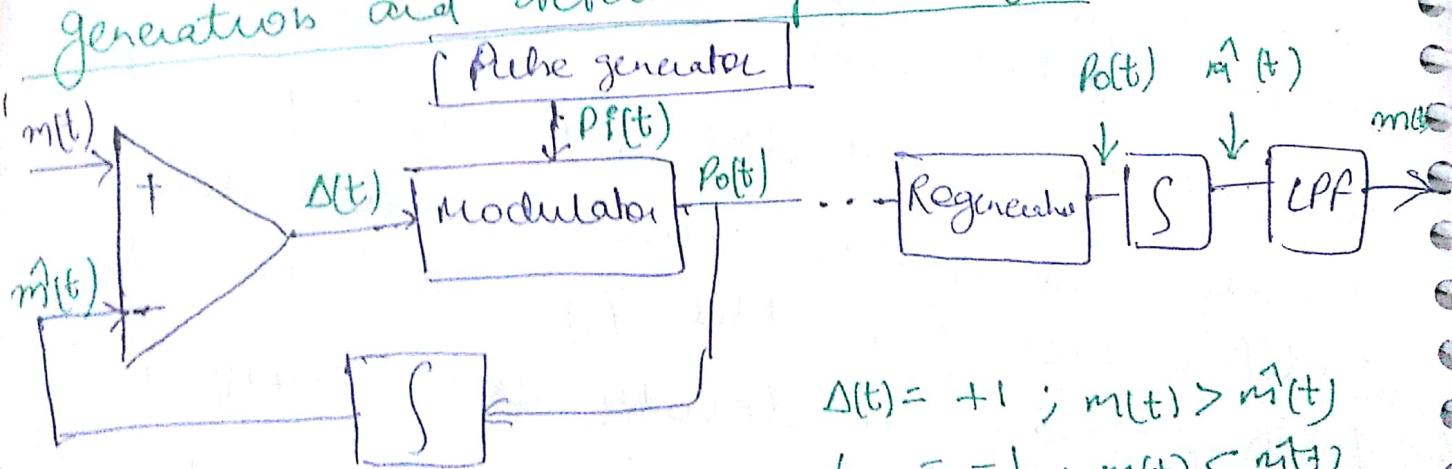
The Receiving & Predictor predicts the same as Txing end predictor if ip to both is same

Delta Modulation

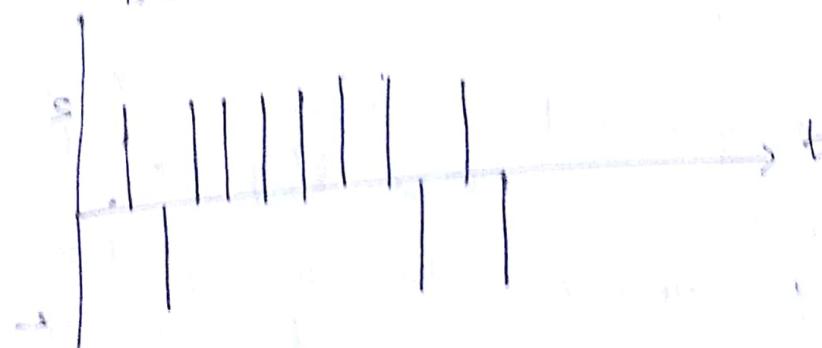
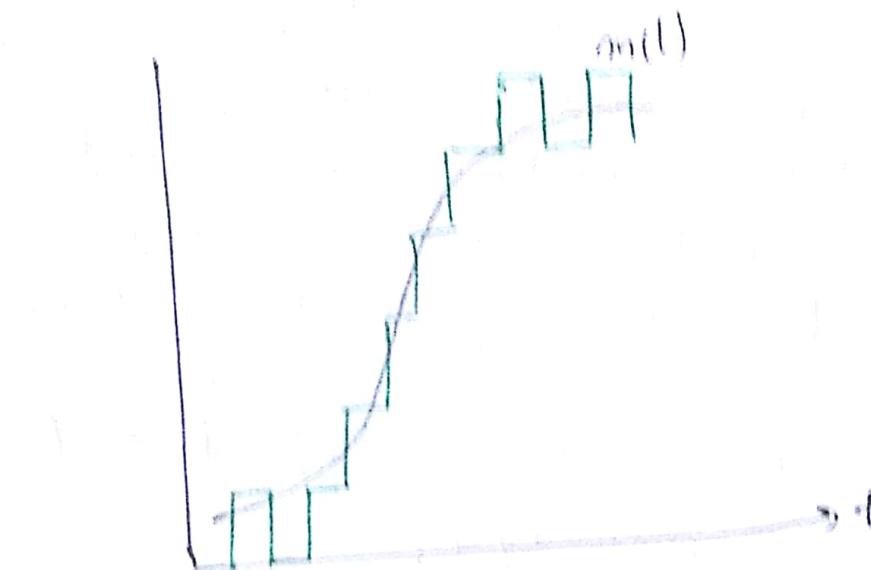
Delta Mod. is a oversampled modulation scheme in which the txed sig is encoded in one bit

In DM, the staircase approximation of original modulating sig $m(t)$ is generated which is passed through a low pass filter to reconstruct the original sig back

Generation and detection of DM sig



$$\begin{aligned}\Delta(t) &= +1 ; m(t) > \hat{m}(t) \\ &= -1 ; m(t) < \hat{m}(t) \\ &= \text{Random} ; m(t) = \hat{m}(t)\end{aligned}$$



Working
the modulating sig $m(t)$ and its approximation value $m(t)$ are compared in an opamp
comparator which generates

$$\Delta t = +1 \quad m(t) > m(t)$$

$$-1 \quad m(t) < m(t)$$

random $m(t) = m(t)$

② the modulator circuit multiplies the O/P of comparator with O/P of pulse generator. $P_i(t)$ to generate O/P impulses $P_o(t)$, the polarity of $P_o(t)$ is dependent on polarity of $\Delta(t)$

③ the $P_o(t)$ impulses are encoded and transmitted as DM sig.

Simultaneously these impulses are integrated to generate the approx. sig $m(t)$.

④ At Rx
at Rxing end, regenerator removes the additive noise from the sig and generates fresh pulses these pulses are decoded to generate $P_o(t)$ at receiving end.

⑤ $P_o(t)$ is again integrated to regenerate approx sig $m(t)$ at Rxing end and finally it is passed through a LPF to convert it into a smooth sig.

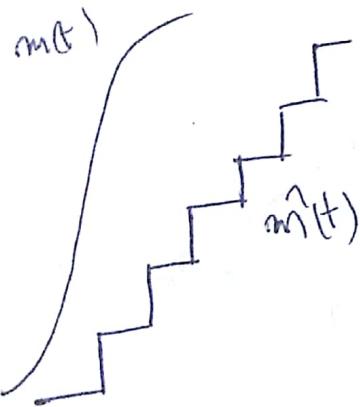
types of error

There are two types of errors present in D/A system.

① Slope overload error

The error produced due to steep slope of I/P sig as compared to slope of approx s/g is known as slope overload error

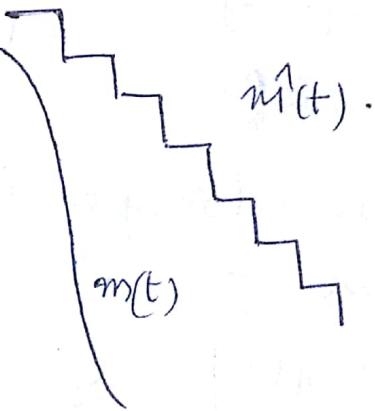
There are two types of slope overload error



+ve slope overload

① error

The error produced due to the steep slope of I/P sig is known as +ve slope overload error



-ve slope overload

② error

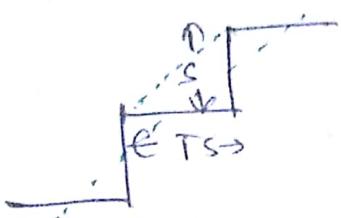
The error produced due to -ve steep slope of I/P sig is known as -ve slope overload error

for a sinusoidal sig

$$u(t) = A \sin(\omega t)$$

$$\frac{du(t)}{dt} = A \omega \cos(\omega t)$$

$$\left. \frac{du(t)}{dt} \right|_{\text{max}} = A \omega m$$



$$\text{slope of } \hat{u}(t) = \frac{S}{T_s}$$
$$= S f_s$$

To avoid slope overload error :-

$$\left. \frac{du(t)}{dt} \right|_{\text{max}} \leq S f_s$$

$$A \omega m \leq S f_s$$

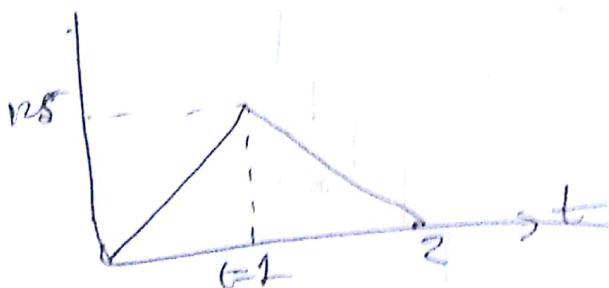
$$A \leq \frac{S f_s}{2 \pi f_m}$$

$$S \geq \frac{A \omega m}{f_s}$$

Consider a modulation with step size S and sampling freq f_s .
Due to this we get the DFT as
 $\hat{u}(t) = 125t(U(t) - U(t-1))$
 $+ (250 - 125t)(U(t-1) - U(t))$

calculate min step size to avoid slope overload error

$$\frac{du(t)}{dt} \leq S f_s$$



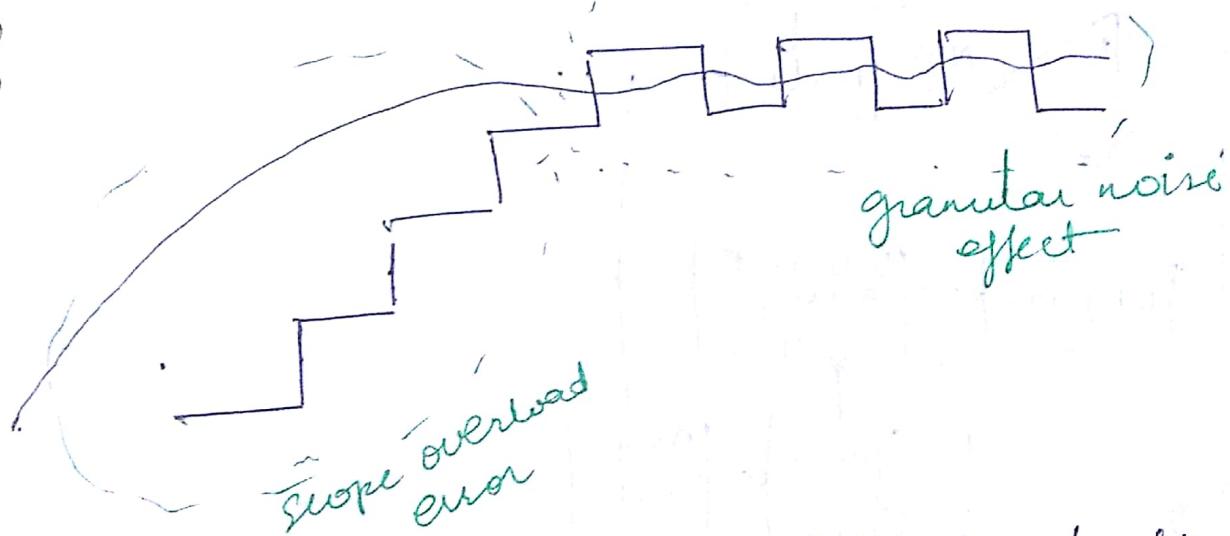
$$m = 125$$

$$S_{\min} = \frac{125}{32 \times 10^3}$$

$$= 2^{-8}$$

② Granular Noise

when step size S is too large relative to the local slope characteristics of its waveform $m(t)$ thereby causing $\hat{m}(t)$ to oscillate around a relatively flat segment of input waveform. This phenomenon is known as granular noise.

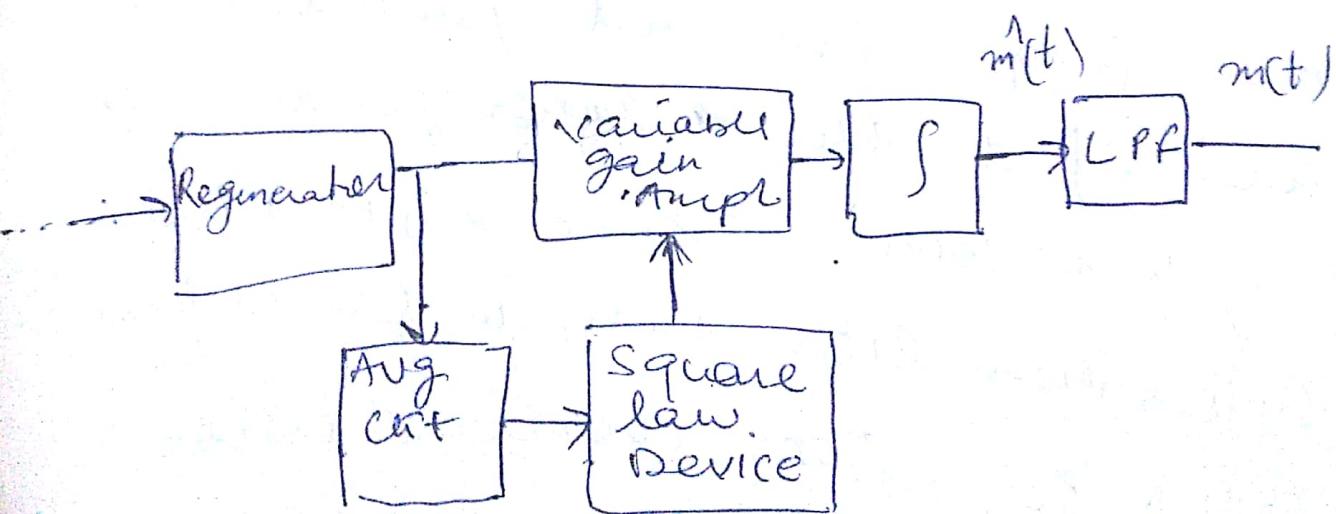
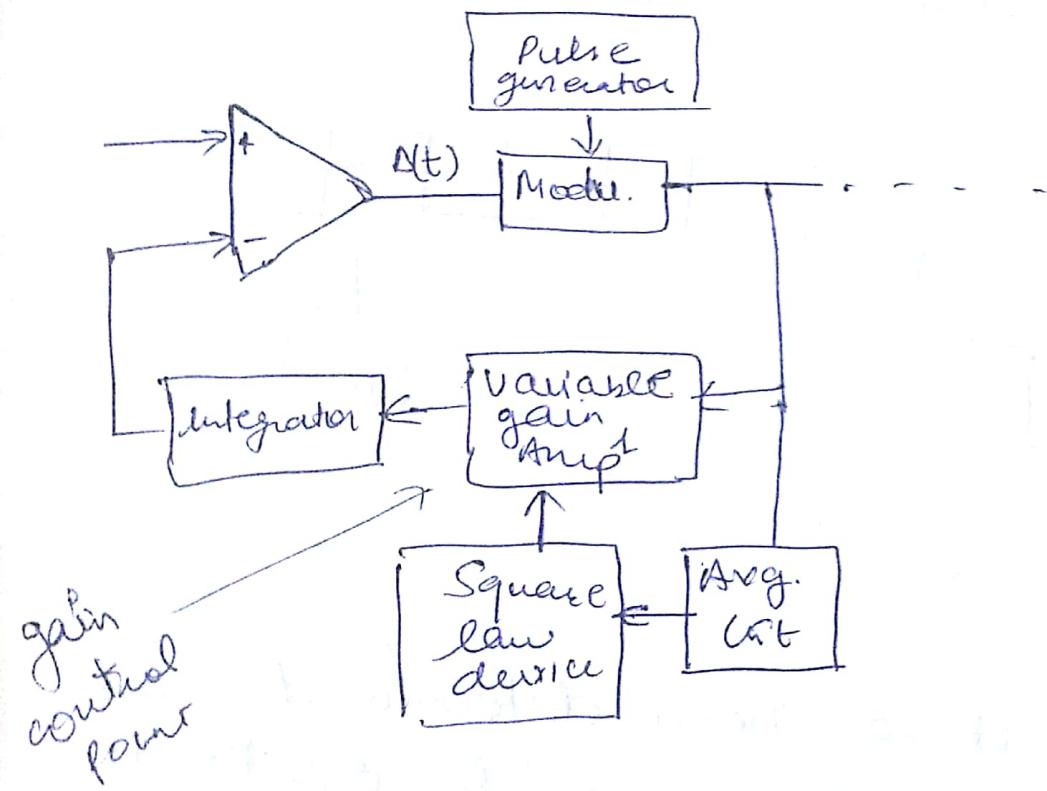


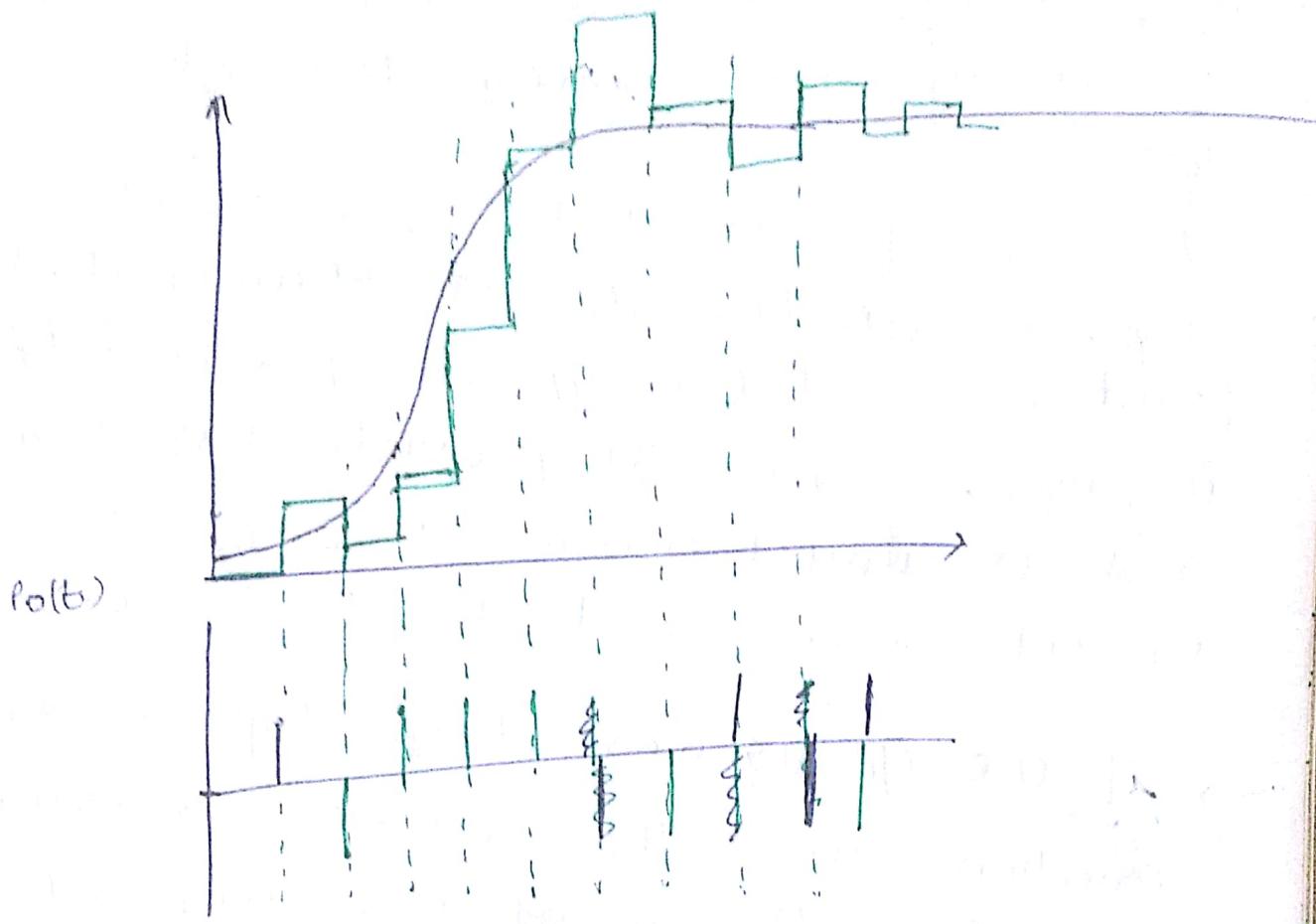
Practically it has been determined that slope overload error mainly affects the lower freq. region and granular noise error affects the higher frequency region of sig waveform. Since most of the information lies only in lower freq. region so slope overload error is more objectionable.

Adaptive delta modulation

ADM

The basic principle of ADM system is to vary the step size in accordance with slope of input sig i.e. when the slope of ip sig is steep, the step size must be increase and if ip sig slope is small then the step size must be reduced.





Working

→ The assembly of averaging circuit, square law and variable gain Amp are used to vary the step size of approximation sig. $m(t)$ according to the slope of input sig. When the i/p to averaging circuit is either continuously +ve or continuously -ve impulse train then its o/p will be +ve & -ve high resp. If the i/p is alternately +ve & -ve impulse. Then its o/p will be zero.

→ the o/p of square law device is always +ve irrespective of i/p.

the o/p of variable gain amp depends on the i/p at gain control point. If the i/p at gain control pt is high, amp will provide high gain and it almost blocks the sig if i/p at gain control pt is zero

→ if the i/p sig has steep slope in +ve direction then pdt will be continuously +ve hence the o/p of averaging crt will be +ve high and amp will provide high gain. So as to minimize +ve slope overload error

→ when i/p sig has steep slope in -ve direction then pdt will be continuously -ve hence o/p of averaging crt will be high in negative direction and amplifier will increase the step size in negative direction to avoid negative slope overload error.

→ when the slope of input sig is almost const. the pdt will be alternately +ve and -ve impulses hence off of averaging out is zero and output will almost block the incoming sig thereby reducing step size

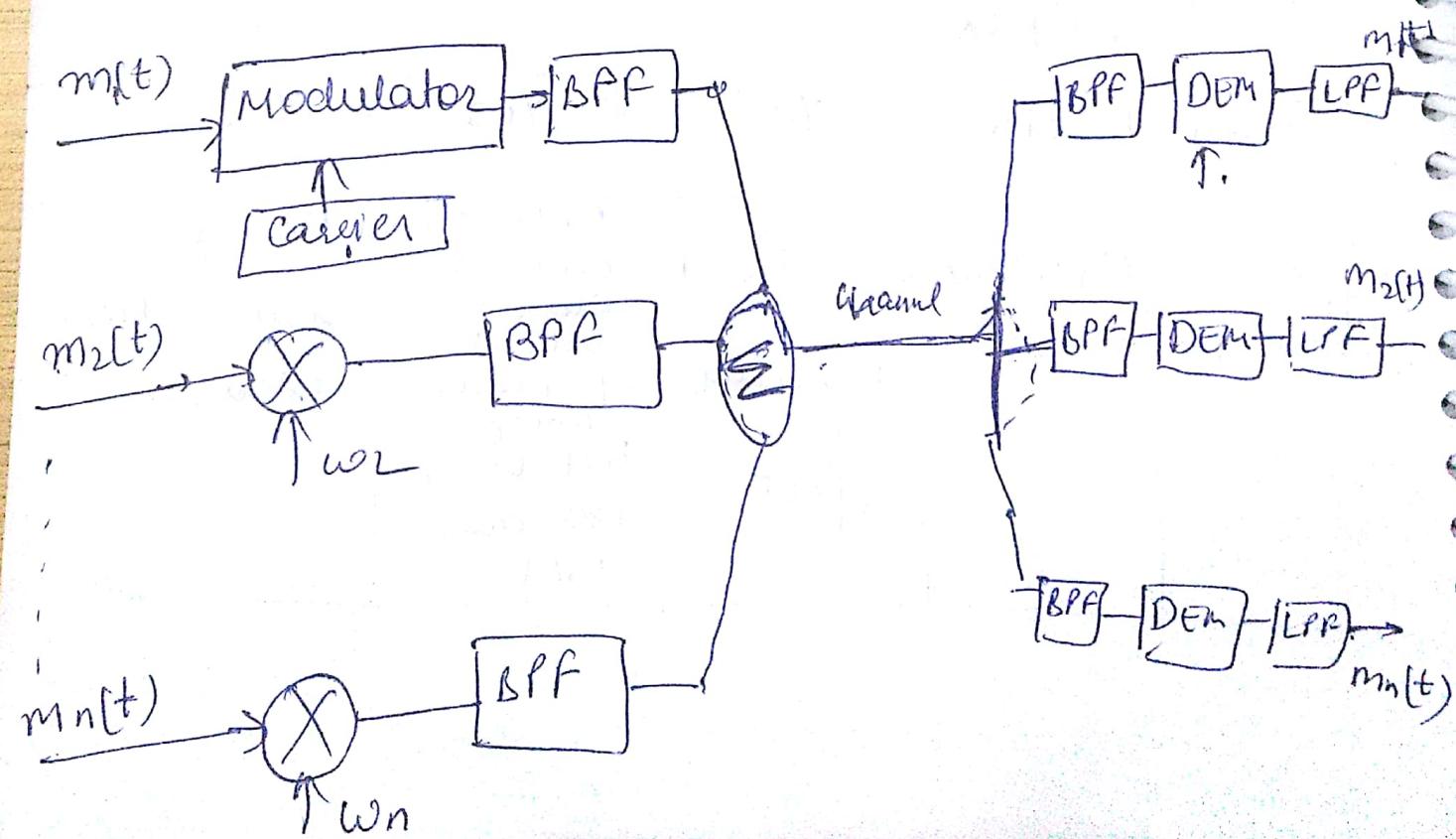
Parameter	PCM	DM	ADM	DPCM
No of bits/ sample	8 bit/sample 12 bit/sample 16 bits/sample	1 bit/sample	1 bit	greater than 1 but less than that require in PCM
Step size	Fixed	Fixed	Variable	fixed
Sampling Rate	Slightly greater than twice of fm	oversampled	oversampled	oversampled
Complexity	highly complex	simplest	simplest	More complex than DM & ADM but less complex than PCM
Feedback	No	Yes	Yes	Yes
types of error	Quantisation error	slope overload & granular noise effect	Both slope overload error as well as granular noise but very less than DM	Mainly Quantisation error

Multiplexing

- Transmission of more than one sig simultaneously over same channel is known as Multiplexing
- multiplexing requires that s/g's must be kept apart so that they do not interfere each other and hence can be separated at the receiving end. This is accomplished by separating the s/g's either in frequency or in time.
- there are two types of multiplexing scheme

① Frequency division Multiplexing

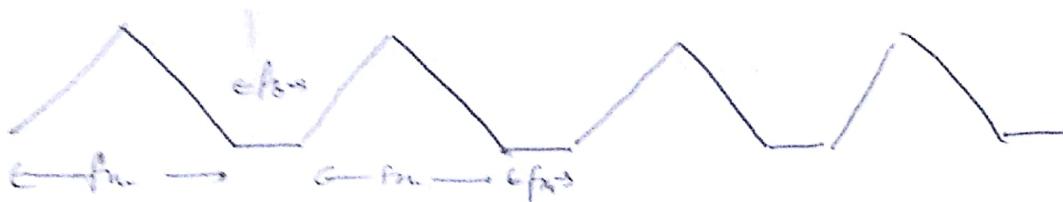
- the technique of separating s/g's in frequency is known as FDM



BW of FDM Sig



$$BW = n f_m$$

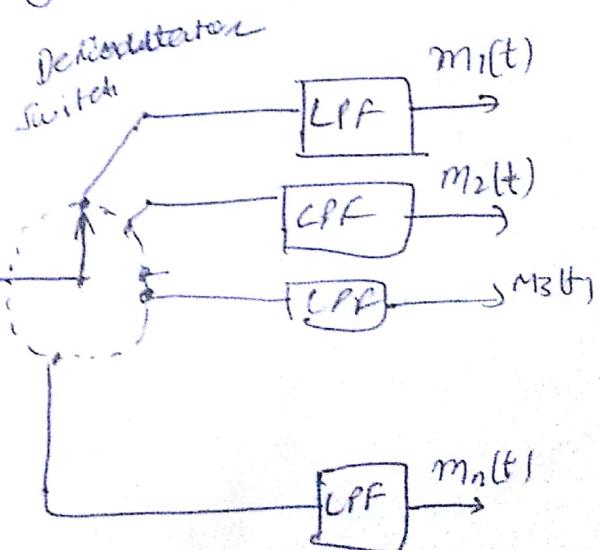
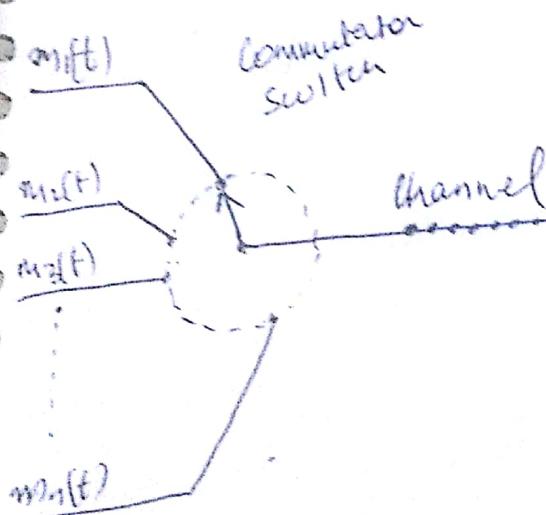


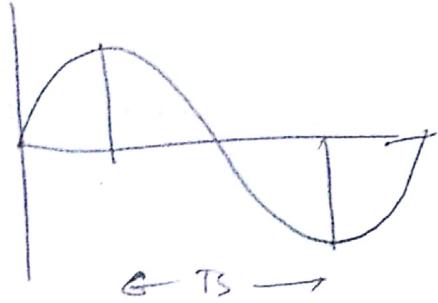
guard bands \rightarrow f_{GB}

$$BW = n f_m + (n-1) f_{GB}$$

time division multiplexing

As no of messages to be transmitted increases the FDM technique presents problem. No of subchannels needed increases and additional circuitry is required both at Txing and Rxing end to handle each added channel these problems are eliminated to a great extend by using TDM



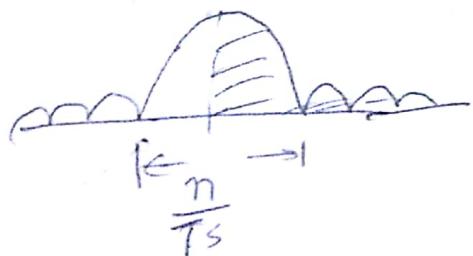
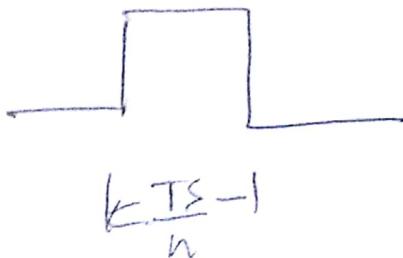


$TS \text{ sec} \longrightarrow n \text{ samples}$

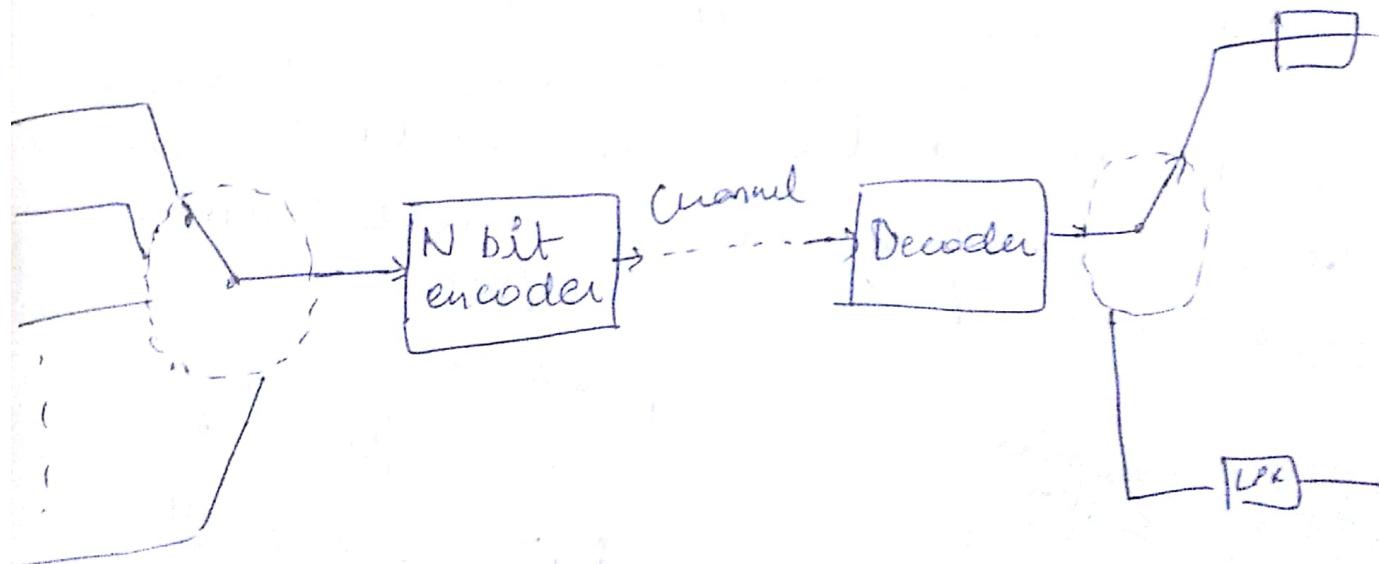
$1 \text{ sec} \longrightarrow \frac{n}{TS} \text{ samples}$

Samples Rate = nfs samples/sec

Max Sample width = $\frac{TS}{n} \text{ sec}$



$$\boxed{BW \geq \frac{nfs}{2}}$$



1 sample \rightarrow N bit

n samples \rightarrow , nN bits

Tsec. \rightarrow nN bits

1 sec \rightarrow $\frac{nN}{T_s}$ bits/sec

Bit rate = nNfs bits/sec

max pulse width $\rightarrow \frac{T_s}{nN}$

$$= \frac{1}{nNfs} \text{ sec}$$

$$\boxed{\text{BW} \geq \frac{nNfs}{2}}$$

Baseband Signal Receiver

