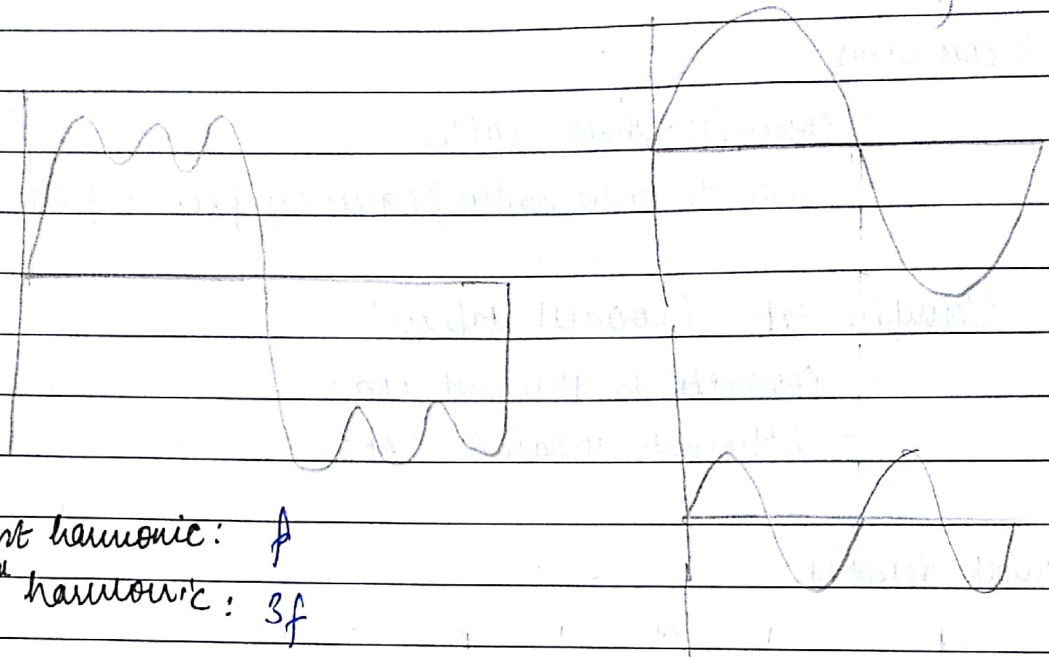


Lecture 4
24/01/2018

- Traffic filtering
- LANS connects to LANS & WANS together
- Dynamically updating routing table

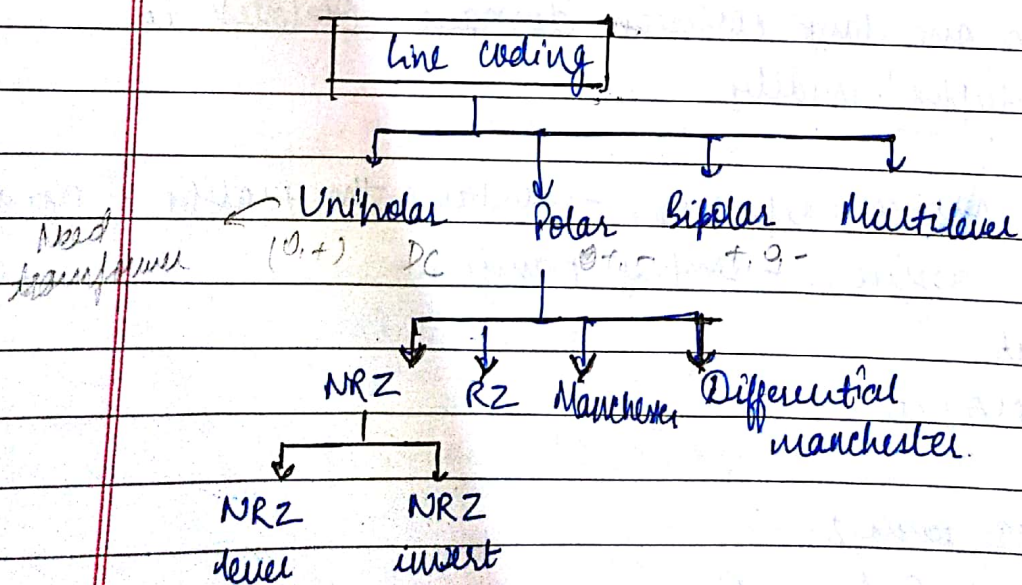


1st harmonic: f
3rd harmonic: $3f$

1st and 3rd harmonic: $f + 3f$

→ superposition

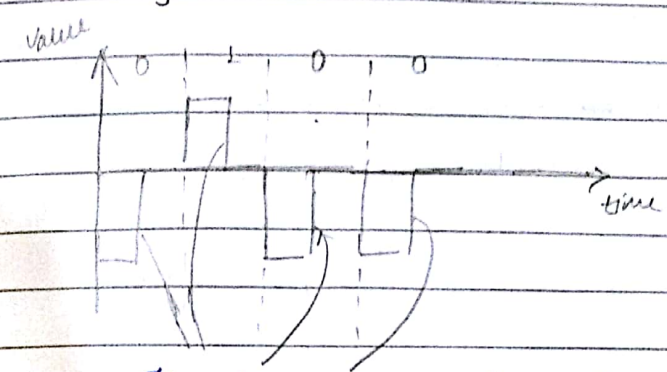
- Coding scheme types: → to remove noise or provide error detection.
- Line coding - bit by bit
 - Block coding - block by block



sender & receiver clocks are not synchronized

NRZ-L The level of voltage determines the value of bit
 NRZ-I: Inversion or the lack of inversion determines the value of bit.

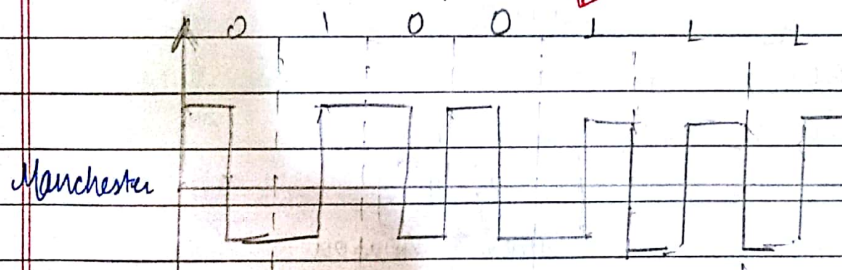
RZ encoding :-



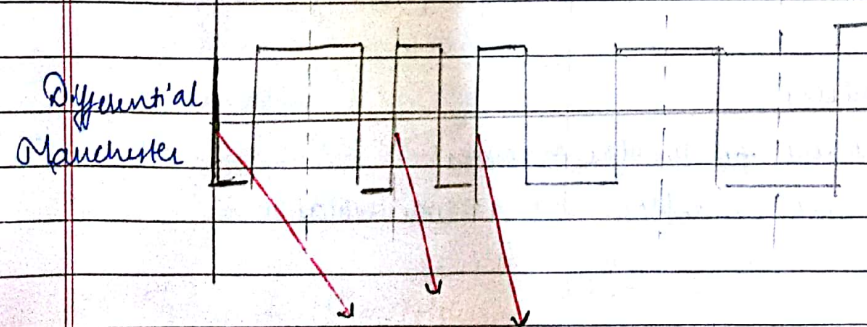
These transitions can be used for synchronization.

- Uses three values : 0, -ve, +ve
- doesn't change b/w the bits but during.

Manchester and differential Manchester

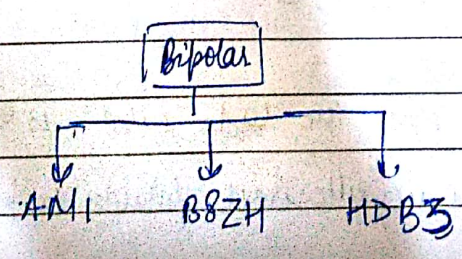


→ During of a bit is divided into two halves.
 The transition at middle provides synch.

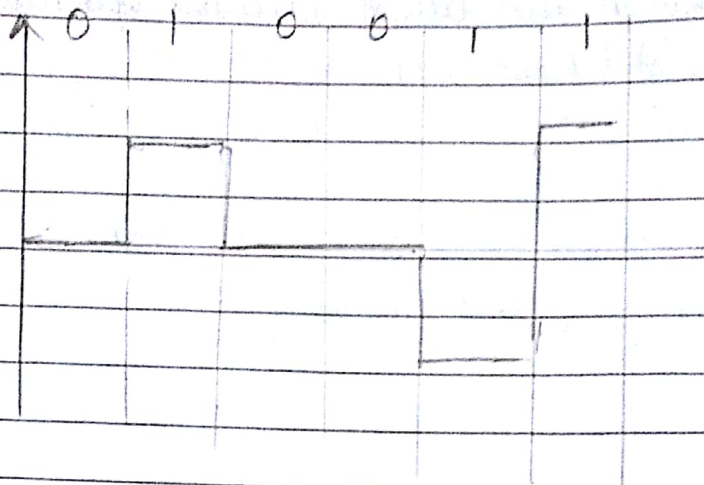


→ There is always transition at the middle of the bit but value is determined is at the beginning of the bit

Presence of transition at the beginning of the bit time means zero



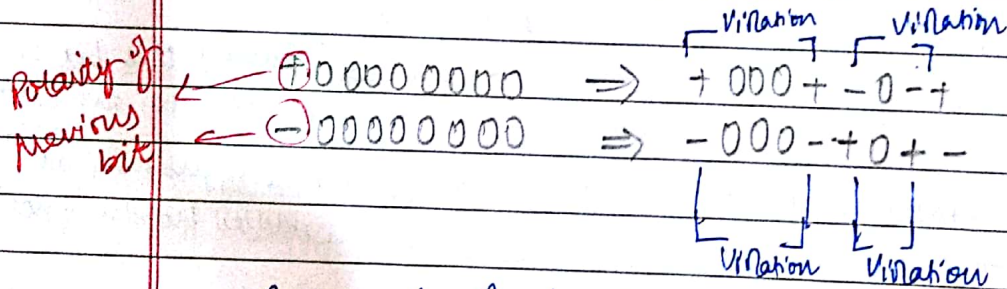
Bipolar AMI encoding



- Alternate voltage inversion
- 0 \rightarrow neutral
- 1 \rightarrow change from non-negative to negative or non-positive to positive.

B8ZS encoding

- Bipolar with 8-Zero substitution



- Pattern of violation is same, just polarity changes.

HDB3 encoding

- High density ~~for~~ bipolar 3 zeros.
- Fourth zero is replaced with a code violation.

$$+0000 \Rightarrow +000+$$

$$-0000 \Rightarrow -000-$$

If the no. of 1s since the last substⁿ is odd

$$+0000 \Rightarrow + -00 -$$

$$-0000 \Rightarrow - +00 +$$

If the no. of 1s since the last substⁿ is even.

Successive violations are ^yalternate polarity to prevent DC component.