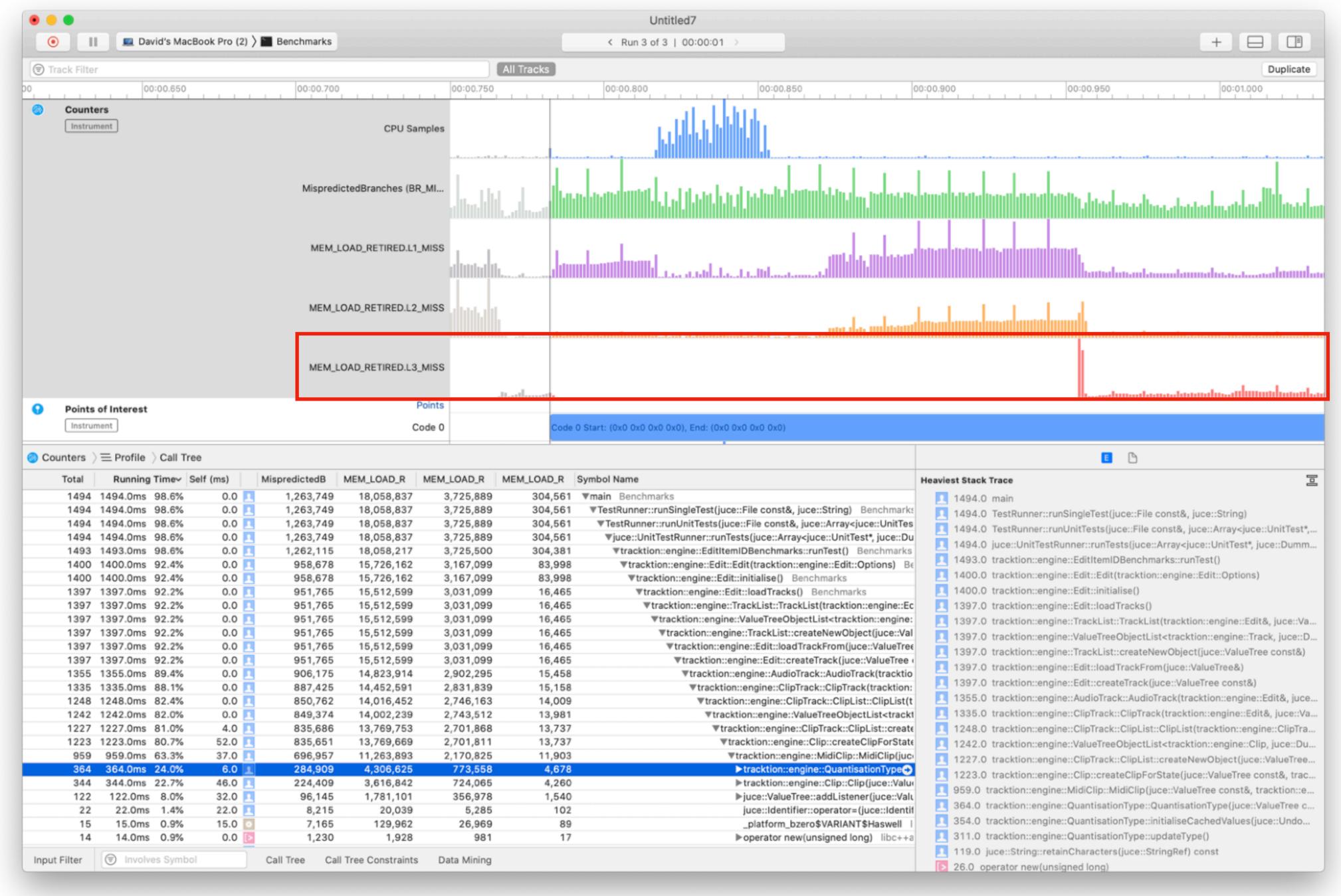
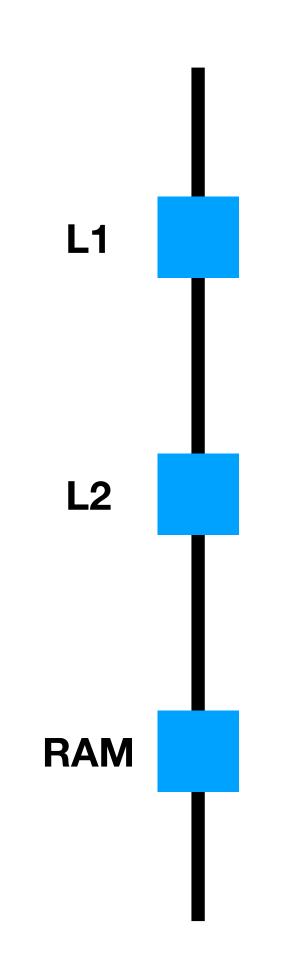


Performance Counters



Performance Counters

Memory Access



3 cycles 20 cycles 200+ cycles