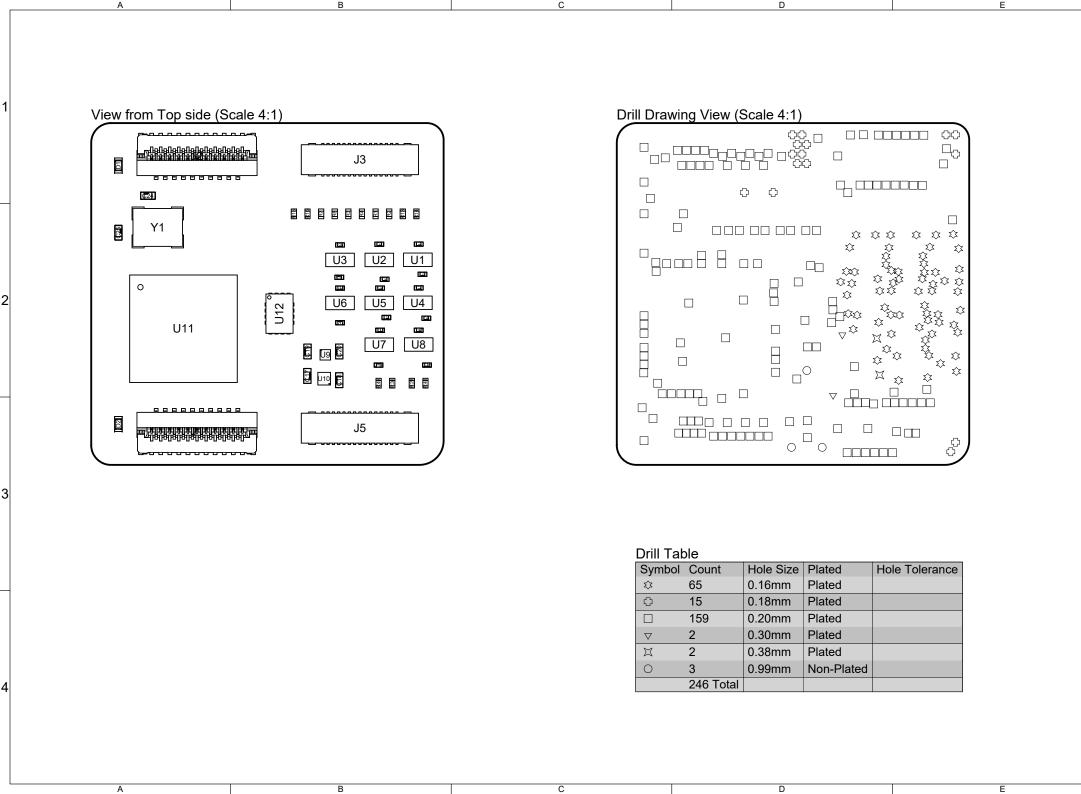


Plexus Sense Logic

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NOTES: UNITS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED 1. STACKUP SUMMARY: A. NUMBER OF COPPER LAYERS: 8 B. BOARD THICKNESS SHALL BE 1.6mm +/- 10% C. COPPER: See Layer Stack D. DEFAULT TRACE/SPACE: 5mil / 5mil E. CONDUCTOR WIDTH TOLERANCE = +/- 0.01mm 2. SURFACE FINISH/PLATING: ENIG 3. SOLDERMASK WITH LIQUID PHOTO IMAGEABLE (LPI) PER IPC-SM-840C, CLASS T. COLOR: MATTE BLACK 4. LOCATE MANUFACTURER'S IDENTIFICATION AND LOT CODE ON PRIMARY SIDE FREE FROM ALL METAL ENTITY RENDERED IN SILKSCREEN. 5. DETAILS NOT SPECIFIED ARE AT MANUFACTURER'S OPTION BUT FINAL APPROVAL MUST BE OBTAINED



Notes: Layer stackup not specificed, vendor decision. Layer Stack Legend Layer Type Gerber Top Overlay Legend GTO Top Solder Solder Mask GTS Top Surface Finish Surface Finish Top Layer **Signal GTL** Dielectric Dielectric Int1 (GND) **Signal** G1 Dielectric Int2 (SIG) Signal G2 Dielectric Dielectric Int3 (GND) **Signal** G3 Dielectric Int4 (SIG) G4 Signal Dielectric Dielectric Int5 (SIG) Signal G5 Dielectric Int6 (GND) Signal G6 Dielectric Dielectric **Bottom Layer GBL Signal** Bottom Surface Finish Surface Finish Bottom Solder Solder Mask GBS Bottom Overlay Legend GBO Total thickness: 1.63mm

	Line Structure Table Target Impedance	e Calculated Impedance	Trace laver	Wide Trace Width	Narrow Trace Width	Reference lavers	Clearance	Target Toleran
			· ·	6.54mil		Int1 (GND)	5.00mil	10%
						Top Layer,Int2 (SIG)	5.00mil	10%
3	90			5.50mil			5.00mil	10%
4							5.00mil	10%
 5	90	86.29	Int4 (SIG)	5.50mil		Int3 (GND),Int5 (SIG)	5.00mil	10%
 6	90			5.50mil			5.00mil	10%
7	90			5.50mil		Int5 (SIG),Bottom Layer	5.00mil	10%
8	90		Bottom Layer			Int6 (GND)	5.00mil	10%