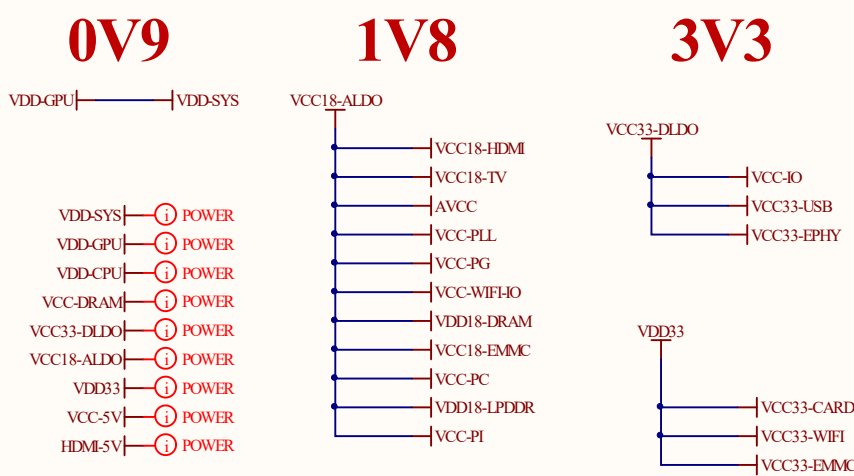


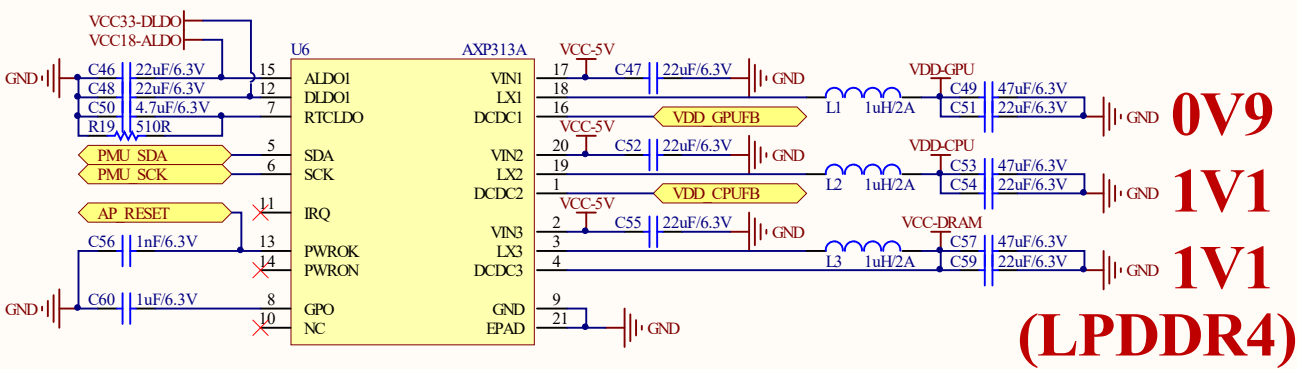
1	2	3	4	5	6	7	8																					
A	<div><div>Power Power.SchDoc</div><div></div></div> <div><div>Soc1 Soc1.SchDoc</div><div></div></div> <div><div>Soc2 Soc2.SchDoc</div><div></div></div> <div><div>Memory Memory.SchDoc</div><div></div></div> <div><div>Wifi Wifi.SchDoc</div><div></div></div> <div><div>Peripheral Peripheral.SchDoc</div><div></div></div> <div><div>Ethernet Ethernet.SchDoc</div><div></div></div> <div><div>Storage Storage.SchDoc</div><div></div></div>							A																				
B	<table><tr><th>Revision</th><th>Description</th><th>Date</th><th>Drawn</th><th>Checked</th></tr><tr><td>1.0</td><td>Initial</td><td>24.04.2024</td><td>firatatas@gmail.com</td><td></td></tr><tr><td>1.1</td><td>PCB optimization with DFM analysis</td><td>17.05.2024</td><td>firatatas@gmail.com</td><td></td></tr><tr><td>1.2</td><td>HDMI Wrong Scl and Sda crossed</td><td>08.07.2024</td><td>firatatas@gmail.com</td><td></td></tr></table>							Revision	Description	Date	Drawn	Checked	1.0	Initial	24.04.2024	firatatas@gmail.com		1.1	PCB optimization with DFM analysis	17.05.2024	firatatas@gmail.com		1.2	HDMI Wrong Scl and Sda crossed	08.07.2024	firatatas@gmail.com		B
Revision	Description	Date	Drawn	Checked																								
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C								C																				
D	<div><div>Title</div><div>ID CORE</div></div> <div><div>Size</div><div>A3</div></div> <div><div>Number</div><div>VERSIONS</div></div> <div><div>Revision</div><div>1.2</div></div> <div><div>Date:</div><div>8.07.2024</div><div>Sheet of</div></div> <div><div>File:</div><div>D:\Projects\Personel\...\ID-CORE.SchDoc</div><div>Drawn By:</div></div>							D																				
1	2	3	4	5	6	7	8																					

Title		
ID CORE		
Size	Number	Revision
A3	VERSIONS	1.2
Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\...\ID-CORE.SchDoc	Drawn By:

POWER DOMAIN

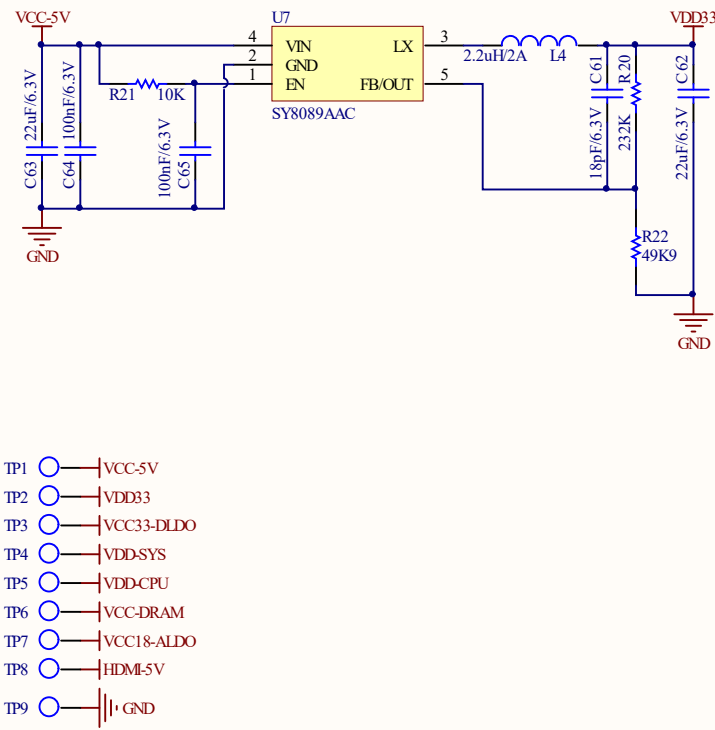


PMIC SYSTEM



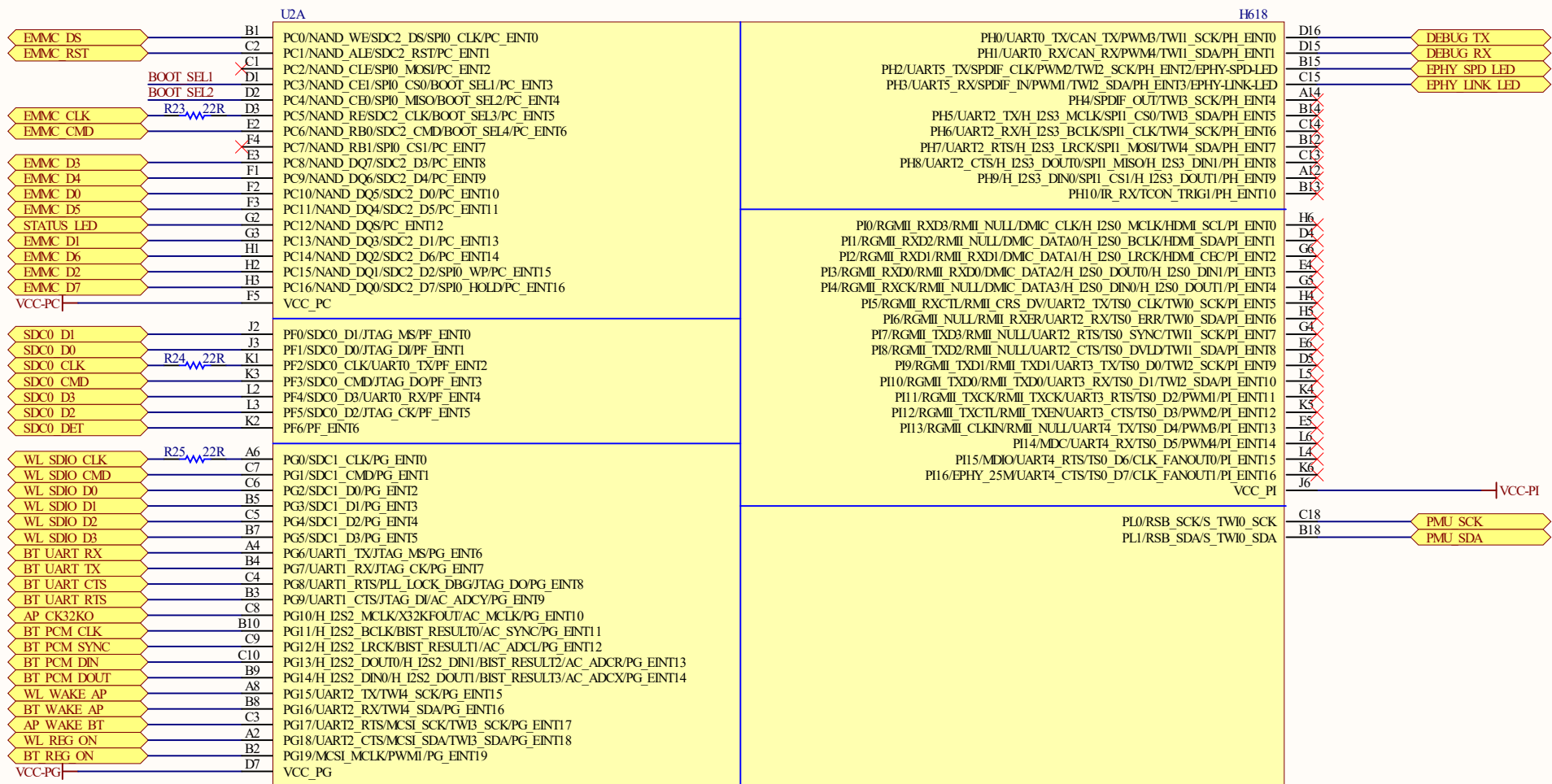
Symbol	Parameter	Min	Typ	Max	Unit
Ta	Ambient Operating Temperature	-20	-	70	°C
Tj	Working Junction Temperature Range	-20	-	110 ⁽¹⁾	°C
AVCC	Analog Part Power	1.764	1.8	1.836	V
AC_AVCC	AC Chip Analog Part Power	1.764	1.8	1.836	V
VCC_PC	Digital GPIO C Power 1.8V Voltage 3.3V Voltage	1.62 2.97	1.8 3.3	1.98 3.63	V
VCC_PG	Digital GPIO G Power 1.8V Voltage 3.3V Voltage	1.62 2.97	1.8 3.3	1.98 3.63	V
VCC_PI	Digital GPIO I Power 1.8V 2.8V 3.3V	1.62 2.52 2.97	1.8 2.8 3.3	1.98 3.08 3.63	V
VCC_IO	GPIO F, GPIO H and System Control Power	2.97	3.3	3.63	V
VCC_USB	USB Analog Power	2.97	3.3	3.63	V
VCC_HDMI	HDMI Power	1.62	1.8	1.98	V
VCC_TV	TV OUT Power	1.78	1.8	1.98	V
VCC_DCXO	DCXO Power	1.62	1.8	1.98	V
VDD_CPU	CPU Power	0.81	-	1.1	V
VDD_GPU	GPU Power	0.81	-	0.99	V
VCC_PLL	System PLL Power	1.62	1.8	1.98	V
VCC_DRAM	DRAM Power DDR4 IO Domain Power LPDDR4 IO Domain Power LPDDR3 IO Domain Power DDR3 IO Domain Power DDR3L IO Domain Power	1.14 1.06 1.14 1.425 1.283	1.2 1.1 1.2 1.5 1.35	1.26 1.17 1.3 1.575 1.45	V
VDD18_DRAM	DRAM 1.8V Internal PAD Power	1.7	1.8	1.98	V
VDD_SYS	Power Supply for System	0.81	-	0.99	V

PMIC PERIPHERAL

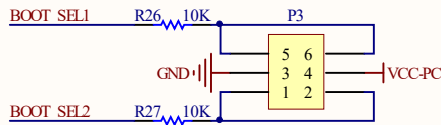


Title ID CORE		
Size A3	Number POWER	Revision
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SOC PERIPHERAL/IO



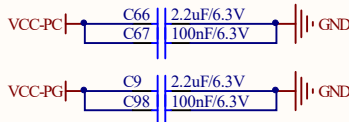
BOOT SELECT



	BOOT_SEL1	BOOT_SEL2
EMMC_BOOT	1	0
EMMC_USER	0	1

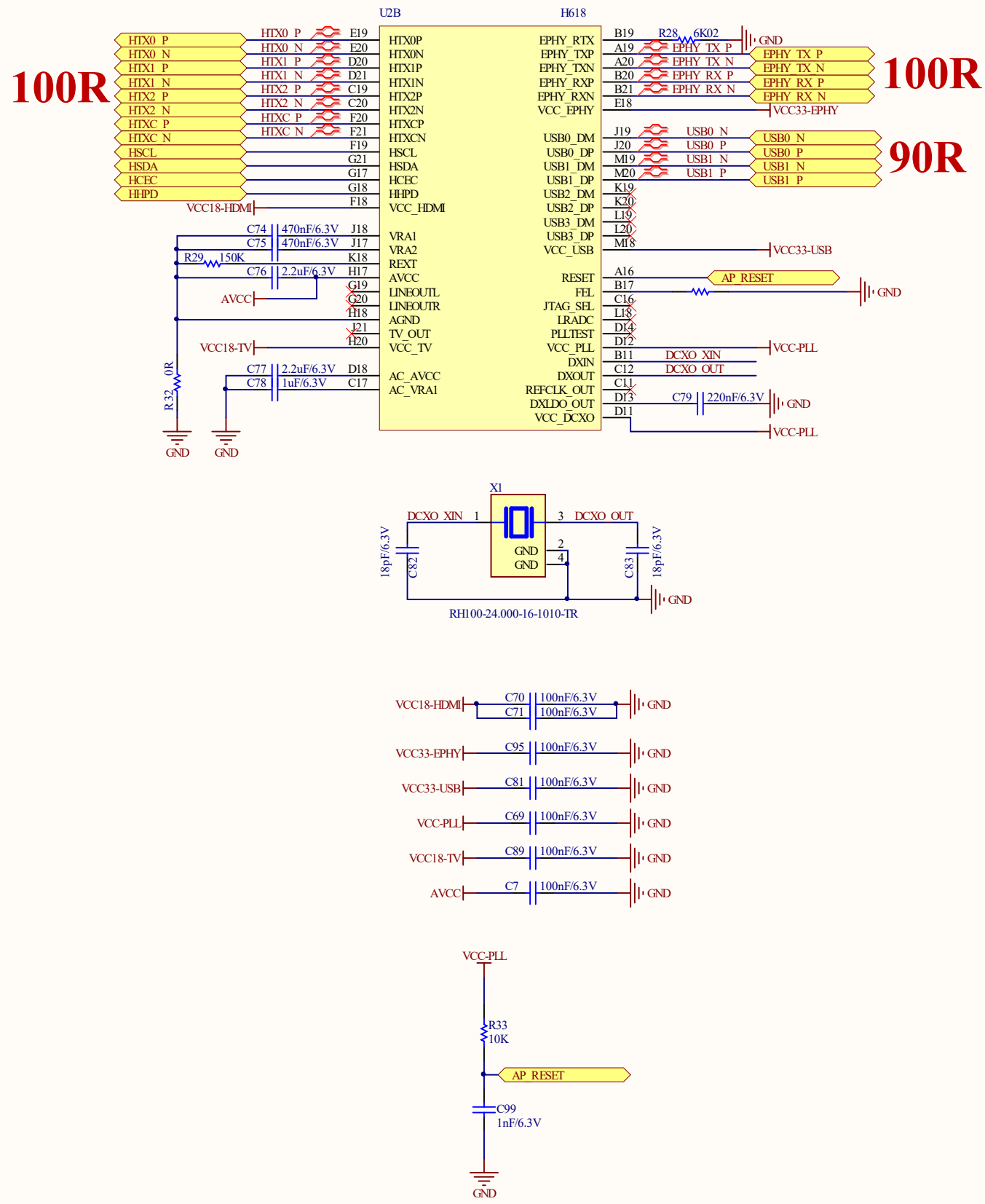
** If SD Card is empty

** For test usage, set EMMC_BOOT

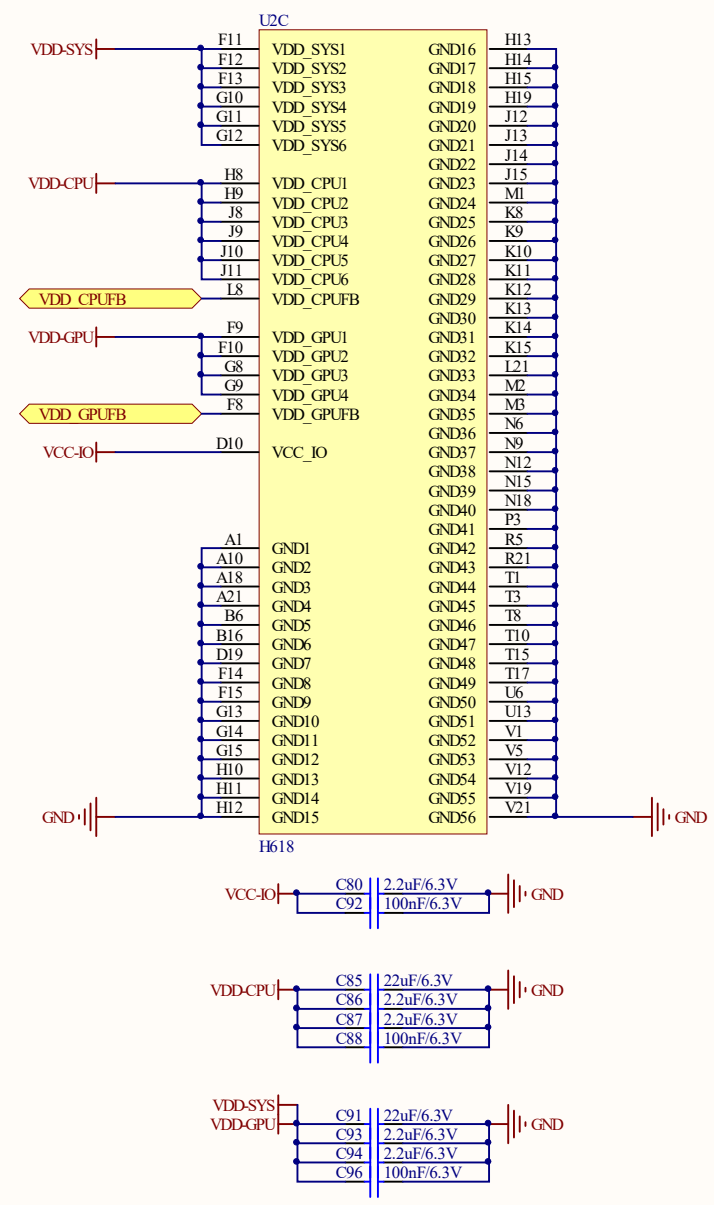


Title ID CORE		
Size A3	Number SOC1	Revision
Date: 8.07.2024	Sheet of	
File: D:\Projects\Personel\...\Soc1.SchDoc	Drawn By:	

SOC PERIPHERAL



SOC POWER



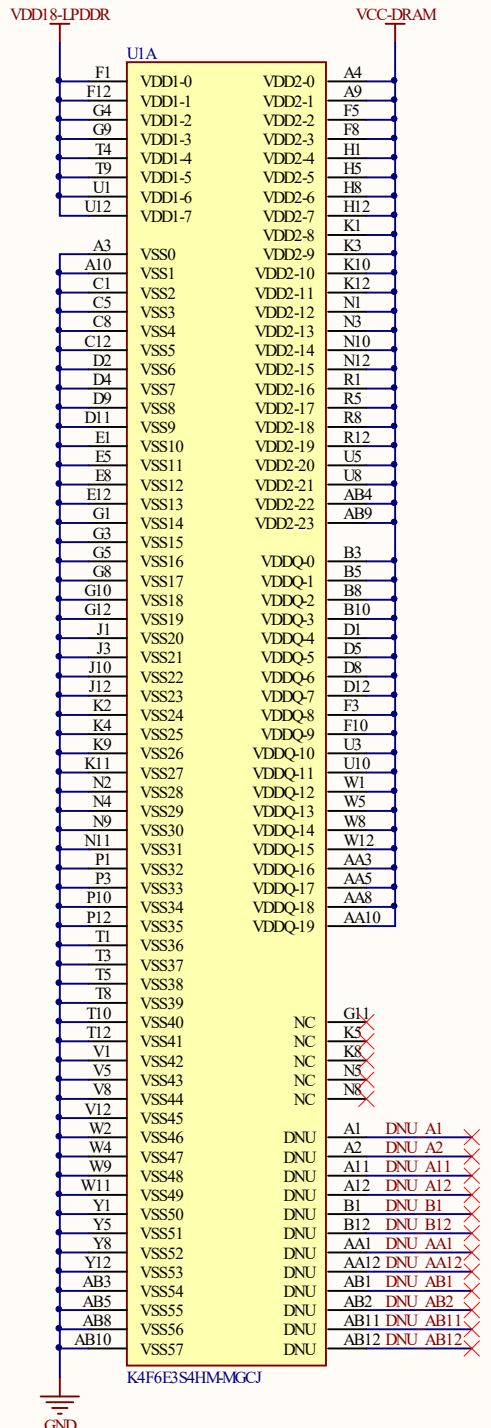
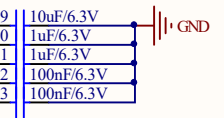
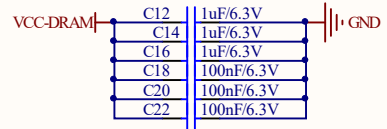
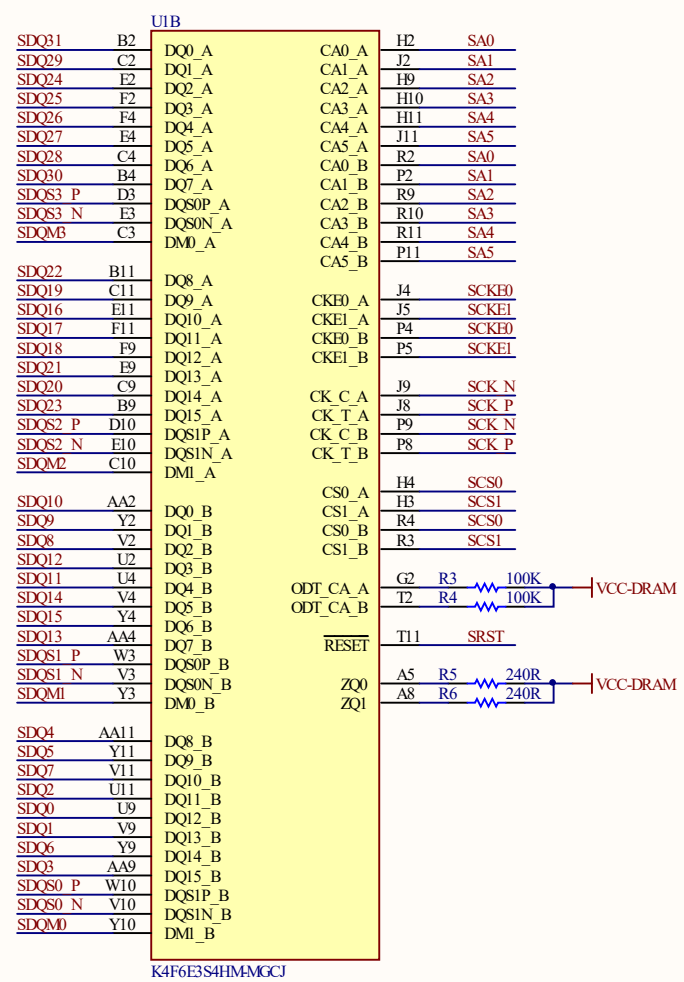
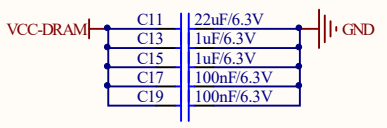
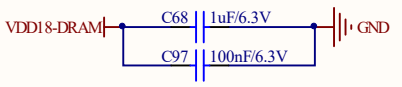
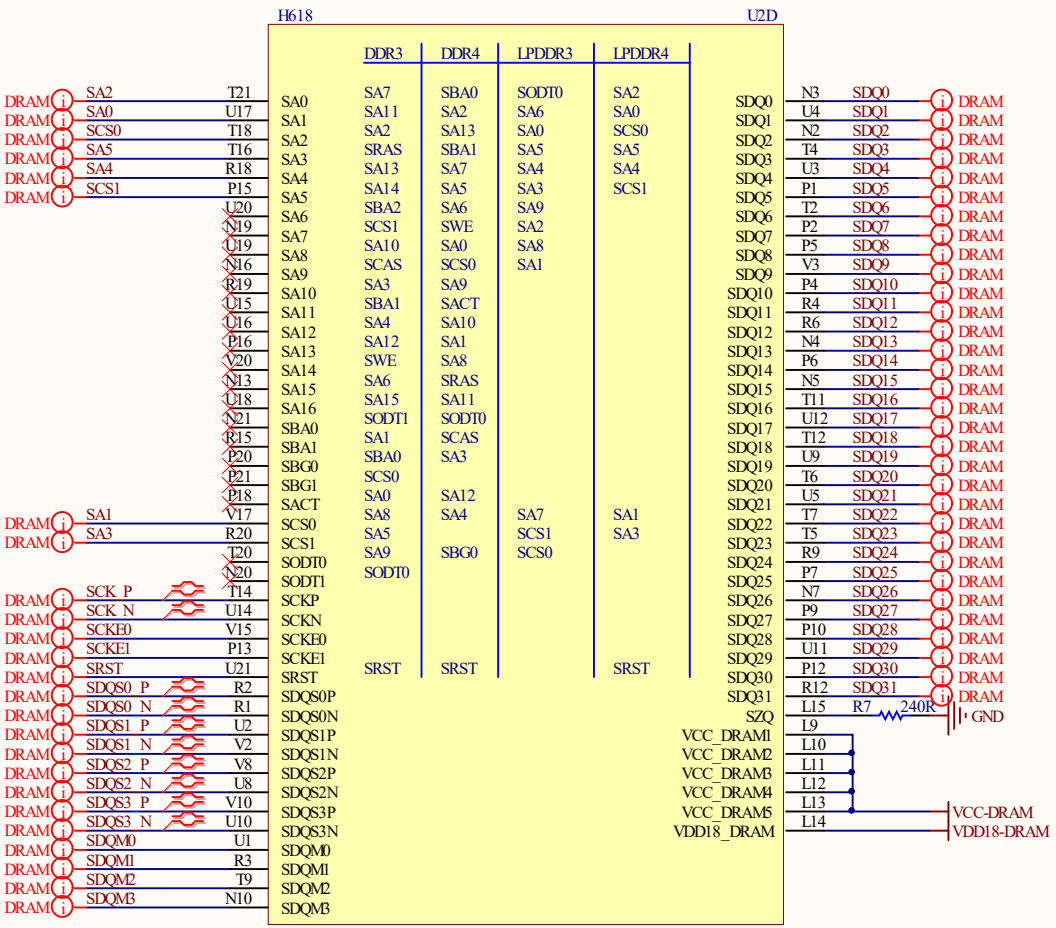
Title		
ID CORE		
Size	Number	Revision
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Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\...\Soc2.SchDoc	Drawn By:

LPDDR4 RAM

SOC RAM

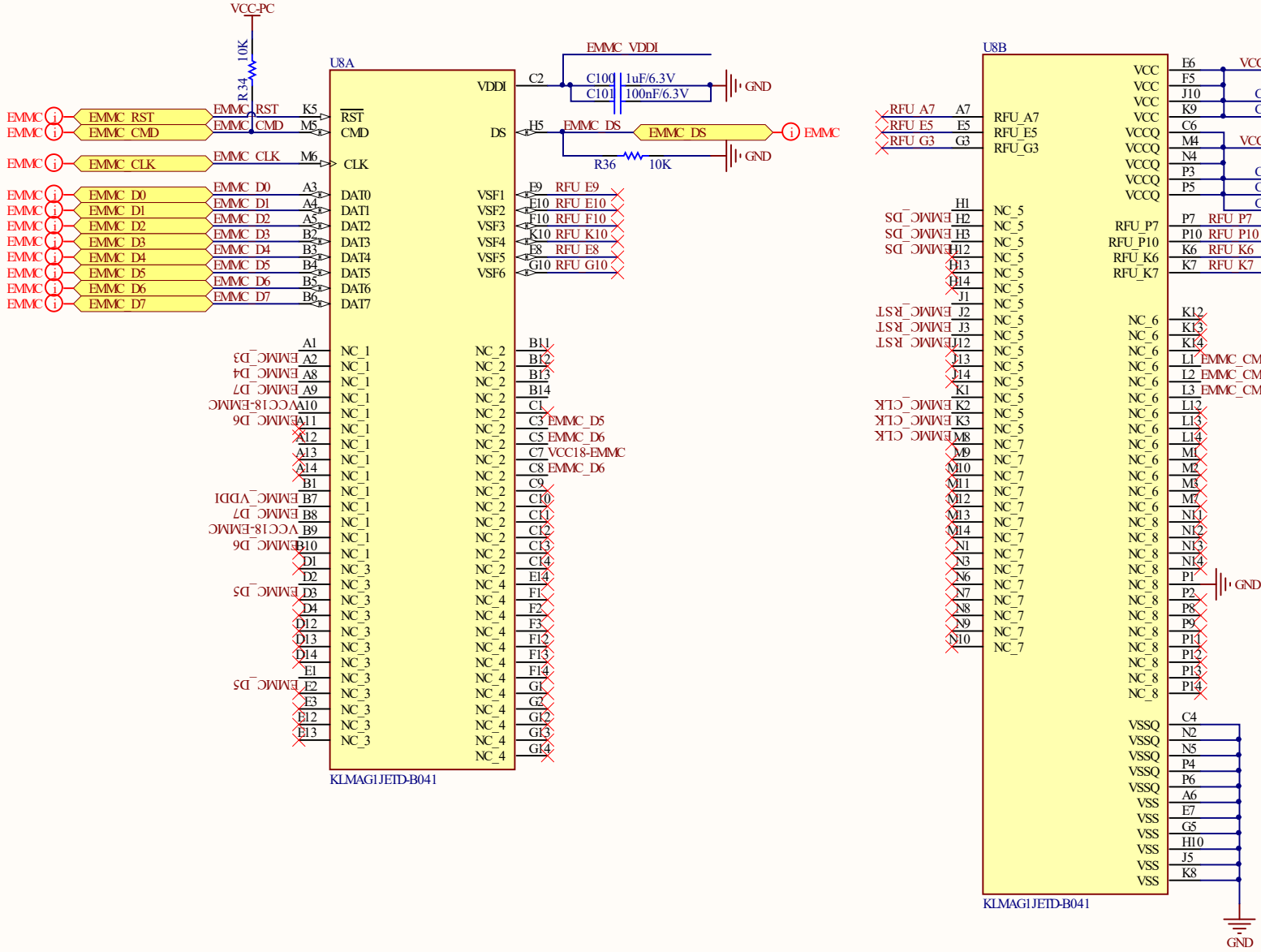
100R

100R

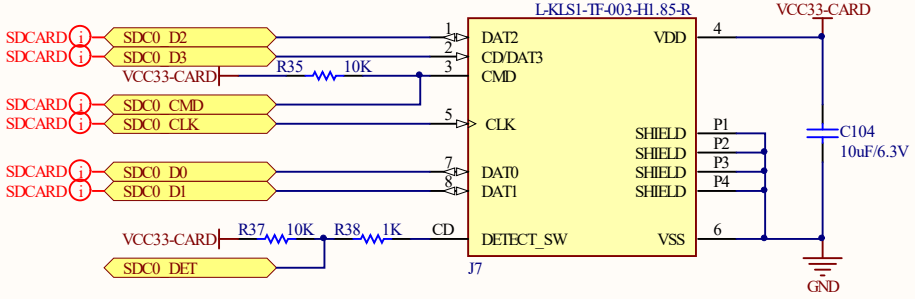


Title		
ID CORE		
Size	Number	Revision
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Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\Memory\SchDoc	Drawn By:

EMMC

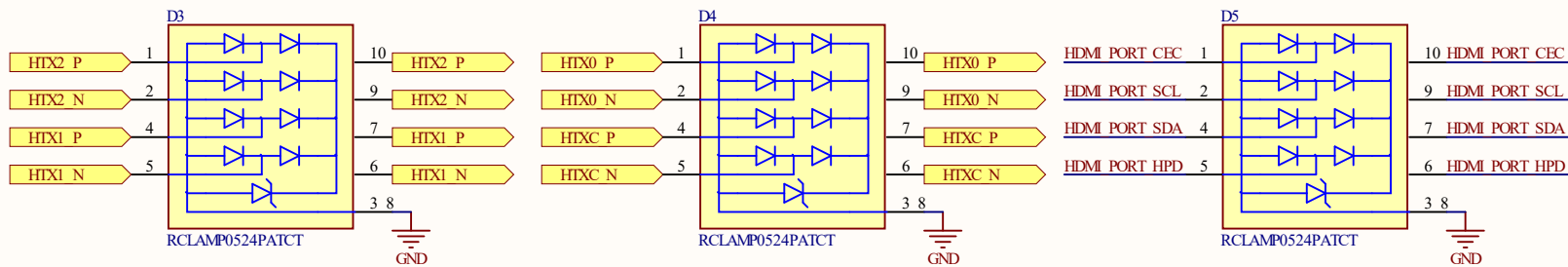
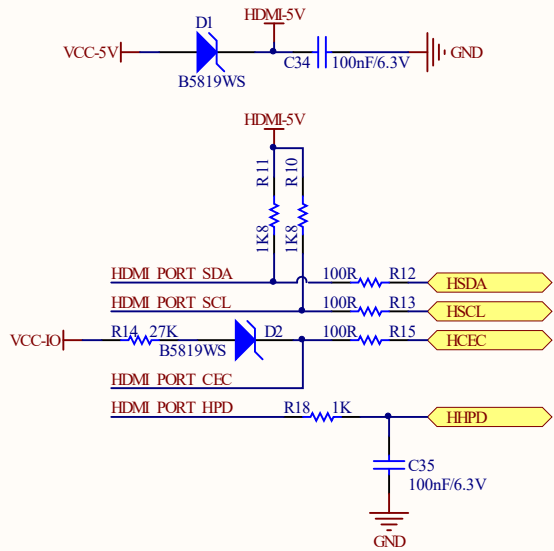
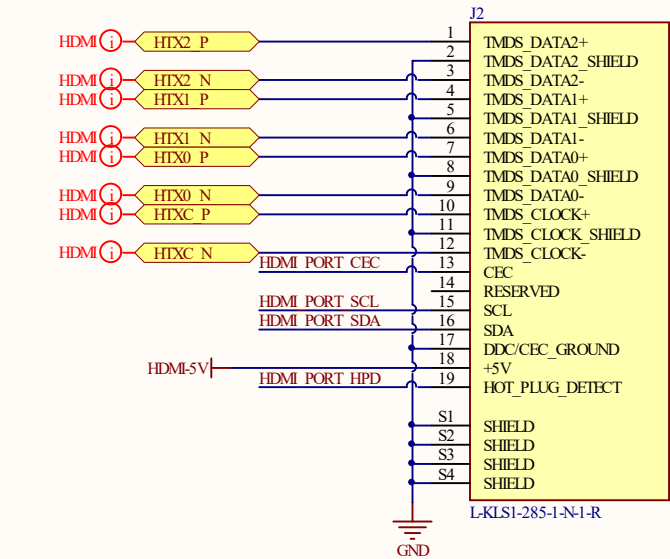


SD CARD

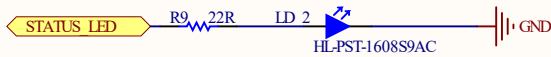


Title		
ID CORE		
Size	Number	Revision
A3	STORAGE	
Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\...\Storage.SchDoc	Drawn By:

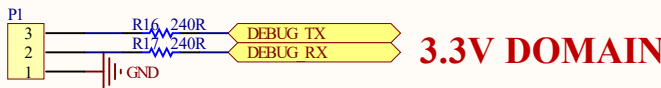
HDMI



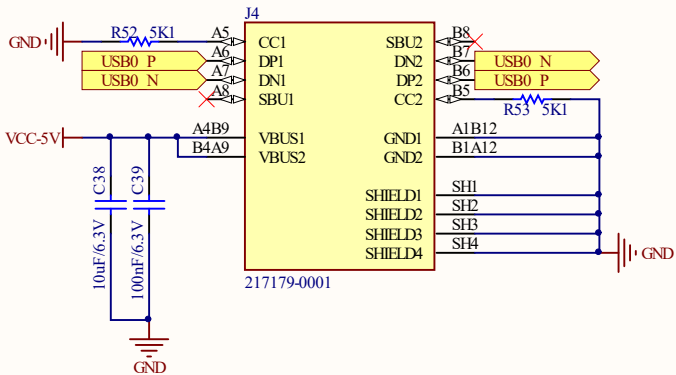
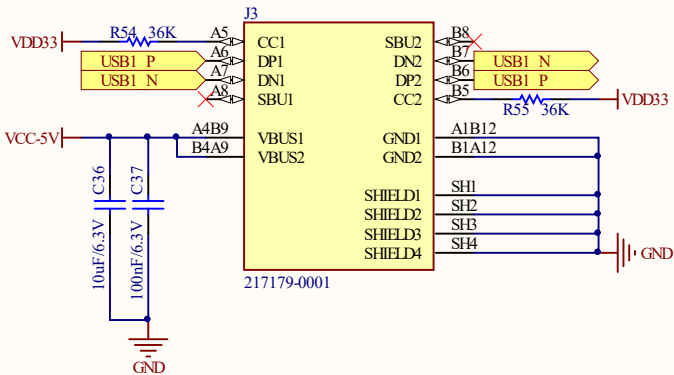
POWER/STATUS LED



DEBUG UART

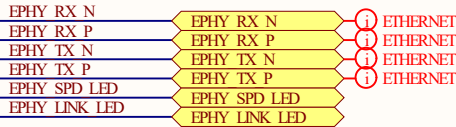
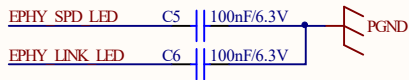
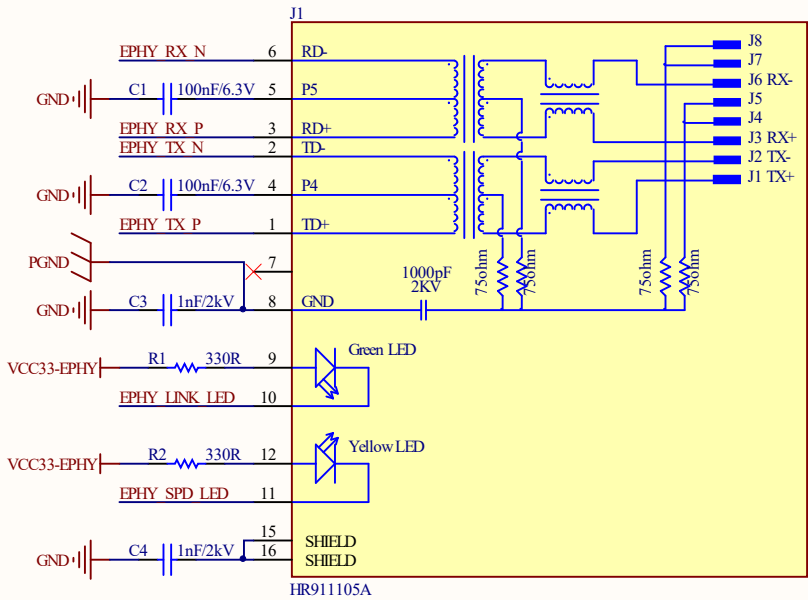


USB HOST X 2 + POWER



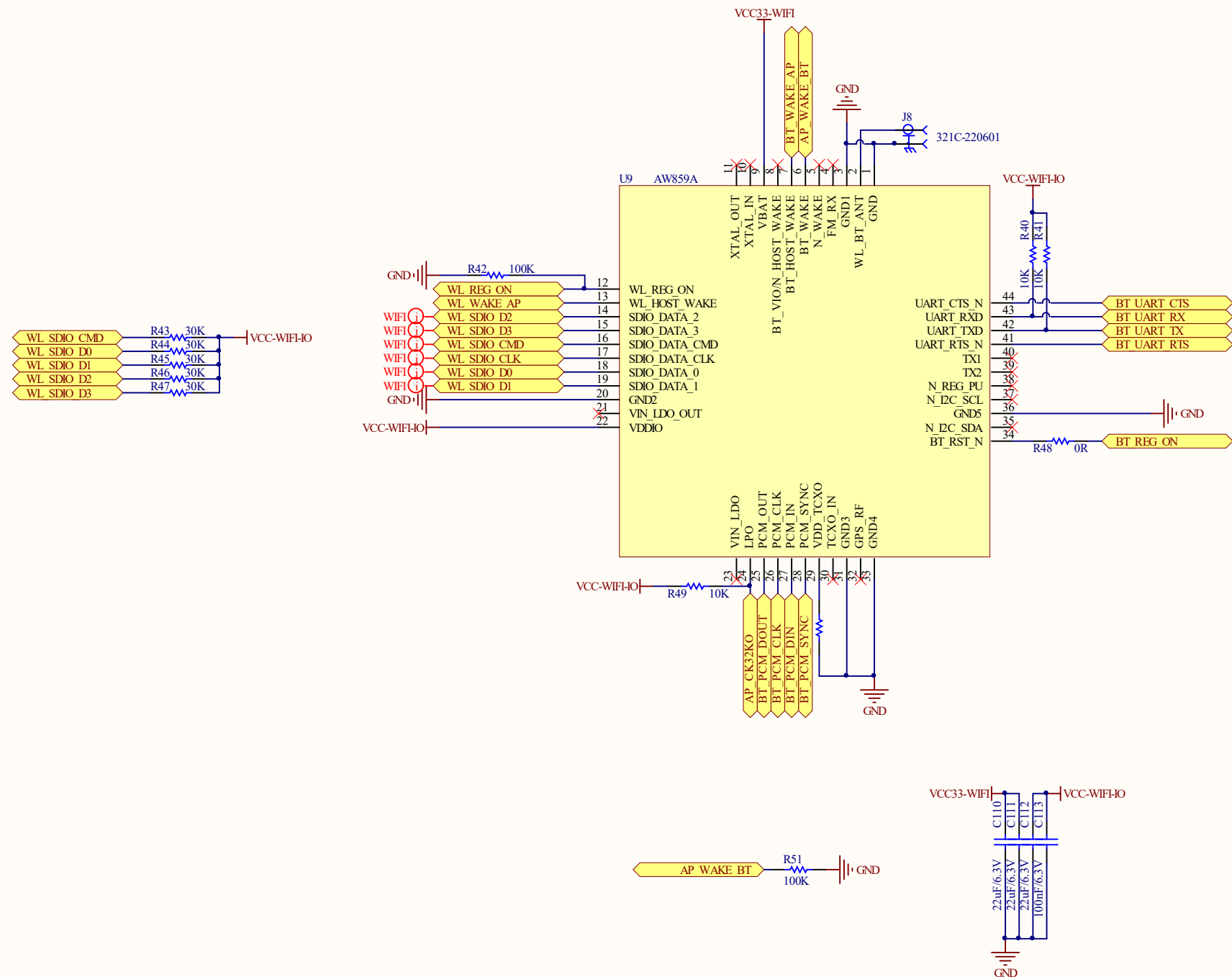
Title ID CORE		
Size A3	Number PERIPHERAL	Revision
Date: 8.07.2024	Sheet of 8	Drawn By:
File: D:\Projects\Personel\...\Peripheral.SchDoc		

ETH 10/100 PHY



Title ID CORE		
Size A3	Number ETHERNET	Revision
Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\...\Ehemet.SchDoc	Drawn By:

WIFI + BT5.0



Title ID CORE		
Size A3	Number WIFI + BT	Revision
Date:	8.07.2024	Sheet of
File:	D:\Projects\Personel\Wifi_SchDoc	Drawn By: