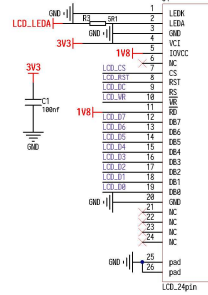
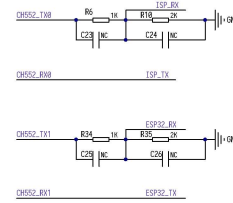


IS		DB	C2	ISV	IS
CHS2_P1.4	1	P1.2/10T1/10T0/WS1/A103	VCC/10B		(1)GND
CHS2_P1.5	2	P1.4/12_P1/CS2_P1/CS2_P1/UC21/A101	NC		
CHS2_P1.6	3	P1.5/WS1/10T1/10T0/UC22/A102	GND/VS5		(1)GND
CHS2_P1.7	4	CHS2_P1.3/CS2_P1/10T1/10T0	P1.2/10T1/10T0		
CHS2_P1.8	5	P1.7/CS2/10T1/10T0	P1.3/10T1/10T0		
CHS2_P1.9	6	WS1/10T2/CS2_P1/10T1	P3.7/10B		
CHS2_P1.10	7	P1.6/10T2/CS2_P1/10T1	P3.4/10B		
CHS2_P1.11	8	P1.1/10T2/CS2_P1/10T1/WS2/A104	CS2_P1.7/10B		
CHS2_P1.12	9	P1.10/10T2/CS2_P1/10T1	P3.4/10B		
CHS2_P1.13	10	P3.6/P1/10T1_10T0	P3.3/10T1		

CHS2P1



Camera

Diagram showing the pinout for the Camera module, connected to the CAMERA_24P connector. The pins are numbered 1 through 24.

Pin 1: GND

Pin 2: AVDD_3V0

Pin 3: EUP_MYSYNC

Pin 4: R208

Pin 5: V1V8

Pin 6: EUP_NCLR

Pin 7: EUP_PCLK

Pin 8: R224

Pin 9: GND

Pin 10: EUP_PCLK

Pin 11: P1

Pin 12: MC

Pin 13: CAM-3.3v

Pin 14: RESET

Pin 15: VSYNC

Pin 16: PWDN

Pin 17: HSYNC

Pin 18: FVSYNC

Pin 19: D0

Pin 20: D1

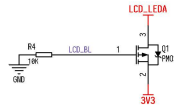
Pin 21: D2

Pin 22: FC_SDA

Pin 23: FC_SCL

Pin 24: GND

Other labels: CAMERA_24P

[illegible]

Pinout diagram for the 16-pin DIP package of the 74VHC163. The diagram shows two rows of pins. The top row, labeled P2, contains pins 1015 (8), 1012 (7), 1014 (6), 1023 (5), 1022 (4), 1021 (3), 15P_TX (2), and 15P_RX (1). The bottom row, labeled P5, contains pins 12C1_SCL (18), 12C1_SDA (9), an unlabeled pin (8), 103 (7), 1010 (6), 1011 (5), 1012 (4), 1013 (3), 1014 (2), and an unlabeled pin (1). A GND symbol is connected to pin 103.

