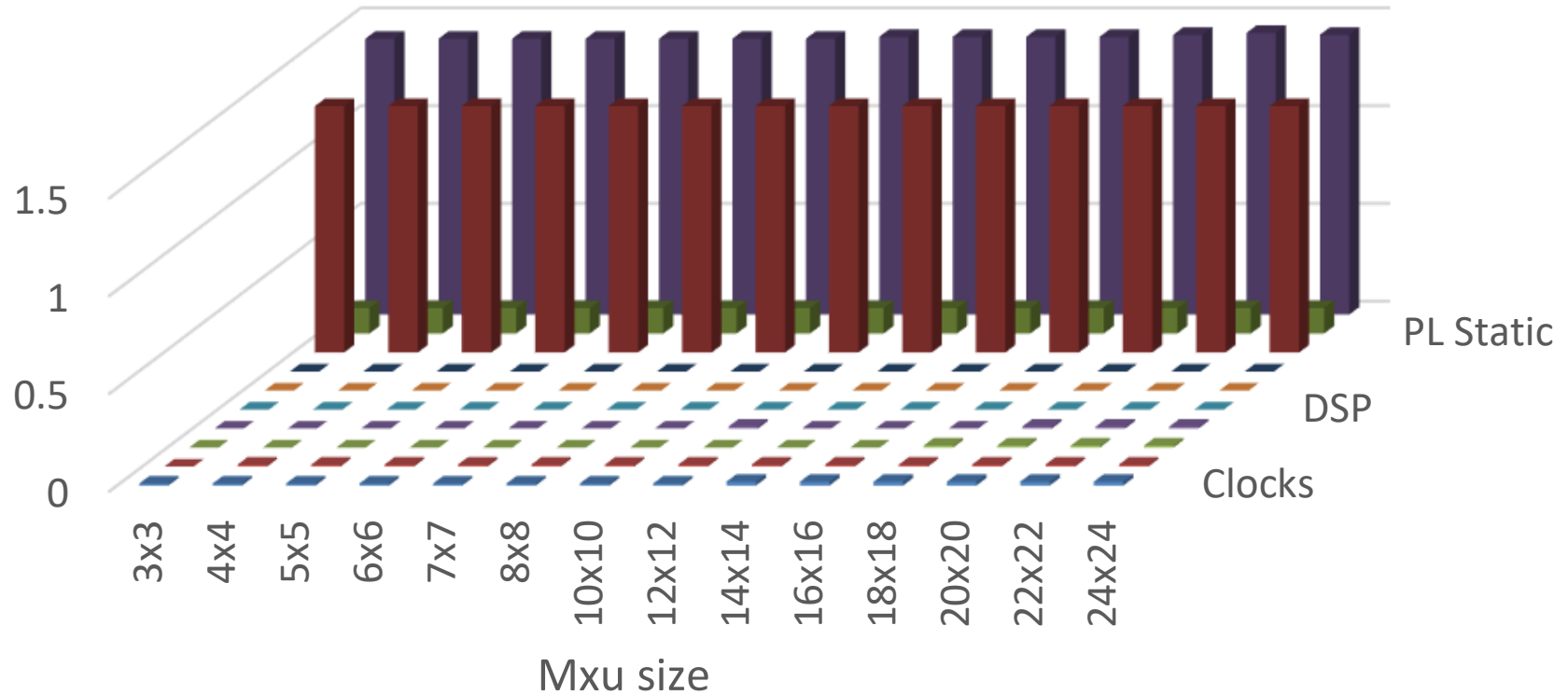


Post Implementation Power



■ Clocks ■ Signals ■ Logic ■ BRAM ■ DSP ■ I/O ■ XADC ■ PS7 ■ PL Static ■ Total Power