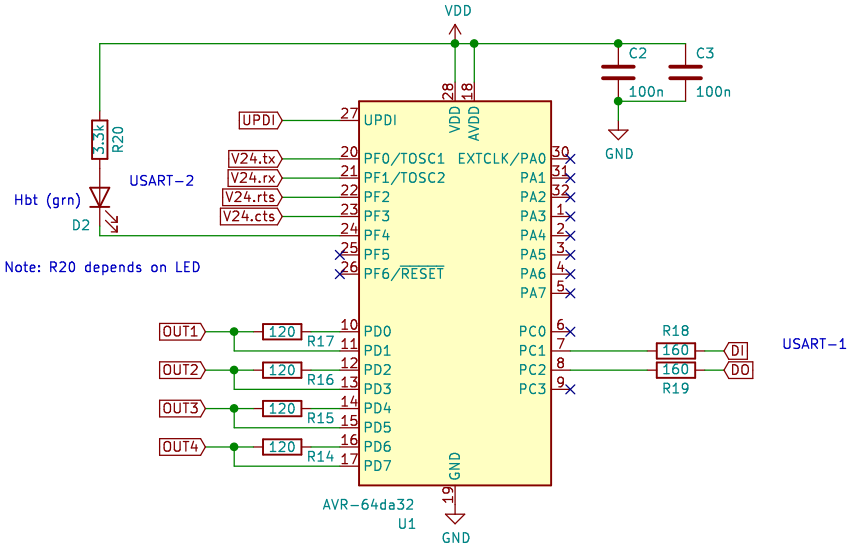
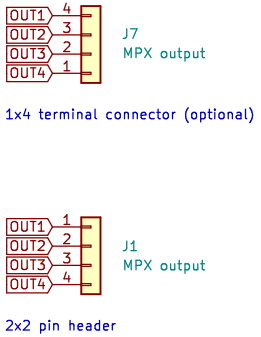
CPU

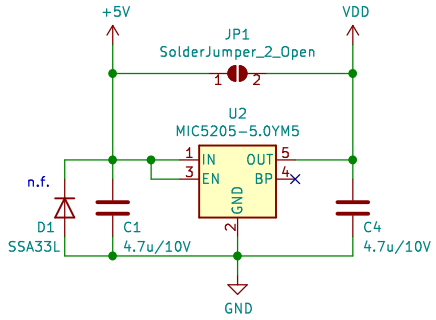


Multiplex Signal connector

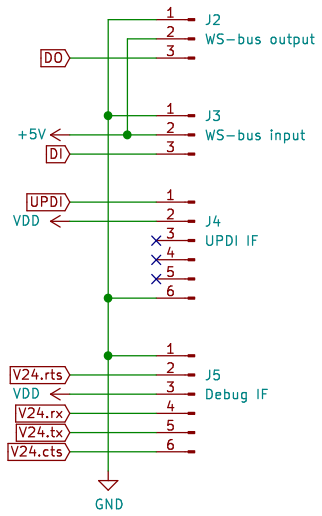


Power

LDO is optional and only to protect CPU against high voltage on the bus. The D1 protects against reverse polarity. Close JP1 if LDO is not fitted.



Interfaces



WS2811 chip emulator for 12 multiplex LEDs (fixed voltage)

Frank Schumacher

Sheet: /
File: E15.sch

Title: RTB E15 module

Size: A4	Date: 2024-11-14	**Rev: 1**
KiCad E.D.A. kicad (5.1.12)-1		Id: 1/1

H1 MountingHole
H2 MountingHole
H3 MountingHole
H4 MountingHole