



Group 10: NXP S32K3X8EVB in QEMU

Implementation and FreeRTOS porting

Master's Degree in Computer Science: Embedded Systems

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In this presentation, we will discuss the implementation of the NXP S32K3X8EVB board in QEMU and the porting of FreeRTOS to this architecture.

This is a collaborative effort by Francesco Mignone, Leonardo Gallina, Silvia Bonenti, Andrea Baraldi, and Lorenzo Parata as part of the Operating Systems course.



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Brief Introduction to QEMU structure

1 QEMU Structure

TODO: Talk about QEMU structure



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Board

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UART

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SPI

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Linker

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Changing Peripheral test

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The implementation of the NXP S32K3X8EVB board in QEMU and the porting of FreeRTOS to this architecture have been successfully completed. This project has provided valuable insights into the workings of QEMU and the challenges associated with porting an operating system to a new architecture.



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Thank you for your attention!