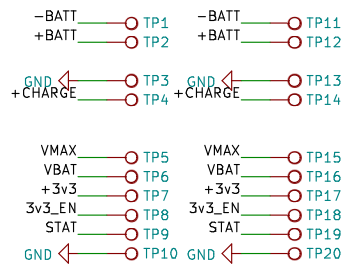
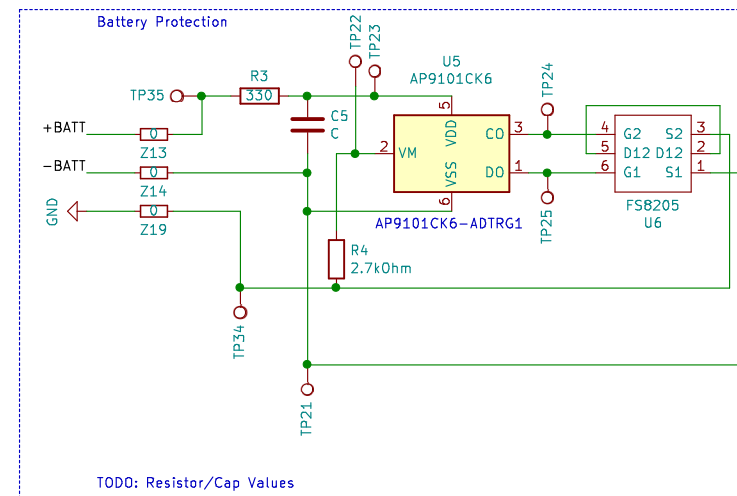
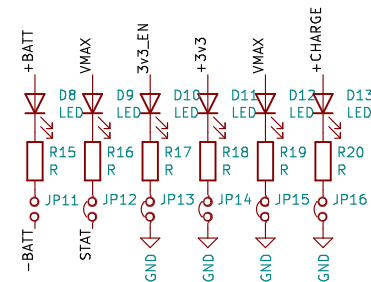
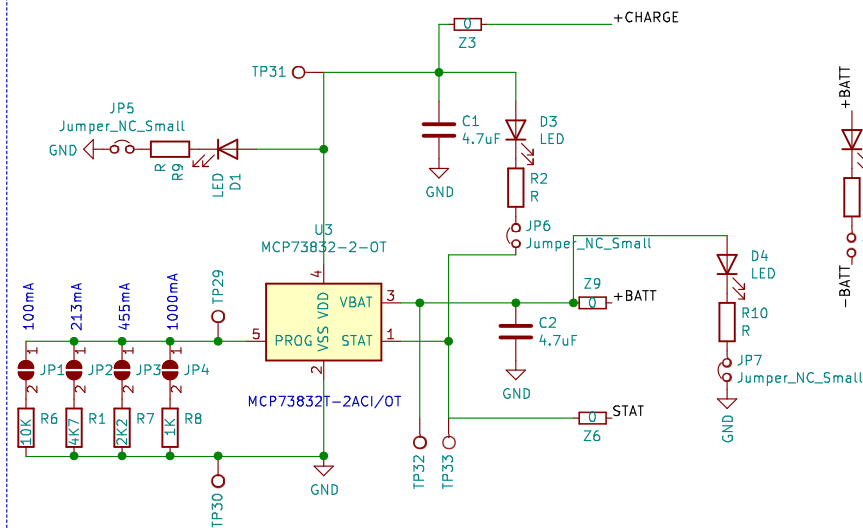


Current In/Out	Voltage In/Out		
		Negative Battery Terminal	-BATTD
		Positive Battery Terminal	+BATTD
		Negative USB Supply	-CHARGED
		Positive USB Supply	+CHARGED
		Logical GND	GND
500mA ...= 2A	+2v5 ...= +5v5	max(+BATT...+CHARGE)	VMAX_OUT
		Positive Battery Source Limited	VBAT_OUT
500mA	0v0_OR +3v3	+3v3 Regulated Output	+3v3
		+3v3 Regulation_EN (disable_low)	3v3_EN
		Charge Status Out	STAT_OUT



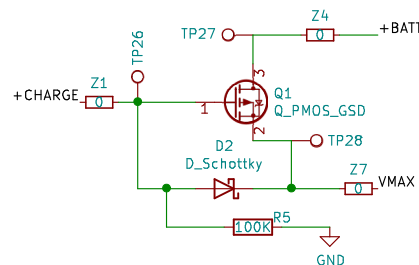
TODD: Polyfuse(s)?

#### Charge Controller

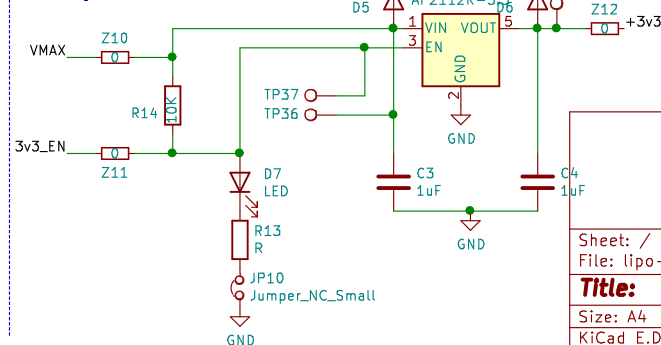


TODD: Resistor/Cap Values

#### Power Path (VMAX selection)



#### +3v3 Regulator



Sheet: /  
File: lipo-stamp-proto.sch

Title:

Size: A4  
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Rev:  
Id: 1/1