



Pin	Default state	Low level	High level
IO2/REFCLK_FREQ_STATUS	Pull down	40MHz (Default)	24MHz
IO6/JTAG_MODE	Pull down	Normal function mode (Default)	DFT Test mode
IO8/JTAG_ENABLE	Pull down	Ordinary ID (Default)	JTAG enable

koendv

Sheet: /
File: hi3861-jtag.sch

Title: HI3861 JTAG SWD Connector

Size: A4
KiCad E.D.A. kicad 5.1.9+dfsg1-1

Rev:
Id: 1/1