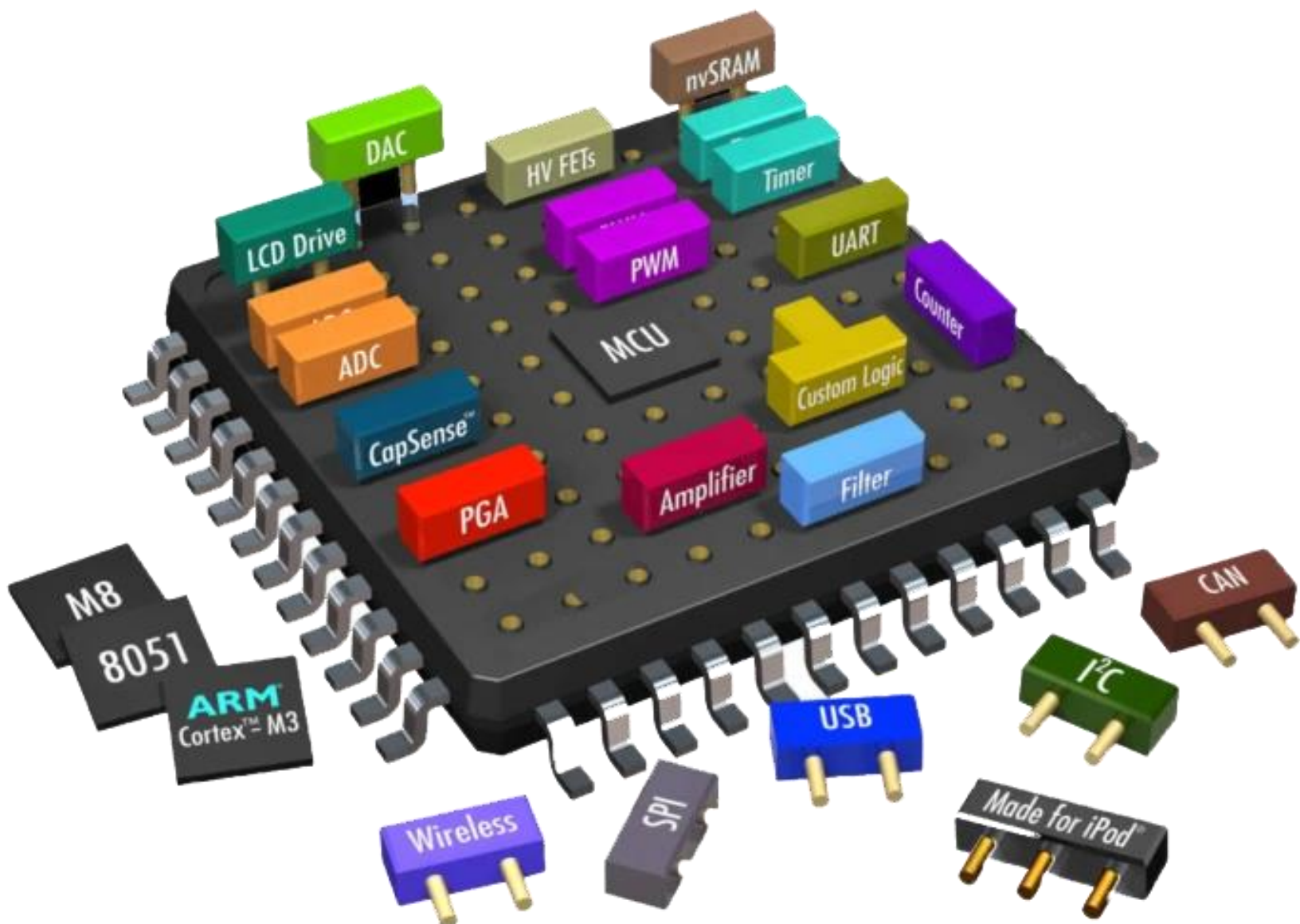


# Design Patterns in Resource-Constrained Embedded Systems







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# 1. Introduction

# 1. Introduction

Designing embedded firmware for resource-constrained systems is a delicate balance between performance, power efficiency, and code clarity. Unlike application-level development on full-fledged operating systems, these systems operate with limited SRAM (often less than 2KB), single-core processors, and no built-in support for multitasking. As such, firmware engineers must architect software with deterministic behavior, minimal stack usage, and absolute control over timing.

# 1. Introduction

**Design patterns**, when properly applied, become powerful tools to manage complexity and reuse tested architectural principles.

However, not all patterns translate well to bare-metal embedded systems. This article explores patterns that have been adapted to low-level environments such as AVR microcontrollers (e.g., ATtiny1616), where interrupts, non-blocking execution, and low-power states are the rule, not the exception.





## 2. Cooperative Scheduler Pattern

## 2. Cooperative Scheduler Pattern

### Purpose

Implements multitasking in a non-preemptive way by explicitly yielding control between tasks.

### Use Case in Embedded

Enables simple task scheduling on single-core MCUs without RTOS support. Tasks must complete quickly or yield periodically to keep the system responsive.

### Best Practice Notes

- Avoid blocking delays (`_delay_ms()`).
- Design tasks to run for a bounded time.
- Use timer flags or counters to defer execution.



## 2. Cooperative Scheduler Pattern

### Example

```
1  typedef void (*TaskFunc)(void);
2
3  typedef struct {
4      TaskFunc func;
5      uint16_t interval_ms;
6      uint16_t elapsed_ms;
7  } Task;
8
9  void task_led_toggle(void);
10 void task_uart_poll(void);
11
12 Task scheduler[] = {
13     {task_led_toggle, 500, 0},
14     {task_uart_poll, 10, 0}
15 };
16
17 #define NUM_TASKS (sizeof(scheduler)/sizeof(Task))
18
19 void run_scheduler(uint16_t tick_ms) {
20     for (uint8_t i = 0; i < NUM_TASKS; ++i) {
21         scheduler[i].elapsed_ms += tick_ms;
22         if (scheduler[i].elapsed_ms >= scheduler[i].interval_ms) {
23             scheduler[i].elapsed_ms = 0;
24             scheduler[i].func(); // Cooperative call
25         }
26     }
27 }
28
29 // Timer ISR increments system tick and triggers run_scheduler()
```



# 3. State Machine Pattern



# 3. State Machine Pattern

## Purpose

Encapsulates system behavior into defined states and transitions, promoting clear logic separation.

## Use Case in Embedded

Used for debouncing buttons, protocol parsing, and user interface handling.

## Best Practice Notes

- Use enums for states.
- Transition logic must remain non-blocking.
- Each state must do minimal work per call.

# 3. State Machine Pattern

## Example: Button Debounce FSM



```
1  typedef enum { IDLE, DEBOUNCING, PRESSED } ButtonState;
2  static ButtonState btn_state = IDLE;
3  static uint16_t debounce_counter = 0;
4
5  void fsm_button_tick(bool input_signal) {
6      switch (btn_state) {
7          case IDLE:
8              if (input_signal) {
9                  btn_state = DEBOUNCING;
10                 debounce_counter = 10;
11             }
12             break;
13
14         case DEBOUNCING:
15             if (--debounce_counter == 0) {
16                 if (input_signal) {
17                     btn_state = PRESSED;
18                     // Do something: button press confirmed
19                 } else {
20                     btn_state = IDLE;
21                 }
22             }
23             break;
24
25         case PRESSED:
26             if (!input_signal)
```



### 3. State Machine Pattern

```
4
5 void fsm_button_tick(bool input_signal) {
6     switch (btn_state) {
7         case IDLE:
8             if (input_signal) {
9                 btn_state = DEBOUNCING;
10                debounce_counter = 10;
11            }
12            break;
13
14        case DEBOUNCING:
15            if (--debounce_counter == 0) {
16                if (input_signal) {
17                    btn_state = PRESSED;
18                    // Do something: button press confirmed
19                } else {
20                    btn_state = IDLE;
21                }
22            }
23            break;
24
25        case PRESSED:
26            if (!input_signal)
27                btn_state = IDLE;
28            break;
29    }
30 }
```



## 4. Event Loop Pattern



## 4. Event Loop Pattern

### Purpose

Processes events in a loop, deferring action logic to event handlers.

### Use Case in Embedded

Ideal for communication stacks, central control loops, or serial command processing.

### Best Practice Notes

- Use event flags or queues.
- Keep handlers fast and deterministic.

## 4. Event Loop Pattern

### Example: Central Event Loop



```
1  typedef enum { EVT_NONE, EVT_UART_RX, EVT_TEMP_READY } EventType;
2  volatile EventType current_event = EVT_NONE;
3
4  void event_loop(void) {
5      while (1) {
6          switch (current_event) {
7              case EVT_UART_RX:
8                  process_uart_data();
9                  break;
10
11             case EVT_TEMP_READY:
12                 read_temperature_sensor();
13                 break;
14
15             default:
16                 break;
17         }
18         current_event = EVT_NONE;
19     }
20 }
21
22 // ISR or polling logic sets current_event
```





# 5. Singleton Pattern

# 5. Singleton Pattern

## Purpose

Ensures a peripheral or manager is accessed through a single instance.

## Use Case in Embedded

Used to abstract hardware peripherals like UART, ADC, or SPI, where duplicate access is unsafe.

## Best Practice Notes

- Implement as a static function-scoped instance.
- Avoid dynamic memory.

# 5. Singleton Pattern

## Example: UART Singleton



```
1 typedef struct {  
2     uint8_t tx_buffer[64];  
3     uint8_t rx_buffer[64];  
4 } UartDriver;  
5  
6 UartDriver* get_uart_instance(void) {  
7     static UartDriver uart;  
8     return &uart;  
9 }
```





# 6. Command Pattern

## 6. Command Pattern

### Purpose

Encapsulates actions as objects to decouple command issuance from execution.

### Use Case in Embedded

Used in CLI interpreters, menu systems, or motor control logic.

### Best Practice Notes

- Define command structs or enums.
- Use function pointers to execute commands.

## 6. Command Pattern

### Example: CLI Command Handler



```
1  typedef void (*CommandFunc)(void);
2
3  typedef struct {
4      const char* name;
5      CommandFunc execute;
6  } Command;
7
8  void cmd_led_on(void) { PORTB.OUTSET = PIN0_bm; }
9  void cmd_led_off(void) { PORTB.OUTCLR = PIN0_bm; }
10
11  Command commands[] = {
12      {"LEDON", cmd_led_on},
13      {"LEDOFF", cmd_led_off}
14  };
15
16  void handle_command(const char* input) {
17      for (int i = 0; i < sizeof(commands)/sizeof(Command); ++i) {
18          if (strcmp(input, commands[i].name) == 0) {
19              commands[i].execute();
20              return;
21          }
22      }
23  }
```





# 7. Observer Pattern

## 7. Observer Pattern

### Purpose

Allows multiple modules to react to state changes in another module.

### Use Case in Embedded

Used in sensor threshold alerting or decoupled UI updates.

### Best Practice Notes

- Use function pointers or callback registries.
- Limit callback duration.

# 7. Observer Pattern

## Example: Temperature Observer



```
1 typedef void (*TempCallback)(int16_t temp);
2 #define MAX_OBSERVERS 4
3 static TempCallback observers[MAX_OBSERVERS];
4
5 void register_temp_observer(TempCallback cb) {
6     for (int i = 0; i < MAX_OBSERVERS; ++i) {
7         if (!observers[i]) {
8             observers[i] = cb;
9             break;
10        }
11    }
12 }
13
14 void notify_temp_change(int16_t temp) {
15     for (int i = 0; i < MAX_OBSERVERS; ++i) {
16         if (observers[i]) observers[i](temp);
17     }
18 }
```





## 8. Strategy Pattern

# 8. Strategy Pattern

## Purpose

Allows runtime selection of behavior among multiple algorithms.

## Use Case in Embedded

Used for selecting communication methods, power modes, or filtering techniques.

## Best Practice Notes

- Define interfaces via function pointers.
- Keep strategy structs static.

# 8. Strategy Pattern

## Example: Power Mode Strategy

```
1  typedef void (*sleepStrategy)(void);
2
3  void sleep_idle(void)    {
4      SLPCTRL.CTRLA = SLPCTRL_SMODE_IDLE_gc |
5                      SLPCTRL_SEN_bm;
6      __sleep();
7  }
8  void sleep_standby(void) {
9      SLPCTRL.CTRLA = SLPCTRL_SMODE_STDBY_gc |
10                     SLPCTRL_SEN_bm;
11     __sleep();
12 }
13
14 typedef struct {
15     const char* name;
16     sleepStrategy sleep_func;
17 } PowerStrategy;
18
19 PowerStrategy modes[] = {
20     {"IDLE",    sleep_idle},
21     {"STANDBY", sleep_standby}
22 };
23
24 void enter_sleep_mode(PowerStrategy* strategy) {
25     strategy->sleep_func();
26 }
```





# 9. Factory Pattern

## 9. Factory Pattern

### Purpose

Provides a way to create objects without exposing the instantiation logic.

### Use Case in Embedded

Used for abstracting different driver backends.

### Best Practice Notes

- Use config constants or init structs to drive instantiation.
- Avoid malloc—use statically allocated buffers.

# 9. Factory Pattern

## Example: Sensor Factory

```
1 typedef enum { SENSOR_DHT, SENSOR_SHT } SensorType;
2
3 SensorBase* create_sensor(SensorType type) {
4     static DHTSensor dht;
5     static SHTSensor sht;
6
7     switch (type) {
8         case SENSOR_DHT: return &dht.base;
9         case SENSOR_SHT: return &sht.base;
10        default: return NULL;
11    }
12 }
```





# 10. Active Object Pattern

# 10. Active Object Pattern

## Purpose

Separates method execution into its own context (thread or task).

## Use Case in Embedded

Used with queues in RTOS environments or ISR-to-task communication.

## Best Practice Notes

- Use FreeRTOS queues or ring buffers.
- Decouple ISR logic from heavy processing.

# 10. Active Object Pattern

## Example: Logger Task



```
1  #define LOG_QUEUE_SIZE 8
2  QueueHandle_t log_queue;
3
4  void logger_task(void* params) {
5      char log_entry[64];
6      while (1) {
7          if (xQueueReceive(log_queue, &log_entry, portMAX_DELAY)) {
8              write_to_flash(log_entry);
9          }
10     }
11 }
12
13 void log_async(const char* msg) {
14     xQueueSend(log_queue, msg, 0);
15 }
```





# 11. Hierarchical State Machine (HSM)

# 11. Hierarchical State Machine (HSM)

## Purpose

Extends FSMs by supporting state nesting and reuse.

## Use Case in Embedded

Used in communication stacks, display logic, or robotic behaviors.

## Best Practice Notes

- Model common substates (e.g., Error, Idle).
- Manage transitions cleanly with events.



# 11. Hierarchical State Machine (HSM)


## Example: BT State Tree

```
1  typedef enum {
2      STATE_DISCONNECTED, STATE_IDLE, STATE_STREAMING, STATE_ERROR
3  } BTState;
4
5  typedef enum {
6      EVT_CONNECT, EVT_STREAM_START, EVT_STREAM_STOP,
7      EVT_DISCONNECT, EVT_ERROR
8  } BTEvent;
9
10 static BTState state = STATE_DISCONNECTED;
11
12 void handle_event(BTEvent event) {
13     switch (state) {
14         case STATE_DISCONNECTED:
15             if (event == EVT_CONNECT) state = STATE_IDLE;
16             break;
17
18         case STATE_IDLE:
19             if (event == EVT_STREAM_START) state = STATE_STREAMING;
20             else if (event == EVT_DISCONNECT) state = STATE_DISCONNECTED;
21             break;
22
23         case STATE_STREAMING:
24             if (event == EVT_STREAM_STOP) state = STATE_IDLE;
25             else if (event == EVT_DISCONNECT) state = STATE_DISCONNECTED;
26             break;
27
28         case STATE_ERROR:
29             // do some error handling
30             break;
31     }
32 }
```



# 11. Hierarchical State Machine (HSM)

```
3  } BTState;  
4  
5  typedef enum {  
6      EVT_CONNECT, EVT_STREAM_START, EVT_STREAM_STOP,  
7      EVT_DISCONNECT, EVT_ERROR  
8  } BTEvent;  
9  
10 static BTState state = STATE_DISCONNECTED;  
11  
12 void handle_event(BTEvent event) {  
13     switch (state) {  
14         case STATE_DISCONNECTED:  
15             if (event == EVT_CONNECT) state = STATE_IDLE;  
16             break;  
17  
18         case STATE_IDLE:  
19             if (event == EVT_STREAM_START) state = STATE_STREAMING;  
20             else if (event == EVT_DISCONNECT) state = STATE_DISCONNECTED;  
21             break;  
22  
23         case STATE_STREAMING:  
24             if (event == EVT_STREAM_STOP) state = STATE_IDLE;  
25             else if (event == EVT_DISCONNECT) state = STATE_DISCONNECTED;  
26             break;  
27  
28         case STATE_ERROR:  
29             // do some error handling  
30             break;  
31     }  
32  
33     if (event == EVT_ERROR)  
34         state = STATE_ERROR;  
35 }
```



# 12. Object Pool Pattern

# 12. Object Pool Pattern

## Purpose

Avoids fragmentation and allocation overhead by reusing pre-allocated objects.

## Use Case in Embedded

Used for packet buffers, command structs, or job queues.

## Best Practice Notes

- Use circular buffers or free lists.
- Never use malloc/free in ISR or runtime.

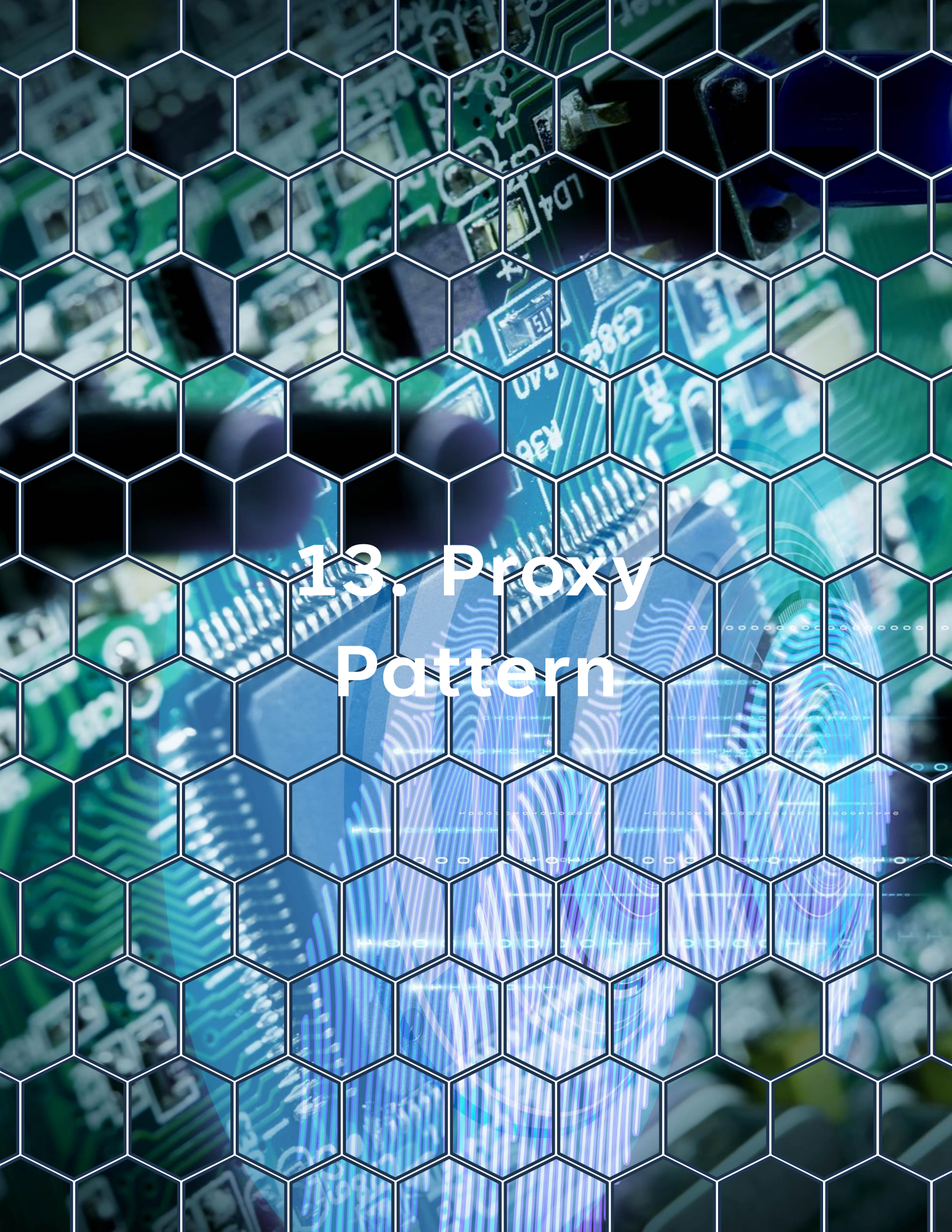


# 12. Object Pool Pattern

## Example: UART Packet Pool



```
1  #define POOL_SIZE 8
2  static UartPacket pool[POOL_SIZE];
3  static bool used[POOL_SIZE] = {false};
4
5  UartPacket* allocate_packet(void) {
6      for (int i = 0; i < POOL_SIZE; ++i) {
7          if (!used[i]) {
8              used[i] = true;
9              return &pool[i];
10         }
11     }
12     return NULL;
13 }
14
15 void release_packet(UartPacket* pkt) {
16     int index = pkt - pool;
17     if (index >= 0 && index < POOL_SIZE)
18         used[index] = false;
19 }
```



# 13. Proxy Pattern

# 13. Proxy Pattern

## Purpose

Acts as a controlled interface to access complex or sensitive resources.

## Use Case in Embedded

Used for EEPROM, Flash, or I2C abstraction with added safety or caching.

## Best Practice Notes

- Use static structs and clear boundaries.
- Implement protection for concurrent or unsafe access.

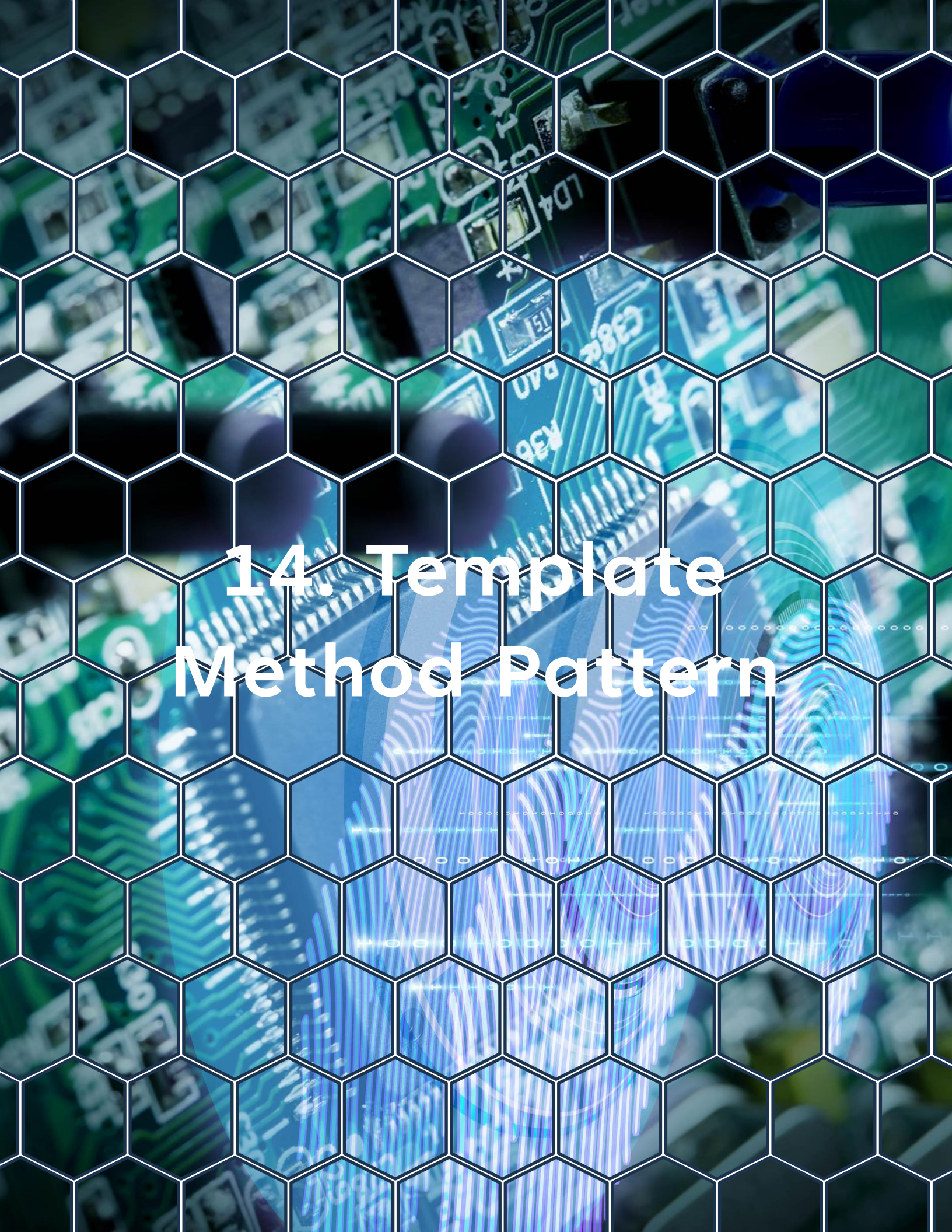


# 13. Proxy Pattern

## Example: EEPROM Proxy



```
1 uint8_t eeprom_proxy_read(uint16_t addr) {  
2     // Adds bounds check and wear leveling  
3     if (addr >= EEPROM_SIZE) return 0xFF;  
4     return EEPROM[map_address(addr)];  
5 }
```



# 14. Template Method Pattern

# 14. Template Method Pattern

## Purpose

Defines the skeleton of an operation, deferring specific steps to subclasses.

## Use Case in Embedded

Used to standardize interface behavior across hardware-specific drivers.

## Best Practice Notes

- Use function pointers in base structs.
- Keep the "template" flow centralized.



# 14. Template Method Pattern

## Example: Sensor Read Template

```
1  #include <stdbool.h>
2  #include <stdint.h>
3  #include <stdio.h>
4
5  // === Sensor Interface ===
6  typedef struct {
7      void    (*start)(void);
8      bool    (*ready)(void);
9      int16_t (*read)(void);
10 } SensorInterface;
11
12 // === Template Method ===
13 void read_sensor(SensorInterface* sensor) {
14     sensor->start();
15     // Blocking wait – replace with timeout logic if needed
16     while (!sensor->ready());
17     int16_t value = sensor->read();
18     // Replace with actual processing
19     printf("Sensor Value: %d\n", value);
20 }
21
22 // === DHT22 Implementation ===
23 void dht22_start(void)      { printf("DHT22: Start\n"); }
24 bool dht22_ready(void)     { return true; } // Simulate immediate readiness
25 int16_t dht22_read(void)   { return 245; } // Dummy value
26
27 SensorInterface DHT22 = {
28     .start = dht22_start,
29     .ready = dht22_ready,
30     .read  = dht22_read
31 };
32
33 // === SHT3x Implementation ===
```

# 14. Template Method Pattern

```
15 // Blocking wait – replace with timeout logic if needed
16 while (!sensor->ready());
17 int16_t value = sensor->read();
18 // Replace with actual processing
19 printf("Sensor Value: %d\n", value);
20 }
21
22 // === DHT22 Implementation ===
23 void dht22_start(void)      { printf("DHT22: Start\n"); }
24 bool dht22_ready(void)     { return true; } // Simulate immediate readiness
25 int16_t dht22_read(void)   { return 245; } // Dummy value
26
27 SensorInterface DHT22 = {
28     .start = dht22_start,
29     .ready = dht22_ready,
30     .read  = dht22_read
31 };
32
33 // === SHT3x Implementation ===
34 void sht3x_start(void)     { printf("SHT3x: Start\n"); }
35 bool sht3x_ready(void)     { return true; }
36 int16_t sht3x_read(void)   { return 278; }
37
38 SensorInterface SHT3X = {
39     .start = sht3x_start,
40     .ready = sht3x_ready,
41     .read  = sht3x_read
42 };
43
44 // === Main Usage ===
45 int main(void) {
46     read_sensor(&DHT22); // Uses DHT22 sequence
47     read_sensor(&SHT3X); // Uses SHT3x sequence
48     return 0;
49 }
```





# 15. Conclusion



## 15. Conclusion

Design patterns offer powerful abstractions and reusable structures for embedded software development, especially under tight memory, power, and performance constraints. When adapted correctly, they provide maintainable and scalable architectures even for bare-metal microcontrollers like the ATtiny1616.

However, using them effectively requires a deep understanding of the system's limitations. Avoid dynamic memory, keep execution non-blocking, and prioritize deterministic behavior. These patterns are not just academic concepts—they're essential tools for delivering reliable embedded firmware in the real world.