

Dhirubhai Ambani Institute of Information and Communication Technology

LAB TIME TABLE

Winter Semester 2015-16

Core/ Day The Latty Company Th										
Core/ Elec	Mon	Tue	Wed	Thu	Fri	Time	Lab No	Course	Title	Faculty
BTech I (Sem II, 2015 Batch)										
Core	Gr 1	Gr 2	Gr 3	Gr 4	Gr 5	14:00 to 17:00	107	EL114	Digital Logic Design	Rajeev Lochan Das
Core	Gr 2	Gr 3	Gr 4	Gr 5	Gr 1	14.00 to 17.00	101	CT 111	Introduction to Communication Systems	Sanjeev Gupta
Core	Gr 3	Gr 4	Gr 5	Gr 1	Gr 2	14:00 to 17:00	001	IT 115	Object Oriented Programming Lab	Archana Nigam (PhD student)
BTech II (Sem IV, 2014 Batch)										
Core	Gr 1	Gr 2	Gr 3	Gr 4	Gr 5	14:00 to 17:00	104	CT214	Analog and Digital Commn ication	Deepak Ghodgaokar
Core	Gr 2	Gr 3	Gr 4	Gr 5	Gr 1	14:00 to 17:00	102	EL213	Analog Circuits	Rutu Parekh
Core	Gr 3	Gr 4	Gr 5	Gr 1	Gr 2	14:00 to 17:00	008	IT215	Systems Software	A Mathuria
Core	CS stud	ents		•	•	15:00 to 18:00	207	CS201	Introductory Computational Physics	Bhaskar Chaudhury
BTech III (Sem VI, 2013 Batch), MTech (as applicable)										
Core	Gr 3	Gr 4	Gr 1	Gr 2	Gr 5	14:00 to 17:00	005	IT314	Software Engineering	A Banerjee
Slot 2	Gr 4	Gr 1	Gr 2	Gr 3		14.00 to 16.00	108	CT421	Adv Digital Signal processing	Hemant Patil
Slot 1	Gr 4	Gr 1	Gr 2	Gr 3		16.00 to 18.00	211	IT478	Internet of Things	Sanjay Srivastava
Slot 1	Gr 1	Gr 2	Gr 3	Gr 4		14:00 to 16:00	205	EL426	Digital System Architecture	Amit Bhatt
Slot 3	Gr 5 (Lab 007))			Gr 1, 2, 3, 4	14.00 to 16.00	004, 007	IT308	Operating Systems	ND Jotwani*
Slot 4	, i			Gr 5 (Lab 007)	Gr 1, 2, 3, 4	16.00 to 18.00	004, 007	IT415	Software Testing and Quality Analysis	Sourav Tiwari
Slot 6	Gr 2	Gr 3	Gr 4	Gr 1		14.00 to 16.00	211	EL428	Topics in Medical Electronics	A Sengupta*
Slot 5				•	ALL	14:00 to 16:00	108	CT314	Statistical Commn Theory	MV Joshi
Core			Gr 5			14.00 to 16.00	207	CS302	Modeling and Simulation	Mukesh Tiwari
Slot 6			Gr 5			16.00 to 18.00	207	CS305	Adavance Techniques in System Biology	Ganesh Baglar
MScIT I (Sem II, 2015 Batch)										
Core	ALL					14.00 to 18.00	002	IT618	Enterprise Computing	Pokhar Mal Jat
Core		ALL				14.00 to 16.00	OO2	IT617	Operating Systems	P Majumdar
Core			ALL			14.00 to 16.00	OO2	IT619	Design of Software Systems	Saurav Tiwari
Core				ALL		14.00 to 16.00	002	IT694	Computer Networks	P Sasidhar
MScICT-ARD I (Sem II, 2015 Batch)										
Core	ALL					14:00 to 16:00	204	IT664	Remote Sensing and GIS	R Ghosh
Core		ALL				14:00 to 16:00	204	ES662	Systems Approaches to Sustain. Dev.	R Ghosh
Core			ALL			14:00 to 16:00	204	PC672	Research Methodology	B Hiremath
Core					ALL	14:00 to 16:00	204	IT667	Database Management Systems	Minal Bhise
Core				ALL		14:00 to 16:00	204	PC667	Quantitative Analysis II	D Sisodia*
MDes I (Sem II, 2015 Batch)										
Core		ALL				14:00 to 16:00	CEP 004		Fundamentals of Design - II	B Desai
Core			ALL			14:00 to 16:00	CEP 004		Introduction to Narratology	A N Sarkar
Core				ALL		14:00 to 18:00	CEP 004		Web Design: Applications, Inter-connectib	
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