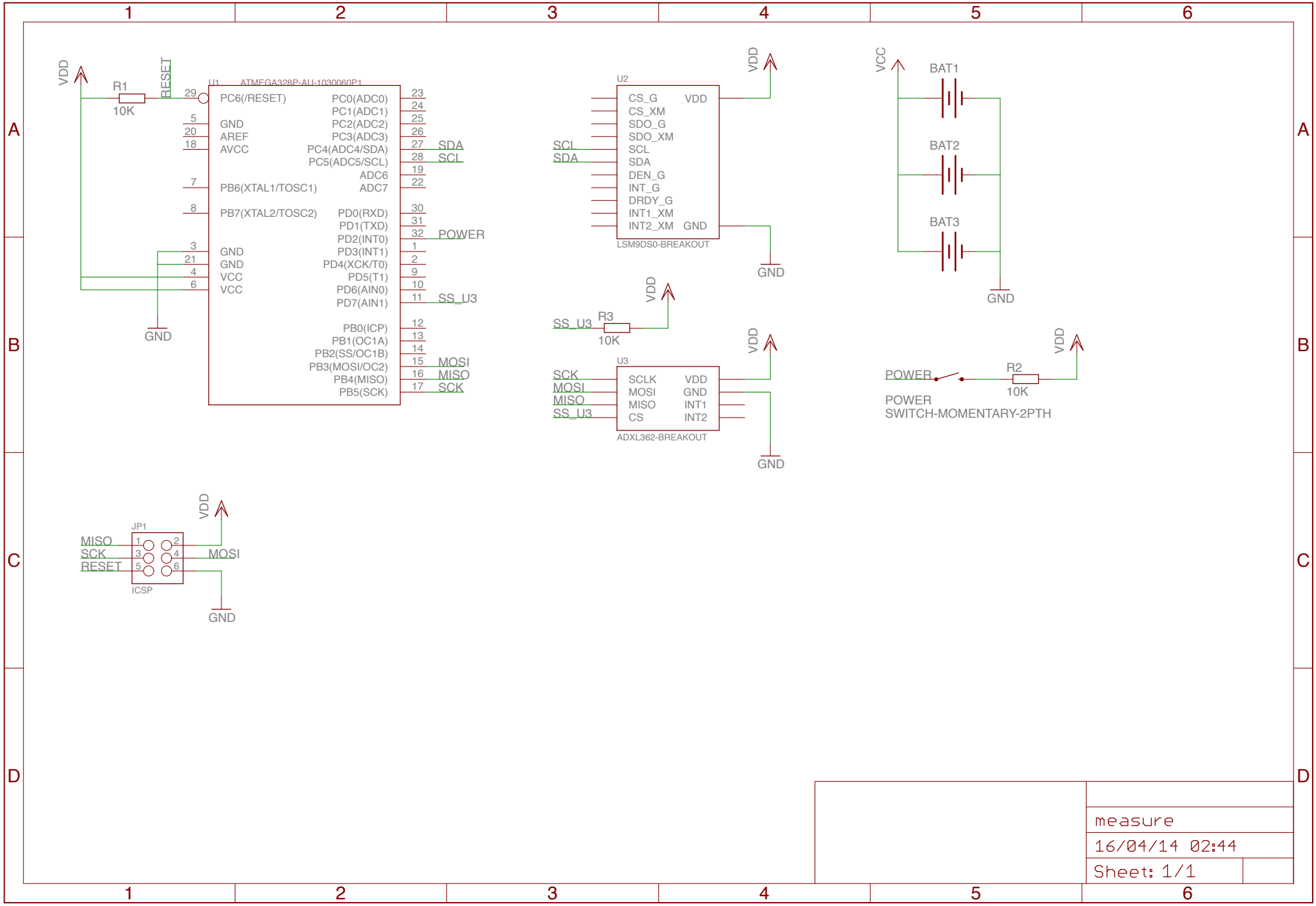
VDD
R1
10K
RESET
U1 ATMEGA328P-AU-1030060P1
29 PC6(/RESET)
5 GND
20 AREF
18 AVCC
7 PB6(XTAL1/TOSC1)
8 PB7(XTAL2/TOSC2)
3 GND
21 GND
4 VCC
6 VCC
GND
PC0(ADC0) 23
PC1(ADC1) 24
PC2(ADC2) 25
PC3(ADC3) 26
PC4(ADC4/SDA) 27 SDA
PC5(ADC5/SCL) 28 SCL
ADC6 19
ADC7 22
PD0(RXD) 30
PD1(TXD) 31
PD2(INT0) 32 POWER
PD3(INT1) 1
PD4(XCK/T0) 2
PD5(T1) 9
PD6(AIN0) 10
PD7(AIN1) 11 SS_U3
PB0(ICP) 12
PB1(OC1A) 13
PB2(SS/OC1B) 14
PB3(MOSI/OC2) 15 MOSI
PB4(MISO) 16 MISO
PB5(SCK) 17 SCK
U2
CS_G
CS_XM
SDO_G
SDO_XM
SCL
SDA
DEN_G
INT_G
DRDY_G
INT1_XM
INT2_XM
VDD
GND
LSM9DS0-BREAKOUT
SCL
SDA
VDD
GND
VCC
BAT1
BAT2
BAT3
GND
SS_U3
R3
10K
VDD
U3
SCLK
MOSI
MISO
CS
VDD
GND
INT1
INT2
ADXL362-BREAKOUT
SCK
MOSI
MISO
SS_U3
VDD
GND
POWER
R2
10K
VDD
POWER
SWITCH-MOMENTARY-2PTH
JP1
MISO 1
SCK 3
RESET 5
2
4 MOSI
6
ICSP
VDD
GND
measure
16/04/14 02:44
Sheet: 1/1