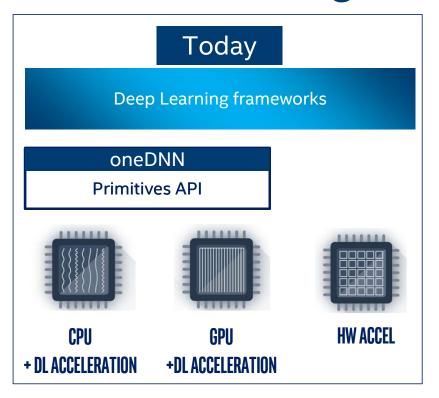


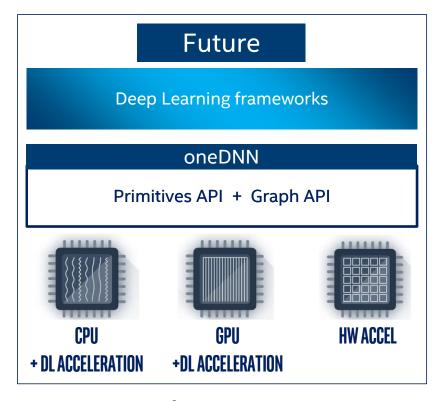
oneDNN Graph API

Jianhui Li Principal Engineer, Intel



oneDNN is evolving...





- Graph API allows HW backend to maximize performance
- Same integration for multiple AI HW: CPU, GPU, and accelerators



Latest Update from oneDNN Graph

1. SPEC v0.2 preview available on oneAPI SPEC website

https://spec.oneapi.com/onednn-graph/latest/

2. oneDNN Graph API code preview branch on oneDNN github

https://github.com/oneapi-src/oneDNN/tree/dev-graph

- 3. Pytorch experimental PR available and received positive feedback from FB https://github.com/pytorch/pytorch/issues/49444
- 4. TensorFlow experimental PR ready for feedback

https://github.com/Intel-tensorflow/tensorflow/tree/dev-graph/third_party/oneDNNGraph



oneDNN Graph SPEC Roadmap

SPEC0.2

- oneDNN Graph programing model
- FP32/FP16/BF16 Ops for Inference and Training

SPEC0.5

- Blocked Layout
- In-place Support

SPEC0.8

• Int8 Inference

SPEC1.0

• V1.0 Finalize

SPEC2.0 and beyond

- Control Flow
- Dynamic Shape
- Custom OP Registration

Q4'20

Q1'21

Q3'21

Q1'22

Future



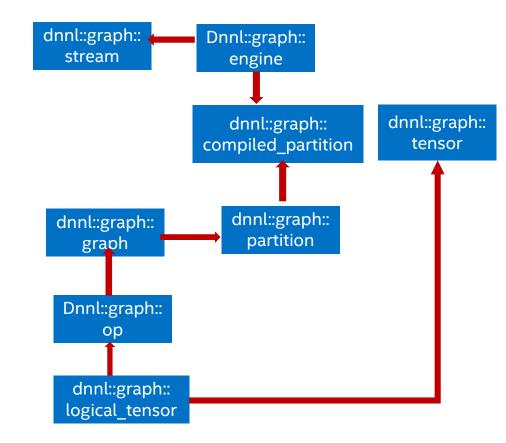
oneDNN Graph programming model

Partition

- Logical tensor: tensor's metadata like dims, data type, layout
- Op: DNN op with attributes, associated with input/output logical tensors
- Graph: a collection of Op and logical tensors
- Partition: a subgraph for target specific optimization

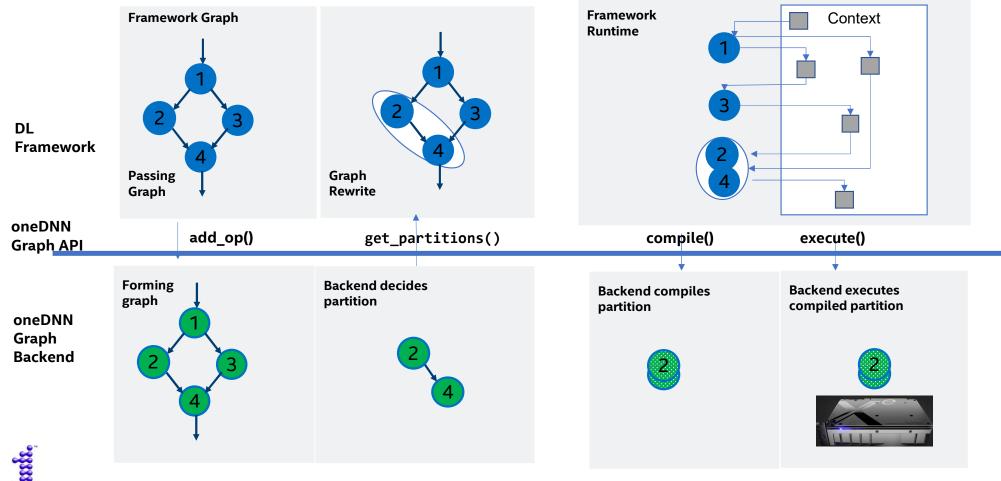
Compilation & Execution

- Engine execution device
- Stream execution context
- Compiled partition: compiled object for partition
- Tensor: data storage + metadata

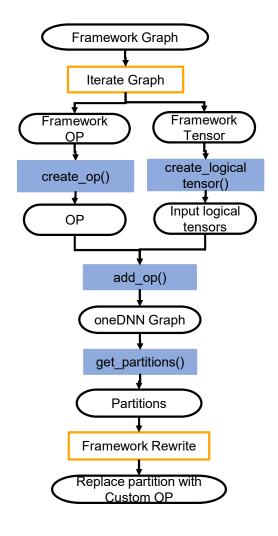


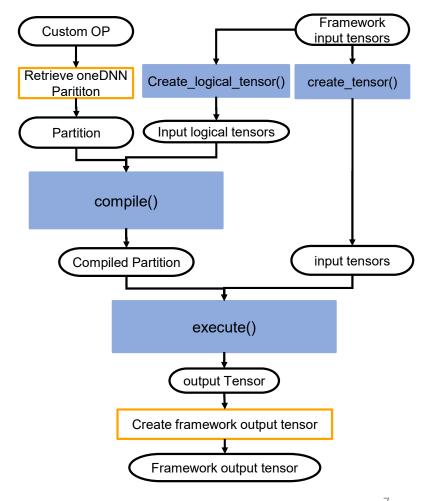


oneDNN Graph API



Framework Integration Flow Graph







Framework Object



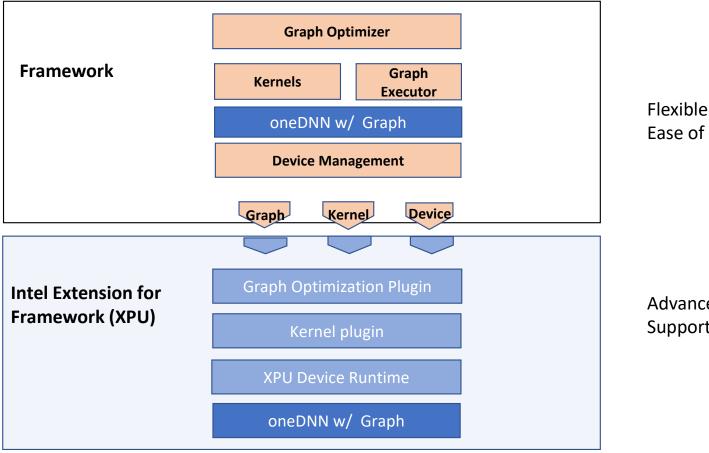
Framework Function



Framework Function using one DNN Graph



Framework Integration Scenario



Flexible fusion
Ease of integration

Advanced optimization Support multiple AI HW





Thank You!

http://oneapi.com