			_	
MIX	∆Inh∶	americ	Codes	

CVMPO	MIX Alphar				od	es					
SYMBOL	Commuter	CODE									
1417	Computer	Paper Tape							+		
MIX	and		Channel				Punch ±		±		
and Printer	Magnetic Tape	X	0	С	8	4	2	1	Card		± ± ± ± ± ±
(Space)	00	Ĥ	Ť	√	Ť	-	Ē	ŕ	(Bla	nk)	÷
A	01	Х	0			Г		1	12	1	±
B	02	Х	0				2		12	2	±
С	03	Χ	0				2	1	12	3	<u>±</u>
D	04	Χ	0			4			12	4	÷
E	05	Χ	0	1		4		1	12	5	±
<u> </u>	06	Х	0	1		4	2	Ц	12	6	±
<u>G</u>	07	Х	0	_	Ļ	4	2	1	12	7	± ± ± ± ± ±
<u>H</u>	10	X	0	-1	8	\vdash		Ц	12	8	÷
1	11	X	0	√ √	8	\vdash	H	1	12	9	±
Δ	12	X	0	\ √				4	12		±
	13 14	X X		\ √			2	1	11 11	1	<u> </u>
	15	^	┝	\ \ \	_	┝	2	1	11	2	÷
M	16	X	\vdash	1	_	4	۲	H	11	3 4	Ξ
N	17	x	Н	Ė	Н	4	Н	1	11	- 5	±
0	20	X				4	2	Ė	11	6	± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ±
 P	21	X		1		4	2	1	11	7	÷
Q	22	X		1	8	Ė		Ė	11	8	±
R	23	Х			8	T		1	11	9	±
Σ	24	Х			Ť				11	_	±
П	25	Г	0			Г		П	0	1	±
S	26		0				2		0	2	Ŧ
T	27		0				2	1	0	3	±
U	30		0	1		4			0	4	<u>±</u>
V	31		0			4		1	0	5	-
W	32		0	L		4	2		0	6	±
X	33		0	1		4	2	1	0	7	±
Υ	34		0	1	8	L		Ш	0	8	± ± ± ±
<u>Z</u>	35		0		8	L	Ļ	1	0	9	-
<u>0 (Zero)</u>	36	L	_	√	8	┝	2	\perp	0		±
1	37		L	_		L	Ļ	1		1	±
2	40		_	1	_	┝	2	4		2	±
3 4	41	H		٧	_	4	2	1		3	-
5	42 43	⊢	⊢	1	⊢	4	H	1		4 5 6 7	±
6	44	Н	H	V	Н	4	2	H			±
7	45			Ė		4	2	1			÷
8	46				8	H	ŕ	H		8	-
9	47			1	8			1		9	±
	50	Х	0	1	8	Т	2	H	12	2-8	±
	51	X	0		8	Г	2	1	12 :	3-8	± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ± ±
	52	X	0	1	8	4			12 -	4-8	Ŧ
	52 53	Х	0		8	4		1	12 · 12 ·	<u>5-8</u>	±
+	54	X	0		8	4	2		12	6-8	±
	55	Х	0		8		2	1	12	<u>7-7</u>	<u>±</u>
*	56	Χ			8		2		11 :	2-8	± +
	57	Х	Ĺ	1	8	Ĺ	2	1	11 :	3-8	±
=	60	Х	Ĺ	Ļ	8	4	\Box	Ц	11 4 11 8	4-8	±
\$	61	Х	L	1	8	4		1	11 :	5-8	Ŧ
<	62	X	L	1	8	4	2	Ц	11 (8-6	±
>	63	Х	Ļ	_	8	4	2	1	11	7-8	\pm
@	64		0	,,	8	lacksquare	2	Ц	0 :	7-8 2-8 3-8	[r]:
;	65	L	0	√	8	Ļ	2	1	0 :	3-8	
:	66	L	0	2,1	8	4	\vdash	H	0 4	4-8	
	67	L	0	1	8	4	L	1	0 :	5-8	

MIX A Summary of Operations in Operation-Code Order								
INSTR.FORMAT				ATION				
±		l F	С	ABR	DF	INAME		
	aaaa		00	NOP	0	NO OPERATION		
±	aaaa	L:R	01	ADD	0:5	ADD		
±	aaaa	i 06	01	FADD		FLOATING ADD		
_		L:R	02	SUB	0:5	SUBTRACT		
<u>±</u>	aaaa		02 03	FSUB MUL	0:5	FLOATING SUBTRACT		
		i L:R	03	FMUL	0.5	FLOATING MULTIPLY		
		L:R	04	DIV	0:5	DIVIDE		
±		06	04	FDIV		FLOATING DIVIDE		
±		i 00	05	NUM		CONVERT TO NUMERIC		
		i 01	05	CHAR		CONVERT TO CHARACTERS		
	aaaa		05	HLT		HALT		
		03 04	05	AND		LOGICAL PRODUCT LOGICAL SUM		
± +		i 04 i 05	05 05	OR XOR		LOGICAL SUM		
±	aaaa		05	FLOT		FIXED TO FLOAT		
		07	05	FIX		FLOATING TO FIXED		
±		10	05	NEG		LOGICAL NEGATION		
±	aaaa	i 11	05	XCH		EXCHANGE A AND X		
	0.0.0.0.	i 12	05	XEQ		EXECUTE		
±		i 00	06	SLA		SHIFT LEFT A		
±		01	06	SRA		SHIFT RIGHT A		
<u>±</u>	0.0.0.0.	i 02 i 03	06 06	SLAX SRAX		SHIFT LEFT AX SHIFT RIGHT AX		
土	aaaa		06	SLC		SHIFT LEFT AX CIRCULARLY		
±		05	06	SRC		SHIFT RIGHT AX CIRCULARLY		
<u>±</u>		06	06	SLB		SHIFT LEFT LOGICAL AX		
±	aaaa	07	06	SRB		SHIFT RIGHT LOGICAL AX		
		i 11	06	INT		INTERRUPT		
		i N	07	MOVE	1	MOVE WORDS		
			10+[r]	LD[r]	0:5	LOAD TAKE CATIVE		
<u>±</u>		i L:R	20+[r] 30+[r]	LD[r]N ST[r]	0:5	LOAD r NEGATIVE STORE		
		L:R	40	STJ	0:3	STORE J		
	aaaa		41	STZ	0:5	STORE ZERO		
±	aaaa	Ū	42	JBUS	0	JUMP BUSY		
±	aaaa	i U	43	IOC	0	I/O CONTROL		
	0.0.0.0.	Ū	44	IN	0	INPUT		
		U	45	OUT	0	OUTPUT		
<u>+</u>	aaaa		46	JRED	0	JUMP READY JUMP		
	aaaa aaaa		47 47	JMP JSJ		JUMP SAVE J		
	aaaa		47	JOV		JUMP ON OVERFLOW		
	aaaa		47	JNOV		JUMP ON NO OVERFLOW		
±	aaaa	04	47	JL		JUMP ON LESS		
	aaaa	i 05	47	JE		JUMP ON EQUAL		
		06	47	JG		JUMP ON GREATER		
	aaaa		47	JGE		JUMP ON GREATER-OR-EQUAL		
		i 10 i 11	47 47	JNE JLE		JUMP ON UNEQUAL JUMP ON LESS-OR-EQUAL		
	aaaa		50+[r]			JUMP r NEGATIVE		
	aaaa		50+[r]			JUMP r ZERO		
	aaaa		50+[r]			JUMP r POSITIVE		
±	aaaa	03	50+[r]	J[r]NN		JUMP r NONNEGATIVE		
		i 04	50+[r]	J[r]NZ		JUMP r NONZERO		
	aaaa		50+[r]			JUMP r NONPOSITIVE		
						JUMP r EVEN		
			50+[r] 60+[r]			JUMP r ODD INCREASE r		
		i 00 i 01	60+Iri	DEC[r]		DECREASE r		
	aaaa	02	60+[r]	ENT[r]		ENTER r		
				ENN[r]		ENTER NEGATIVE r		
	aaaa	04	60+[r]	CP[r]M		COMPARE r WITH M		
±	aaaa	i L:R	70+[r]	CMP[r]	0:5	COMPARE r		
				FCMP		FLOATING COMPARE		
[r]:	rA=0, rl	1, rl2, i	rı3, rl4,	r15, r16, r)	K= 7, i	: I1:I2, 7 is indirect addressing		