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jminusminus

Class NLIRLoad

- [java.lang.Object](#)
 - [jminusminus.NLIRInstruction](#)
 - [jminusminus.NLIRLoad](#)

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```
class NLIRLoad  
extends NLIRInstruction
```

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LIR instruction representing a load from memory to register.

- [Field Summary](#)
- [Fields inherited from class jminusminus.NLIRInstruction](#)
[block](#), [id](#), [lirMnemonic](#), [reads](#), [write](#)
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Constructors
Constructor and Description

```
NLIRLoad(NBasicBlock block, int id, int offset,  
OffsetFrom offsetFrom, NRegister register)  
Construct an NLIRLoad instruction.
```

- [Method Summary](#)

Methods

**Modifier and
Type**

Method and Description

void	toSpim(PrintWriter out) Translate this LIR instruction into SPIM and write it out to the specified output stream.
String	toString() Return a string representation of this instruction.

- **Methods inherited from class jminusminus.NLIRInstruction**
 allocatePhysicalRegisters
- **Methods inherited from class java.lang.Object**
 clone, equals, finalize, getClass, hashCode, notify, notifyAll, wait, wait, wait

- **Constructor Detail**

- **NLIRLoad**

```
public NLIRLoad(NBasicBlock block,
               int id,
               int offset,
               OffsetFrom offsetFrom,
               NRegister register)
```

Construct an NLIRLoad instruction

Parameters:

block - enclosing block.

id - identifier of the instruction.

offset - stack offset to load from.

offsetFrom - whether offset relative to stack pointer (sp) or frame pointer (fp).

register - register to load to.

- **Method Detail**

- **toSpim**

```
public void toSpim(PrintWriter out)
```

Description copied from class: NLIRInstruction

Translate this LIR instruction into SPIM and write it out to the specified output stream.

Overrides:

toSpim in class NLIRInstruction

Parameters:

out - output stream for SPIM code.

- **toString**

```
public String toString()
```

Description copied from class: NLIRInstruction

Return a string representation of this instruction.

Overrides:

toString in class NLIRInstruction

Returns:

string representation of this instruction.

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