

Screw_Cage_3.5mm-10

1 SDA2_I/O

2 SCL2_I/O

3 MEventB_I

4 MCTS2_I

5 MRTS2_O

6 MXT2_O

7 MRX2_I

8 MCTS2_I

9 DC_IN-

10 DC_IN+

VCCIO

C53 10µF 10V

JP15

GND

MCTS2_I

Close jumper to use CTS for VCCIO power output

[illegible]

Power

USB

GNSS

Ether

File: Etho

10

1



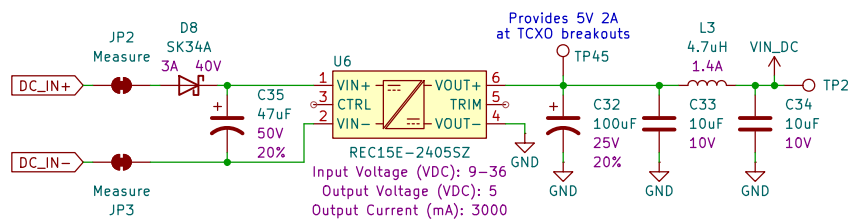
SPARK PNT

Title: GPSDO (mosaic-T, SiT5358)

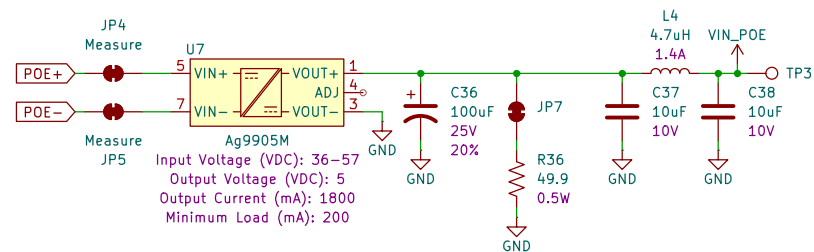
Rev: v10

Id: 1/7

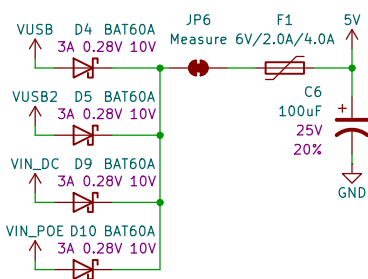
DC Power In



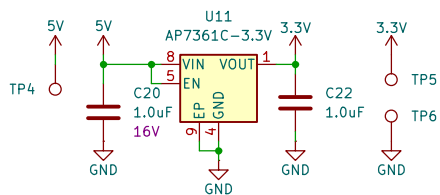
Power Over Ethernet



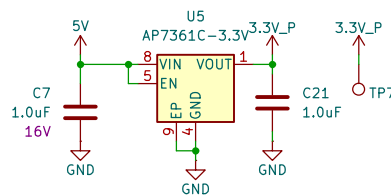
Power Mux



Main 3.3V



Peripheral 3.3V



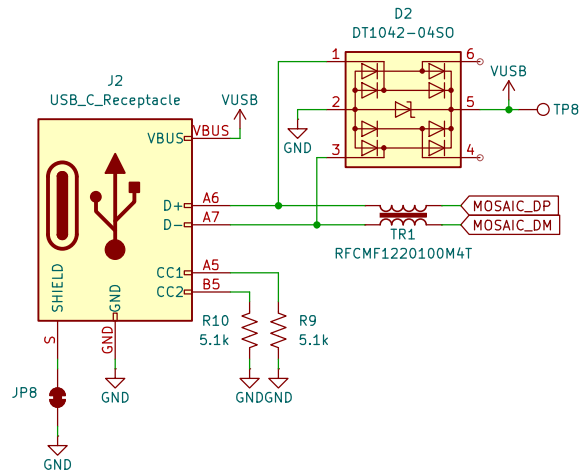
Sheet: /Power/
 File: Power.kicad_sch

Title: Power

Size: USLetter Date:
 KiCad E.D.A. 8.0.5

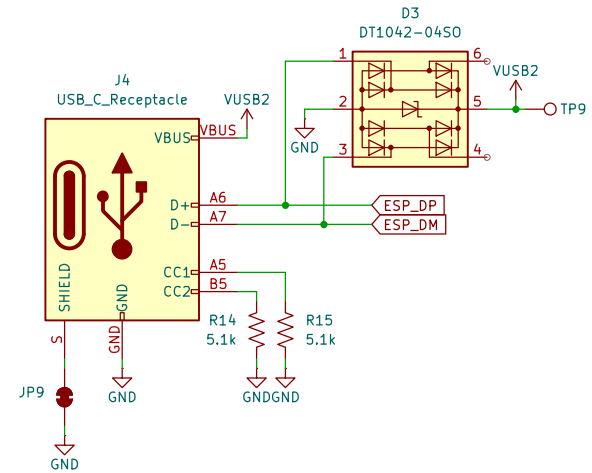
Rev:
 Id: 2/7

Mosaic USB

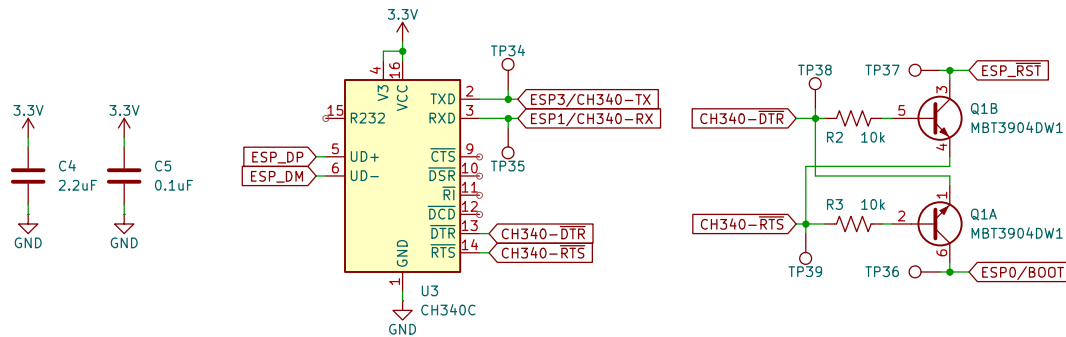


USB Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
 10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

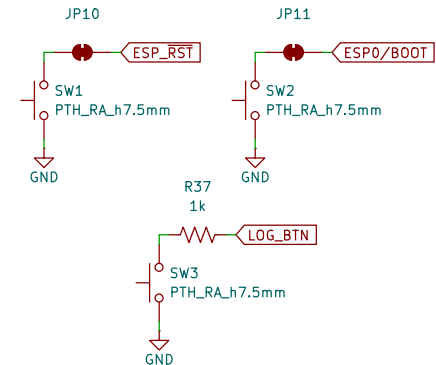
ESP32 USB



ESP32 USB to Serial



Buttons

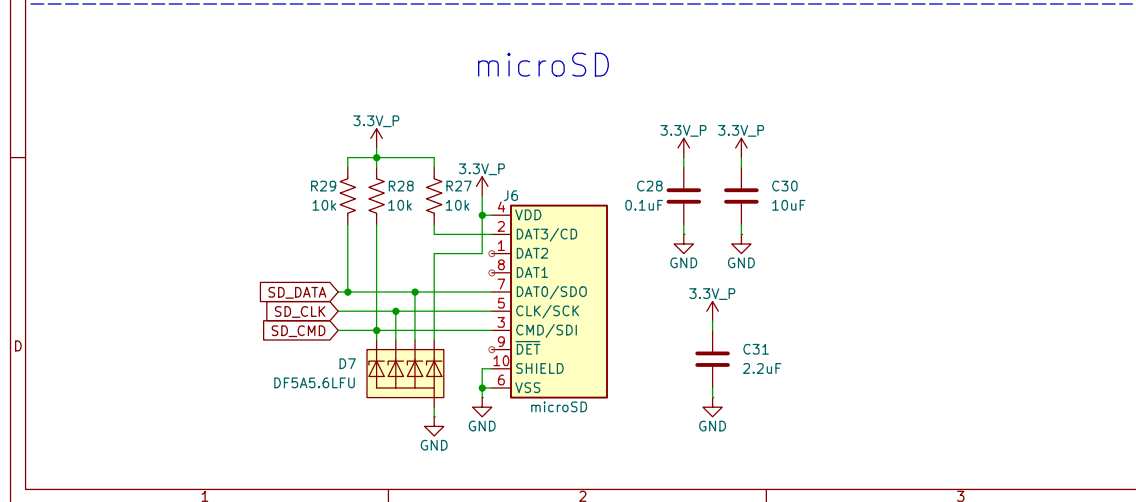
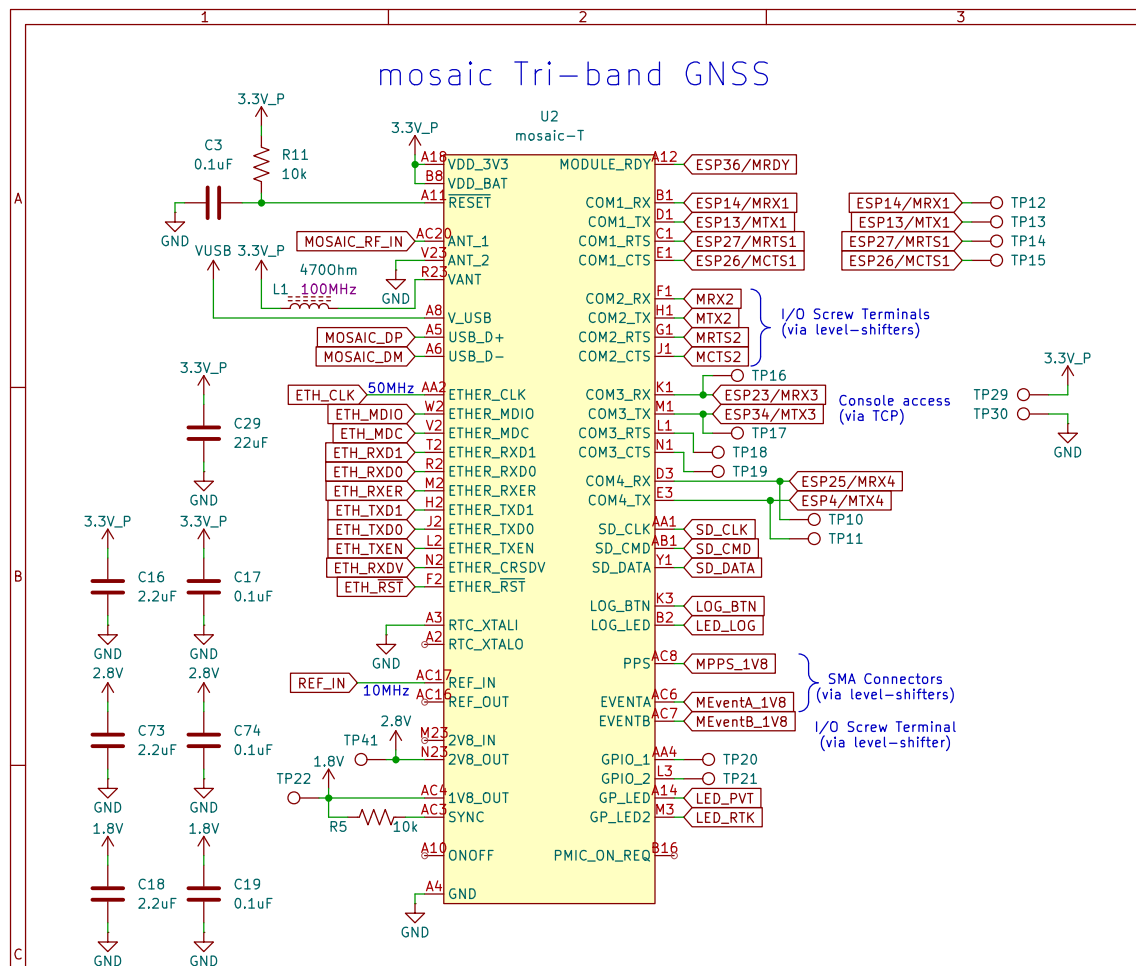


Sheet: /USB/
 File: USB.kicad_sch

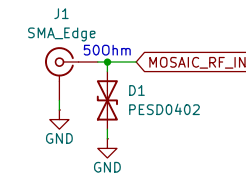
Title: USB

Size: USLetter Date:
 KiCad E.D.A. 8.0.5

Rev:
 Id: 3/7

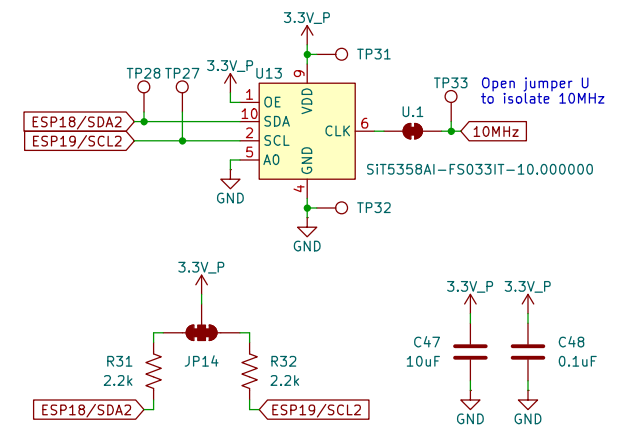


GNSS Antenna



Microstrip Calculation:
Copper Thickness (1oz): 1.4mil/0.035mm
Board thickness: 1.6mm
Dielectric thickness (layer 1 to 2): 0.2mm
Er: 4.6
Polygon Isolation: 6mil/0.1524mm
RF Trace Width: 13mil/0.33mm
<https://chemandy.com/calculators/coplanar-waveguide-with-ground-calculator.htm>

10MHz Oscillator



Sheet: /GNSS/
File: GNSS.kicad_sch

Title: GNSS

Size: USLetter	Date:
KiCad E.D.A. 8.0.5	

Rev:
Id: 4/7

Ethernet

The diagram shows a PCB layout for an Ethernet interface. On the left, a MagJack_PoE module (J5) is connected to a 3.3V_P supply. It includes a transformer (T1) and several resistors (R17, R18, R19, R20, R21, R22) and capacitors (C9, C10, C11, C12). A diode bridge (D6) is used for PoE. The Ethernet PHY (U4, KSZ8041NL/I) is connected to the 3.3V_P supply and various pins for MDIO, MDC, TX, RX, and control signals. The PHY is also connected to a 50MHz clock source (ETH_CLK) and a 100pF capacitor (C15). Various other components like resistors (R23, R24, R25, R26, R33) and capacitors (C13, C14, C23, C25, C26, C27) are shown for signal conditioning and decoupling.

Ethernet Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
Prepreg thickness: 8.3 mil (JLC7628), Er = 4.6
9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
Each pair should match in length to better than 0.5mm

Sheet: /Ethernet/
File: Ethernet.kicad_sch

Title: Ethernet

Size: USLetter Date: Rev:

KiCad E.D.A. 8.0.5 Id: 5/7

Ethernet Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
 9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
 Each pair should match in length to better than 0.5mm

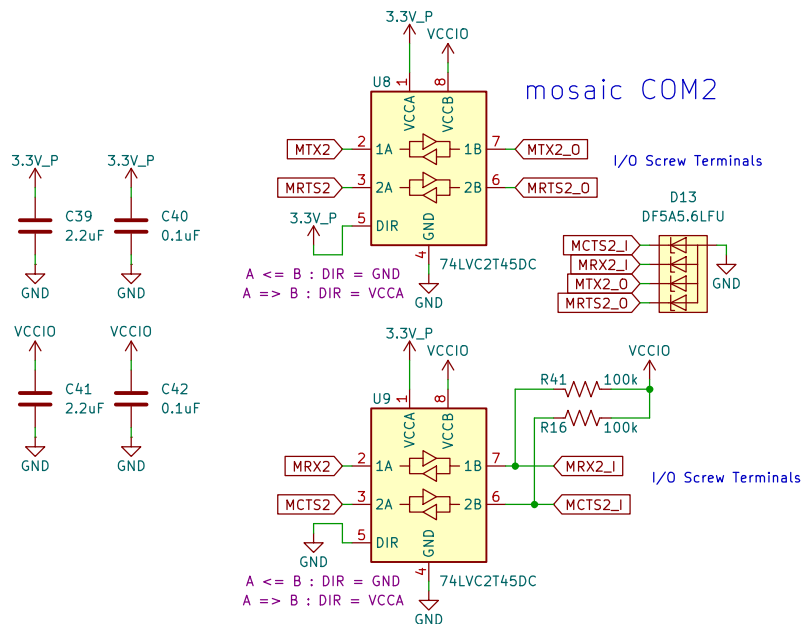
Sheet: /Ethernet/
File: Ethernet.kicad_sch

Title: Ethernet

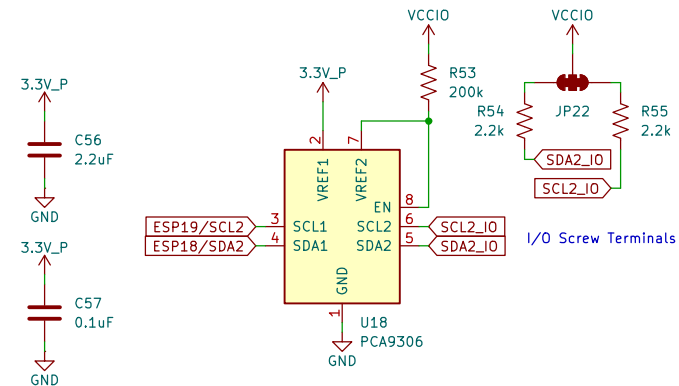
Size: USLetter	Date:
KiCad E.D.A. 8.0.5	

Rev:
Id: 5/7

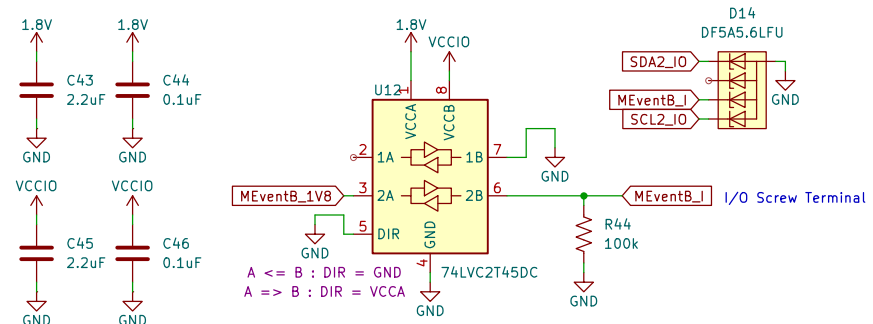
mosaic COM2



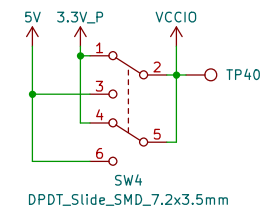
SDA2 SCL2



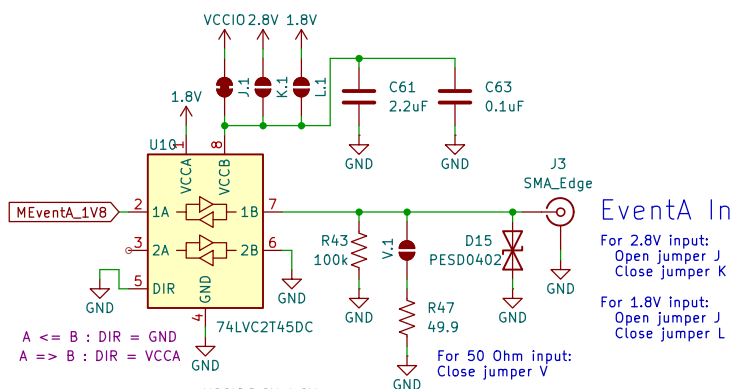
EventB In



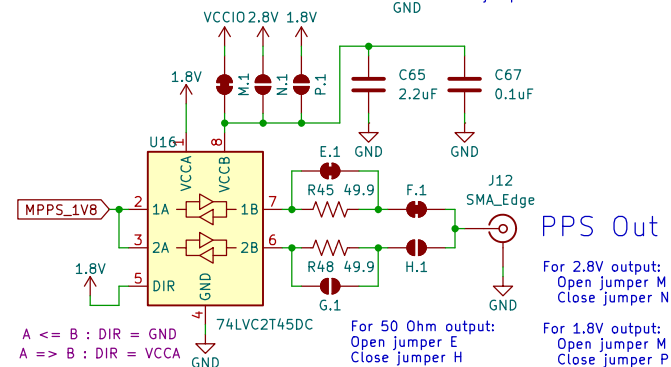
VCCIO voltage selection



EventA In



PPS Out



Level-Shifting

Sheet: /LevelShifting/
File: LevelShifting.kicad_sch

Title: Level Shifting

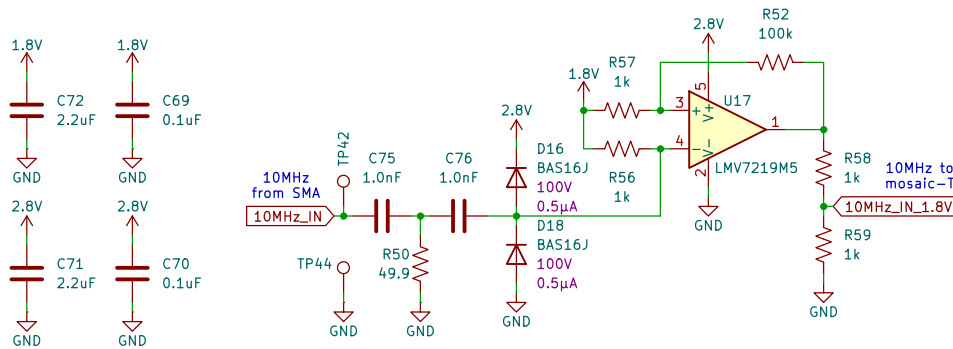
Size: USLetter Date:

KiCad E.D.A. 8.0.5

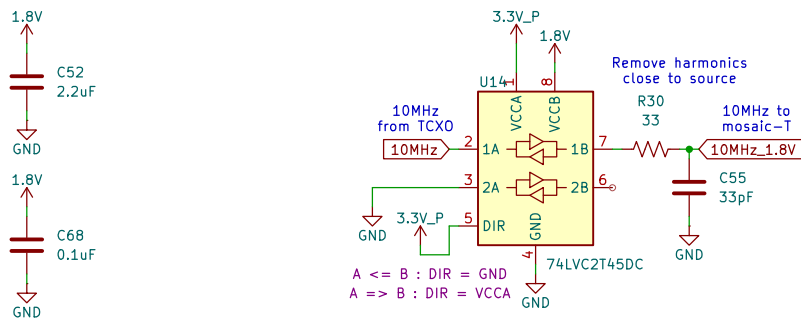
Rev:

Id: 6/7

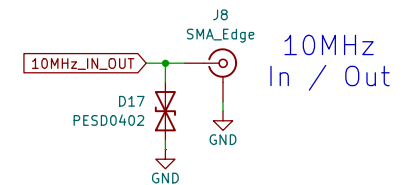
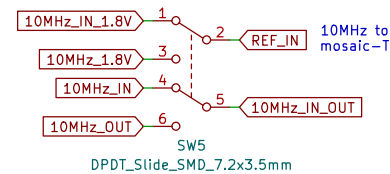
10MHz In
Input impedance: 50Ω
Detection level: -14dBm
Max supported level: +12dBm



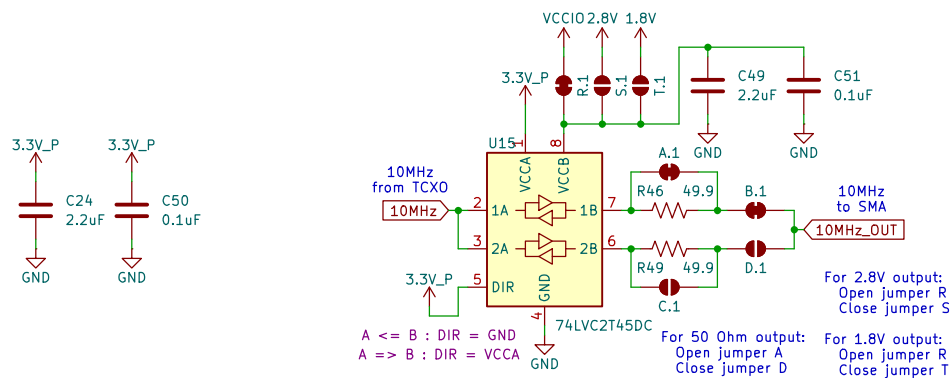
10MHz 1.8V for mosaic-T



10MHz In / Out



10MHz Out



Level-Shifting 10MHz

Sheet: /LevelShifting_10MHz/
File: LevelShifting_10MHz.kicad_sch

Title: Level Shifting 10MHz

Size: USLetter Date:

KiCad E.D.A. 8.0.5

Rev:

Id: 7/7