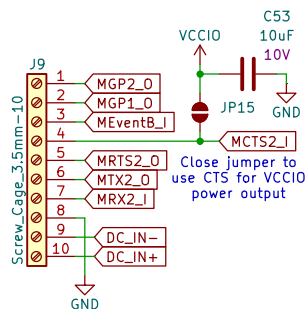
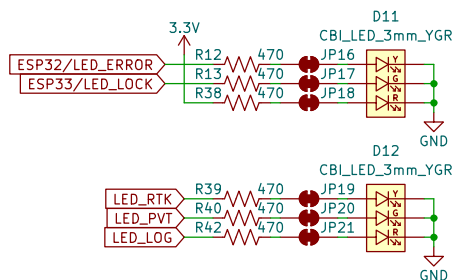


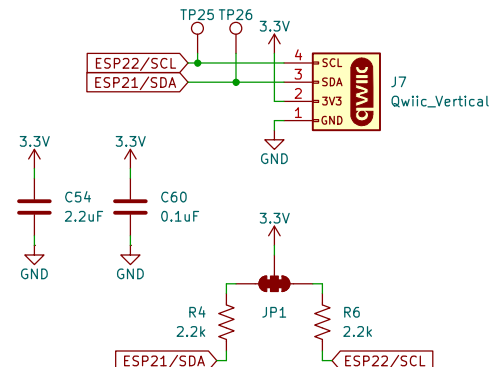
I/O Connector



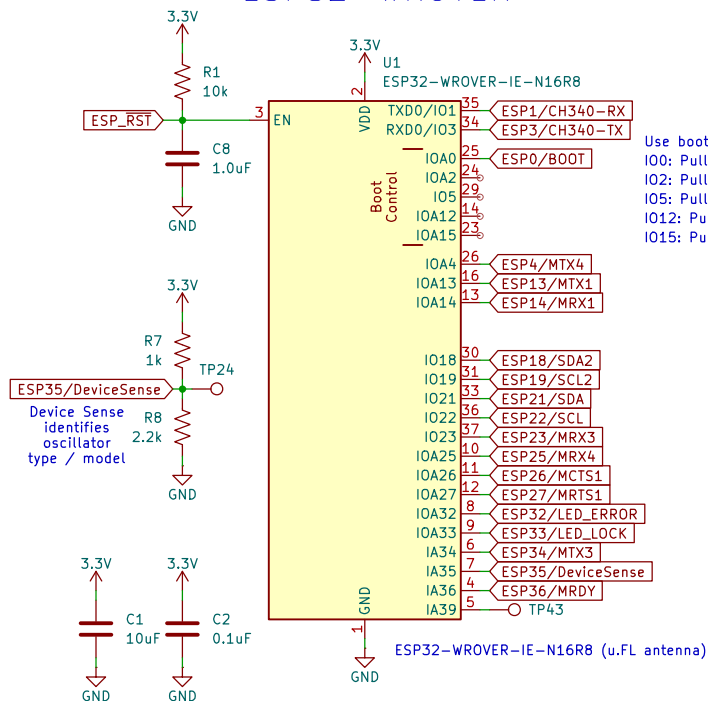
LEDs



Qwiic I²C (for OLED)



ESP32-WROVER



Use boot control pins with caution: 0, 2, 5, 12, 15
 IO0: Pull-up at boot. Can be used a stat LED.
 IO2: Pull-down at boot. Boot mode.
 IO5: Pull-up at boot. SDIO timing.
 IO12: Pull-down at boot. LDO voltage.
 IO15: Pull-up. TX0 debug active.

Power

File: Power.kicad_sch

USB

File: USB.kicad_sch

GNSS

File: GNSS.kicad_sch

Ethernet

File: Ethernet.kicad_sch

LevelShifting

File: LevelShifting.kicad_sch

LevelShifting_10MHz

File: LevelShifting_10MHz.kicad_sch

Oscillator

File: Oscillator.kicad_sch



SPARKPNT



Designed by: P.C.

Sheet: /

File: SparkPNT_GNSSDO_Plus.kicad_sch

Title: GNSSDO Plus (mosaic-T, STP3593LF)

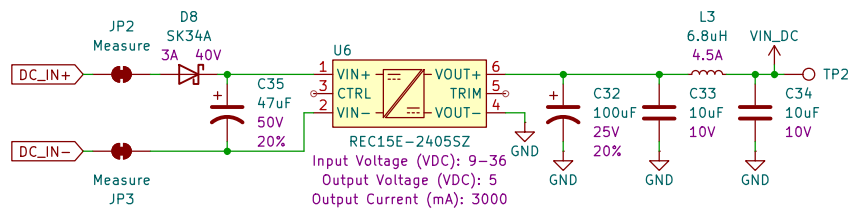
Size: USLetter Date: 2025-01-02

KiCad E.D.A. 8.0.7

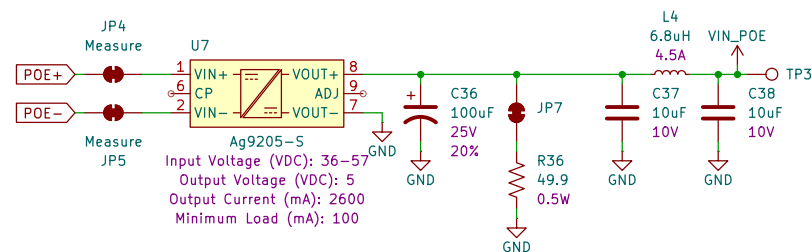
Rev: v10

Id: 1/8

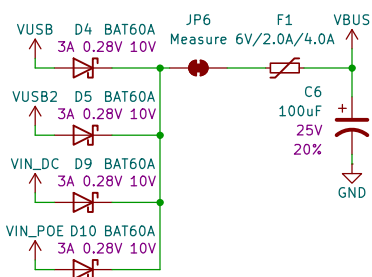
DC Power In



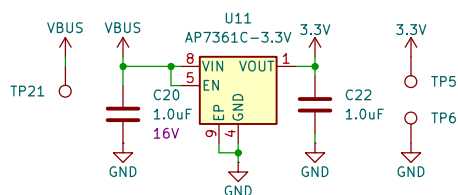
Power Over Ethernet



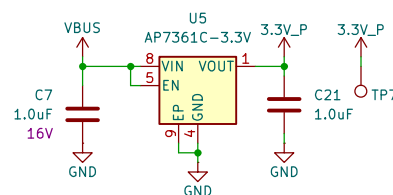
Power Mux



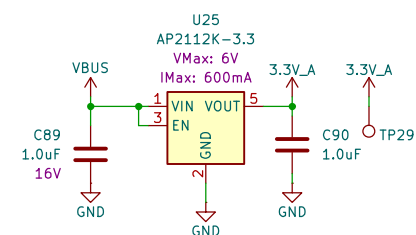
Main 3.3V



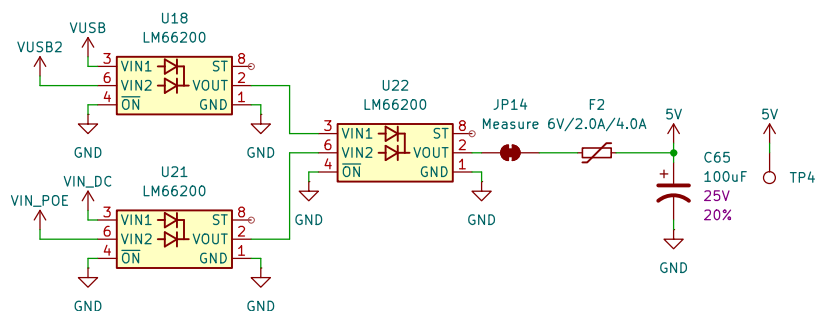
Peripheral 3.3V



Analog 3.3V



OCX0 Power Mux



Sheet: /Power/
File: Power.kicad_sch

Title: Power

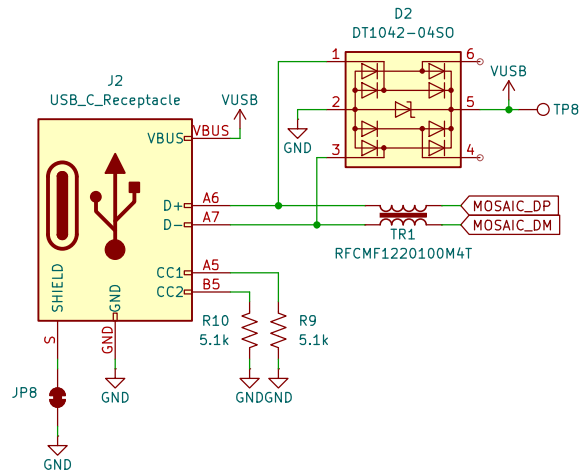
Size: USLetter Date:

KiCad E.D.A. 8.0.7

Rev:

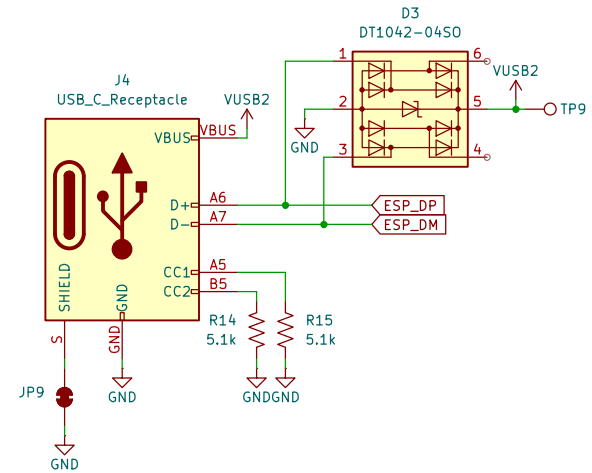
Id: 2/8

Mosaic USB

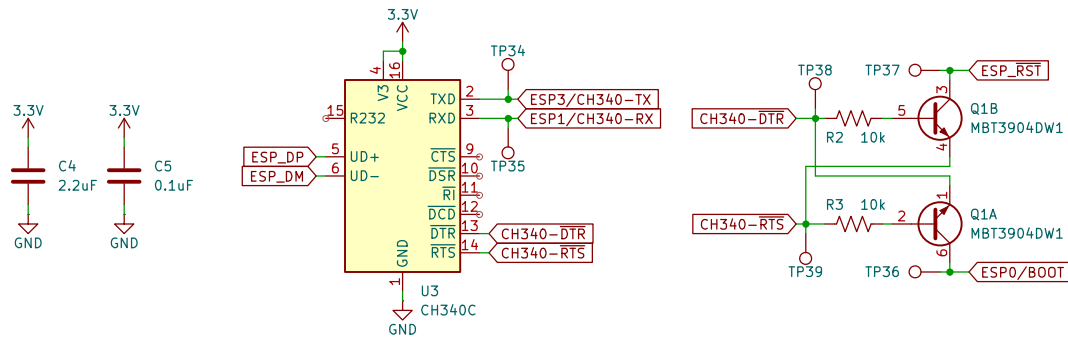


USB Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
 10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

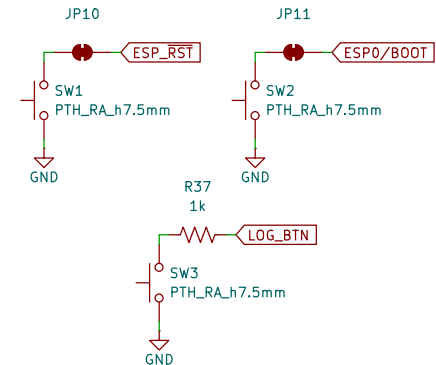
ESP32 USB



ESP32 USB to Serial – CH340C



Buttons



Sheet: /USB/
 File: USB.kicad_sch

Title: USB

Size: USLetter Date:
 KiCad E.D.A. 8.0.7

Rev:
 Id: 3/8

Ethernet

The schematic illustrates the Ethernet interface circuit. It begins with a MagJack_PoE module (J5) connected to a 3.3V_P supply. The module's pins are connected to a network of resistors (R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R33, R34, R35) and capacitors (C9, C10, C11, C12, C13, C14, C15, C23, C25, C26, C27). The circuit includes a 4700hm 100MHz inductor (L2) and a 4.7k resistor (R33). The Ethernet PHY (U4, KSZ8041NL/I) is connected to the 3.3V_P supply and the network of components. The PHY's pins are connected to the 3.3V_P supply and the network of components. The circuit also includes a 50MHz clock source (ETH_CLK) and a 100pF capacitor (C15). The schematic is labeled with various component values and pin numbers, and includes a title block at the bottom right.

Ethernet Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
Each pair should match in length to better than 0.5mm

Sheet: /Ethernet/ File: Ethernet.kicad_sch		Rev:
Size: USLetter	Date:	Id: 5/8
KiCad E.D.A. 8.0.7		

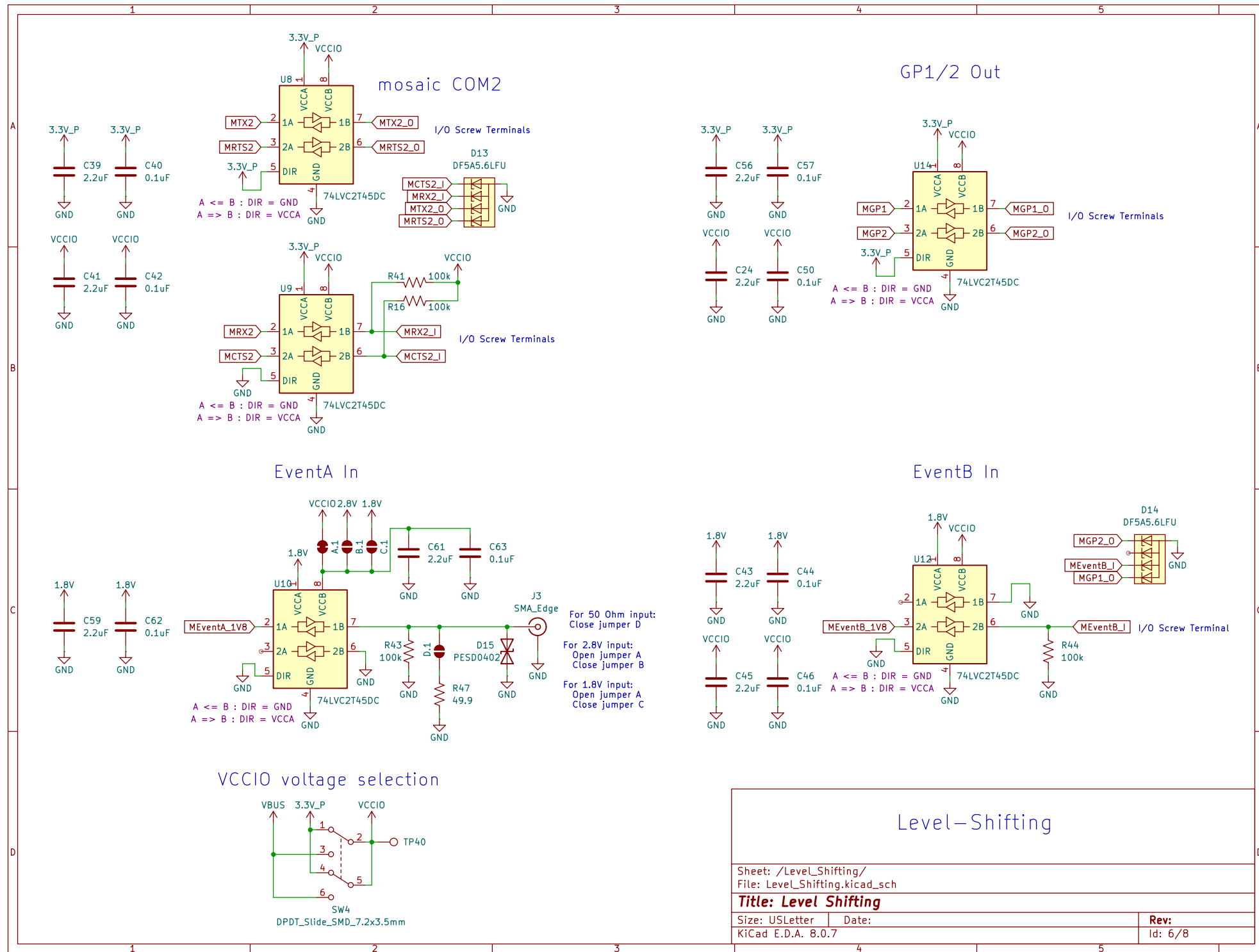
Ethernet Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
 9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
 Each pair should match in length to better than 0.5mm

Sheet: /Ethernet/
File: Ethernet.kicad_sch

Title: Ethernet

Size: USLetter	Date:
KiCad E.D.A. 8.0.7	

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GP1/2 Out

EventB In

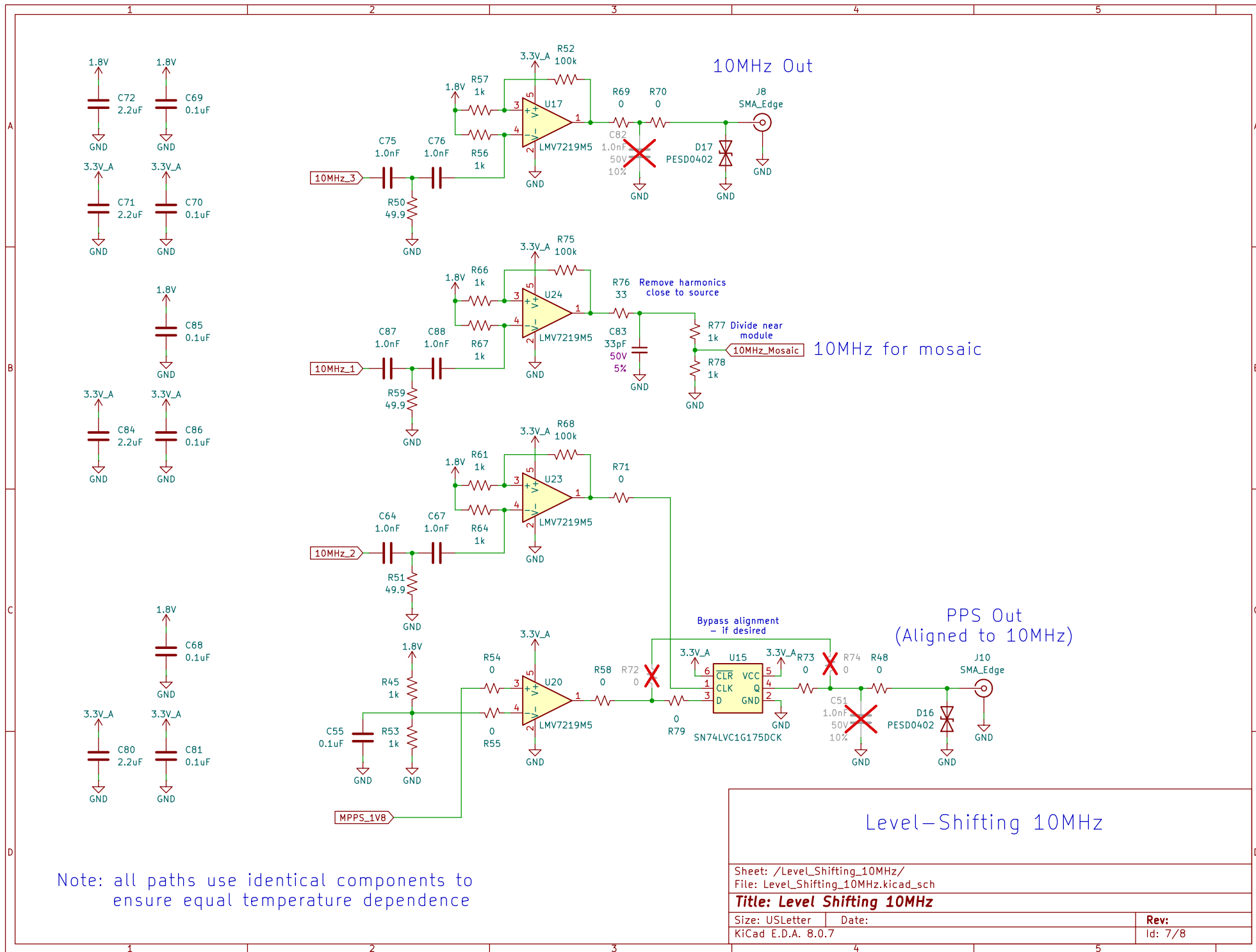
Level-Shifting

Sheet: /Level_Shifting/
File: Level_Shifting.kicad_sch

Title: Level Shifting

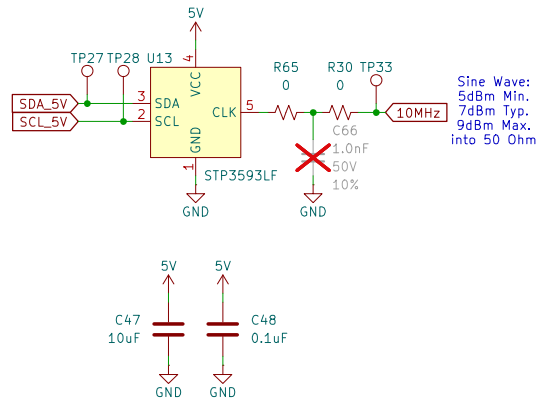
Size: USLetter Date:
KiCad E.D.A. 8.0.7

Rev:
Id: 6/8



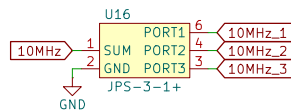
10MHz Oscillator – STP3593LF

Supply Voltage: 5.0V (4.75V Min., 5.25V Max.)
Current Consumption: 1500mA (Warm Up), 600mA (Steady State)

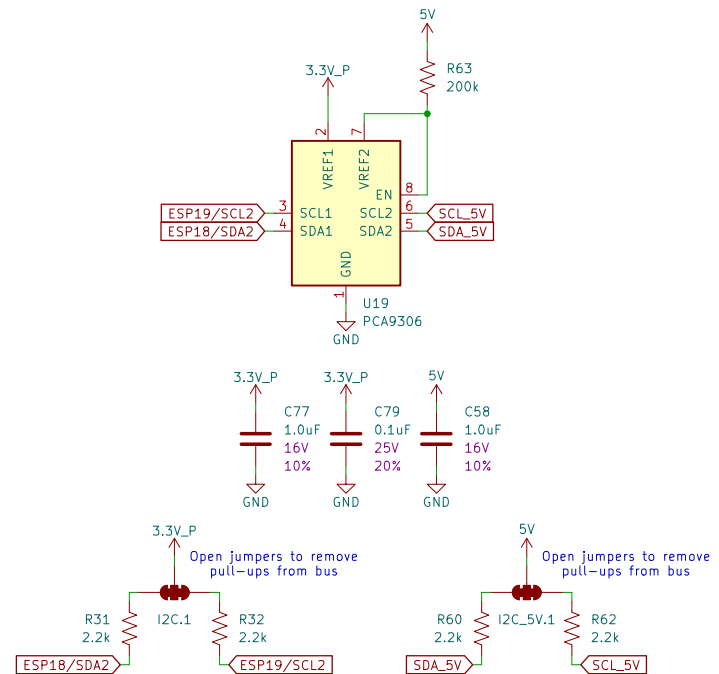


3-Way Splitter – JPS-3-1+

Typical Total Loss: 5.0dB at 10MHz



I2C Level Shifting – PCA9306



Sheet: /Oscillator/
File: Oscillator.kicad_sch

Title: Oscillator

Size: A4
KiCad E.D.A. 8.0.7

Date:

Rev:
Id: 8/8