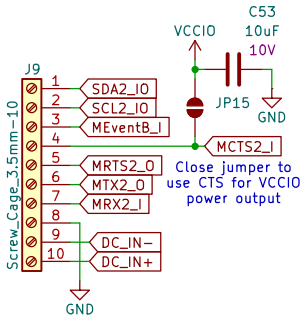
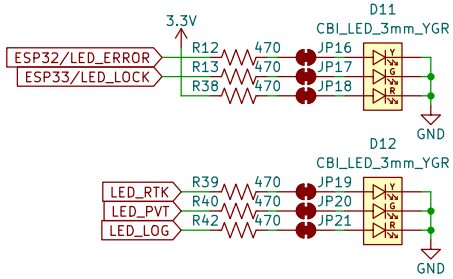
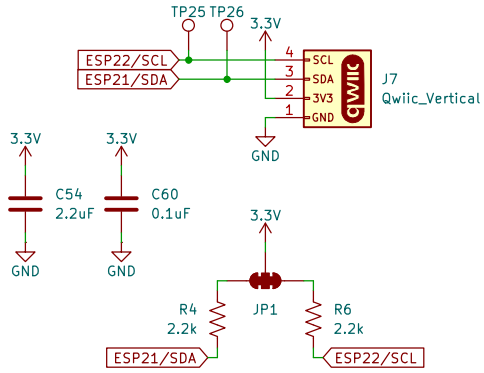
I/O Connector



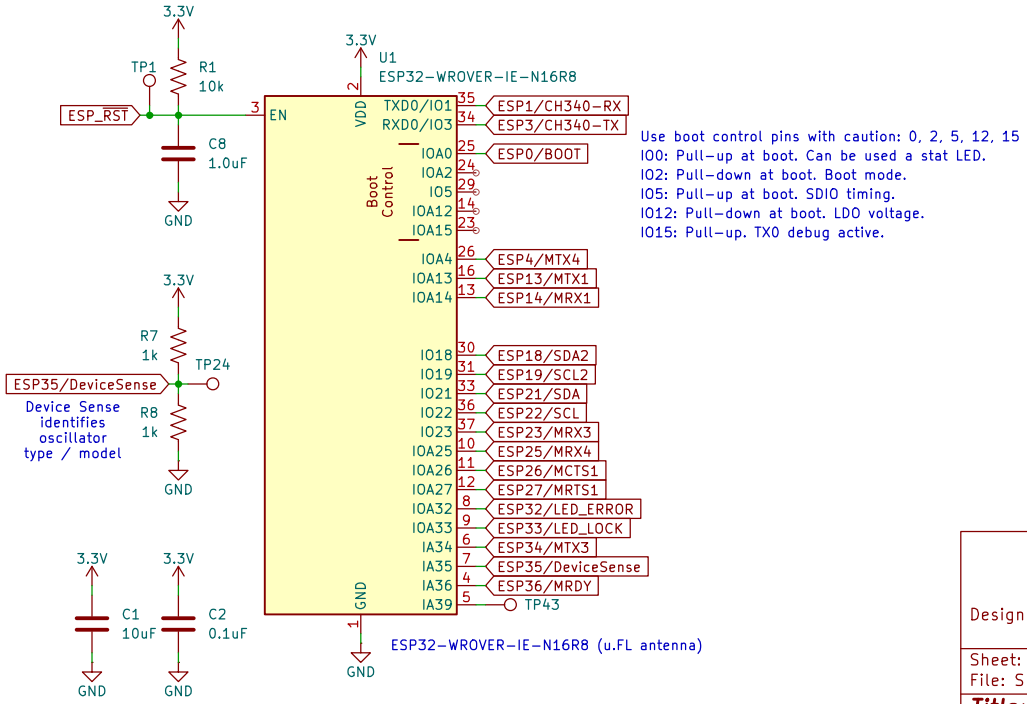
LEDs



Qwiic I²C (for OLED)



ESP32-WROVER



Use boot control pins with caution: 0, 2, 5, 12, 15
IO0: Pull-up at boot. Can be used a stat LED.
IO2: Pull-down at boot. Boot mode.
IO5: Pull-up at boot. SDIO timing.
IO12: Pull-down at boot. LDO voltage.
IO15: Pull-up. TXO debug active.

Power
File: Power.kicad_sch

USB
File: USB.kicad_sch

GNSS
File: GNSS.kicad_sch

Ethernet
File: Ethernet.kicad_sch

Level_Shifting
File: Level_Shifting.kicad_sch

Level_Shifting_10MHz
File: Level_Shifting_10MHz.kicad_sch

open source hardware

Designed by: P.C.

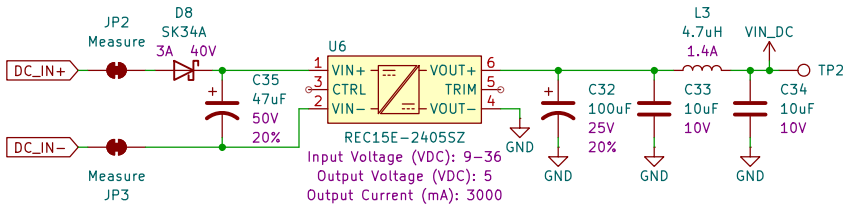
SPARKPNT

Sheet: /
File: SparkFun_RTK_mosaic-T.kicad_sch

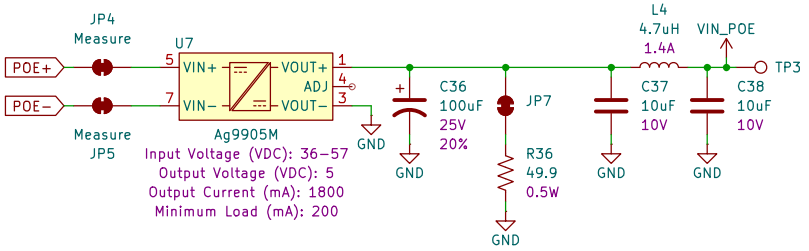
Title: GPSDO (mosaic-T, SiT5358)

Size: USLetter	Date: 2024-10-17	**Rev: v10**
KiCad E.D.A. 8.0.5		Id: 1/7

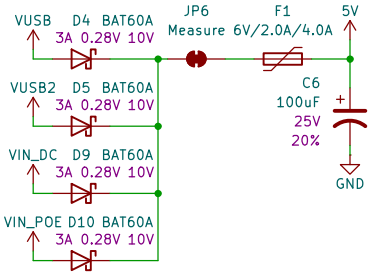
DC Power In



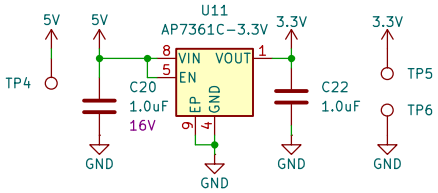
Power Over Ethernet



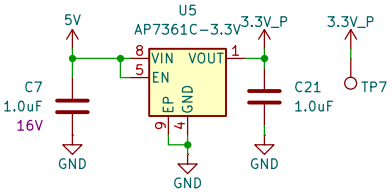
Power Mux



Main 3.3V



Peripheral 3.3V



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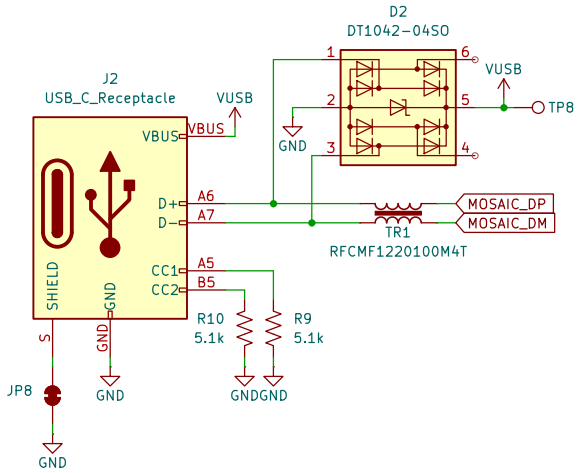
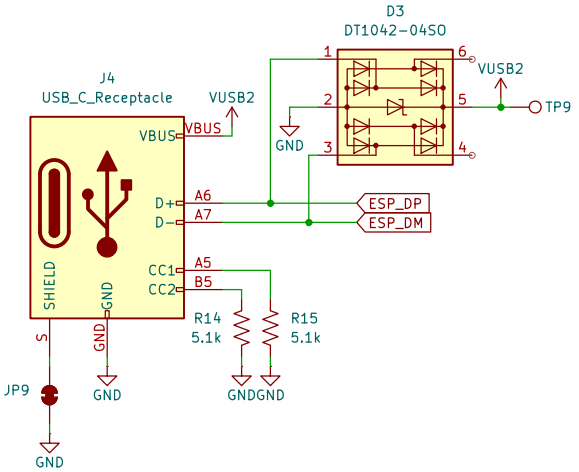
Title: Power	
Size: USLetter | Date:	**Rev:**
KiCad E.D.A. 8.0.5	Id: 2/7

Mosaic USB

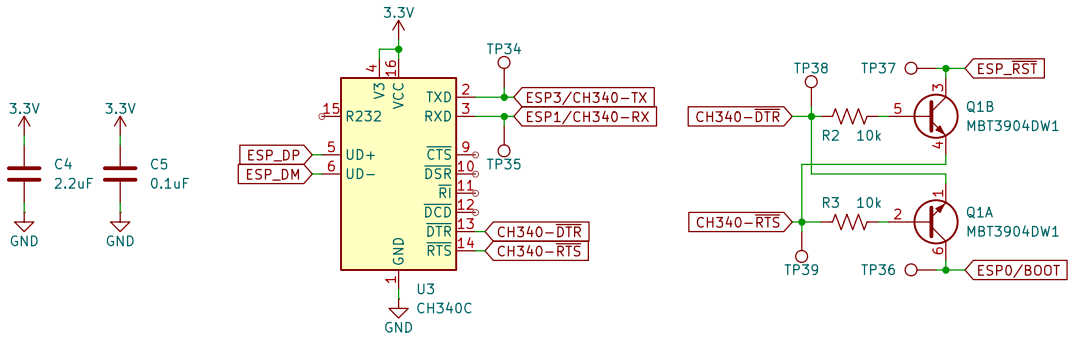


USB Track Impedance: Differential Pair
https://saturnpcb.com/saturn-pcb-toolkit/
Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

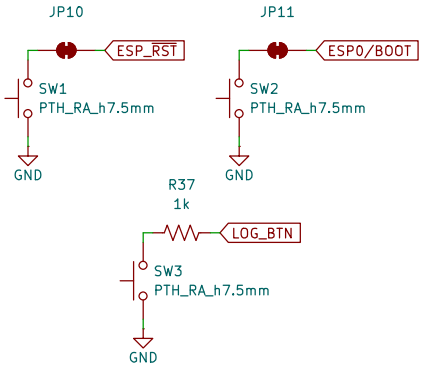
ESP32 USB



ESP32 USB to Serial



Buttons



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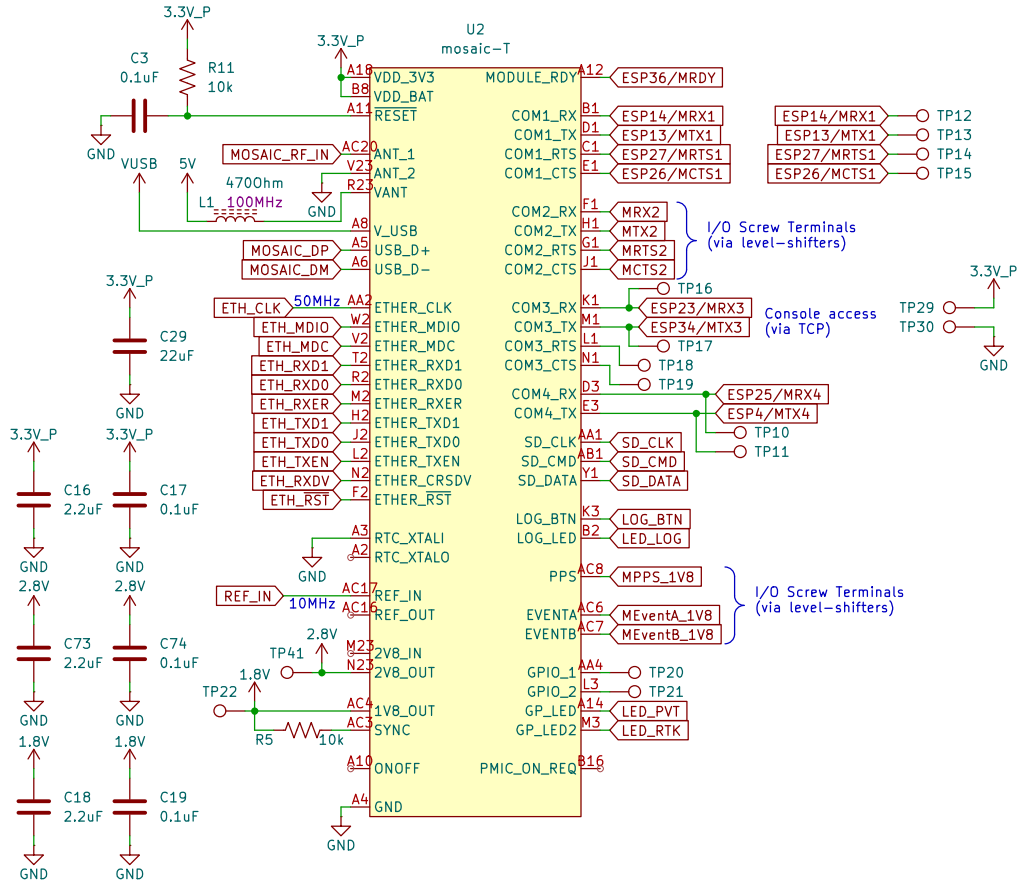
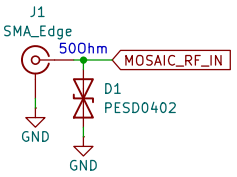
File: USB.kicad_sch	
Title: USB	
Size: USLetter	Date:
KiCad E.D.A. 8.0.5	Rev:
	Id: 3/7

mosaic Tri-band GNSS

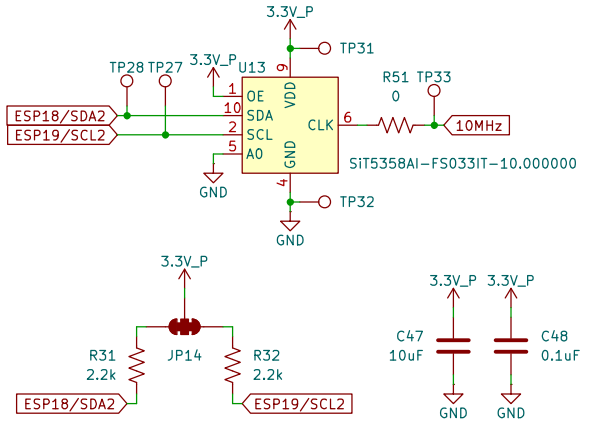


GNSS Antenna

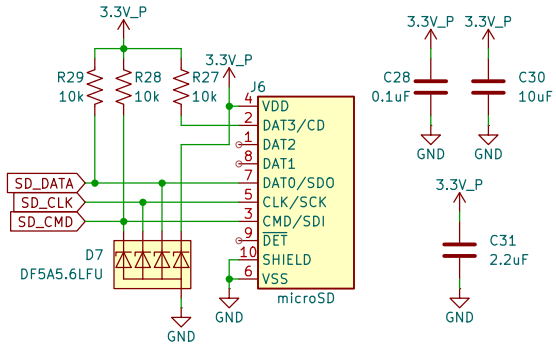


Microstrip Calculation:
Copper Thickness (1oz): 1.4mil/0.035mm
Board thickness: 1.6mm
Dielectric thickness (layer 1 to 2): 0.2mm
Er: 4.6
Polygon Isolation: 6mil/0.1524mm
RF Trace Width: 13mil/0.33mm
https://chemandy.com/calculators/coplanar-waveguide-with-ground-calculator.htm

10MHz Oscillator



microSD



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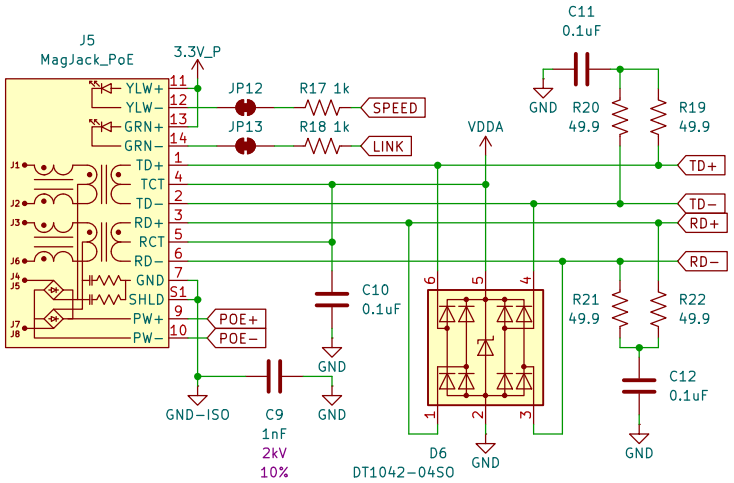
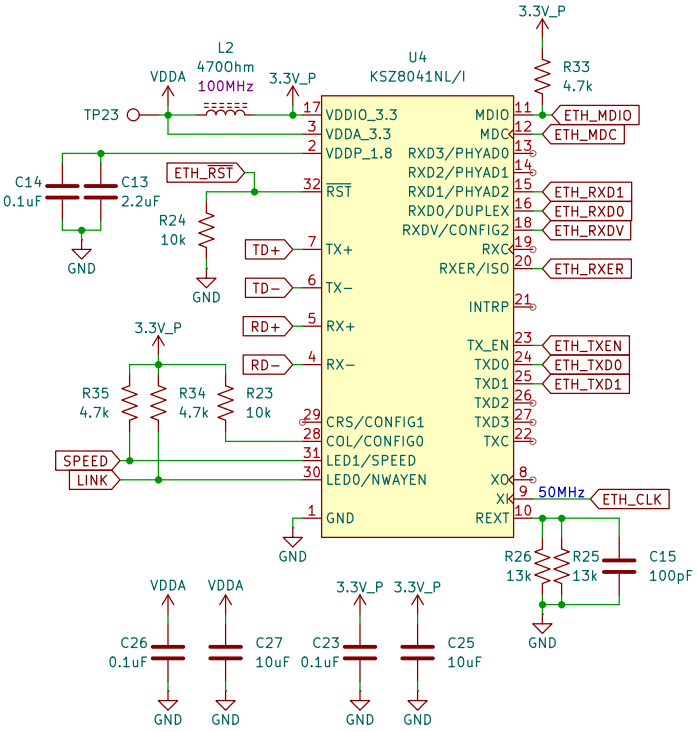
File: GNSS.kicad_sch	
Title: GNSS	
Size: USLetter Date:	**Rev:**
KiCad E.D.A. 8.0.5	Id: 4/7

Ethernet





Ethernet Track Impedance: Differential Pair
https://saturnpcb.com/saturn-pcb-toolkit/
Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
Each pair should match in length to better than 0.5mm

Sheet: /Ethernet/	
File: Ethernet.kicad_sch	
Title: Ethernet	
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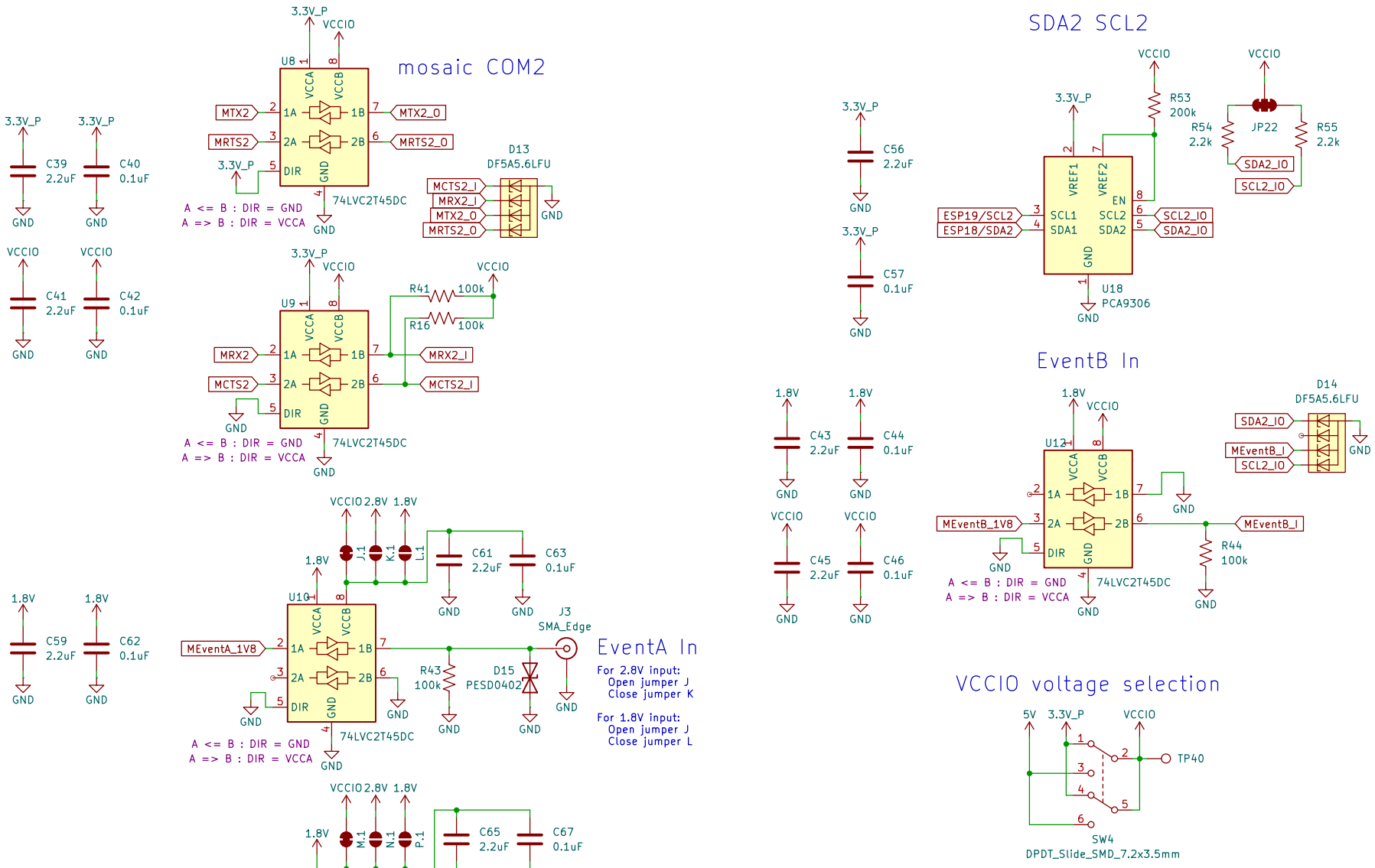
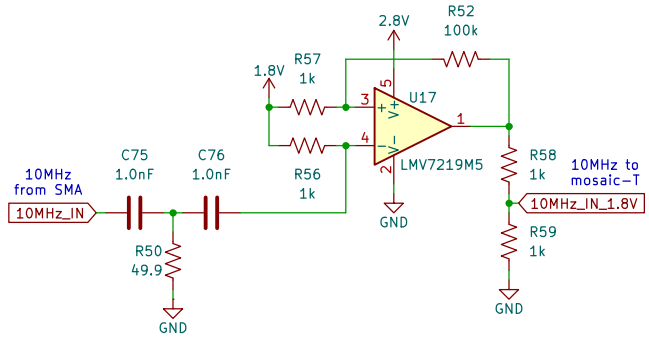


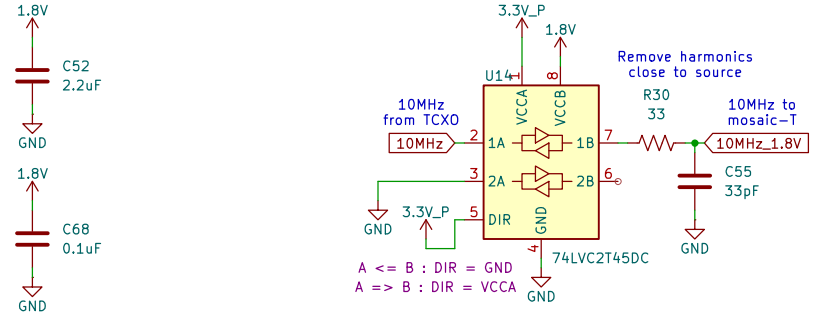
Level_Shifting

Sheet: /Level_Shifting/	
File: Level_Shifting.kicad_sch	
Title: Level Shifting	
Size: USLetter	Date:
KiCad E.D.A. 8.0.5	Id: 6/7

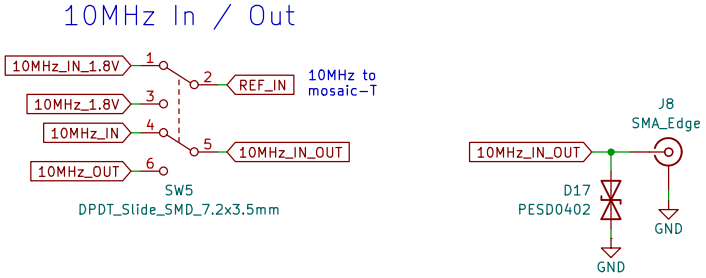
10MHz In



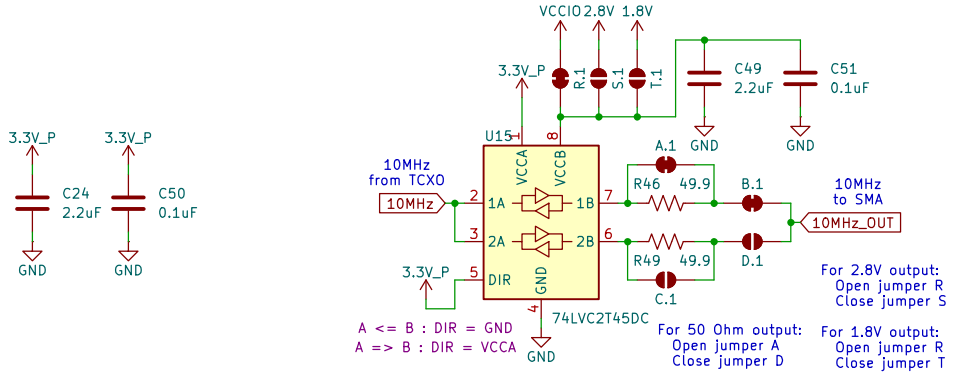
10MHz 1.8V for mosaic-T



10MHz In / Out



10MHz Out



Level-Shifting 10MHz

Sheet: /Level_Shifting_10MHz/
File: Level_Shifting_10MHz.kicad_sch

Title: Level Shifting 10MHz

Size: USLetter	Date:	Rev:
KiCad E.D.A. 8.0.5		Id: 7/7