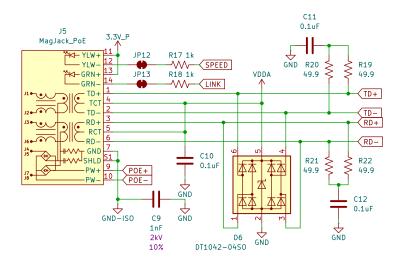
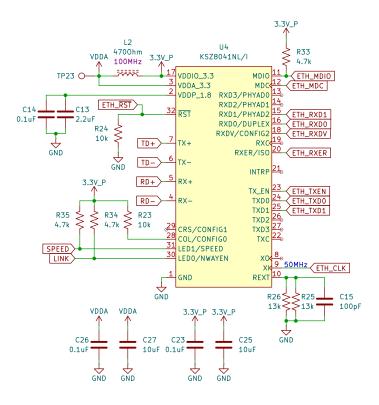


Ethernet





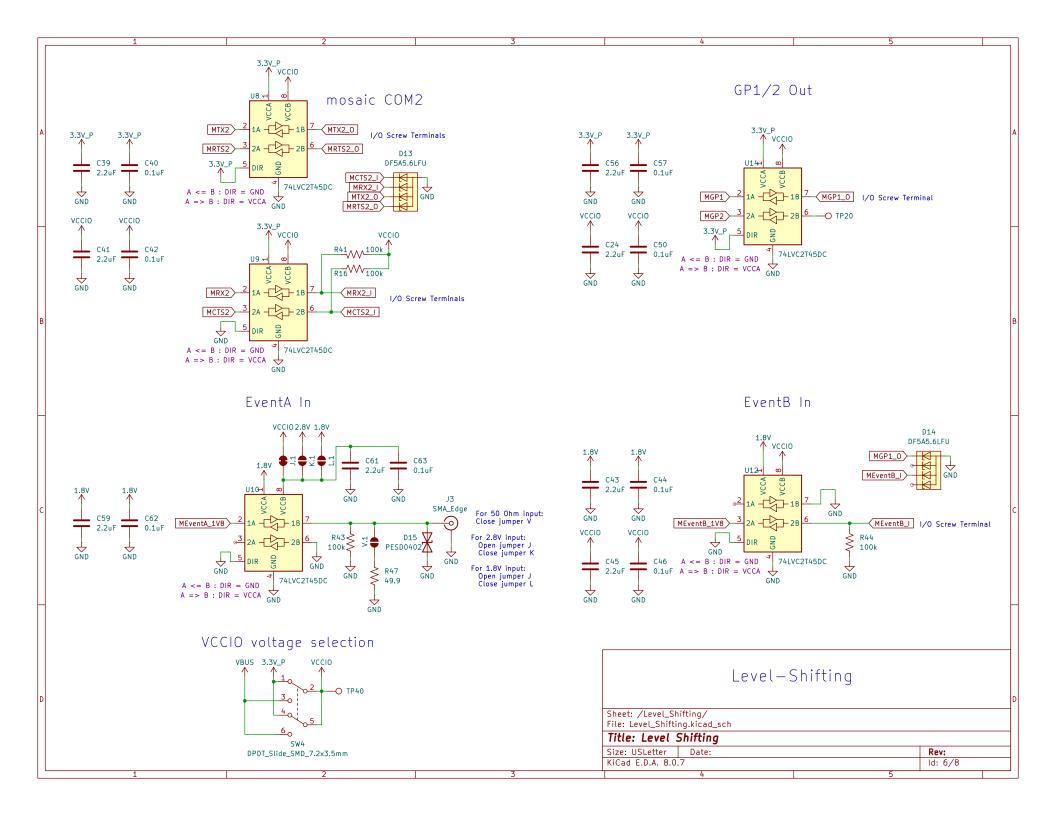
Ethernet Track Impedance: Differential Pair https://saturnpcb.com/saturn-pcb-toolkit/ Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6 9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms Each pair should match in length to better than 0.5mm

Sheet: /Ethernet/ File: Ethernet.kicad_sch

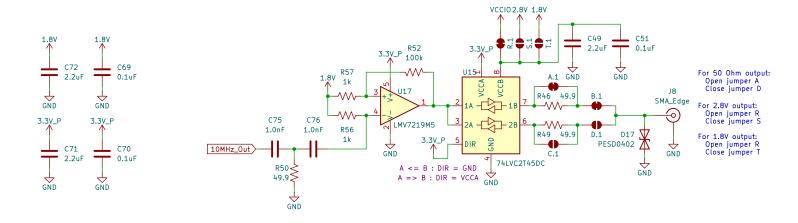
Title: Ethernet

 Size: USLetter
 Date:
 Rev:

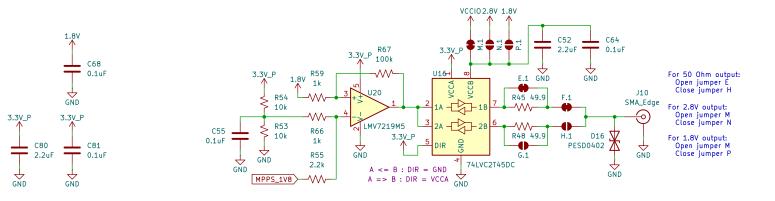
 KiCad E.D.A. 8.0.7
 Id: 5/8



10MHz Out



PPS Out (Inverting)



Note: 10MHz and PPS Out use identical components to ensure equal temperature dependence

Level-Shifting 10MHz

Sheet: /Level_Shifting_10MHz/ File: Level_Shifting_10MHz.kicad_sch

Title: Level Shifting 10MHz

 Size: USLetter
 Date:
 Rev:

 KiCad E.D.A. 8.0.7
 Id: 7/8

