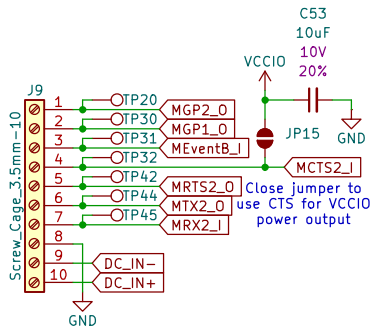
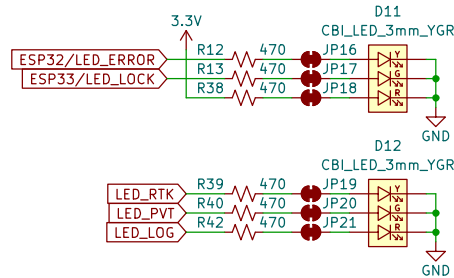


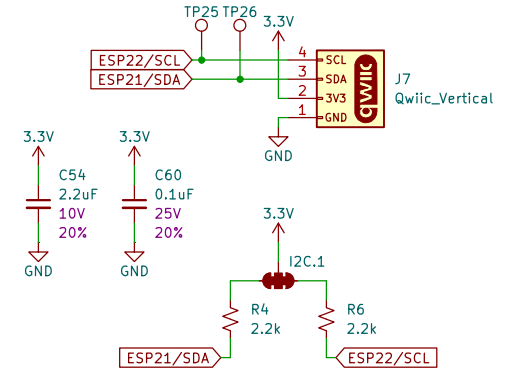
I/O Connector



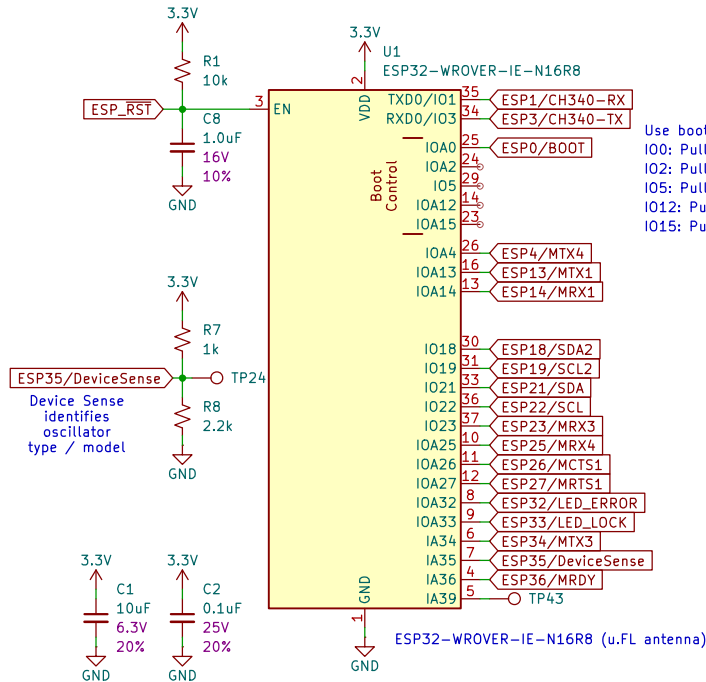
LEDs



Qwiic I²C (for OLED)



ESP32-WROVER



Power

File: Power.kicad_sch

USB

File: USB.kicad_sch

GNSS

File: GNSS.kicad_sch

Ethernet

File: Ethernet.kicad_sch

LevelShifting

File: LevelShifting.kicad_sch

LevelShifting_10MHz

File: LevelShifting_10MHz.kicad_sch

Oscillator

File: Oscillator.kicad_sch



SPARKPNT



Designed by: P.C.

Sheet: /

File: SparkPNT_GNSSDO_Plus.kicad_sch

Title: GNSSDO Plus (mosaic-T, STP3593LF)

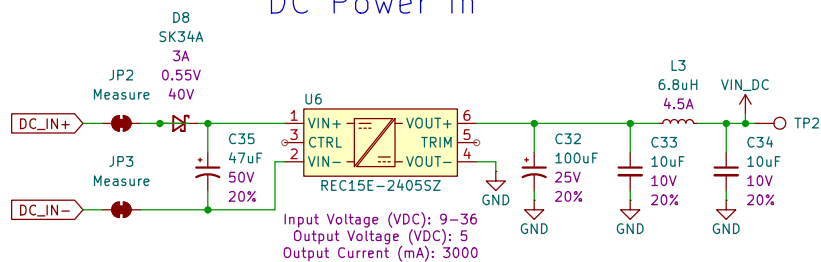
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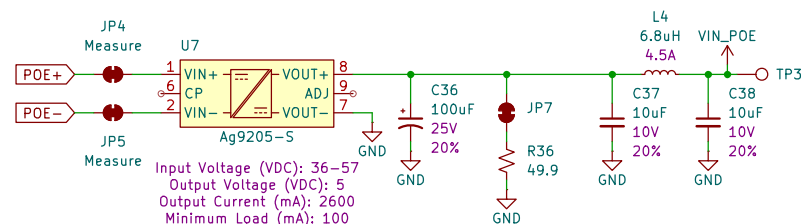
Rev: v01

Id: 1/8

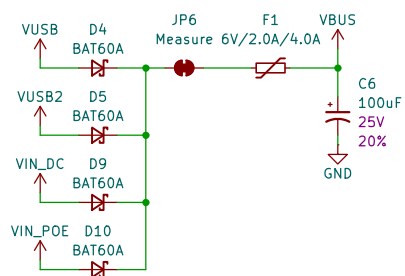
DC Power In



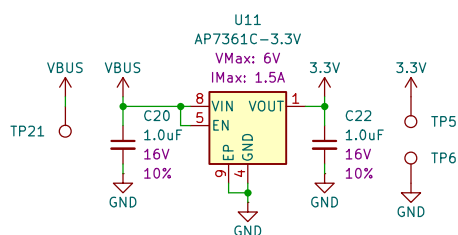
Power Over Ethernet



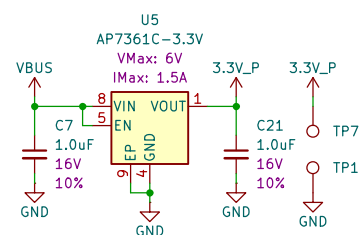
Power Mux



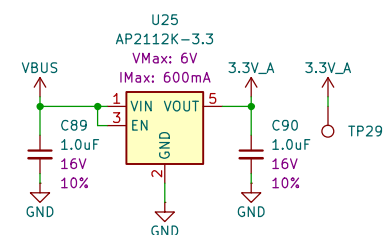
Main 3.3V



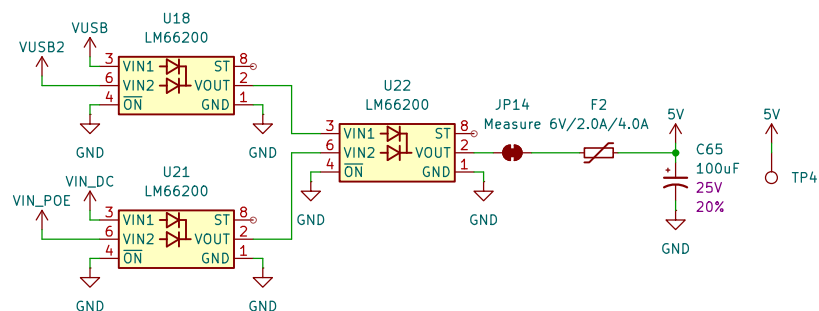
Peripheral 3.3V



Analog 3.3V



OCX0 Power Mux



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Mosaic USB

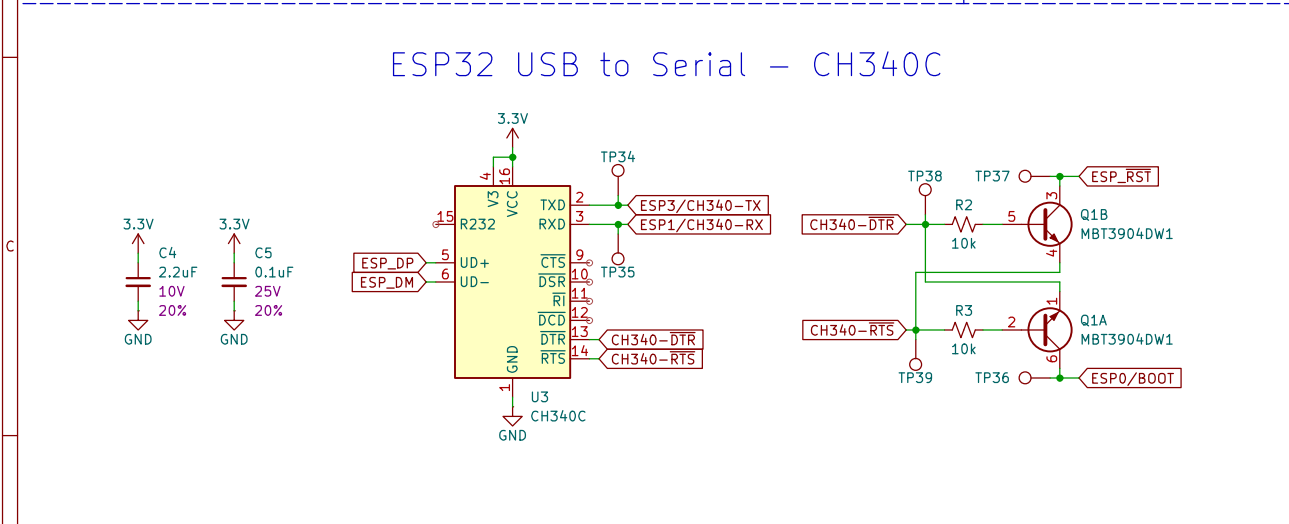
The schematic diagram illustrates the Mosaic USB interface circuit. It features a yellow rectangular component labeled J2 USB_C_Receptacle. The receptacle has pins for SHIELD, GND, CC1, CC2, D+, D-, VBUS, and TP8. The SHIELD pin is connected to a ground plane via a solder joint (S). The GND pin is connected to a ground plane. The CC1 and CC2 pins are connected to a common ground plane. The D+ and D- pins are connected to a differential pair of traces, A6 and A7, which pass through a resistor network consisting of R10 (5.1k) and R9 (5.1k) to a common ground plane. The VBUS pin is connected to a trace that passes through a diode (D2, DT1042-0450) and a resistor (R1, RFCMF1220100M4T) to a MOSAIC_DP/MOSAIC_DM input. The TP8 pin is connected to a trace that passes through a diode (D2, DT1042-0450) and a resistor (R1, RFCMF1220100M4T) to a MOSAIC_DP/MOSAIC_DM input.

USB Track Impedance: Differential Pair
https://saturnpcb.com/saturn-pcb-toolkit/
Prepreg thickness: 8.3 mil (JLC7628). Er = 4.6
10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms

[illegible][illegible]

ESP32 USB to Serial – CH340C

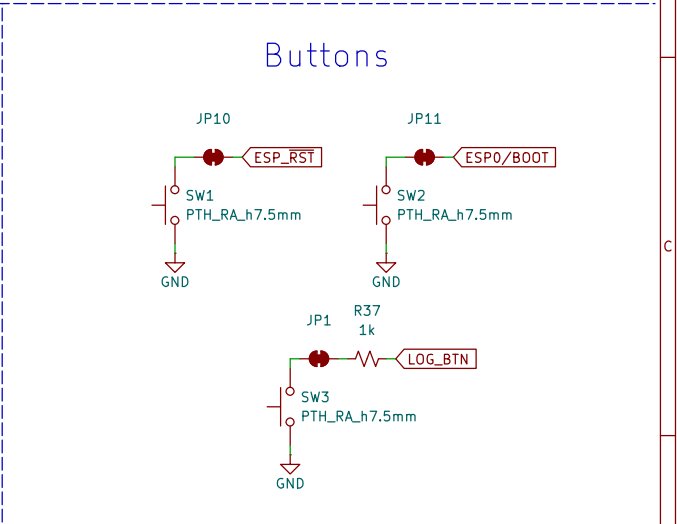
The diagram illustrates the wiring for an ESP32 USB to Serial module using a CH340C chip. The power supply section shows two 3.3V regulators (C4 and C5) connected to the CH340C chip. The CH340C chip is connected to the ESP32's VCC, GND, and various control pins (ESP_DP, ESP_DM, ESP_RST, ESP0/BOOT). The CH340C's TXD and RXD pins are connected to the ESP32's TXD and RXD pins. The CH340C's DTR and RTS pins are connected to the ESP32's DTR and RTS pins. The CH340C's CH340-DTR and CH340-RTS pins are connected to the ESP32's CH340-DTR and CH340-RTS pins. The CH340C's CH340-TX and CH340-RX pins are connected to the ESP32's CH340-TX and CH340-RX pins. The CH340C's CH340-RST and CH340-RTS pins are connected to the ESP32's CH340-RST and CH340-RTS pins. The CH340C's CH340-RTS and CH340-RST pins are connected to the ESP32's CH340-RTS and CH340-RST pins.



Buttons

The diagrams show three button connections:

- ESP_RST:** A button labeled SW1 (PTH_RA_h7.5mm) connected to JP10 and GND.
- ESP0/BOOT:** A button labeled SW2 (PTH_RA_h7.5mm) connected to JP11 and GND.
- LOG_BTN:** A button labeled SW3 (PTH_RA_h7.5mm) connected to JP1 through a 1k resistor (R37) and GND.



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GNSS Antenna

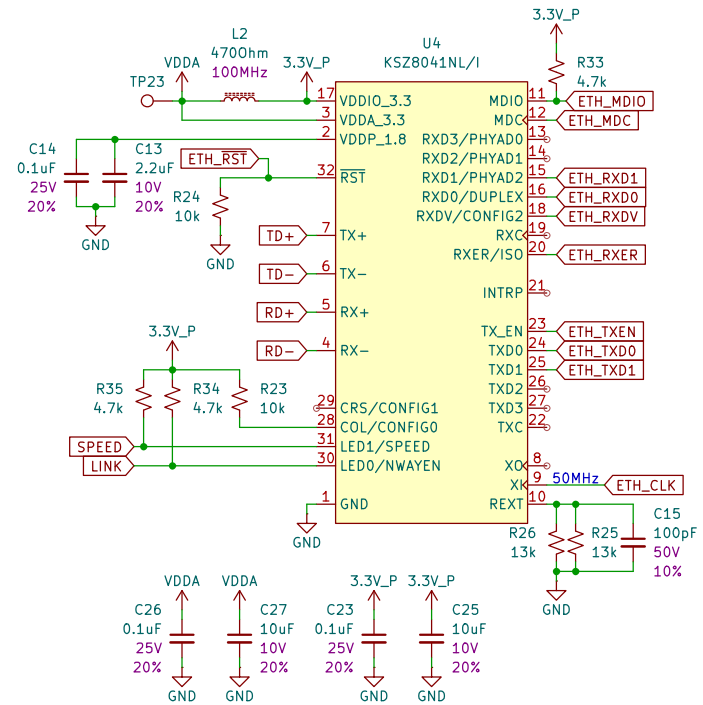
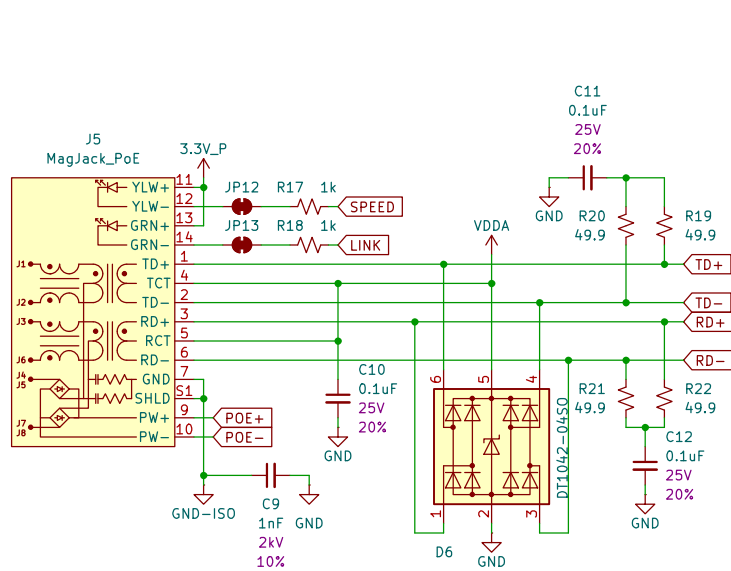
J1
SMA_Edge
500ohm
MOSAIC_RF_IN
D1
PESD0402
GND
GND

Microstrip Calculation:
Copper Thickness (1oz): 1.4mil/0.035mm
Board thickness: 1.6mm
Dielectric thickness (layer 1 to 2): 0.2mm
Er: 4.6
Polygon Isolation: 6mil/0.1524mm
RF Trace Width: 13mil/0.33mm
<https://chemandy.com/calculators/coplanar-waveguide-with-ground-calculator.htm>

[illegible]

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Ethernet



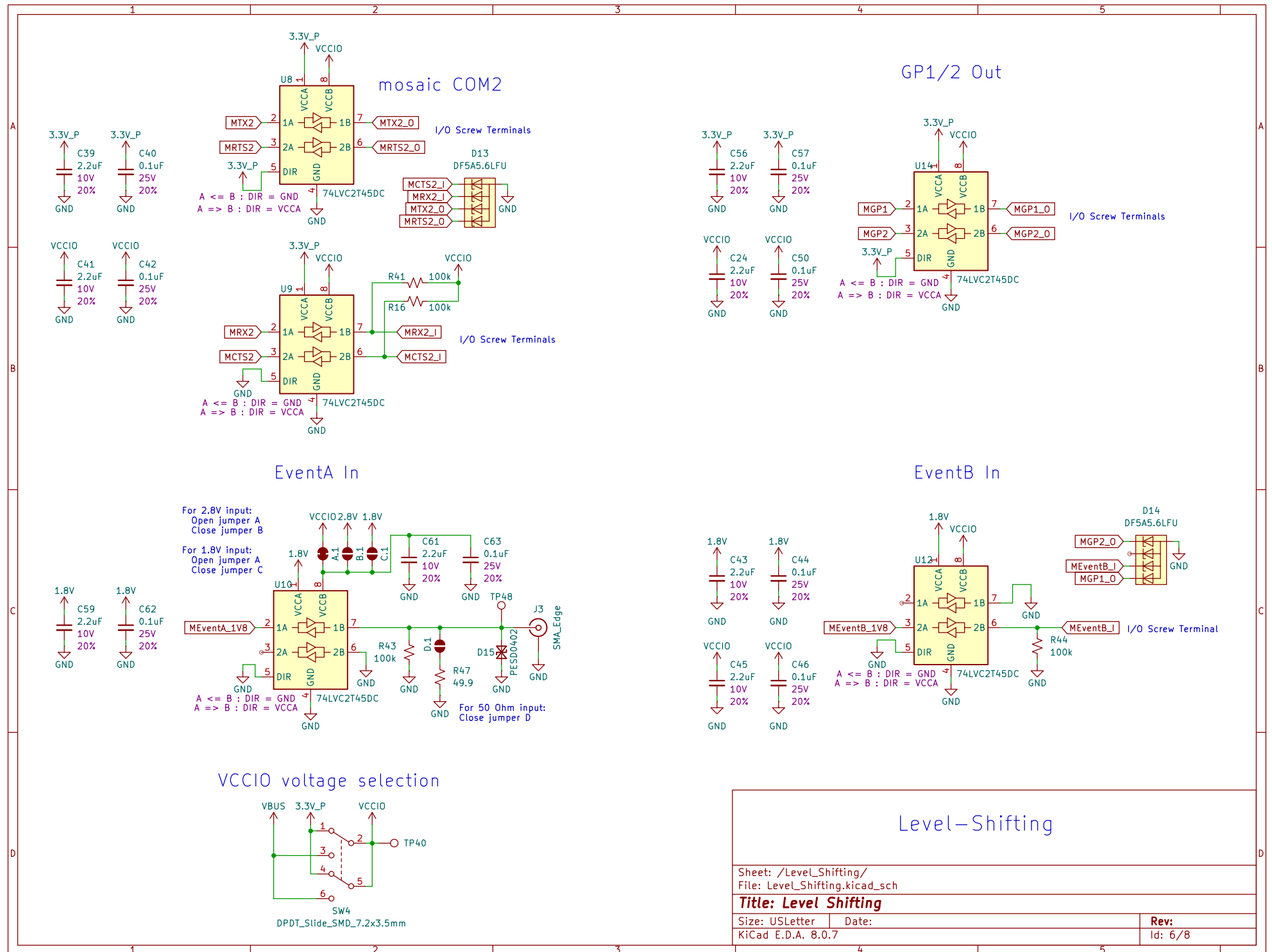
Ethernet Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
 Prepreg thickness: 8.3 mil (JLC7628), Er = 4.6
 9.0 mil track with 11.0 mil gap (20 mil center to center) = 100 Ohms
 Each pair should match in length to better than 0.5mm

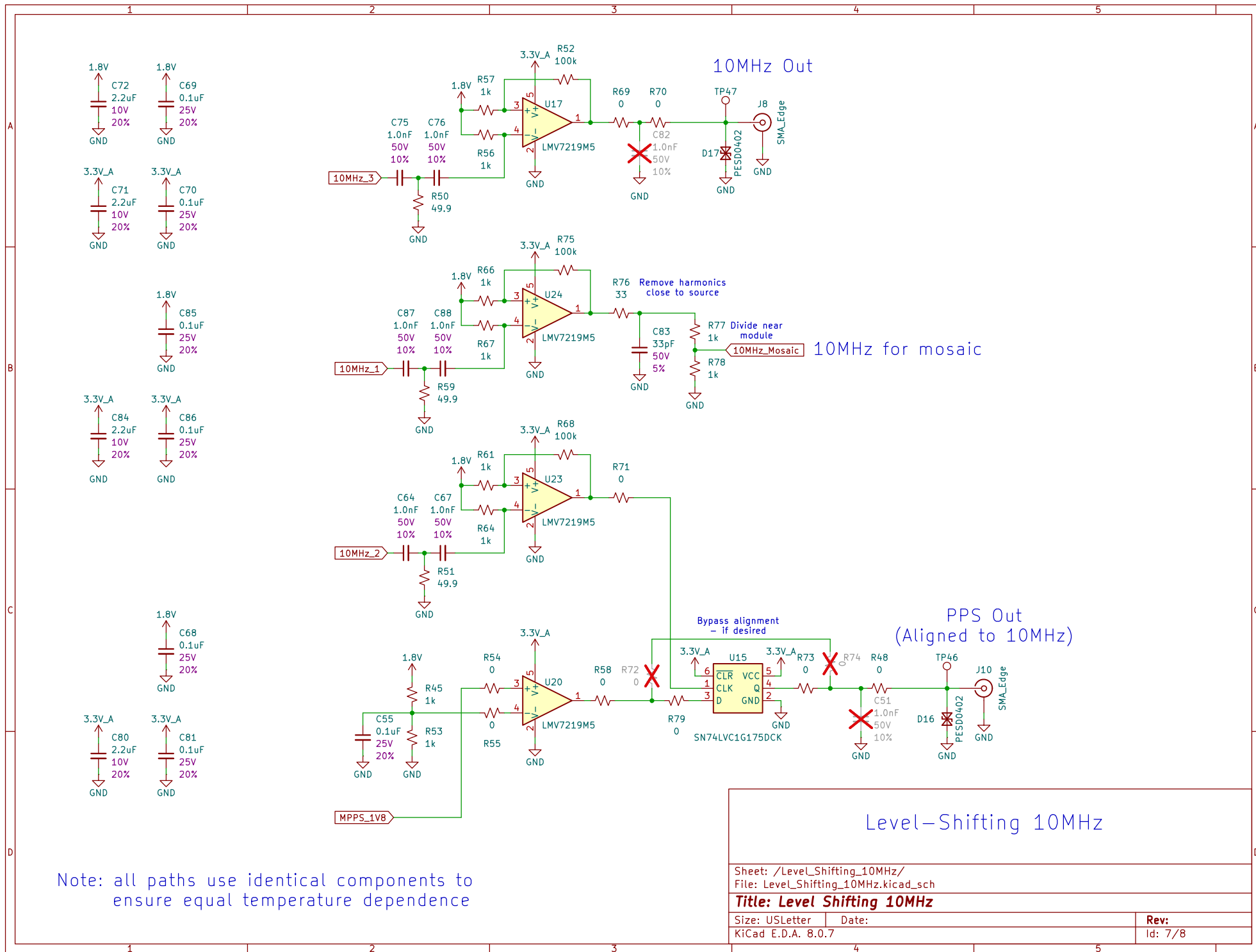
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Title: Ethernet

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 Id: 5/8

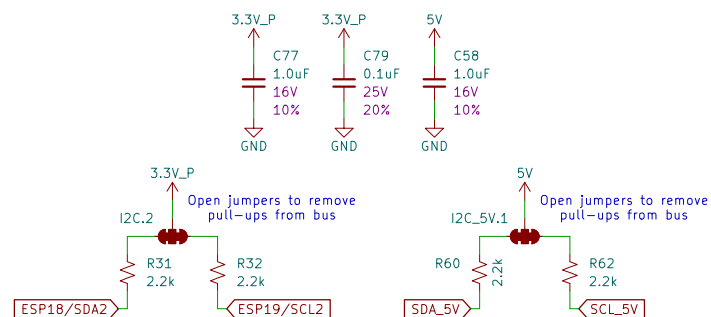




Supply Voltage: 5.0V (4.75V Min., 5.25V Max.)
Current Consumption: 1500mA (Warm Up), 600mA (Steady State)



Typical Total Loss: 5.0dB at 10MHz



Rev:
Id: 8/8