



## 1. Description

### 1.1. Project

Project Name	vccgnd_bluepill_stm32f030c8
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	08/07/2025

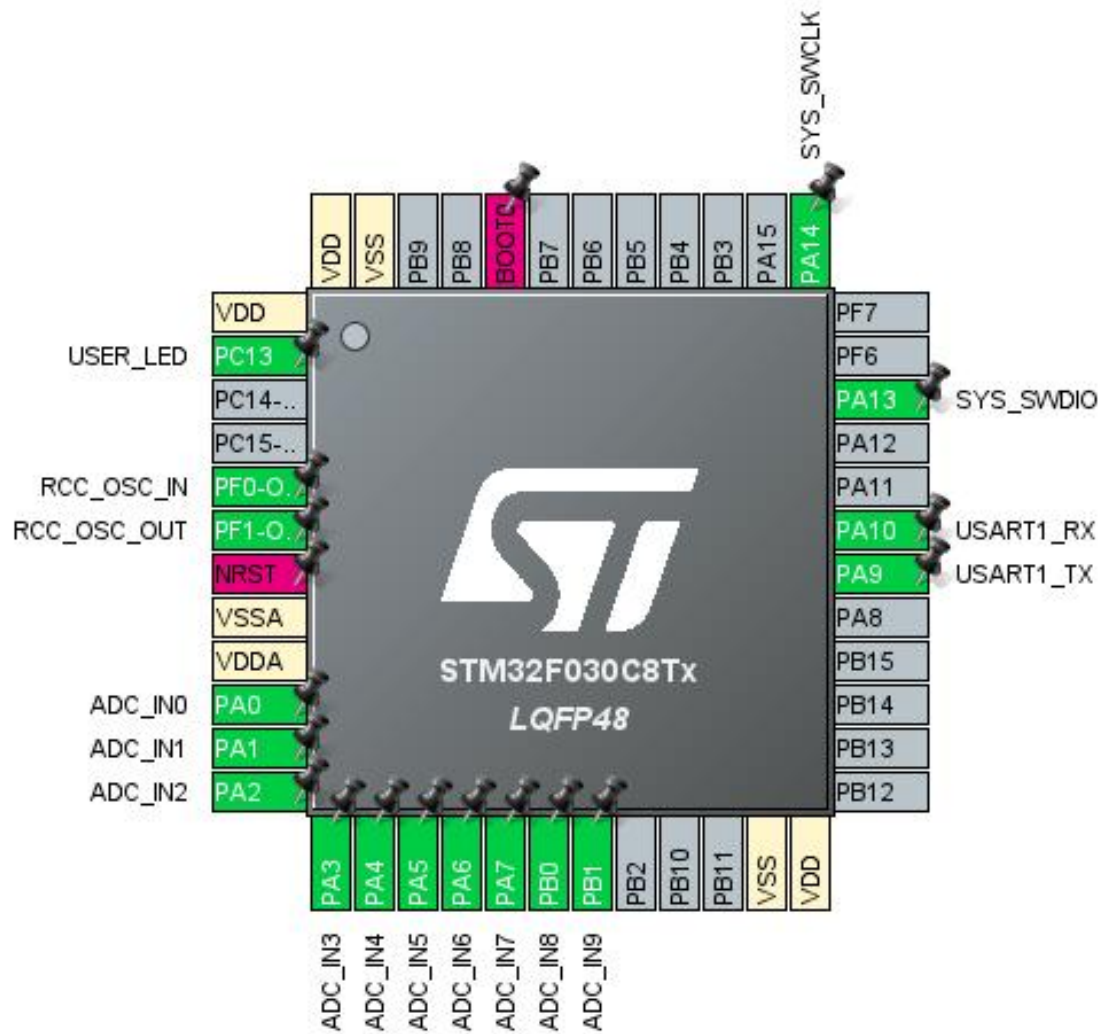
### 1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x0 Value Line
MCU name	STM32F030C8Tx
MCU Package	LQFP48
MCU Pin number	48

### 1.3. Core(s) information

Core(s)	Arm Cortex-M0
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## 2. Pinout Configuration

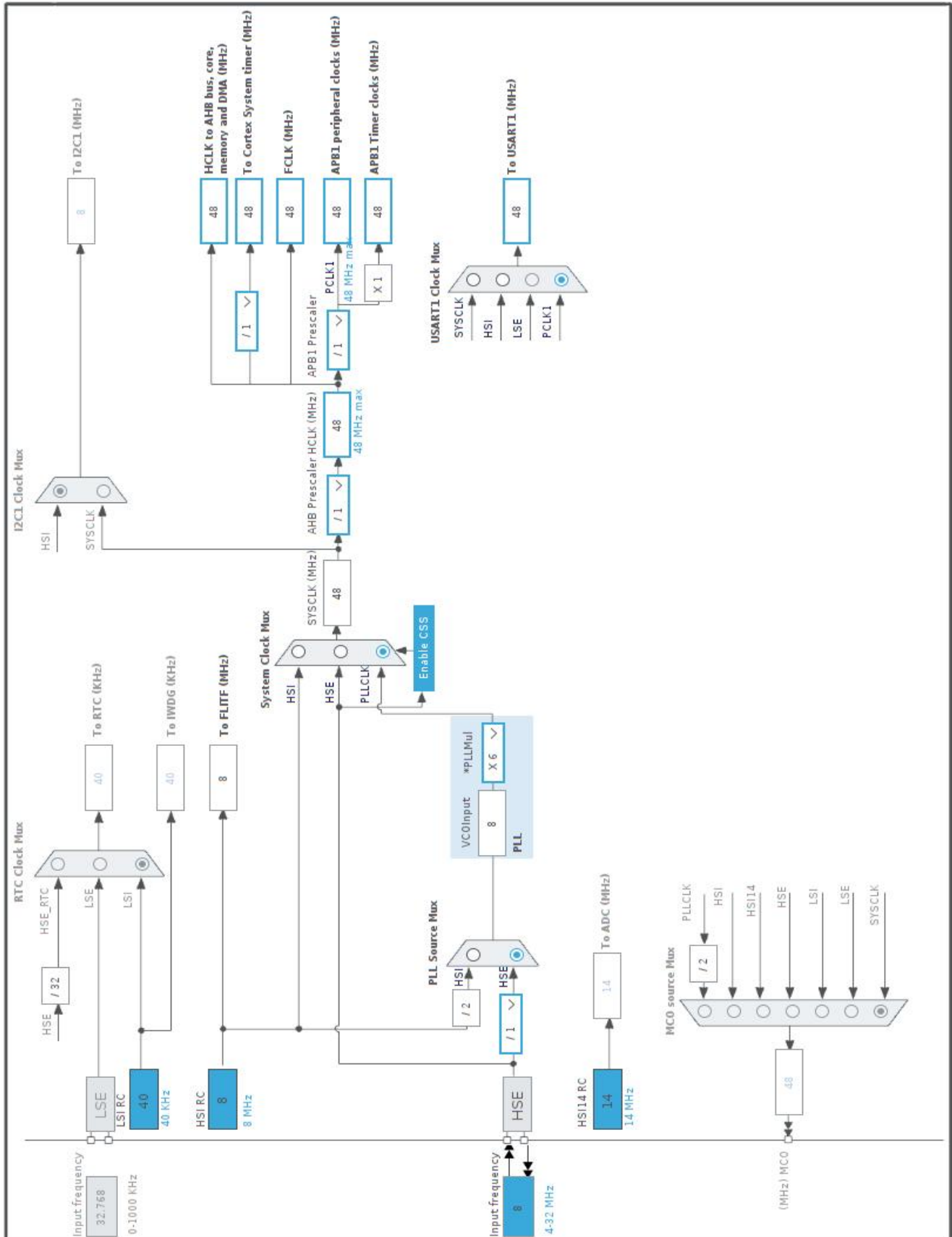


### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VDD	Power		
2	PC13 *	I/O	GPIO_Output	USER_LED
5	PF0-OSC_IN	I/O	RCC_OSC_IN	
6	PF1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0	I/O	ADC_IN0	
11	PA1	I/O	ADC_IN1	
12	PA2	I/O	ADC_IN2	
13	PA3	I/O	ADC_IN3	
14	PA4	I/O	ADC_IN4	
15	PA5	I/O	ADC_IN5	
16	PA6	I/O	ADC_IN6	
17	PA7	I/O	ADC_IN7	
18	PB0	I/O	ADC_IN8	
19	PB1	I/O	ADC_IN9	
23	VSS	Power		
24	VDD	Power		
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
34	PA13	I/O	SYS_SWDIO	SYS_SWDIO
37	PA14	I/O	SYS_SWCLK	SYS_SWCLK
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x0 Value Line
MCU	STM32F030C8Tx
Datasheet	DS9773_Rev2

### 1.2. Parameter Selection

Temperature	25
Vdd	3.6

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

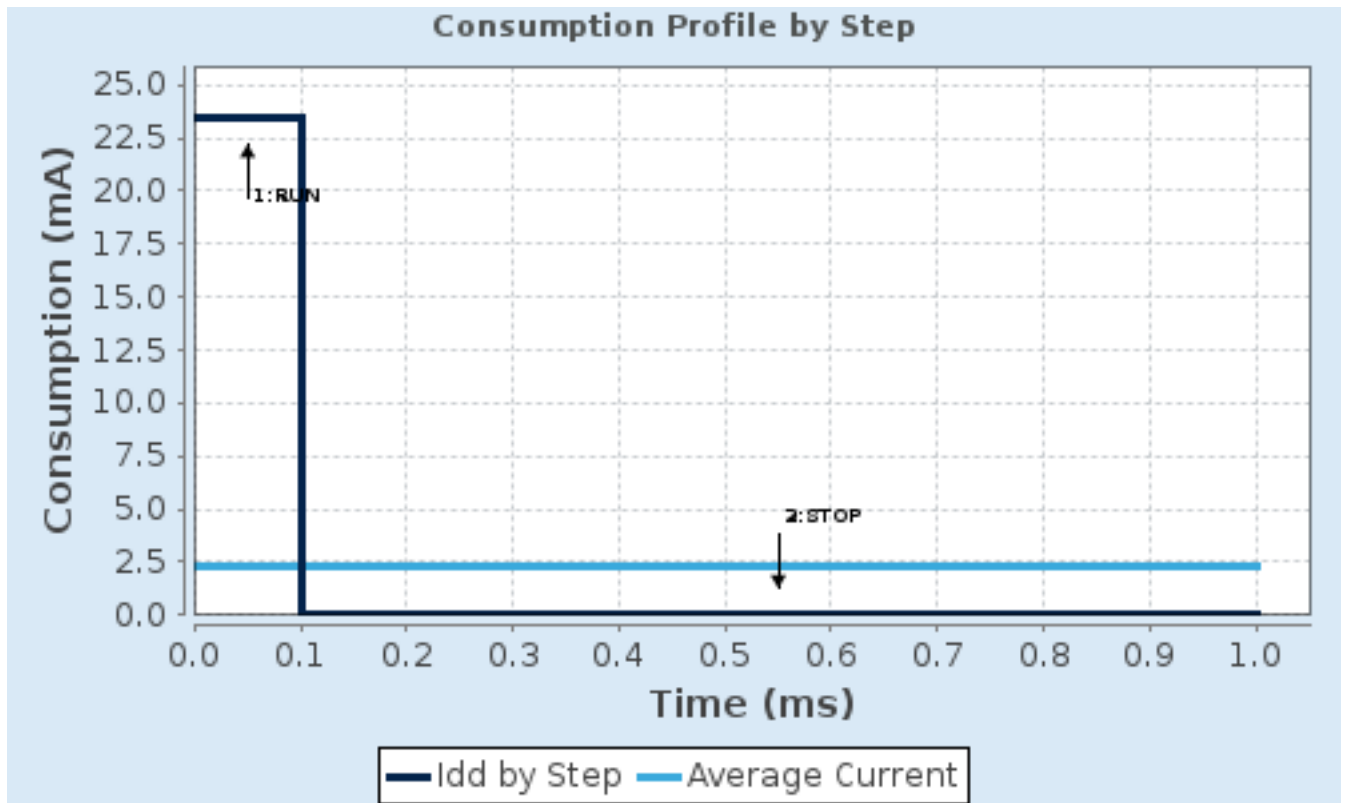
#### 1.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.6	3.6
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	No Scale	No Scale
<b>Fetch Type</b>	FLASH	n/a
<b>CPU Frequency</b>	48 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL All IPs ON	Regulator LP
<b>Clock Source Frequency</b>	8 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	23.46 mA	7.9 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	0.0	0.0
<b>Ta Max</b>	100.36	105
<b>Category</b>	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	2.35 mA
Battery Life	1 month, 29 days, 16 hours	Average DMIPS	0.0 DMIPS

#### 1.6. Chart





## 2. Software Project

### 2.1. Project Settings

Name	Value
Project Name	vccgnd_bluepill_stm32f030c8
Project Folder	/store/EmbedTools/STM32CubeMX/vccgnd_bluepill/vccgnd_bluepill_stm32f030c
Toolchain / IDE	CMake
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.5
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	Yes

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_USART1_UART_Init	USART1
4	MX_ADC_Init	ADC

## 3. *Peripherals and Middlewares Configuration*

### 3.1. ADC

mode: IN0

mode: IN1

mode: IN2

mode: IN3

mode: IN4

mode: IN5

mode: IN6

mode: IN7

mode: IN8

mode: IN9

mode: Temperature Sensor Channel

mode: Vrefint Channel

#### 3.1.1. Parameter Settings:

##### **ADC\_Settings:**

Clock Prescaler

Resolution

Data Alignment

Scan Conversion Mode

Continuous Conversion Mode

Discontinuous Conversion Mode

DMA Continuous Requests

End Of Conversion Selection

Overrun behaviour

Low Power Auto Wait

Low Power Auto Power Off

**Synchronous clock mode divided by 4 \***

ADC 12-bit resolution

Right alignment

Forward

Disabled

Disabled

Disabled

End of single conversion

Overrun data preserved

Disabled

Disabled

##### **ADC\_Regular\_ConversionMode:**

Sampling Time

External Trigger Conversion Source

External Trigger Conversion Edge

1.5 Cycles

Regular Conversion launched by software

None

##### **WatchDog:**

Enable Analog WatchDog Mode

Low Threshold

false

0

### 3.2. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 3.2.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

###### RCC Parameters:

HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

### 3.3. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

### 3.4. USART1

Mode: Asynchronous

##### 3.4.1. Parameter Settings:

###### Basic Parameters:

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

###### Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

###### Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

**\* User modified value**

## 4. System Configuration

### 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA0	ADC_IN0	Analog mode	No pull-up and no pull-down	n/a	
	PA1	ADC_IN1	Analog mode	No pull-up and no pull-down	n/a	
	PA2	ADC_IN2	Analog mode	No pull-up and no pull-down	n/a	
	PA3	ADC_IN3	Analog mode	No pull-up and no pull-down	n/a	
	PA4	ADC_IN4	Analog mode	No pull-up and no pull-down	n/a	
	PA5	ADC_IN5	Analog mode	No pull-up and no pull-down	n/a	
	PA6	ADC_IN6	Analog mode	No pull-up and no pull-down	n/a	
	PA7	ADC_IN7	Analog mode	No pull-up and no pull-down	n/a	
	PB0	ADC_IN8	Analog mode	No pull-up and no pull-down	n/a	
	PB1	ADC_IN9	Analog mode	No pull-up and no pull-down	n/a	
RCC	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	SYS_SWDIO
	PA14	SYS_SWCLK	n/a	n/a	n/a	SYS_SWCLK
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
GPIO	PC13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	USER_LED

### 4.2. DMA configuration

nothing configured in DMA service

### 4.3. NVIC configuration

#### 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
RCC global interrupt	true	0	0
ADC global interrupt	true	0	0
USART1 global interrupt	true	0	0
Flash global interrupt	unused		

#### 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
RCC global interrupt	false	true	false
ADC global interrupt	false	true	true
USART1 global interrupt	false	true	true

\* User modified value

## 5. System Views

### 5.1. Category view

#### 5.1.1. Current

## 6. Docs & Resources

Type	Link
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32f0_ibis.zip">https://www.st.com/resource/en/ibis_model/stm32f0_ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f0_svd.zip">https://www.st.com/resource/en/svd/stm32f0_svd.zip</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/gt_stm32f0-l0.pdf">https://www.st.com/resource/en/product_presentation/gt_stm32f0-l0.pdf</a>
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Brochures	<a href="https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf">https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/fldpstpfc11120.pdf">https://www.st.com/resource/en/flyer/fldpstpfc11120.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>
Security Bulletin	<a href="https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf">https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>
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- Application Notes [https://www.st.com/resource/en/application\\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf)
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- Application Notes [https://www.st.com/resource/en/application\\_note/an4617-migrating-between-stm32f0-and-stm32l0-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4617-migrating-between-stm32f0-and-stm32l0-microcontrollers-stmicroelectronics.pdf)
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