



## 1. Description

### 1.1. Project

Project Name	vccgnd_bluepill_stm32f072cb
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	08/08/2025

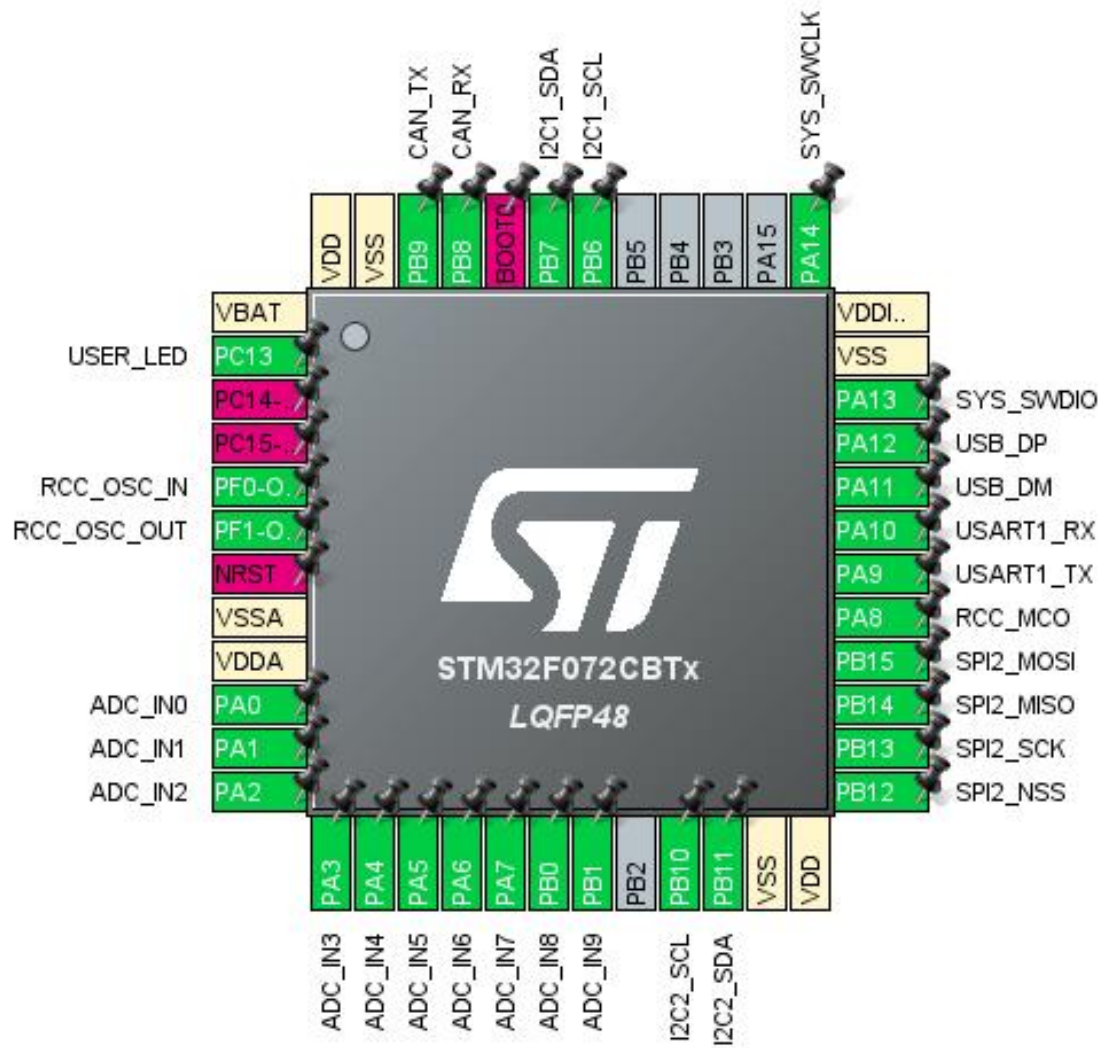
### 1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x2
MCU name	STM32F072CBTx
MCU Package	LQFP48
MCU Pin number	48

### 1.3. Core(s) information

Core(s)	Arm Cortex-M0
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## 2. Pinout Configuration



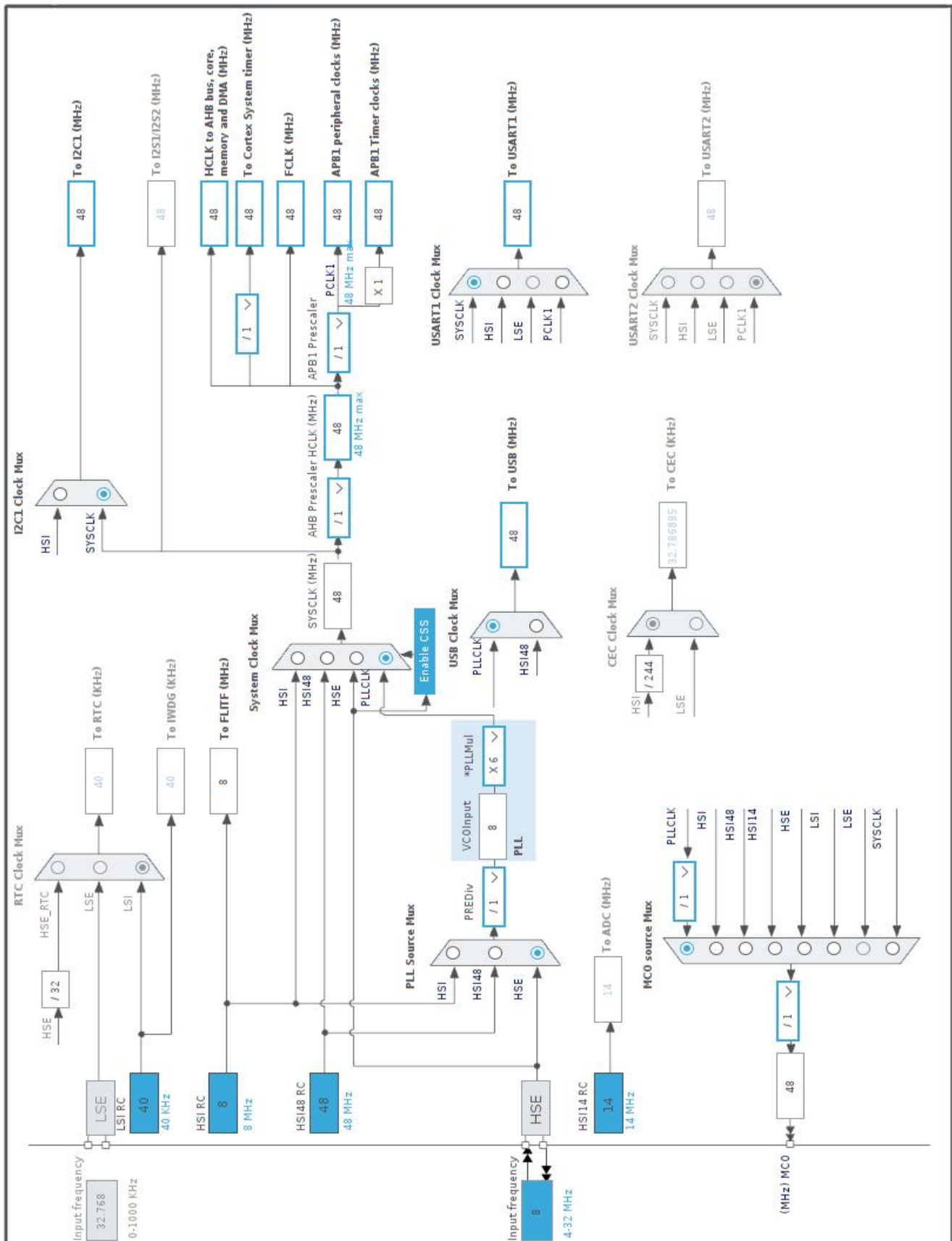
### 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13 *	I/O	GPIO_Output	USER_LED
3	PC14-OSC32_IN	I/O		
4	PC15-OSC32_OUT	I/O		
5	PF0-OSC_IN	I/O	RCC_OSC_IN	
6	PF1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
10	PA0	I/O	ADC_IN0	
11	PA1	I/O	ADC_IN1	
12	PA2	I/O	ADC_IN2	
13	PA3	I/O	ADC_IN3	
14	PA4	I/O	ADC_IN4	
15	PA5	I/O	ADC_IN5	
16	PA6	I/O	ADC_IN6	
17	PA7	I/O	ADC_IN7	
18	PB0	I/O	ADC_IN8	
19	PB1	I/O	ADC_IN9	
21	PB10	I/O	I2C2_SCL	
22	PB11	I/O	I2C2_SDA	
23	VSS	Power		
24	VDD	Power		
25	PB12	I/O	SPI2_NSS	
26	PB13	I/O	SPI2_SCK	
27	PB14	I/O	SPI2_MISO	
28	PB15	I/O	SPI2_MOSI	
29	PA8	I/O	RCC_MCO	
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
32	PA11	I/O	USB_DM	
33	PA12	I/O	USB_DP	
34	PA13	I/O	SYS_SWDIO	SYS_SWDIO
35	VSS	Power		
36	VDDIO2	Power		
37	PA14	I/O	SYS_SWCLK	SYS_SWCLK

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
42	PB6	I/O	I2C1_SCL	
43	PB7	I/O	I2C1_SDA	
44	BOOT0	Boot		
45	PB8	I/O	CAN_RX	
46	PB9	I/O	CAN_TX	
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x2
MCU	STM32F072CBTx
Datasheet	DS9826_Rev5

### 1.2. Parameter Selection

Temperature	25
Vdd	3.6

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

#### 1.4. Sequence

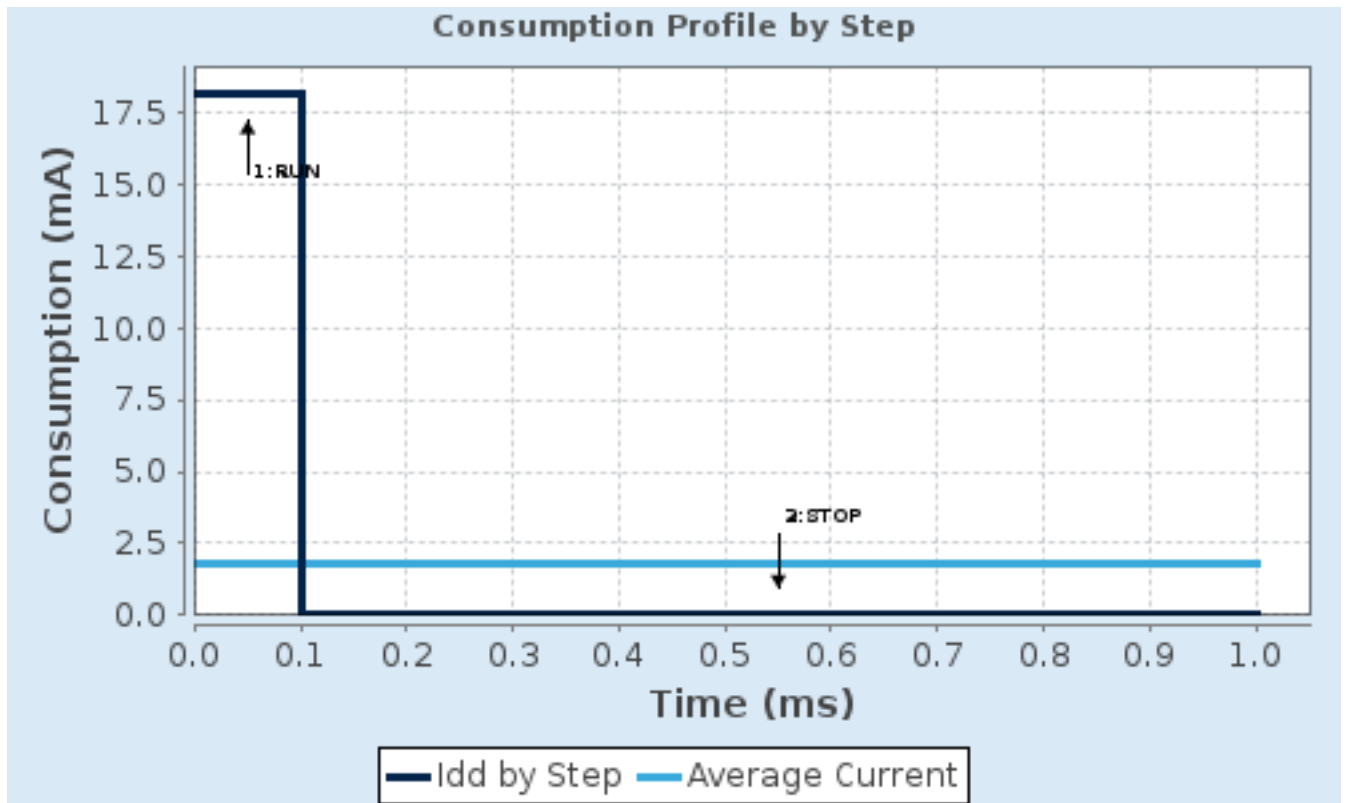
<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.6	3.6
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	No Scale	No Scale
<b>Fetch Type</b>	FLASH	n/a
<b>CPU Frequency</b>	48 MHz	0 Hz
<b>Clock Configuration</b>	HSI	Regulator LP
<b>Clock Source Frequency</b>	48 MHz	0 Hz
<b>Peripherals</b>	ADC CAN GPIOA GPIOB GPIOC GPIOF I2C1 I2C2 SPI2 SYS/COMP1:High_Speed USART1 USB	
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	18.19 mA	6.5 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	0.0	0.0
<b>Ta Max</b>	101.46	105
<b>Category</b>	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	1.82 mA
Battery Life	2 months, 16 days, 15 hours	Average DMIPS	0.0 DMIPS

#### 1.6. Chart





## 2. Software Project

### 2.1. Project Settings

Name	Value
Project Name	vccgnd_bluepill_stm32f072cb
Project Folder	/store/EmbedTools/STM32CubeMX/vccgnd_bluepill/vccgnd_bluepill_stm32f072c
Toolchain / IDE	CMake
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.5
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	Yes

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_USART1_UART_Init	USART1
4	MX_ADC_Init	ADC
5	MX_I2C1_Init	I2C1
6	MX_I2C2_Init	I2C2
7	MX_SPI2_Init	SPI2
8	MX_CAN_Init	CAN
9	MX_USB_PCD_Init	USB



## 3. Peripherals and Middlewares Configuration

### 3.1. ADC

mode: IN0

mode: IN1

mode: IN2

mode: IN3

mode: IN4

mode: IN5

mode: IN6

mode: IN7

mode: IN8

mode: IN9

mode: Temperature Sensor Channel

mode: Vrefint Channel

mode: Vbat Channel

#### 3.1.1. Parameter Settings:

##### **ADC\_Settings:**

Clock Prescaler

Resolution

Data Alignment

Scan Conversion Mode

Continuous Conversion Mode

Discontinuous Conversion Mode

DMA Continuous Requests

End Of Conversion Selection

Overrun behaviour

Low Power Auto Wait

Low Power Auto Power Off

##### **Synchronous clock mode divided by 4 \***

ADC 12-bit resolution

Right alignment

Forward

Disabled

Disabled

Disabled

End of single conversion

Overrun data preserved

Disabled

Disabled

##### **ADC\_Regular\_ConversionMode:**

Sampling Time

External Trigger Conversion Source

External Trigger Conversion Edge

1.5 Cycles

Regular Conversion launched by software

None

##### **WatchDog:**

Enable Analog WatchDog Mode

Low Threshold

false

0

## 3.2. CAN

**mode: Activated**

### 3.2.1. Parameter Settings:

#### **Bit Timings Parameters:**

Prescaler (for Time Quantum)	<b>3 *</b>
Time Quantum	<b>62.5 *</b>
Time Quanta in Bit Segment 1	<b>13 Times *</b>
Time Quanta in Bit Segment 2	<b>2 Times *</b>
Time for one Bit	1000
Baud Rate	<b>1000000 *</b>
ReSynchronization Jump Width	1 Time

#### **Basic Parameters:**

Time Triggered Communication Mode	Disable
Automatic Bus-Off Management	Disable
Automatic Wake-Up Mode	Disable
Automatic Retransmission	Disable
Receive Fifo Locked Mode	Disable
Transmit Fifo Priority	Disable

#### **Advanced Parameters:**

Operating Mode	Normal
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## 3.3. I2C1

**I2C: I2C**

### 3.3.1. Parameter Settings:

#### **Timing configuration:**

I2C Speed Mode	<b>Fast Mode *</b>
I2C Speed Frequency (KHz)	400
Rise Time (ns)	100
Fall Time (ns)	100
Coefficient of Digital Filter	0
Analog Filter	Enabled
Timing	<b>0x0090194B *</b>

#### **Slave Features:**

Clock No Stretch Mode	Disabled
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General Call Address Detection	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0

### 3.4. I2C2

#### mode: I2C

##### 3.4.1. Parameter Settings:

###### Timing configuration:

I2C Speed Mode	<b>Fast Mode *</b>
I2C Speed Frequency (KHz)	400
Rise Time (ns)	0
Fall Time (ns)	0
Coefficient of Digital Filter	0
Analog Filter	Enabled
Timing	<b>0x2010091A *</b>

###### Slave Features:

Clock No Stretch Mode	Disabled
General Call Address Detection	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0

### 3.5. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

#### mode: Master Clock Output

##### 3.5.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

###### RCC Parameters:

HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

### 3.6. SPI2

**Mode: Full-Duplex Master**

**Hardware NSS Signal: Hardware NSS Output Signal**

#### 3.6.1. Parameter Settings:

##### **Basic Parameters:**

Frame Format	Motorola
Data Size	<b>16 Bits *</b>
First Bit	MSB First

##### **Clock Parameters:**

Prescaler (for Baud Rate)	<b>4 *</b>
Baud Rate	<b>12.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

##### **Advanced Parameters:**

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Output Hardware

### 3.7. SYS

**mode: Debug Serial Wire**

**Timebase Source: SysTick**

### 3.8. USART1

**Mode: Asynchronous**

#### 3.8.1. Parameter Settings:

##### **Basic Parameters:**

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

##### **Advanced Parameters:**

Data Direction	Receive and Transmit
Over Sampling	16 Samples

Single Sample	Disable
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**Advanced Features:**

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

### 3.9. USB

#### mode: Device (FS)

##### 3.9.1. Parameter Settings:

**Basic Parameters:**

Speed	Full Speed 12MBit/s
Physical interface	Internal Phy

**Power Parameters:**

Low Power	Disabled
Link Power Management	Disabled

\* User modified value



## 4. System Configuration

### 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PA0	ADC_IN0	Analog mode	No pull-up and no pull-down	n/a	
	PA1	ADC_IN1	Analog mode	No pull-up and no pull-down	n/a	
	PA2	ADC_IN2	Analog mode	No pull-up and no pull-down	n/a	
	PA3	ADC_IN3	Analog mode	No pull-up and no pull-down	n/a	
	PA4	ADC_IN4	Analog mode	No pull-up and no pull-down	n/a	
	PA5	ADC_IN5	Analog mode	No pull-up and no pull-down	n/a	
	PA6	ADC_IN6	Analog mode	No pull-up and no pull-down	n/a	
	PA7	ADC_IN7	Analog mode	No pull-up and no pull-down	n/a	
	PB0	ADC_IN8	Analog mode	No pull-up and no pull-down	n/a	
	PB1	ADC_IN9	Analog mode	No pull-up and no pull-down	n/a	
CAN	PB8	CAN_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB9	CAN_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	High *	
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	High *	
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	No pull-up and no pull-down	High *	
	PB11	I2C2_SDA	Alternate Function Open Drain	No pull-up and no pull-down	High *	
RCC	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
	PA8	RCC_MCO	Alternate Function Push Pull	No pull-up and no pull-down	Low	
SPI2	PB12	SPI2_NSS	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB13	SPI2_SCK	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB14	SPI2_MISO	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PB15	SPI2_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	High *	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	SYS_SWDIO
	PA14	SYS_SWCLK	n/a	n/a	n/a	SYS_SWCLK
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
GPIO	PC13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	USER_LED

#### **4.2. DMA configuration**

nothing configured in DMA service

### 4.3. NVIC configuration

#### 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
RCC and CRS global interrupts	true	0	0
ADC and COMP interrupts (COMP interrupts through EXTI lines 21 and 22)	true	0	0
I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23	true	0	0
I2C2 global interrupt	true	0	0
SPI2 global interrupt	true	0	0
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	true	0	0
HDMI-CEC and CAN interrupts / HDMI-CEC wake-up interrupt through EXTI line 27	true	0	0
USB global interrupt / USB wake-up interrupt through EXTI line 18	true	0	0
PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31	unused		
Flash global interrupt	unused		

#### 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
RCC and CRS global interrupts	false	true	false
ADC and COMP interrupts (COMP interrupts through EXTI lines 21 and 22)	false	true	true
I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23	false	true	true
I2C2 global interrupt	false	true	true
SPI2 global interrupt	false	true	true
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	false	true	true

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
HDMI-CEC and CAN interrupts / HDMI-CEC wake-up interrupt through EXTI line 27	false	true	true
USB global interrupt / USB wake-up interrupt through EXTI line 18	false	true	true

\* User modified value

## 5. System Views

### 5.1. Category view

#### 5.1.1. Current

Middleware					
System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA	ADC		CAN		
GPIO			I2C1		
NVIC			I2C2		
RCC			SPI2		
STS			USART1		
			USB		

## 6. Docs & Resources

Type	Link
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