



1. Description

1.1. Project

Project Name	vccgnd_bluepill_stm32f072cb
Board Name	custom
Generated with:	STM32CubeMX 6.15.0
Date	08/06/2025

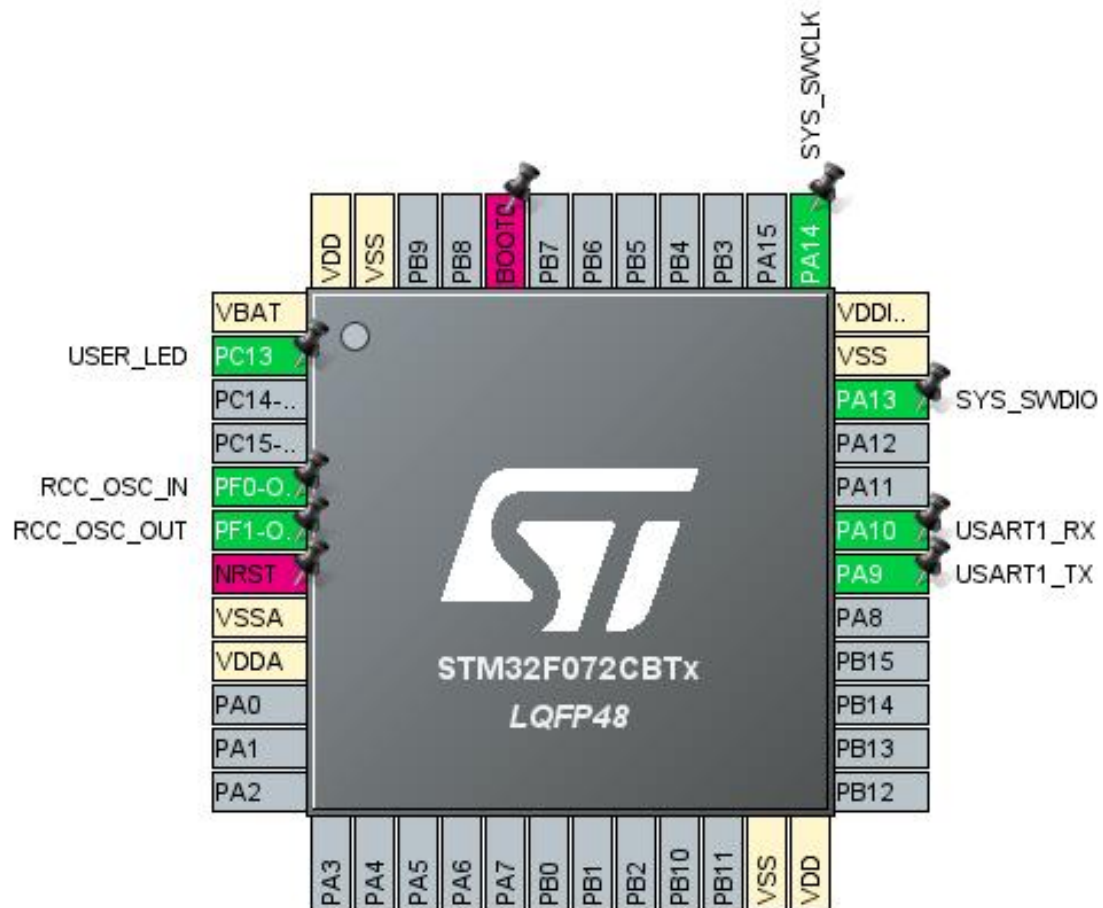
1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x2
MCU name	STM32F072CBTx
MCU Package	LQFP48
MCU Pin number	48

1.3. Core(s) information

Core(s)	Arm Cortex-M0
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2. Pinout Configuration

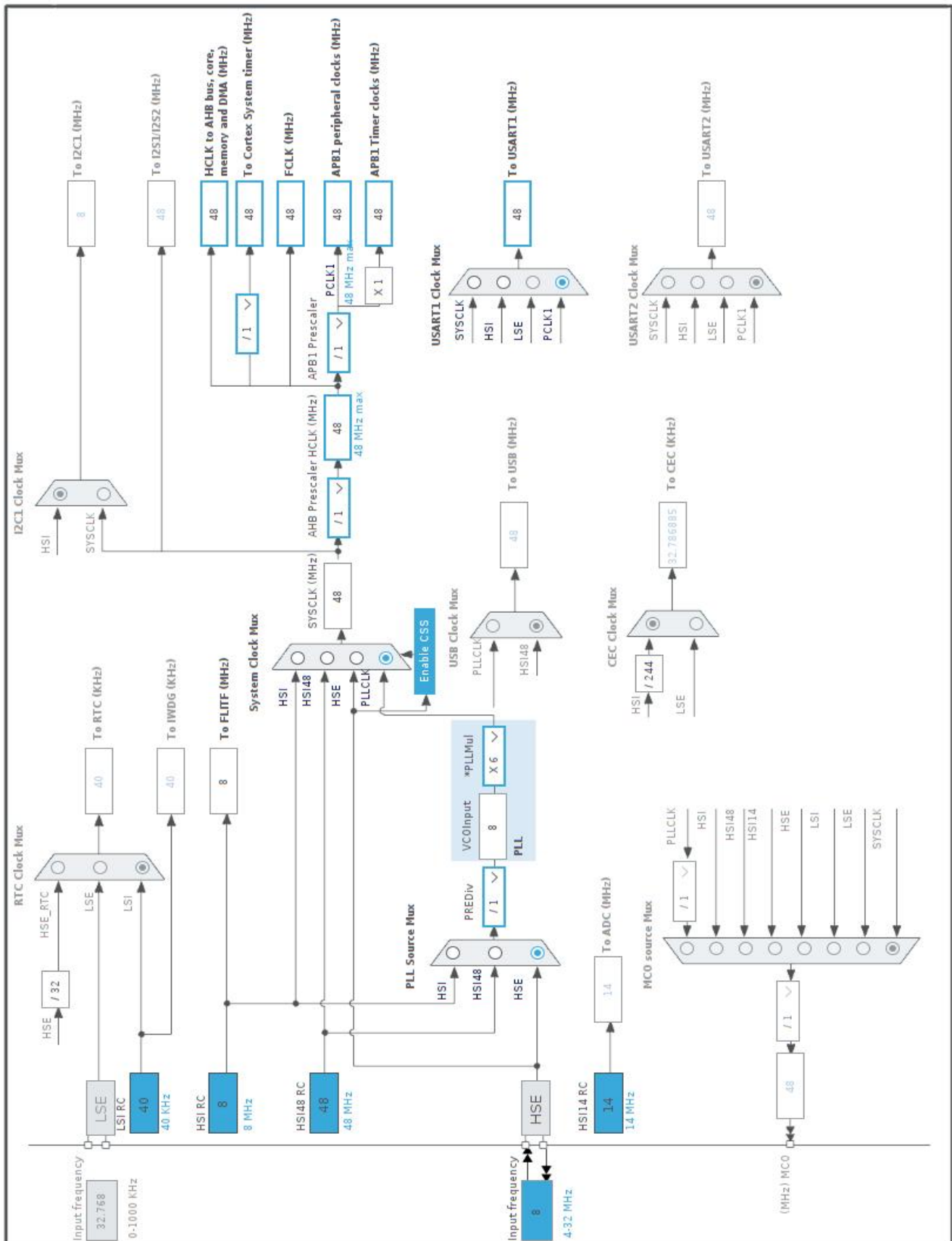


3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13 *	I/O	GPIO_Output	USER_LED
5	PF0-OSC_IN	I/O	RCC_OSC_IN	
6	PF1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
23	VSS	Power		
24	VDD	Power		
30	PA9	I/O	USART1_TX	
31	PA10	I/O	USART1_RX	
34	PA13	I/O	SYS_SWDIO	SYS_SWDIO
35	VSS	Power		
36	VDDIO2	Power		
37	PA14	I/O	SYS_SWCLK	SYS_SWCLK
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x2
MCU	STM32F072CBTx
Datasheet	DS9826_Rev5

1.2. Parameter Selection

Temperature	25
Vdd	3.6

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

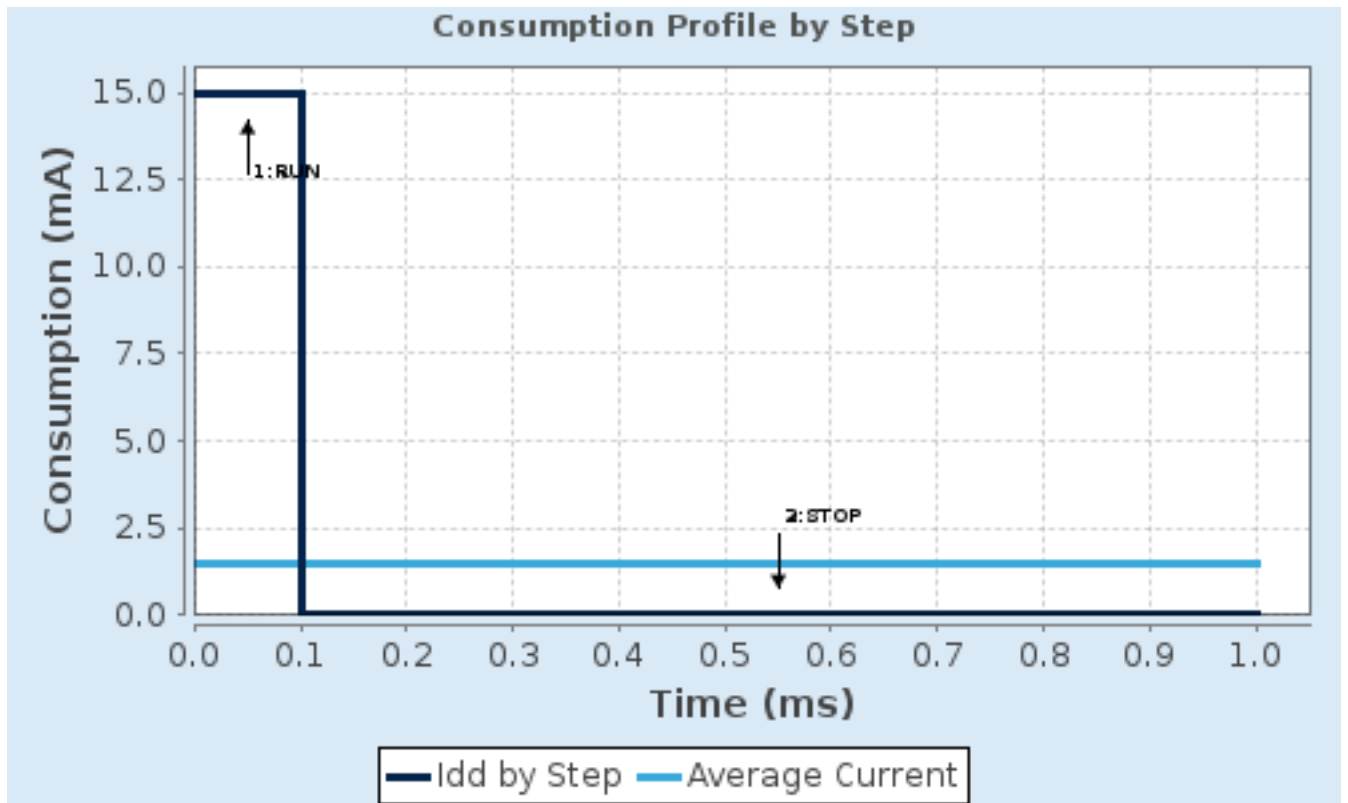
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP
Vdd	3.6	3.6
Voltage Source	Battery	Battery
Range	No Scale	No Scale
Fetch Type	FLASH	n/a
CPU Frequency	48 MHz	0 Hz
Clock Configuration	HSI	Regulator LP
Clock Source Frequency	48 MHz	0 Hz
Peripherals	GPIOA GPIOC GPIOF SYS/COMP1:High_Speed USART1	
Additional Cons.	0 mA	0 mA
Average Current	14.96 mA	6.5 μ A
Duration	0.1 ms	0.9 ms
DMIPS	0.0	0.0
Ta Max	102.09	105
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	1.5 mA
Battery Life	3 months, 2 days, 20 hours	Average DMIPS	0.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	vccgnd_bluepill_stm32f072cb
Project Folder	/store/EmbedTools/STM32CubeMX/vccgnd_bluepill/vccgnd_bluepill_stm32f072c
Toolchain / IDE	CMake
Firmware Package Name and Version	STM32Cube FW_F0 V1.11.5
Application Structure	Advanced
Generate Under Root	No
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	Yes
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	Yes
Enable Full Assert	Yes

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_USART1_UART_Init	USART1

3. *Peripherals and Middlewares Configuration*

3.1. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

3.1.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

RCC Parameters:

HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

3.2. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

3.3. USART1

Mode: Asynchronous

3.3.1. Parameter Settings:

Basic Parameters:

Baud Rate	38400
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable

DMA on RX Error	Enable
MSB First	Disable

* **User modified value**

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PF0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PF1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	SYS_SWDIO
	PA14	SYS_SWCLK	n/a	n/a	n/a	SYS_SWCLK
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	High *	
GPIO	PC13	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	USER_LED

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	3	0
RCC and CRS global interrupts	true	0	0
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	true	0	0
PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31	unused		
Flash global interrupt	unused		

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
RCC and CRS global interrupts	false	true	false
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	false	true	true

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

6. Docs & Resources

Type	Link
IBIS models	https://www.st.com/resource/en/ibis_model/stm32f0_ibis.zip
System View Description	https://www.st.com/resource/en/svd/stm32f0_svd.zip
Presentations	https://www.st.com/resource/en/product_presentation/gt_stm32f0-io.pdf
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Product Certifications	https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf
Security Bulletin	https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf
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