


m\_verify::dp\_accdig



```
graph LR; A[m_verify::dp_accdig] --> B[m_verify::anyscalar_to_real128]
```

A diagram showing a mapping from a source type to a target type. On the left, a gray rectangular box contains the text 'm\_verify::dp\_accdig'. A blue arrow points from the right side of this box to the left side of a white rectangular box on the right. The white box contains the text 'm\_verify::anyscalar\_to\_real128'.

m\_verify::anyscalar  
\_to\_real128