

tikz

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1 Introduction

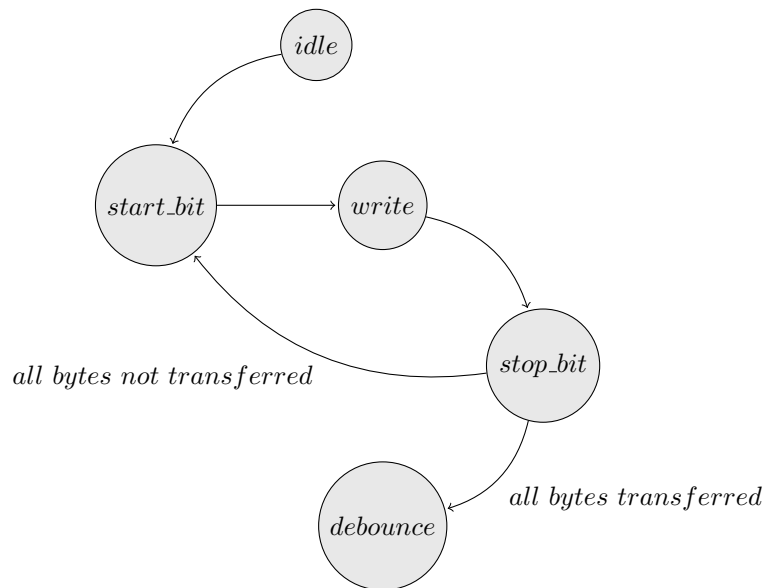


Figure 1: State diagram for UART Protocol

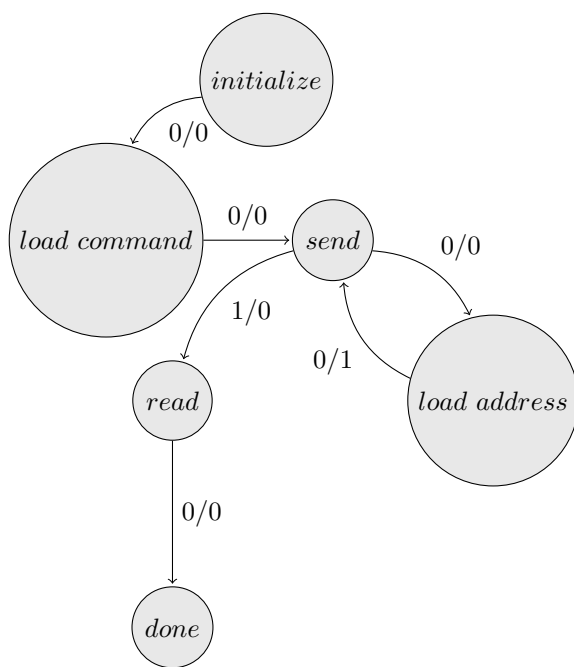


Figure 2: State diagram for SPI Flash Protocol

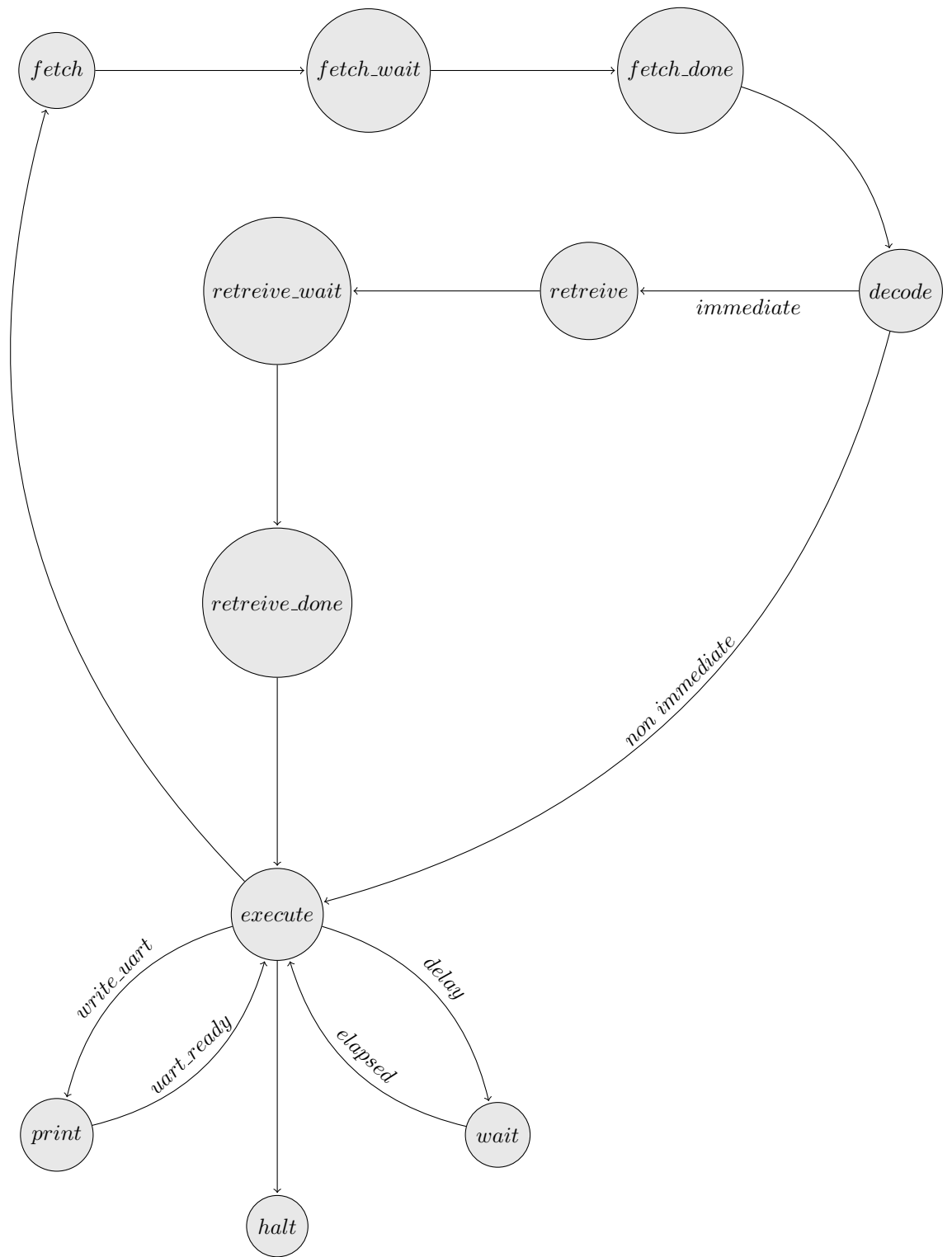


Figure 3: State diagram for CPU

<i>CM</i>	<i>COMMANDS</i>					<i>VARIATION</i>									

Figure 4: ISA