

Разработчик: _____

Изд. № _____

Взвеш. иб. № _____

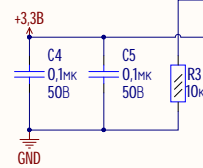
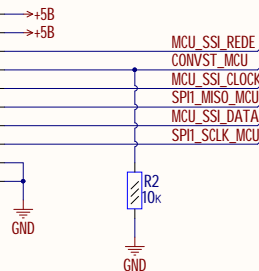
Подп. и дата _____

Стор. № _____

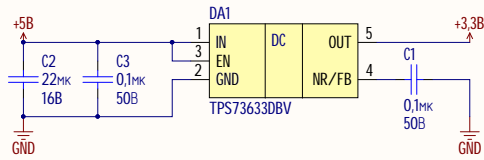
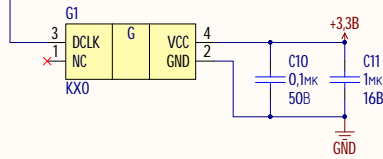
Лист. № _____

Цепь	←	X1
+5В	1	→+5В
+5В	2	→+5В
SSI_REDE	3	
CONVST_MCU	4	
SSI_CLOCK	5	
SSI_MISO_MCU	6	
SSI_DATA	7	
SCLK_MCU	8	
GND	9	
GND	10	

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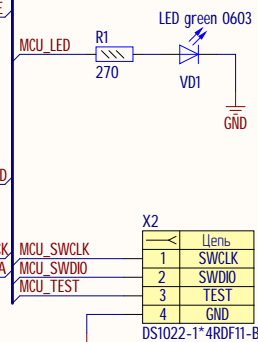
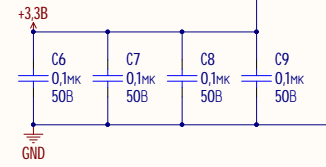


DD1	MCU
10 PA0/WKUP/USART2_CTS/ADC12_IN0/TIM2_CH1_ETR	
11 PA1/USART2_RTS/TIM2_CH2/ADC12_IN1	
12 PA2/USART2_TX/TIM2_CH3/ADC12_IN2	
13 PA3/USART2_RX/TIM2_CH4/ADC12_IN3	
14 PA4/SPI1_NSS/SPI3_NSS/USART2_CK/ADC12_IN4	
15 PA5/SPI1_SCK/ADC12_IN5	
16 PA6/SPI1_MISO/ADC12_IN6/TIM3_CH1 (TIM1_BKIN)	
17 PA7/SPI1_MOSI/ADC12_IN7/TIM3_CH2 (TIM1_CH1N)	
29 PA8/USART1_CK/TIM1_CH1/MCO FT	
30 PA9/USART1_TX/TIM1_CH2 FT	
31 PA10/USART1_RX/TIM1_CH3 FT	
32 PA11/USART1_CTS/CANRX/USBDM/TIM1_CH4 FT	
33 PA12/USART1_RTS/USART6_RX/CANTX/USBDM/TIM1_ETR FT	
34 JTMS/SWDIO (PA13) FT	
37 JTCK/SWCLK (PA14) FT	
38 JTDI (SPI1_NSS/TIM2_CH1_ETR/PA15) FT	



18 PB0/ADC12_IN8/TIM3_CH3 (TIM1_CH2N)	
19 PB1/TIM3_CH4T/ADC12_IN9 (TIM1_CH3N)	
20 PB2/BOOT1	
39 FT JTDO (SPI1_SCK/TIM2_CH2/TRACESWO/PB3)	
40 FT NJTRST (SPI1_MISO/TIM3_CH1/PB4)	
41 PB5/I2C1_SMBAL (TIM3_CH2/SPI1_MOSI)	
42 FT PB6/I2C1_SCL/TIM4_CH1 (USART1_TX)	
43 FT PB7/I2C1_SDA/TIM4_CH2 (USART1_RX)	
44 FT PB8/TIM4_CH3 (I2C1_SCL/CANRX)	
45 FT PB9/TIM4_CH4 (I2C1_SDA/CANTX)	
46 FT PB10/USART3_TX/I2C2_SCL (TIM2_CH3)	
21 FT PB11/I2S2_SDA/USART3_RX (TIM2_CH4)	
22 FT PB12/SPI2_NSS/I2C2_SMBAL/USART3_CK/TIM1_BKIN	
23 FT PB13/SPI2_SCK/USART3_CTS/TIM1_CH1N	
24 FT PB14/SPI2_MISO/USART3_RTS/TIM1_CH2N	
25 FT PB15/SPI2_MOSI/TIM1_CH3N	

9 VDDA/VREF+	
8 VSSA/VREF-	
24 VDD_1	
36 VDD_2	
48 VDD_3	
23 VSS_1	
35 VSS_2	
47 VSS_3	



Изм.	Лист	№ докум.	Подп.	Дата	Контроллерный модуль	Лист	Масштаб
Разработ.	Подписанный				Схема электрическая принципиальная	Лист	Масштаб 1
Проб.	Осужден						
Т.контр.							
Н.контр.							
Умб.	Осужден						

Копировал

Формат A2