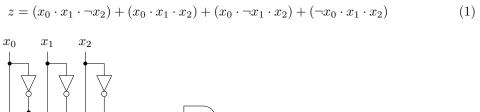
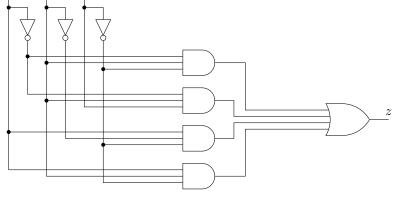
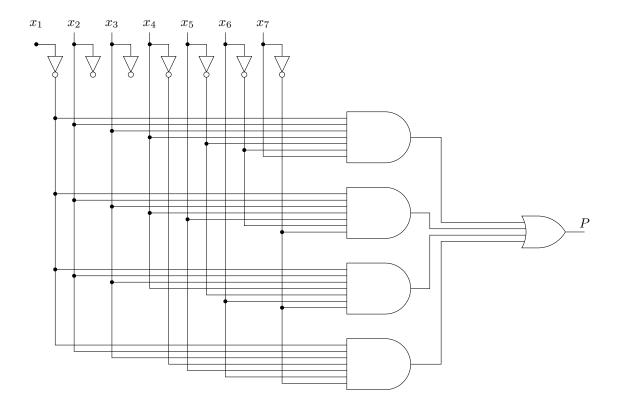
1 Logic circuit 1



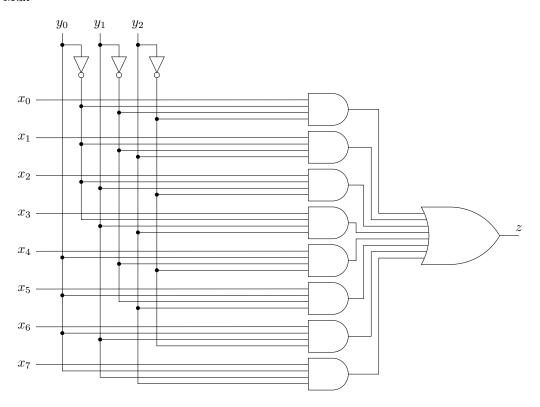


2 Logic circuit 2

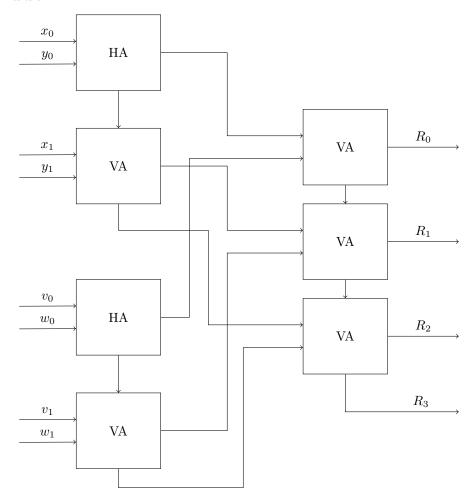


3 Logic circuit 3

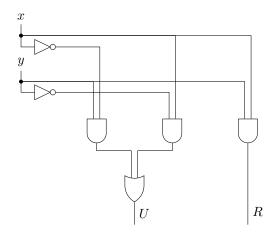
3-Mux



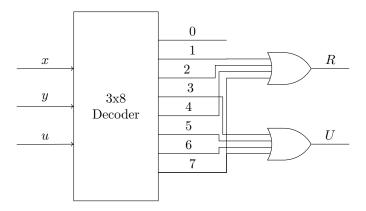
4 Adder



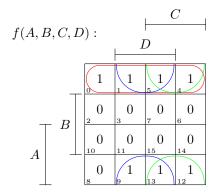
5 Halfadder



6 Decoder



7 Karnaugh Map



$$\neg A \neg B + \neg BD + \neg BC \tag{2}$$

8 K2

