

Detector unit of one module  
feeding one DAQ fragment

Readout electronics  
TPC+PDS

(fiber)

DAQ front-end fragment hardware and software  
(N≈100 per module)

Front End Computer	
receiver	dispatcher
primary RAM buffer	
Data selector	
recv	send

data stream (network)

Trigger farm fragment	
host 1	host 2
RAM buffer	RAM buffer
ssd buffer	ssd buffer
trigger primitive pipeline	trigger primitive pipeline
trigger candidate pipeline	

External trigger  
logic unit  
(beam, SNEWS  
other modules)

bidirectional  
exchange of  
trigger candidates  
(network)

trigger  
candidates  
(network)

DAQ back-end computing  
for one detector module  
(eg, SP / DP / M3 / M4)

Module-Level  
Trigger Logic

data selection  
request  
(network)

data  
fragment  
(network)

trigger  
commands  
(network)

Event Builder  
(instance for one trigger)

module-level  
DAQ event block  
(network)

raw DAQ files  
(Internet)

Output  
disk buffer

DAQ/Offline  
boundary

Offline  
permanent storage  
and computing

Fermilab  
and others