

```

void trig_rept_task() {
    #if EBOARD_NANO == 0x0 |
    | defined(DOC) if (_pwm
    Value != _OpwmValue){ ana
    logWrite(PIN_MOTOR_SPE,
    commentstyle_pwmValue); _OpwmValue =
    commentstyle_pwmValue;}#endif #ifde
    f REPT_TASK rept_task(
    );#endif} int timer_cou
    nt = 0; bool commentstyletimer_ofl=
    false; ISR(TIMER2_OVF_v
    ect) { commentstyletimer_count++; i
    f( commentstyletimer_count >= EBOARD
    _PWM_SPE*1000 && EBOARD
    _PWM_SPE >? 0 && !timer
    _ofl){ commentstyletimer_ofl = true
    ; commentstyletimer_count -= EBOARD
    _PWM_SPE * 1000; trig_r
    ept_task(); commentstyletimer_ofl =
    false; } TCNT2 = 256 -
    (int)((float)F_CPU * 0.
    001 / 64); } struct LCD
    { #if EBOARD_NANO == 0
    LCD(SoccerBoard &soccer
    Board, optVAL_t id=0x3C
    ); #else LCD(optVAL_t i
    d=0x3C);#endif bool changeID(optVAL_t new
    ID = 0x3C); bool clear(void); void print(co
    nst char* data); void print(int data); void
    print(optVAL_t line, optVAL_t cols, const

```