

Inhaltsverzeichnis

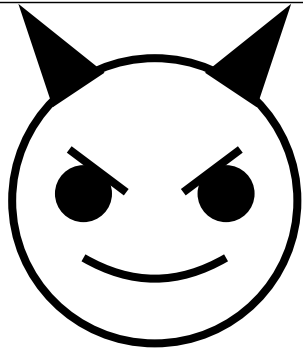
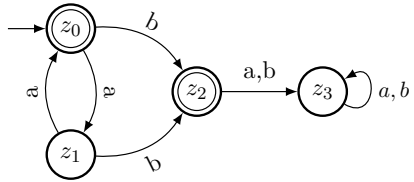
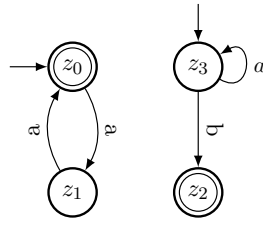
Allerlei	2
Teufel	2
Automat	2
AutomatDFA	2
AutomatNFA	2
CYKAlgorithmus	3
Demo-1	3
Demo-2	3
Demo	3
Header	3
MealyAutomat	3
Minimalautomat-Beispiel/MinimalautomatBeispiel1	4
Minimalautomat-Beispiel/MinimalautomatBeispiel2	4
Minimalautomat-Beispiel/MinimalautomatBeispiel3	4
Minimalautomat-Beispiel/MinimalautomatBeispiel4	5
Minimalautomat-Beispiel/MinimalautomatBeispiel5	5
Minimalautomat-Beispiel/MinimalautomatBeispiel6	5
MooreAutomat	6
Datenbanken	6
ERMExample	6
Eigene	6
Proseminar/Cluster/en-circles	6
Proseminar/Cluster/en-clusters	6
Proseminar/Cluster/en-moons	6
Proseminar/Cluster/en-special	7
Proseminar/Cluster/km-circles	7
Proseminar/Cluster/km-clusters	7
Proseminar/Cluster/km-moons	7
Proseminar/Cluster/km-special	8
Proseminar/Cluster/kn-circles	8
Proseminar/Cluster/kn-clusters	8
Proseminar/Cluster/kn-moons	8
Proseminar/Cluster/kn-special	9
Proseminar/Cluster/rolf-circles	9
Proseminar/Cluster/rolf-clusters	9
Proseminar/Cluster/rolf-moons	9

Proseminar/Cluster/rolf-special	10
Proseminar/Cluster/thumb-circles	10
Proseminar/Cluster/thumb-clusters	10
Proseminar/Cluster/thumb-moons	10
Proseminar/Cluster/thumb-special	11
Graphen	11
GraphNachbarschaftGrad	11
GraphNichtPlanarK33	11
GraphNichtPlanarK5	11
GraphTopologie	12
GraphWegPfad	12
GraphZyklus	12
Haskell	12
HaskellTypen	12
Listenoperationen	13
Java	13
StreamDemo	13
Logik	13
KVDiagramm	13
KVWuerfel	13
QuineMCCluskeyTabelle	14
QuineMCCluskeyZusammenfassen	14
Mengen	14
FunktionBijektiv	14
FunktionInjektiv	14
FunktionSurjektiv	15
Mengenmultiplikation/Mengenmultiplikation2	15
Mengenmultiplikation/Mengenmultiplikation3	15
Mengenmultiplikation/Mengenmultiplikation4	15
VennDifferenz	15
VennSchnitt	16
VennVereinigung	16
Prozesse	16
FCFS-WorstCase	16
FCFS	16
Prozesszustaende	16
Rechner	17
ALU	17
AmpelPLA	17
BarrelShifter	17
Beispielprozessor	17
CLA	17

CPLD	18
CSA	18
DreiTorRegister	18
Eintorspeicher	18
GALPAL	19
Geraeteverwaltung	19
HardwareSkizze	19
ISA	19
LUT	19
LUTOder	20
MIPS	20
MuxDemuxKommunikation	20
MuxShannon	20
NAdressmaschine	20
PLA	21
PLAZuAmpel	21
PROM	21
Pages	21
Physik/DiodenStromstaerke	22
Physik/Metastabil	22
Physik/TransistorStoertoleranz	22
RegisterParallel	22
RegisterSeriell	22
Shiftregister	23
Speicherhierarchie	23
StackExample	23
Stackmaschine	23
StackmaschineSimpler	24
Schaltkreis	24
Addier-Subtrahierer	24
Demultiplexer	24
SynchronzaehlerDFF	24
SynchronzaehlerTFF	24
Volladdierer	24
Software	25
DreiSchichtenArchitektur	25
Meta/ProgrammierparadigmenUeberblick	25
ModelViewController	25
RegexExample	25
ThreadStates	25
UMLCompositePattern	26
UMLDecoratorPattern	26
UMLExample	26
UMLObserverPattern	26
UMLSEQObserverPattern	26
UMLSEQObserverPatternAdapted	26

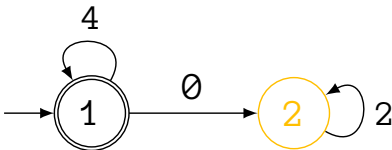
UMLStateDiagramExample	27
UMLThread	27
Sprachen	27
CYKAlgorithmus	27
ChomskyHierarchie	27
Grammatik	27

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%%Einbindung erfolgt über:
\getGraphics{\Pfad}
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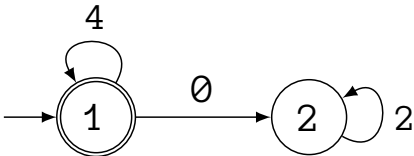
Pfad	Ergebnis
Allerlei/Teufel	
Automat/AutomatDFA	<p>Deterministic Finite Automaton</p>  $D = \left(\begin{array}{ccccc} \text{Zustände} & \text{Eingabe} & \text{Übergangsfkt.} & \text{Start(s)} & \text{Endzust.} \\ Z & \Sigma & \delta & S & E \\ \text{Menge} & \text{Alphabet} & Z \times \Sigma \rightarrow Z & \in Z & \subseteq Z \end{array} \right)$ $T(D) = \{x \in \Sigma^* \mid \hat{\delta}(S, x) \cap E \neq \emptyset\}$
Automat/AutomatNFA	<p>Nondeterministic Finite Automaton</p>  $N = \left(\begin{array}{ccccc} \text{Zustände} & \text{Eingabe} & \text{Übergangsfkt.} & \text{Start(s)} & \text{Endzust.} \\ Z & \Sigma & \delta & S & E \\ \text{Menge} & \text{Alphabet} & Z \times \Sigma \rightarrow Z^* & \subseteq Z & \subseteq Z \end{array} \right)$ $T(N) = \{x \in \Sigma^* \mid \hat{\delta}(S, x) \cap E \neq \emptyset\}$

		1 a	2 b	3 c	4 c
T[1,j]	1	A	B	CE	CE
T[2,j]	2	D	-	E	
T[3,j]	3	S	-		
T[4,j]	4	S			

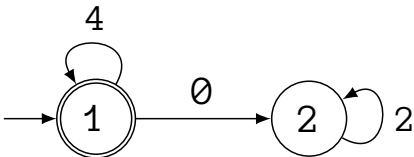
Automat/CYKAlgorithmus



Automat/Demo-1



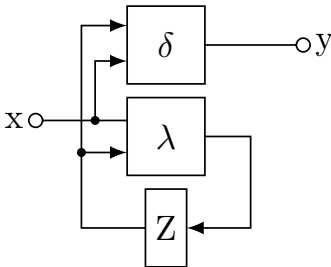
Automat/Demo-2



Automat/Demo

Isch bin a Hädder!

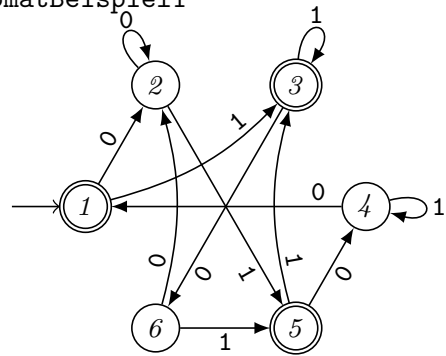
Automat/Header



Ausgabe von Zustand & Eingabe abhängig

Automat/MealyAutomat

Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel1



2	
3	
4	
5	
6	

Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel2

2	0
3	
4	0
5	
6	0

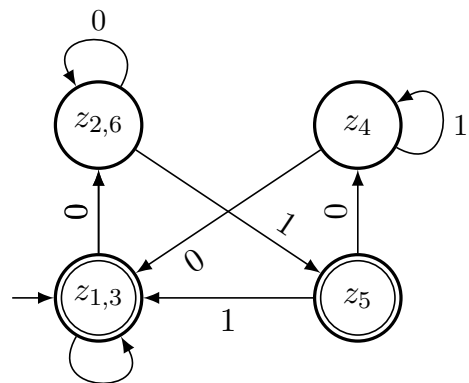
Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel3

2	0				
3		0			
4	0	1	0		
5		0		0	
6	0		0	1	0

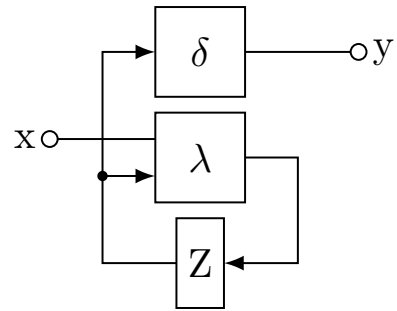
Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel4

2	0				
3		0			
4	0	1	0		
5	2	0	2	0	
6	0		0	1	0

Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel5

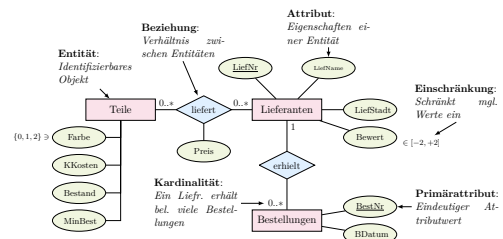


Automat/Minimalautomat-Beispiel/MinimalautomatBeispiel6

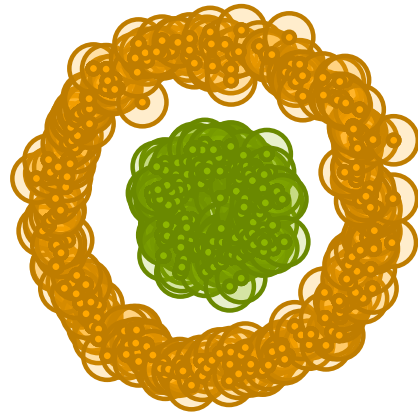


Automat/MooreAutomat

Ausgabe nur vom Zustand abhängig

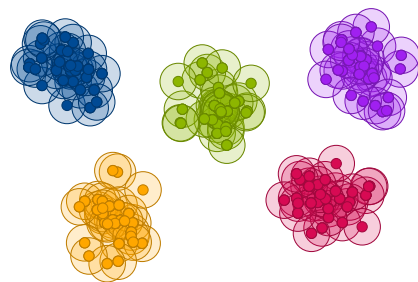


Datenbanken/ERExample



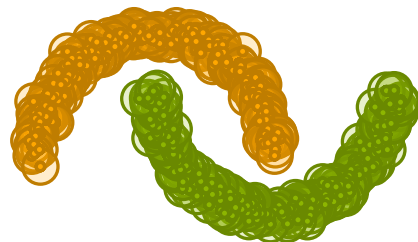
Eigene/Proseminar/Cluster/en-circles

pdf



Eigene/Proseminar/Cluster/en-clusters

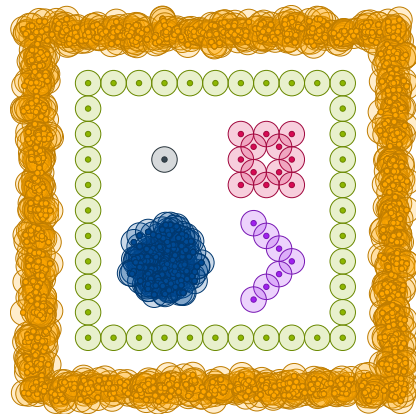
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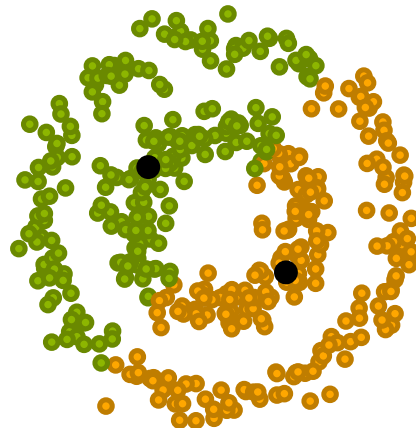
Eigene/Proseminar/Cluster/en-moons

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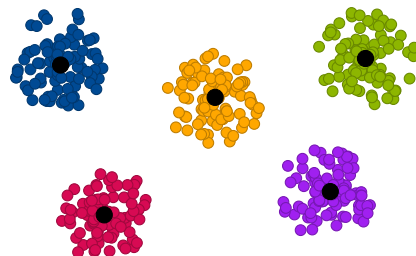
Eigene/Proseminar/Cluster/en-special
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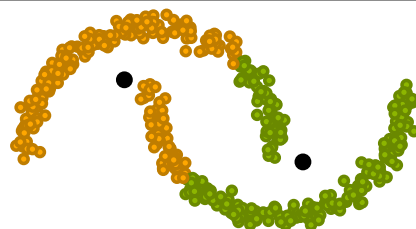
Eigene/Proseminar/Cluster/km-circles
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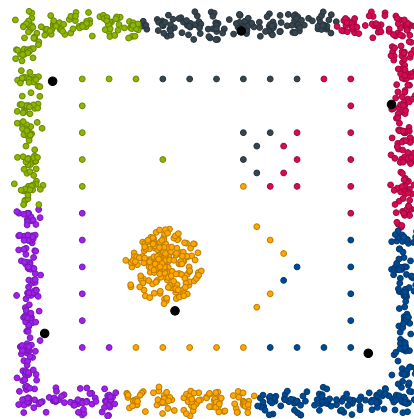
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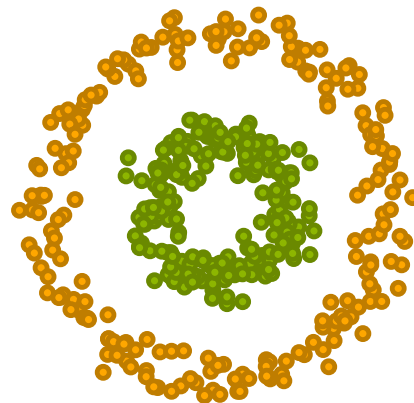
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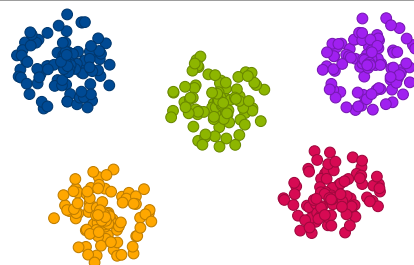
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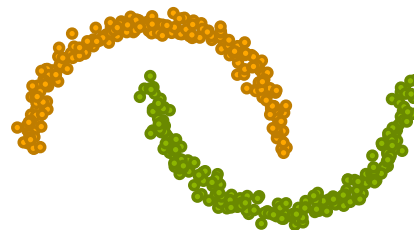
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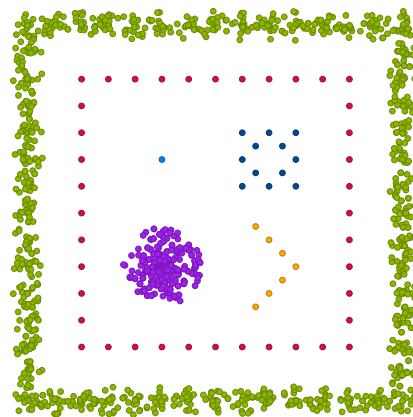
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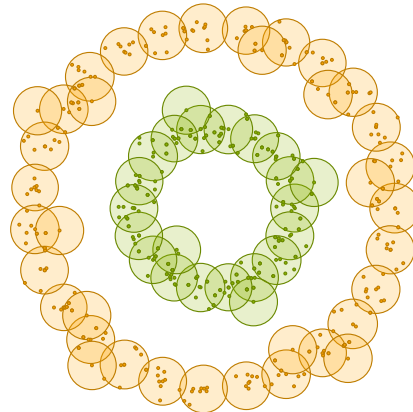
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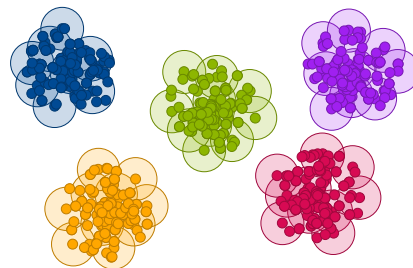
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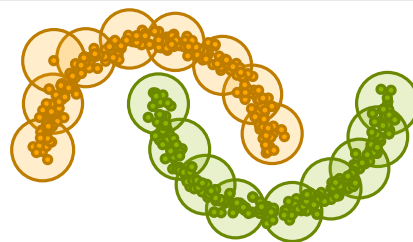
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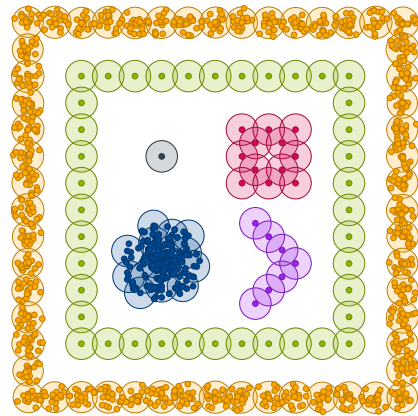


Eigene/Proseminar/Cluster/rolf-clusters
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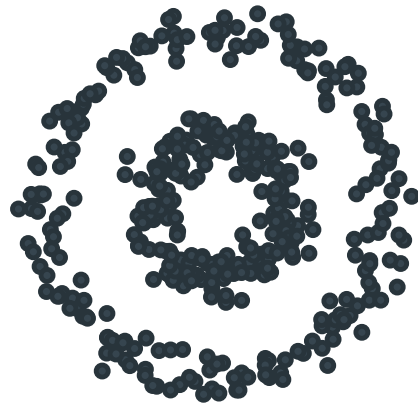


Eigene/Proseminar/Cluster/rolf-moons
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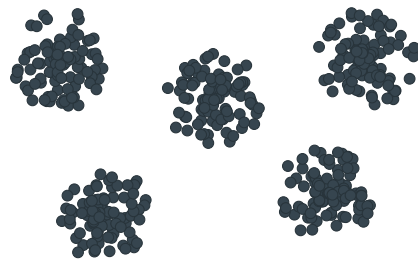




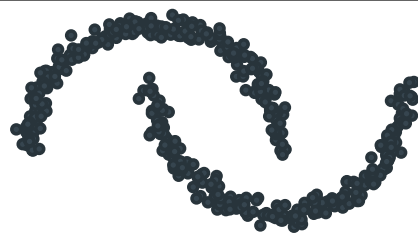
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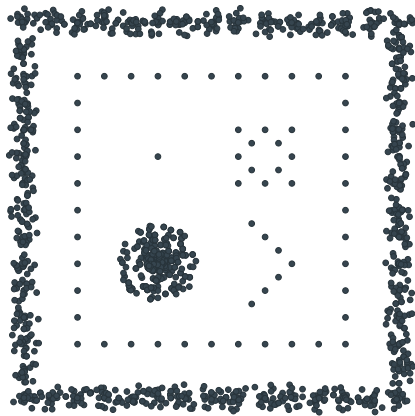
Eigene/Proseminar/Cluster/thumb-circles
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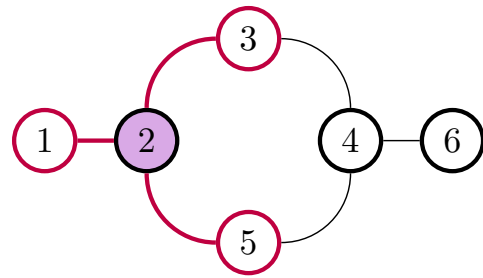
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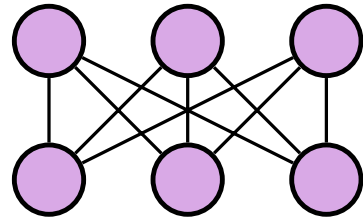


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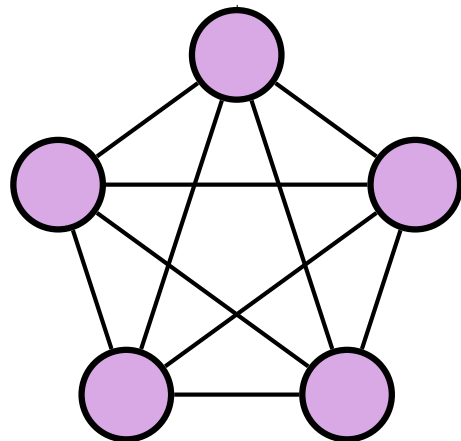
Graphen/GraphNachbarschaftGrad

$K_{3,3}$

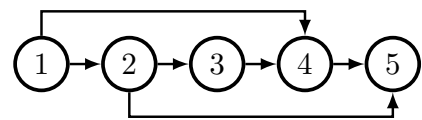


Graphen/GraphNichtPlanarK33

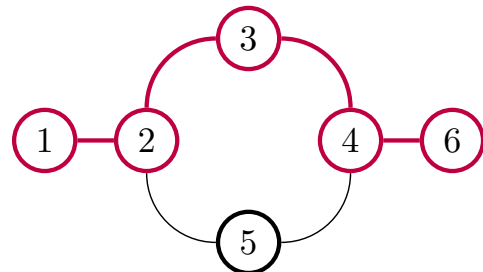
K_5



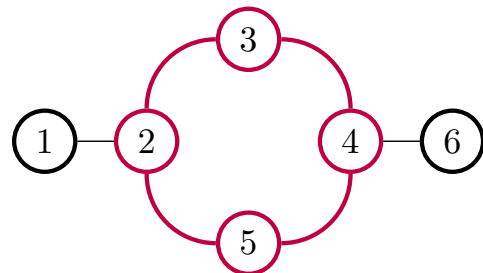
Graphen/GraphNichtPlanarK5



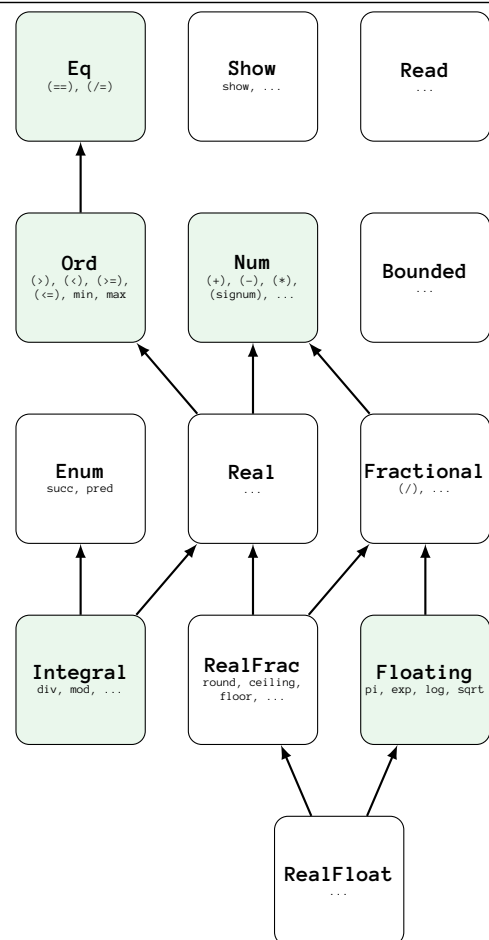
Graphen/GraphTopologie



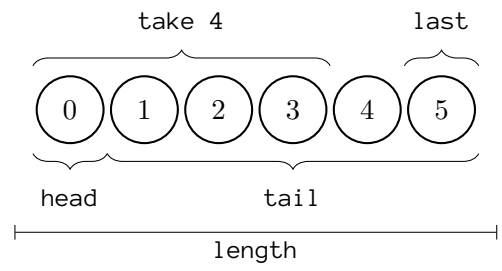
Graphen/GraphWegPfad



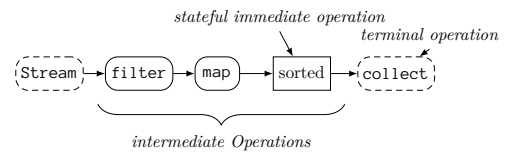
Graphen/GraphZyklus



Haskell/HaskellTypen



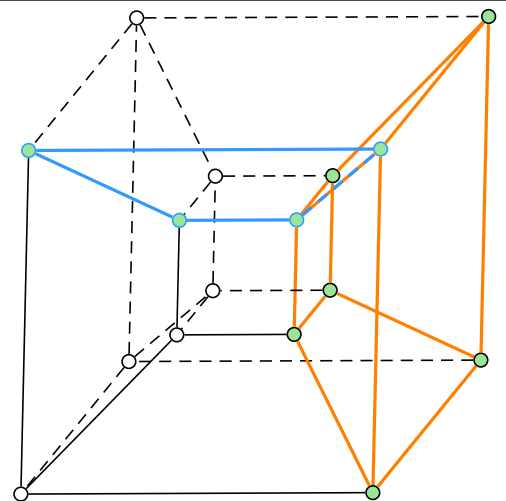
Haskell/Listenoperationen



Java/StreamDemo

	\bar{a}	a	a	\bar{a}	
\bar{b}	0	0	0	0	\bar{d}
b	1	1	1	1	\bar{d}
b	1	1	1	1	d
\bar{b}	0	0	1	1	d
	\bar{c}	\bar{c}	c	c	

Logik/KVDiagramm

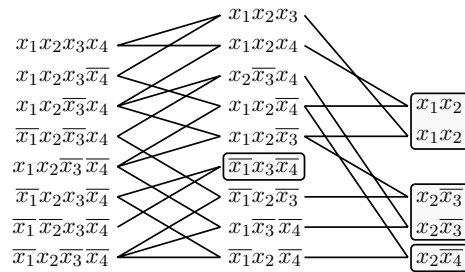


Logik/KVWuerfel

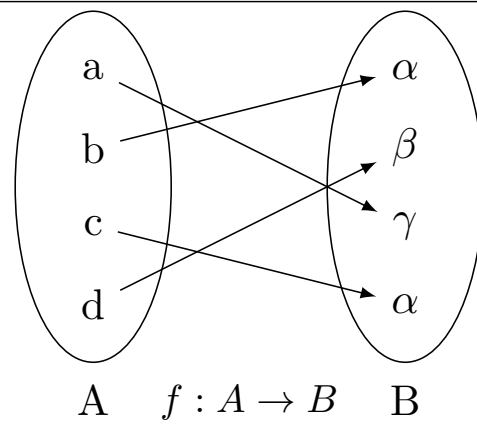
Logik/QuineMCCluskeyTabelle

	1	2	3	4	5	6	7	8
x_1x_2	+	+	+		+			
$x_2\bar{x}_3$			+	+	+			+
$x_2\bar{x}_4$		+			+	+		+
$\bar{x}_1x_3\bar{x}_4$						+	+	

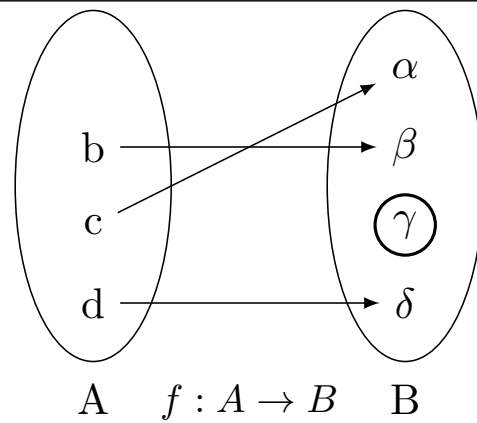
Logik/QuineMCCluskeyZusammenfassen

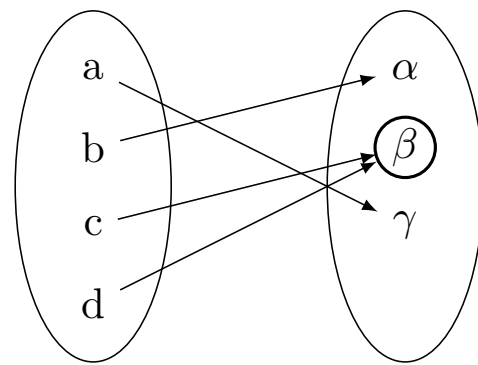


Mengen/FunktionBijektiv



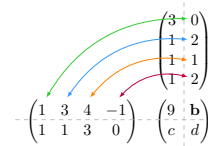
Mengen/FunktionInjektiv





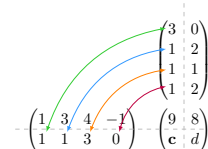
$A \quad f : A \rightarrow B \quad B$

Mengen/FunktionSurjektiv



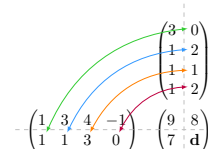
Für b ergibt sich also:
 $b = 1 * 0 + 3 * 2 + 4 * 1 + (-1) * 2$
 $= 8$

Mengen/Mengenmultiplikation/Mengenmultiplikation2



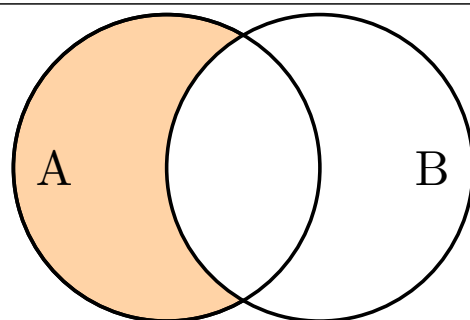
Für c ergibt sich also:
 $c = 1 * 3 + 1 * 1 + 3 * 1 + 0 * 1$
 $= 7$

Mengen/Mengenmultiplikation/Mengenmultiplikation3

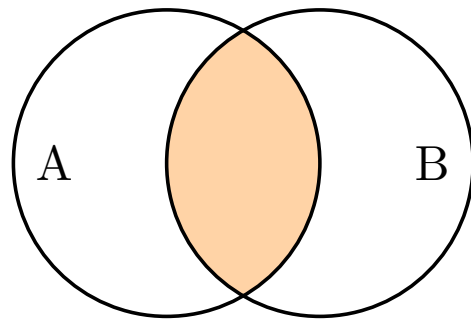


Für d ergibt sich also:
 $d = 1 * 0 + 1 * 2 + 3 * 1 + 0 * 2$
 $= 5$

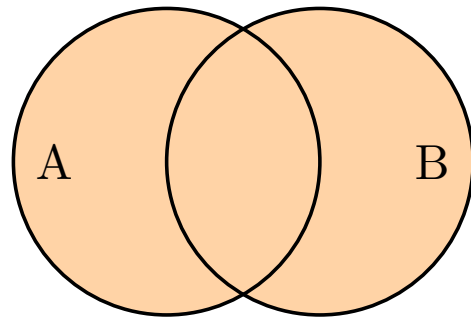
Mengen/Mengenmultiplikation/Mengenmultiplikation4



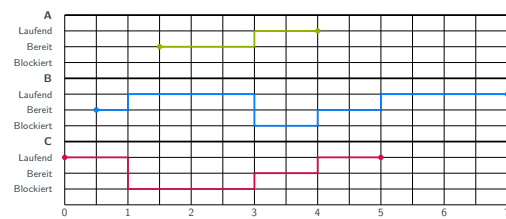
Mengen/VennDifferenz



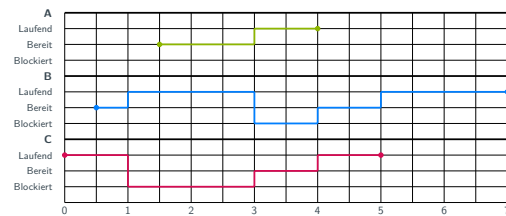
Mengen/VennSchnitt



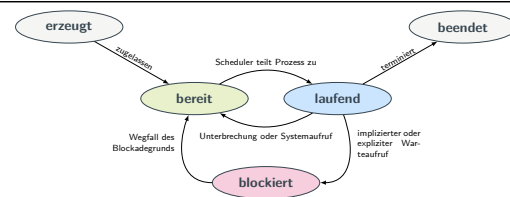
Mengen/VennVereinigung



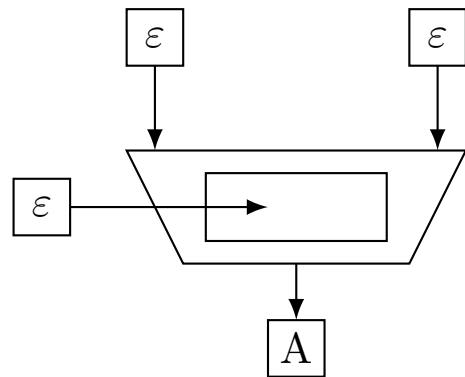
Prozesse/FCFS-WorstCase



Prozesse/FCFS



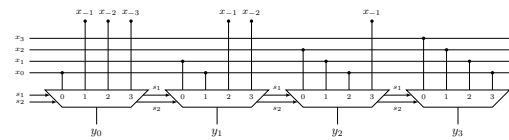
Prozesse/Prozesszustaende



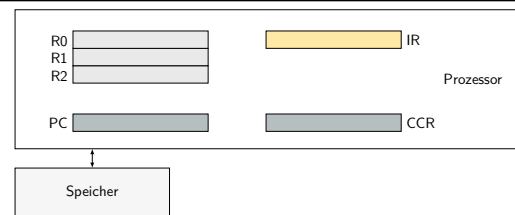
Rechner/ALU

δ	Zustand			Folgez.		NS			WO			T
	h_0	z_0	z_1	z'_0	z'_1	\bullet	\circ	\circ	\bullet	\circ	\circ	
δ_0	0	0	0	0	1	\bullet	\circ	\circ	\circ	\circ	\bullet	$7 \Rightarrow h'_0 = 1$
δ_1	1	0	1	0	1	\bullet	\circ	\circ	\circ	\circ	\bullet	$T - 1$
δ_3	0	0	1	1	0	\circ	\bullet	\circ	\circ	\bullet	\circ	$2 \Rightarrow h'_0 = 1$
δ_4	1	1	0	1	0	\circ	\bullet	\circ	\circ	\bullet	\circ	$T - 1$
δ_6	0	1	0	1	1	\circ	\circ	\bullet	\circ	\circ	\circ	$7 \Rightarrow h'_0 = 1$
δ_7	1	1	1	1	1	\circ	\circ	\bullet	\circ	\circ	\circ	$T - 1$
δ_9	0	1	1	0	0	\circ	\bullet	\circ	\circ	\bullet	\circ	$2 \Rightarrow h'_0 = 1$
δ_{10}	1	0	0	0	0	\circ	\bullet	\circ	\circ	\bullet	\circ	$T - 1$

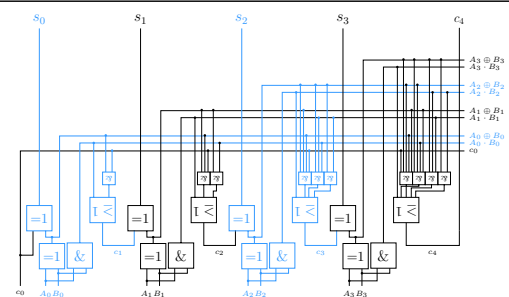
Rechner/AmpelPLA



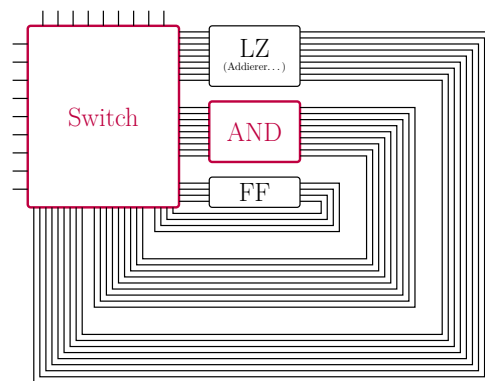
Rechner/BarrelShifter



Rechner/Beispielprozessor

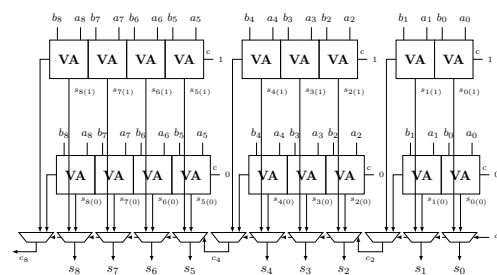


Rechner/CLA

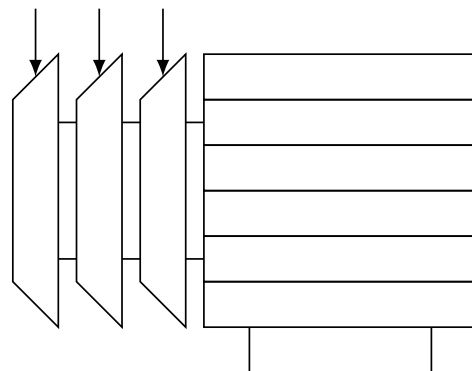


Rechner/CPLD

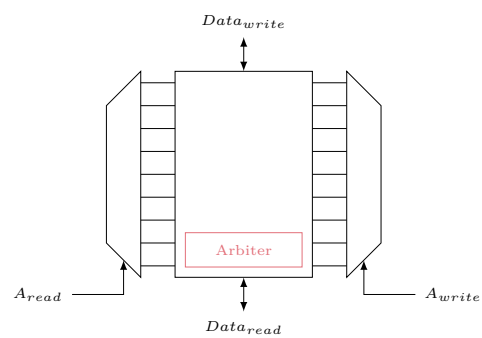
CSA - 9-bit Carry-Select-Addierer



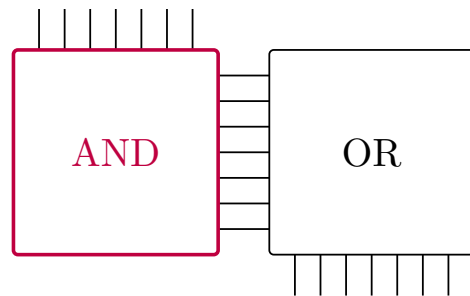
Rechner/CSA



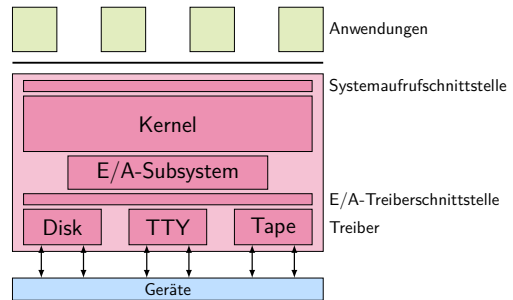
Rechner/DreiTorRegister



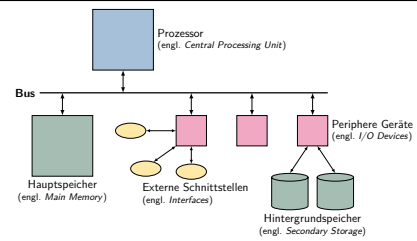
Rechner/Eintorspeicher



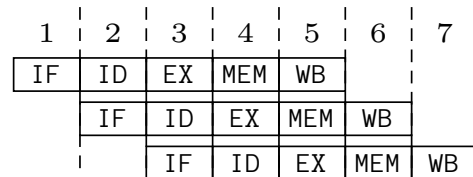
Rechner/GALPAL



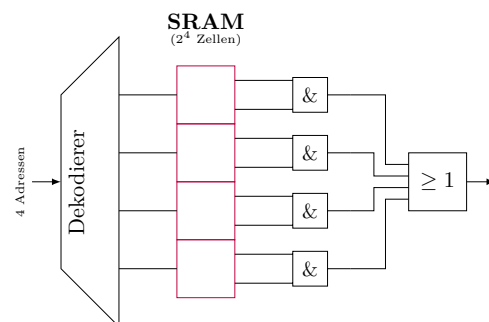
Rechner/Geraeteverwaltung



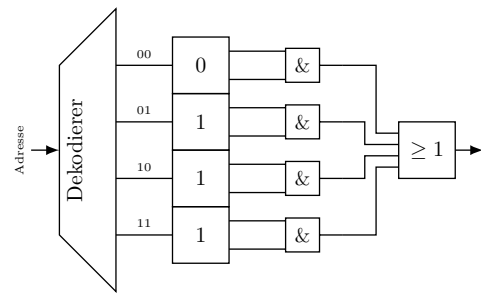
Rechner/HardwareSkizze



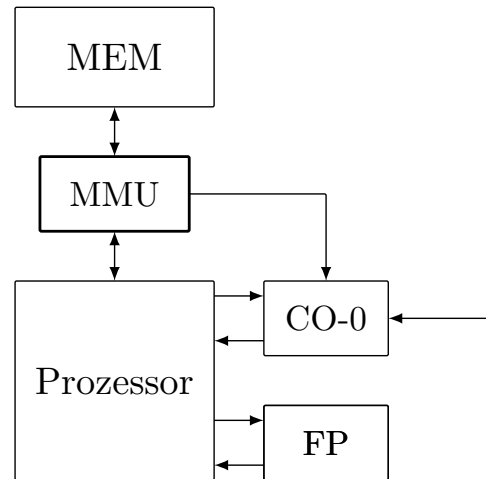
Rechner/ISA



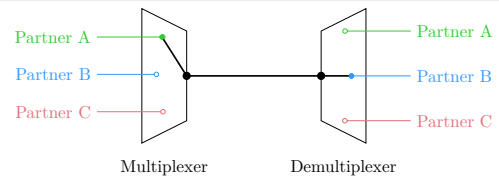
Rechner/LUT



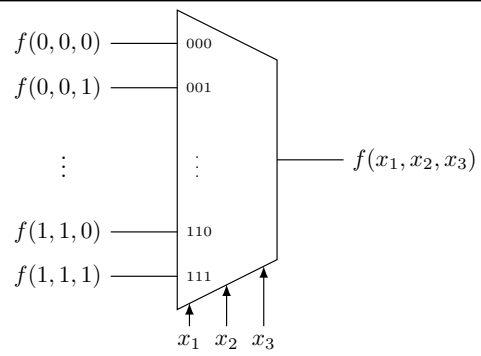
Rechner/LUT0der



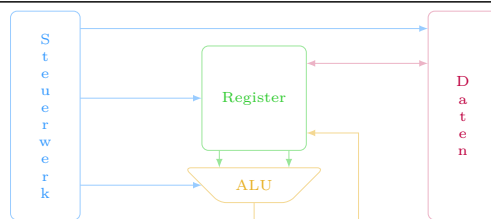
Rechner/MIPS



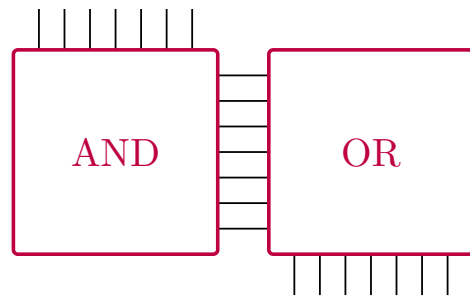
Rechner/MuxDemuxKommunikation



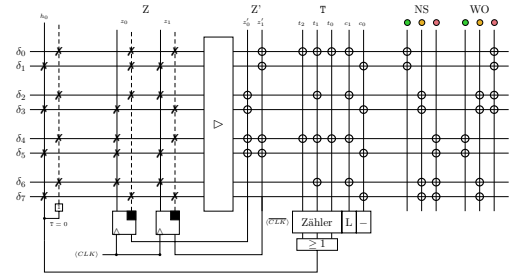
Rechner/MuxShannon



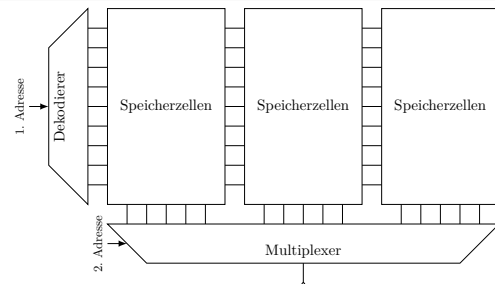
Rechner/NAdressmaschine



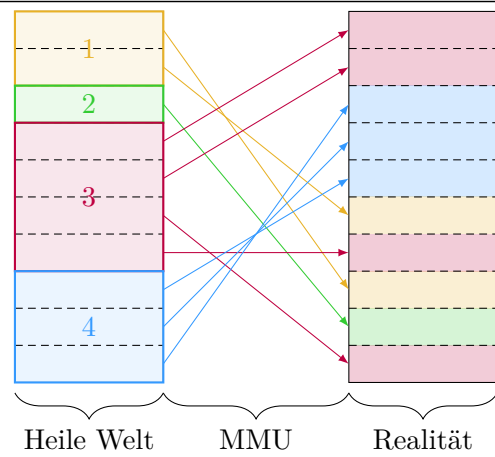
Rechner/PLA



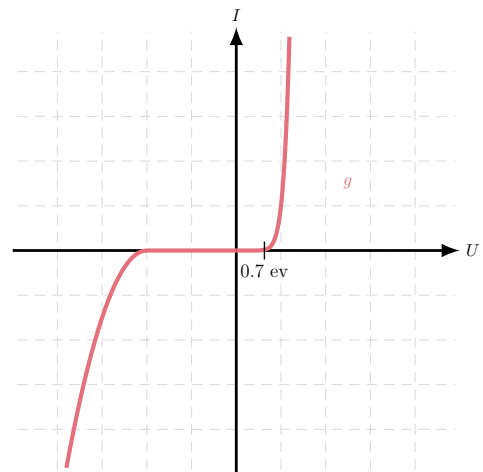
Rechner/PLAZuAmpel



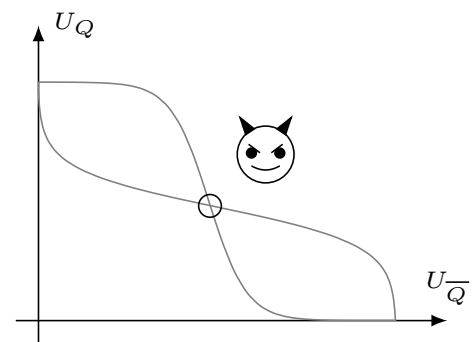
Rechner/PROM



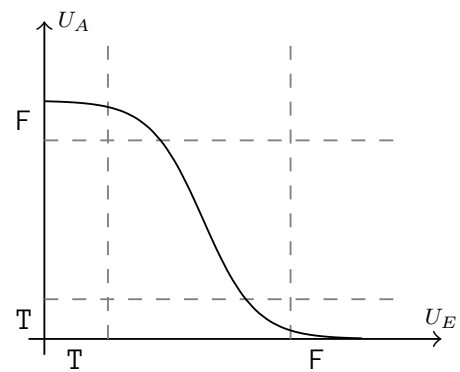
Rechner/Pages



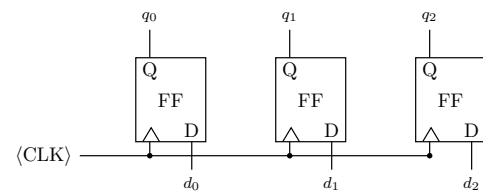
Rechner/Physik/DiodenStromstaerke



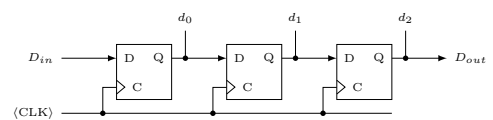
Rechner/Physik/Metastabil



Rechner/Physik/TransistorStoertoleranz

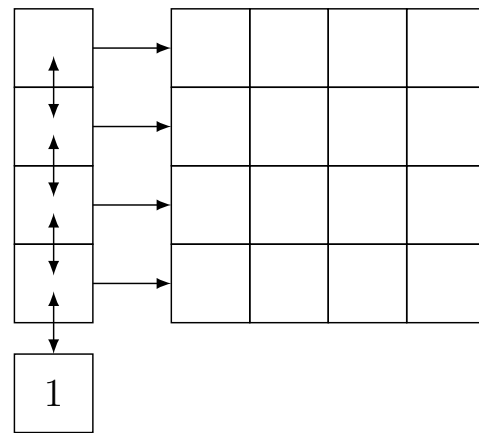


Rechner/RegisterParallel

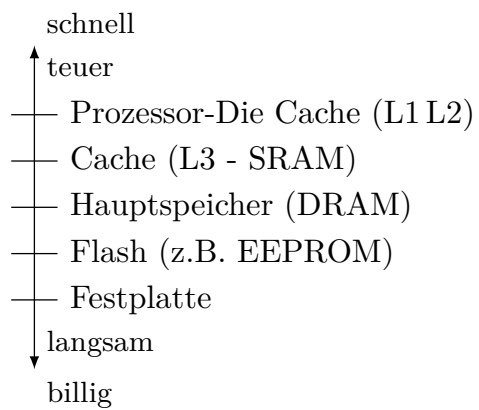


Rechner/RegisterSeriell

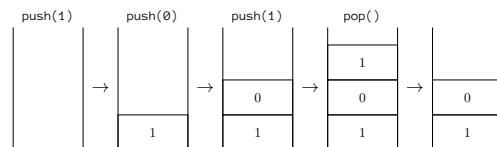
Rechner/Shiftregister



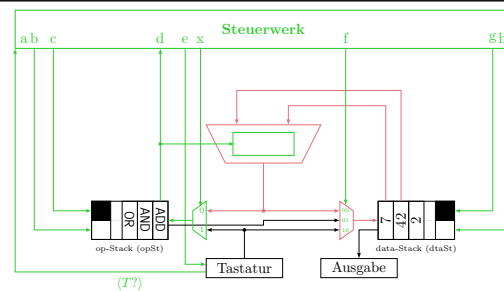
Rechner/Speicherhierarchie



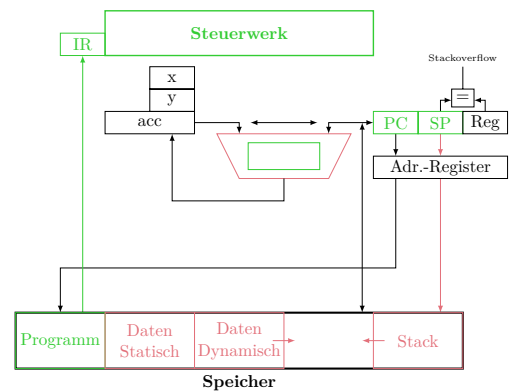
Rechner/StackExample



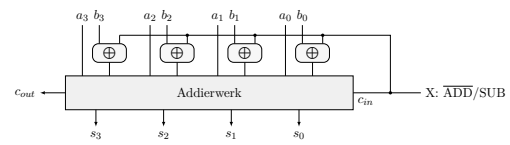
Rechner/Stackmaschine



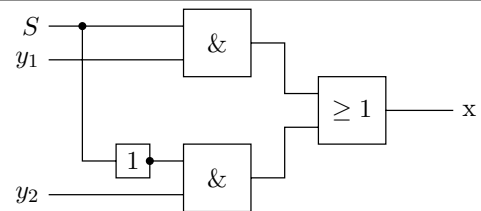
Rechner/StackmaschineSimpler



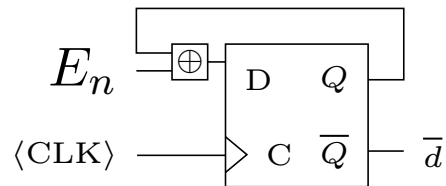
Schaltkreis/Addier-Subtrahierer



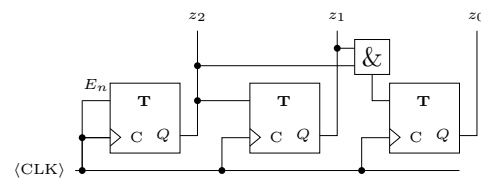
Schaltkreis/Demultiplexer



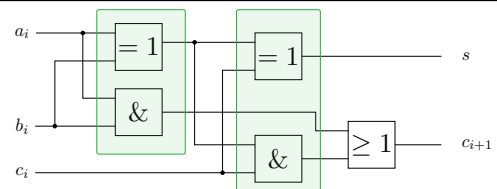
Schaltkreis/SynchroneZählerDFF

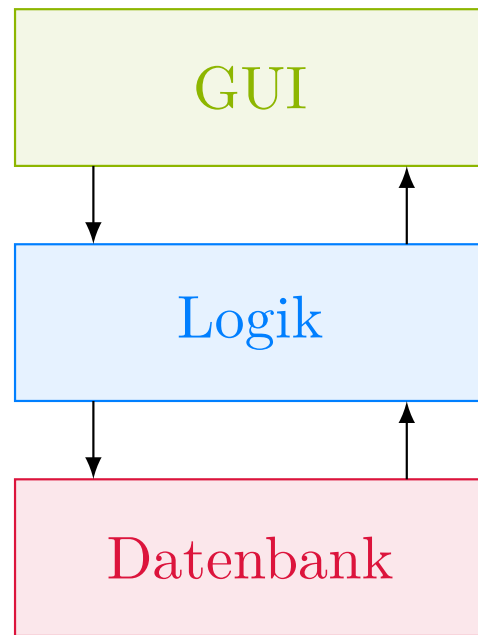


Schaltkreis/SynchroneZählerTFF

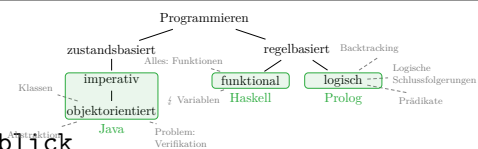


Schaltkreis/Volladdierer

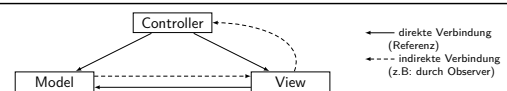




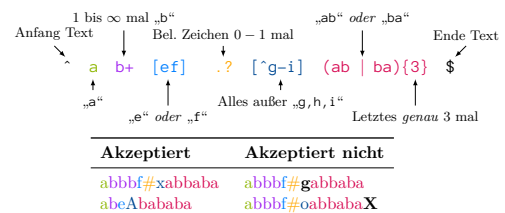
Software/DreiSchichtenArchitektur



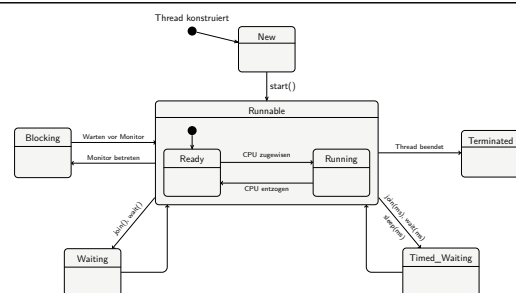
Software/Meta/ProgrammierparadigmenUeberblick



Software/ModelViewController

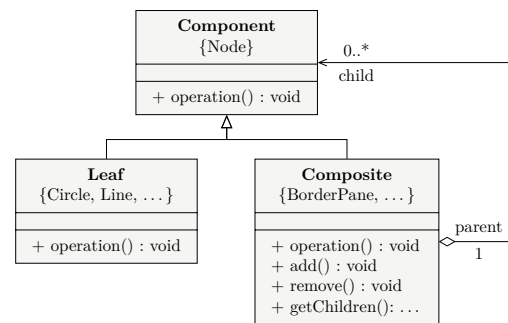


Software/RegexExample

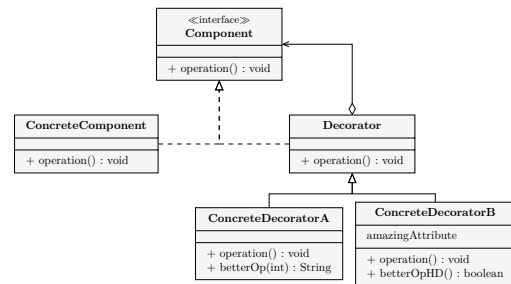


Software/ThreadStates

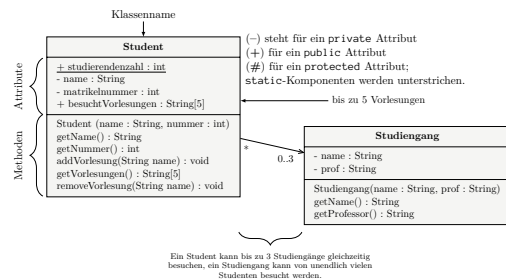
Software/UMLCompositePattern



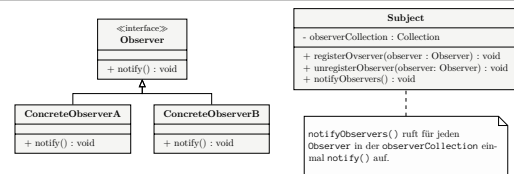
Software/UMLDecoratorPattern



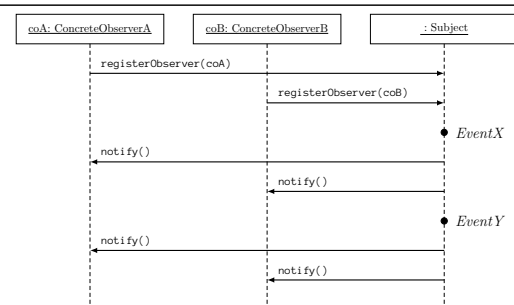
Software/UMLExample



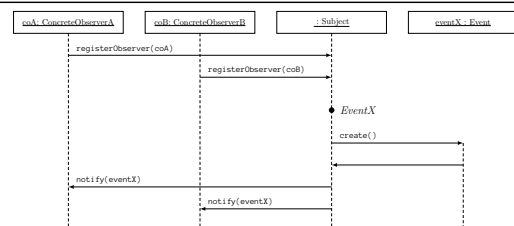
Software/UMLObserverPattern

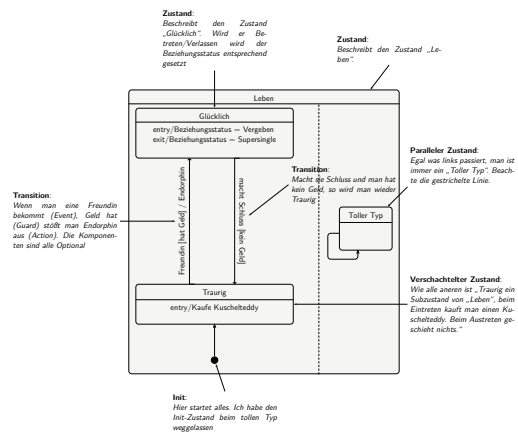


Software/UMLSEQObserverPattern

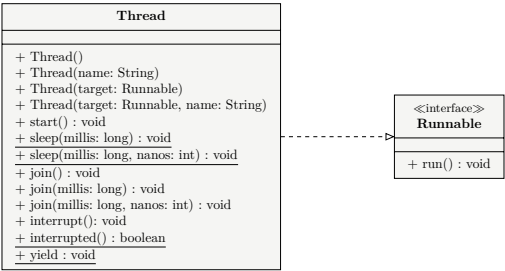


Software/UMLSEQObserverPatternAdapted





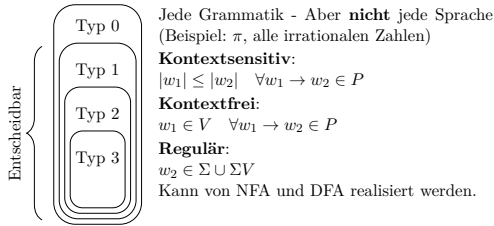
Software/UMLStateDiagramExample



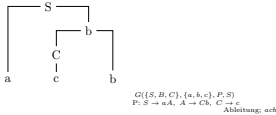
Software/UMLThread

		1 a	2 b	3 c	4 c
T[1,j]	1	A	B	CE	CE
T[2,j]	2	D	—	E	
T[3,j]	3	S	—		
T[4,j]	4	S			

Sprachen/CYKAlgorithmus



Sprachen/ChomskyHierarchie



Sprachen/Grammatik