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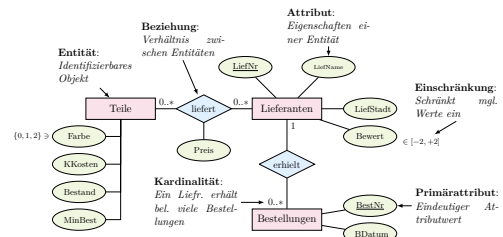
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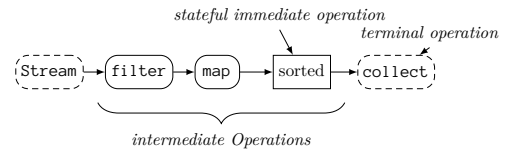
%%Einbindung erfolgt über:
`\getGraphics{\Pfad}`

Pfad	Ergebnis
Automat/Demo-1	
Automat/Demo-2	
Automat/Demo	
Automat/Header	<p>Isch bin a Hädder!</p>
Automat/MealyAutomat	<p>Ausgabe von Zustand & Eingabe abhängig</p>
Automat/MooreAutomat	<p>Ausgabe nur vom Zustand abhängig</p>

Datenbanken/ERExample

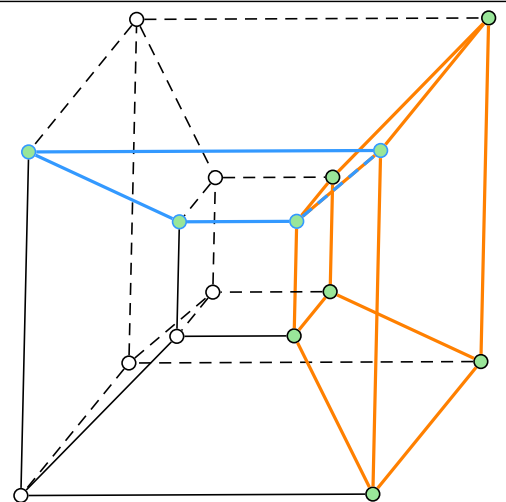


Java/StreamDemo



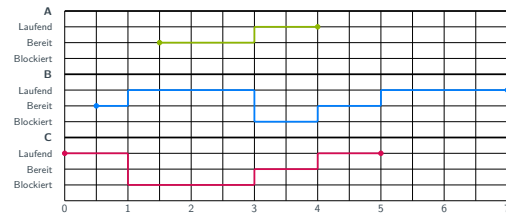
	\bar{a}	a	a	\bar{a}	
\bar{b}	0	0	0	0	\bar{d}
b	1	1	1	1	\bar{d}
b	1	1	1	1	d
\bar{b}	0	0	1	1	d
	\bar{c}	\bar{c}	c	c	

Logik/KVDiagramm

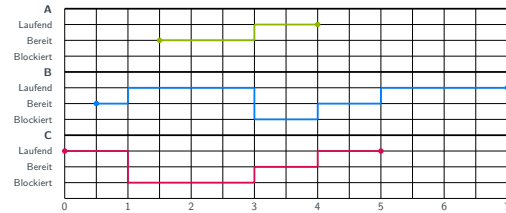


Logik/KVWuerfel

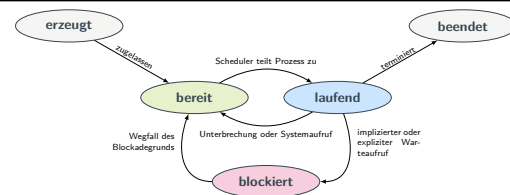
Prozesse/FCFS-WorstCase



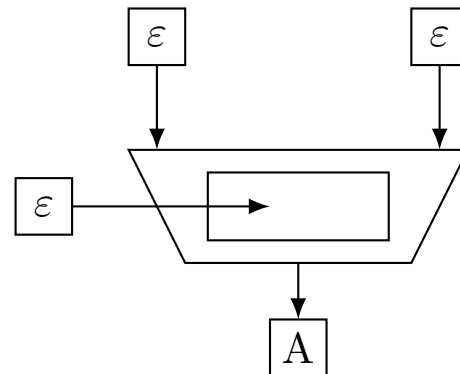
Prozesse/FCFS



Prozesse/Prozesszustände



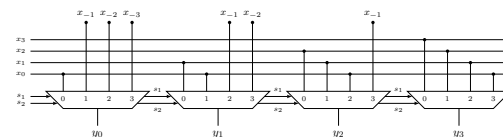
Rechner/ALU



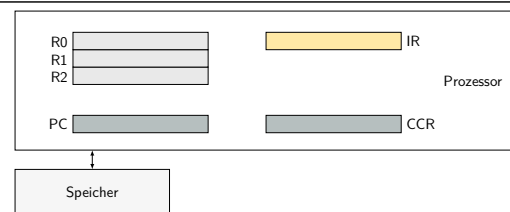
Rechner/AmpelPLA

δ	Zustand			Folgez.		NS			WO			T
	h_0	z_0	z_1	z'_0	z'_1	●	○	○	●	○	○	
δ_0	0	0	0	0	1	●	○	○	○	○	○	$7 \Rightarrow h'_0 = 1$
δ_1	1	0	1	0	1	○	○	○	○	○	○	$T - 1$
δ_3	0	0	1	1	0	○	○	○	○	○	○	$2 \Rightarrow h'_0 = 1$
δ_4	1	1	0	1	0	○	○	○	○	○	○	$T - 1$
δ_6	0	1	0	1	1	○	○	○	○	○	○	$7 \Rightarrow h'_0 = 1$
δ_7	1	1	1	1	1	○	○	○	○	○	○	$T - 1$
δ_9	0	1	1	0	0	○	○	○	○	○	○	$2 \Rightarrow h'_0 = 1$
δ_{10}	1	0	0	0	0	○	○	○	○	○	○	$T - 1$

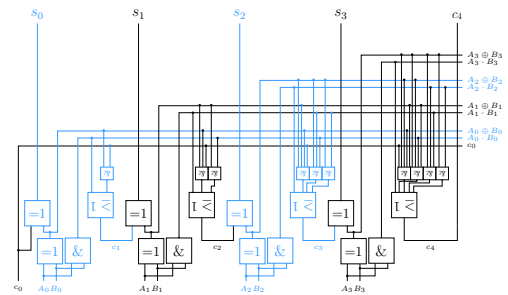
Rechner/BarrelShifter



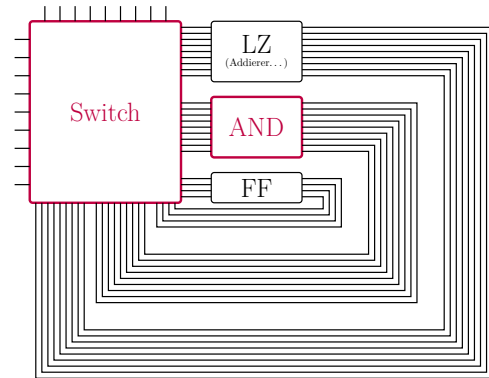
Rechner/Beispielprozessor



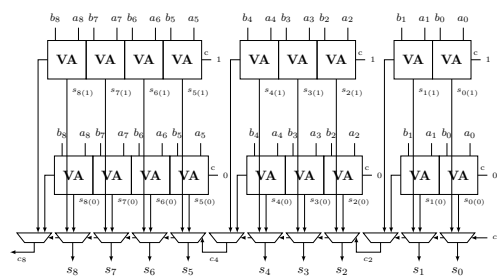
Rechner/CLA



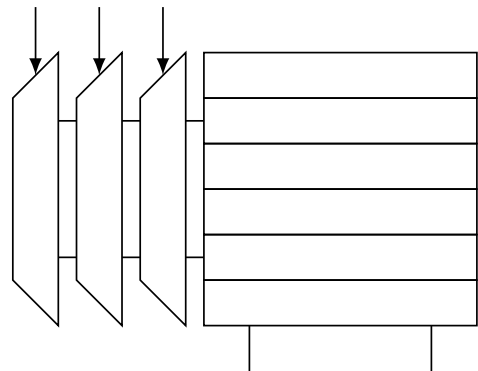
Rechner/CPLD



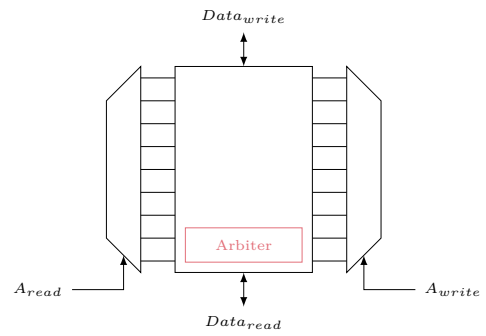
CSA - 9-bit Carry-Select-Addierer



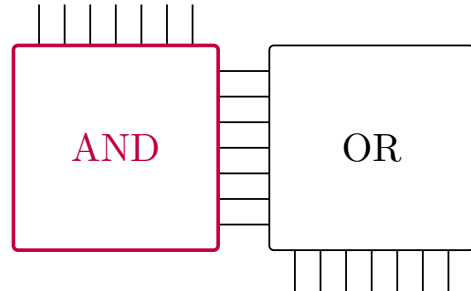
Rechner/CSA



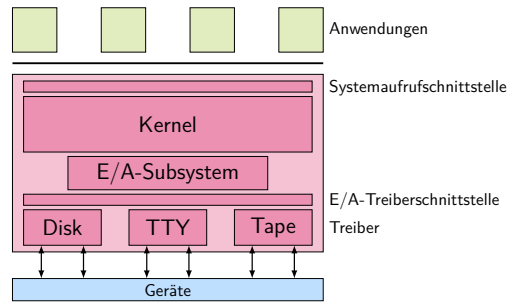
Rechner/DreiTorRegister



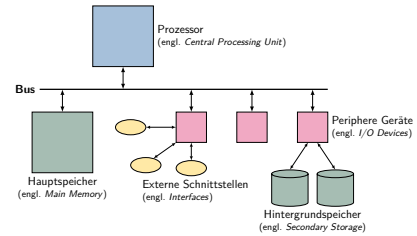
Rechner/Eintorspeicher



Rechner/GALPAL



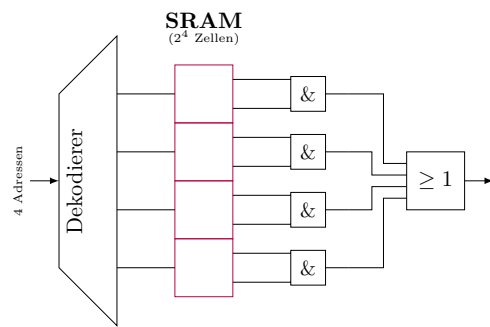
Rechner/Geraeteverwaltung



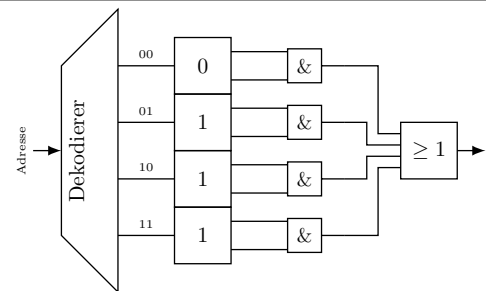
Rechner/HardwareSkizze

1	2	3	4	5	6	7
IF	ID	EX	MEM	WB		
	IF	ID	EX	MEM	WB	
		IF	ID	EX	MEM	WB

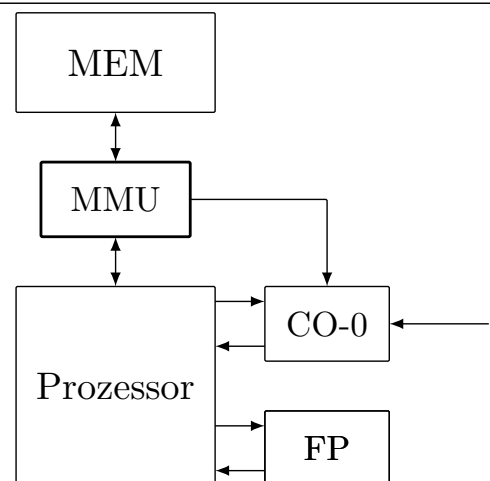
Rechner/ISA



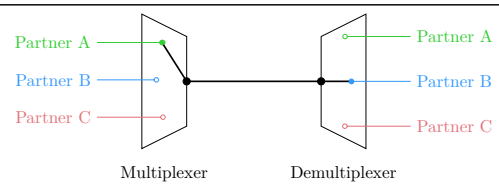
Rechner/LUT



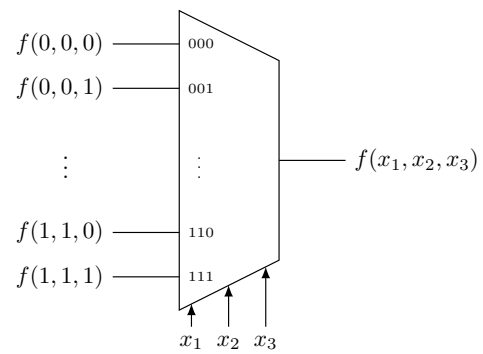
Rechner/LUT0der



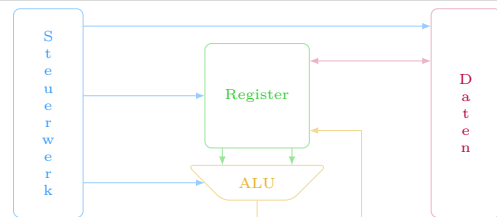
Rechner/MIPS



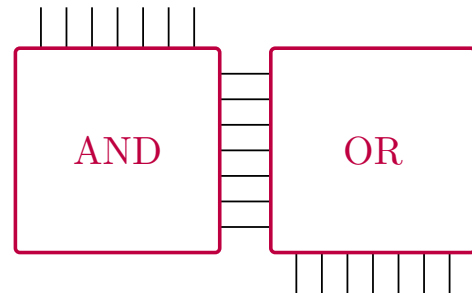
Rechner/MuxDemuxKommunikation



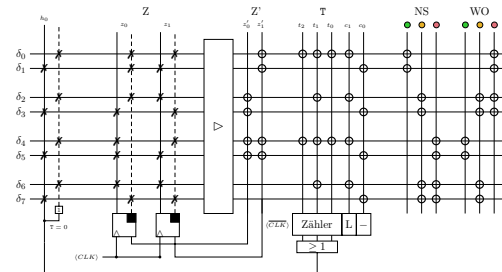
Rechner/MuxShannon



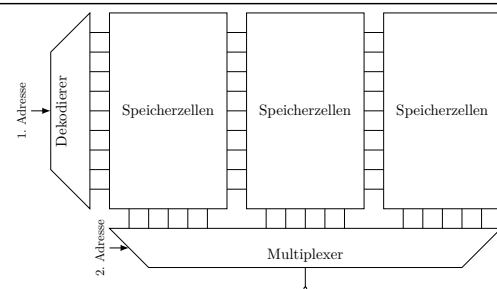
Rechner/Adressmaschine



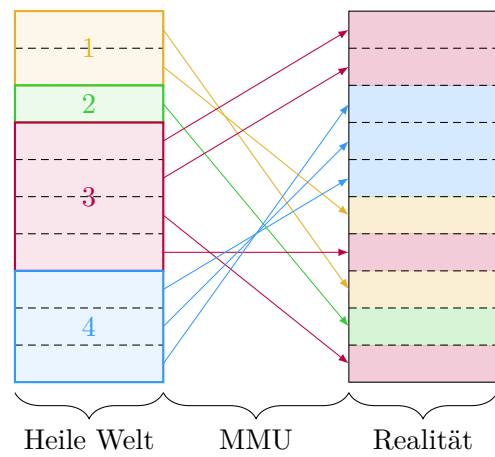
Rechner/PLA



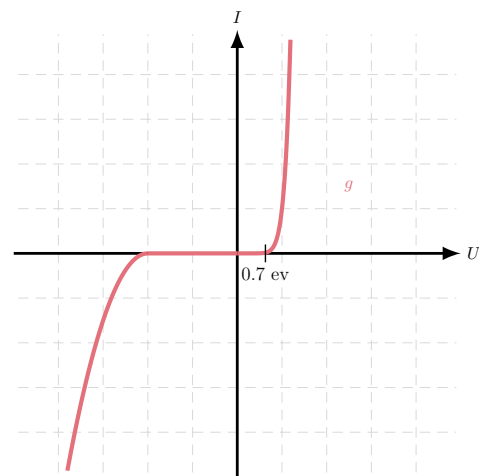
Rechner/PLAZuAmpel



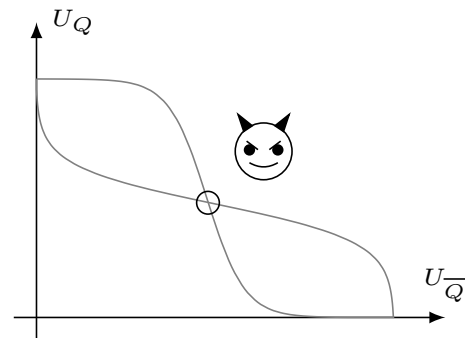
Rechner/PROM



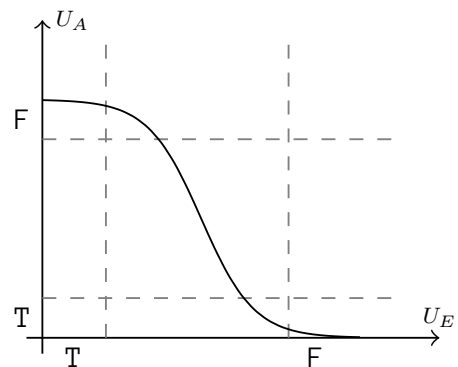
Rechner/Pages



Rechner/Physik/DiodenStromstaerke

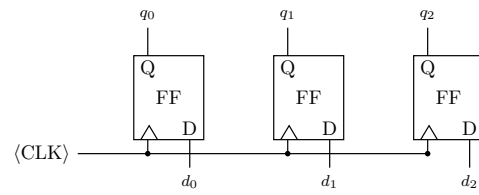


Rechner/Physik/Metastabil

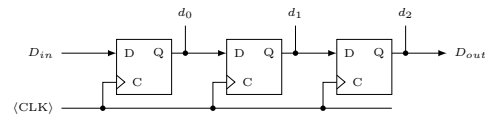


Rechner/Physik/TransistorStoertoleranz

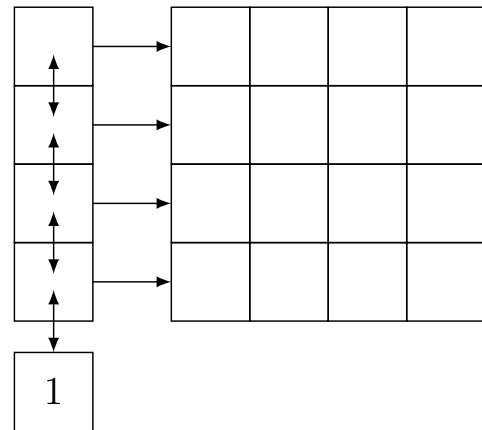
Rechner/RegisterParallel



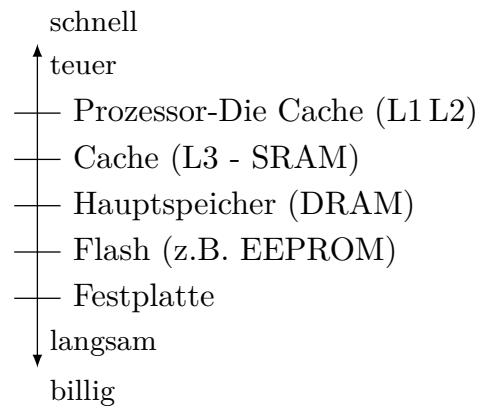
Rechner/RegisterSeriell



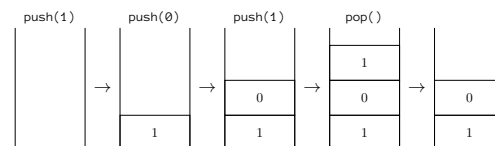
Rechner/Shiftregister



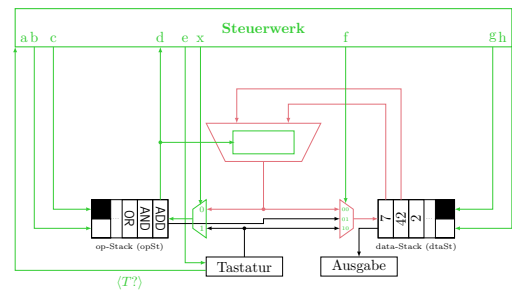
Rechner/Speicherhierarchie



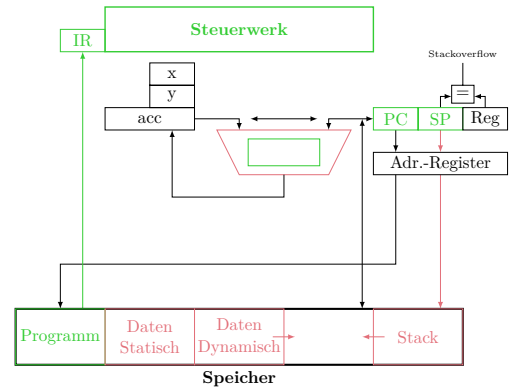
Rechner/StackExample



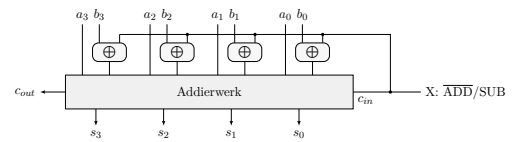
Rechner/Stackmaschiene



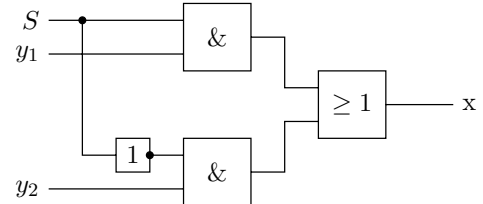
Rechner/StackmaschieneSimpler



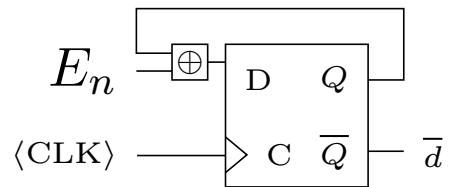
Schaltkreis/Addier-Subtrahierer



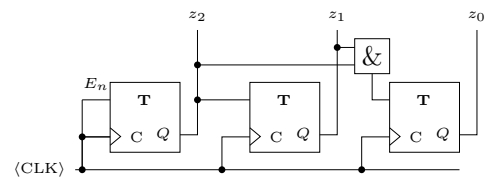
Schaltkreis/Demultiplexer



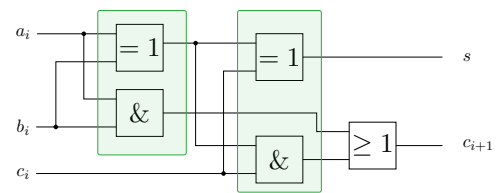
Schaltkreis/SynchronzaehlerDFF



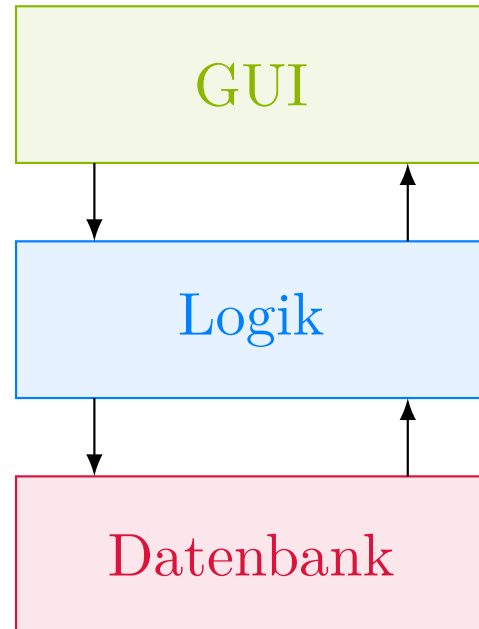
Schaltkreis/SynchronzaehlerTFF



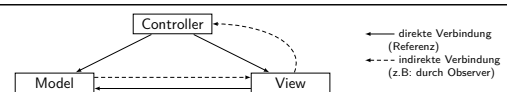
Schaltkreis/Volladdierer



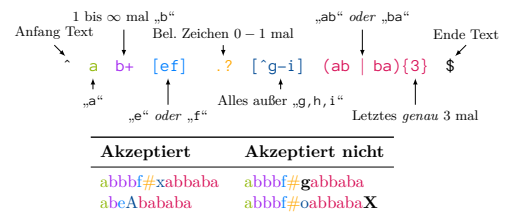
Software/DreiSchichtenArchitektur



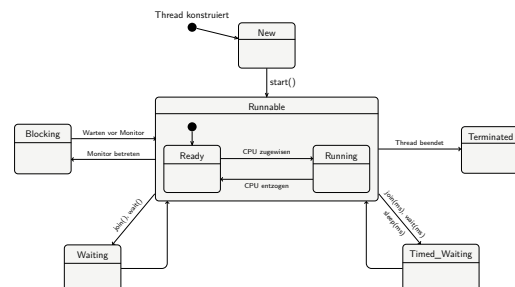
Software/ModelViewController

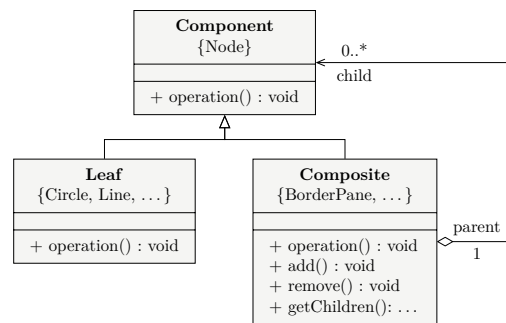


Software/RegexExample

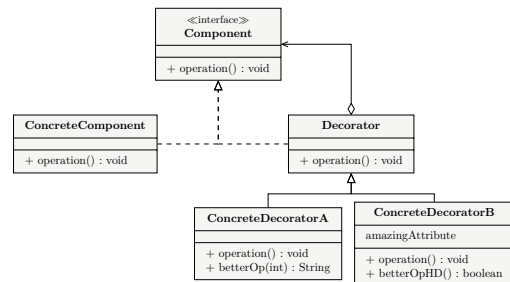


Software/ThreadStates

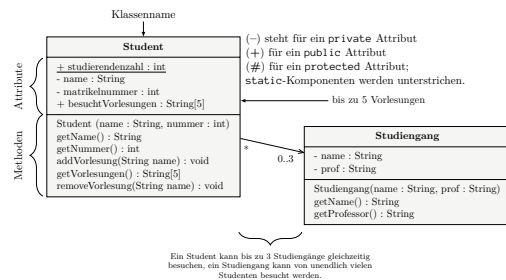




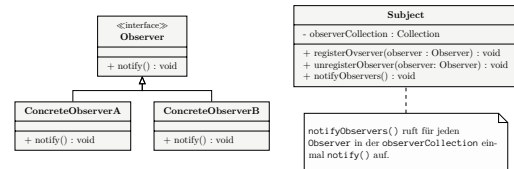
Software/UMLCompositePattern



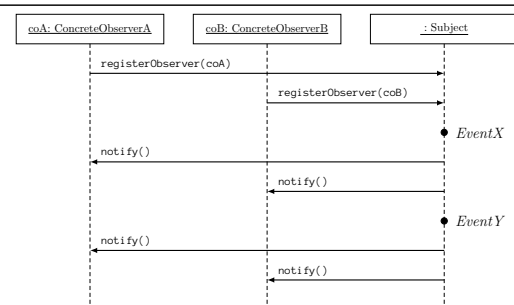
Software/UMLDecoratorPattern



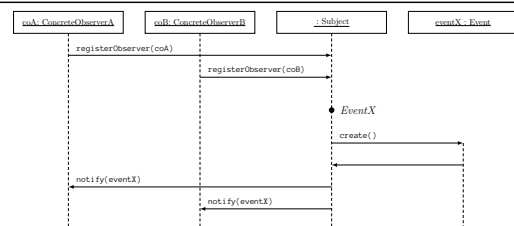
Software/UMLExample



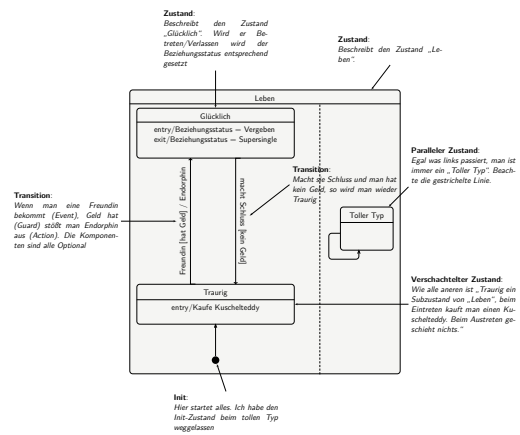
Software/UMLObserverPattern



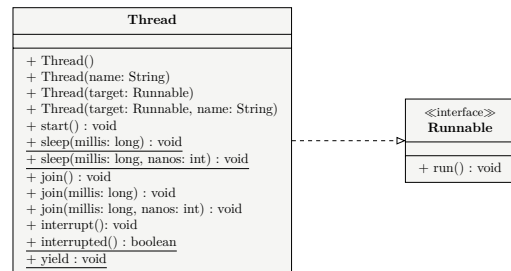
Software/UMLSEQObserverPattern



Software/UMLSEQObserverPatternAdapted



Software/UMLStateDiagramExample



Software/UMLThread