

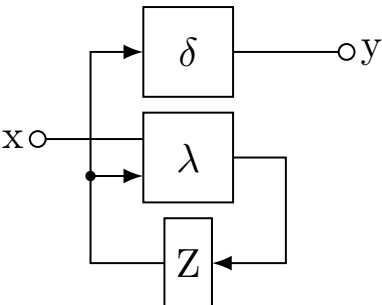
```
1 %%Einbindung erfolgt über:
2 \getGraphics{\Pfad}
```

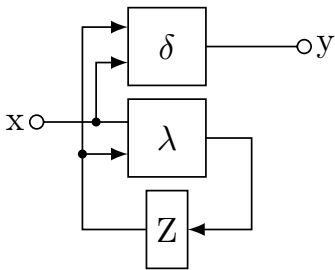
Pfad	Ergebnis
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Automat/Demo-2.tex	
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Automat/Demo-1.tex	
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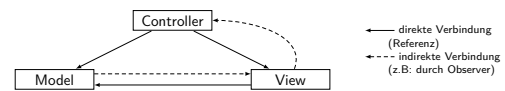
Automat/Header.tex	Isch bin a Hädder!
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Automat/MooreAutomat.tex	 <p>Ausgabe nur vom Zustand abhängig</p>
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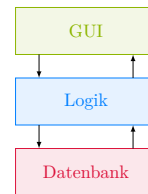
Automat/MealyAutomat.tex	 <p>Ausgabe von Zustand & Eingabe abhängig</p>
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Automat/Demo.tex	
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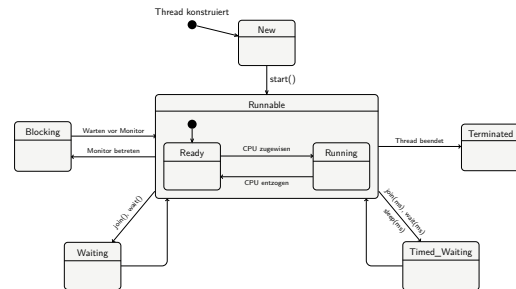
Software/ModelViewController.tex



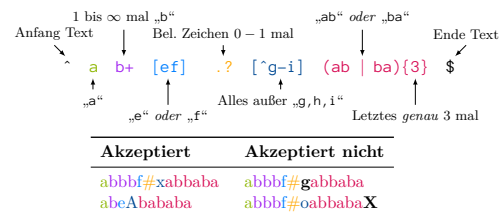
Software/DreiSchichtenArchitektur.tex



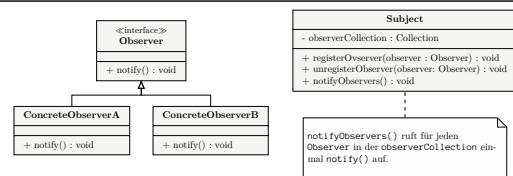
Software/ThreadStates.tex



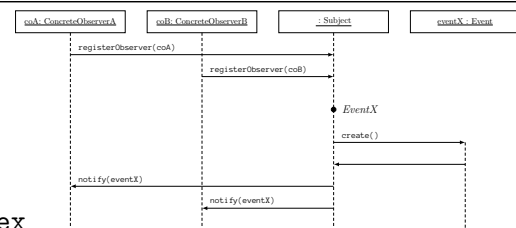
Software/RegexExample.tex



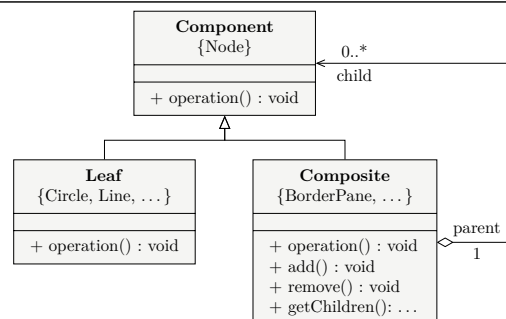
Software/UMLObserverPattern.tex

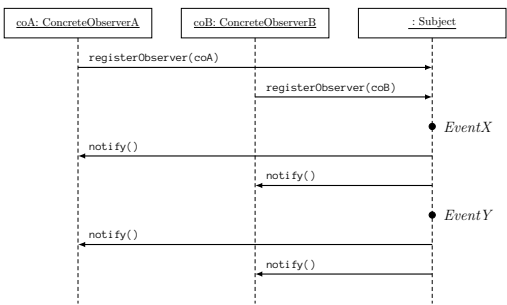


Software/UMLSEQObserverPatternAdapted.tex

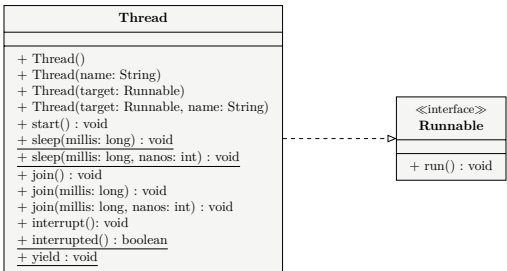


Software/UMLCompositePattern.tex

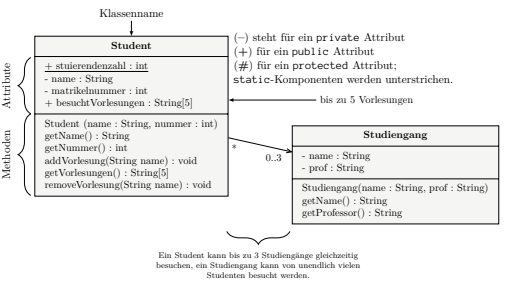




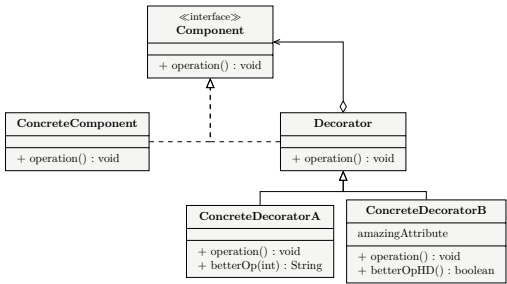
Software/UMLSEQObserverPattern.tex



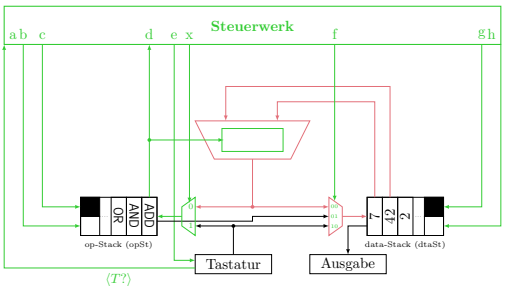
Software/UMLThread.tex



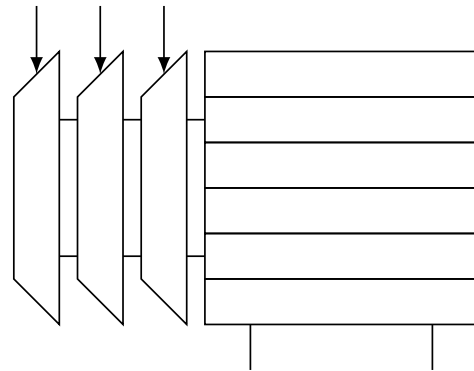
Software/UMLExample.tex



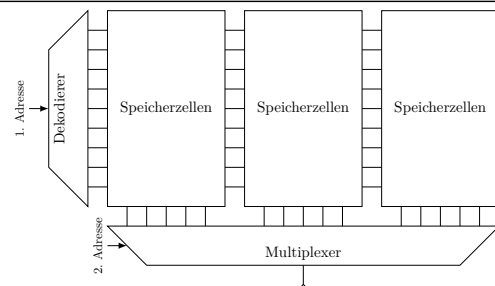
Software/UMLDecoratorPattern.tex



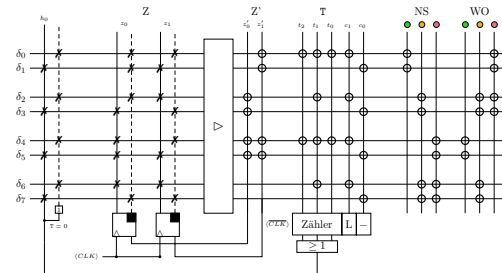
Rechner/Stackmaschine.tex



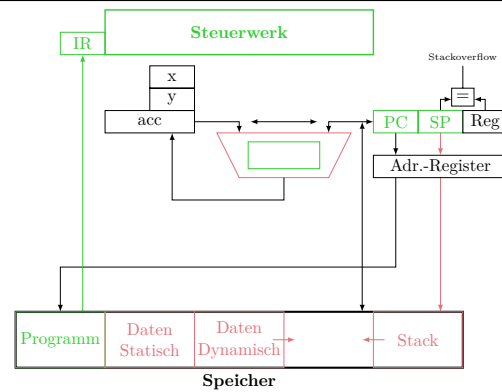
Rechner/DreiTorRegister.tex



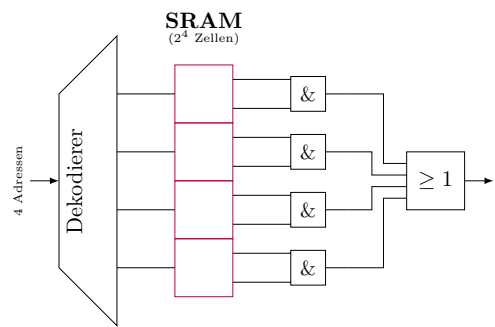
Rechner/PROM.tex



Rechner/PLAZuAmpel.tex

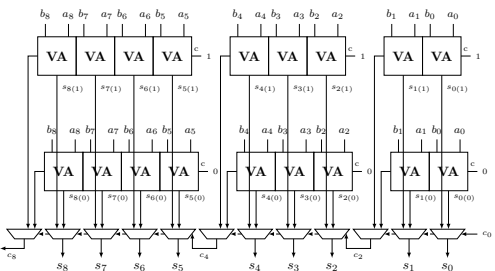


Rechner/StackmaschineSimpler.tex

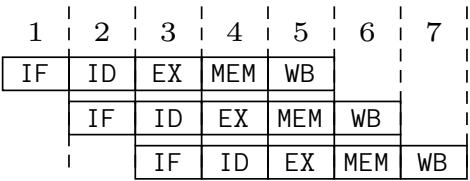


Rechner/LUT.tex

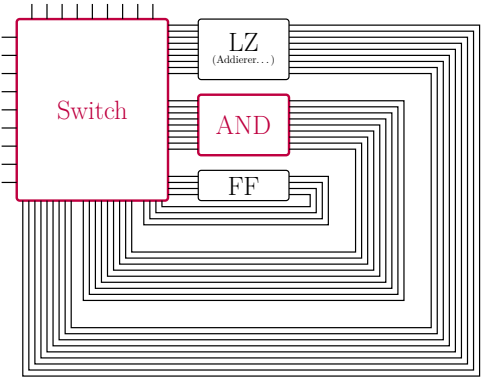
CSA - 9-bit Carry-Select-Addierer



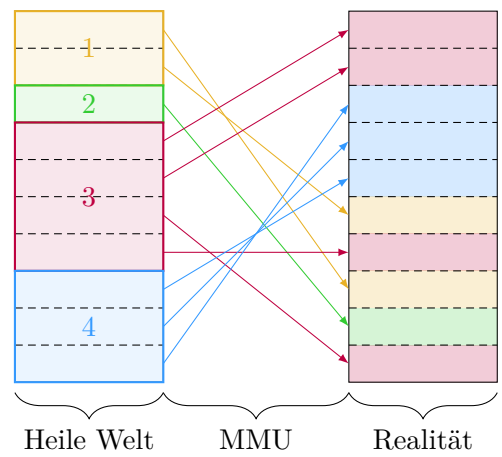
Rechner/CSA.tex



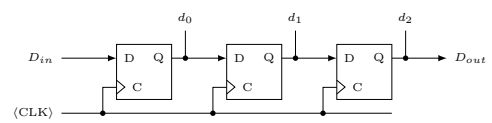
Rechner/ISA.tex



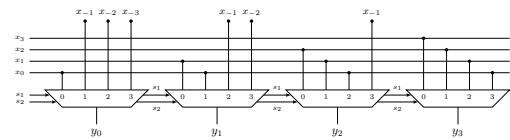
Rechner/CPLD.tex



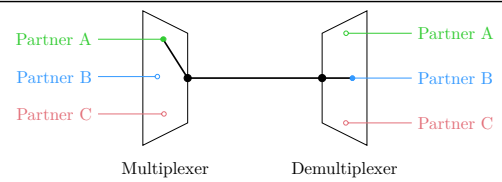
Rechner/Pages.tex



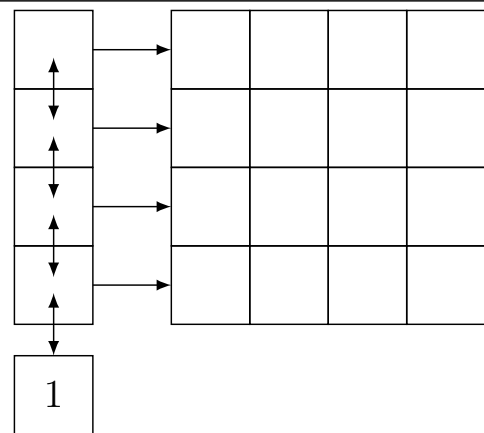
Rechner/RegisterSeriell.tex



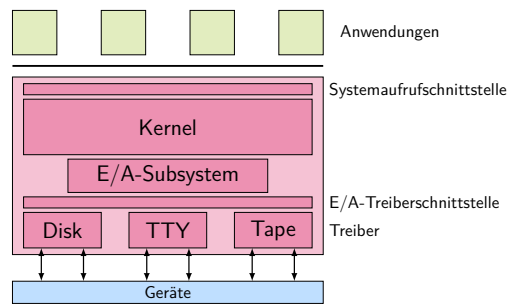
Rechner/BarrelShifter.tex



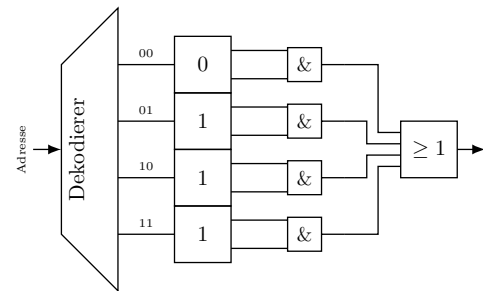
Rechner/MuxDemuxKommunikation.tex



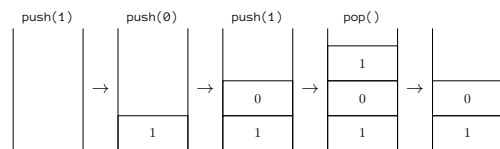
Rechner/Shiftregister.tex



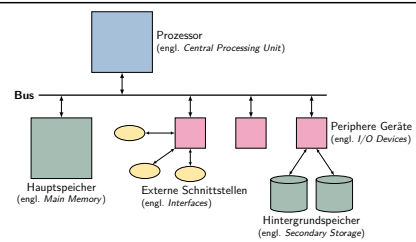
Rechner/Geraeteverwaltung.tex



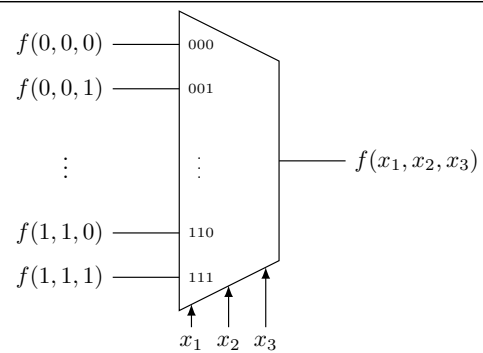
Rechner/LUT0der.tex



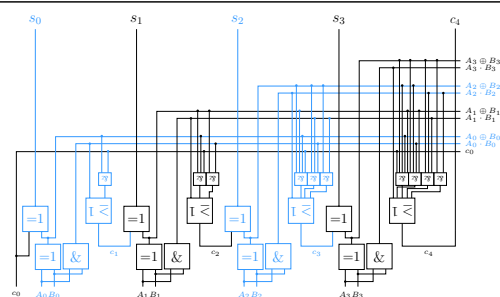
Rechner/Stack.tex



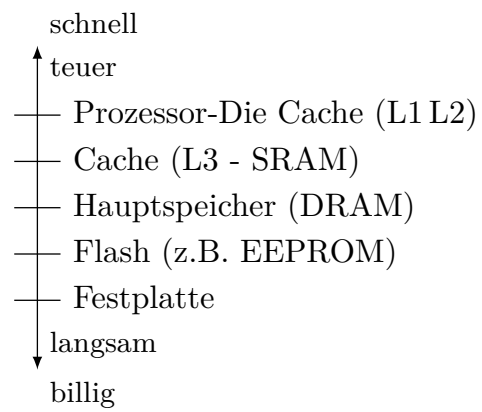
Rechner/HardwareSkizze.tex



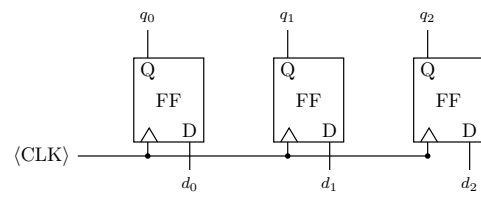
Rechner/MuxShannon.tex



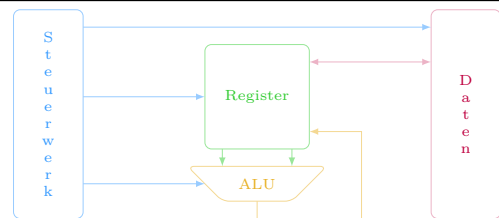
Rechner/CLA.tex



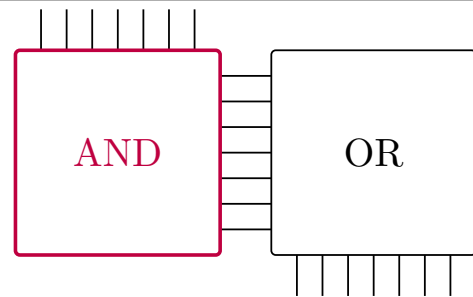
Rechner/Speicherhierarchie.tex



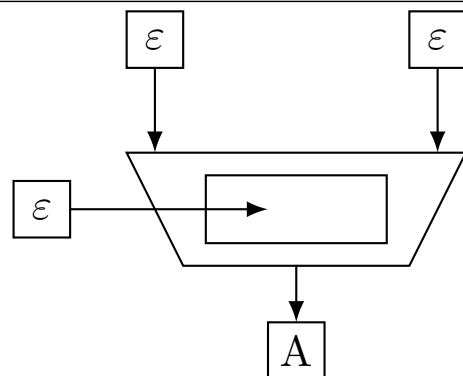
Rechner/RegisterParallel.tex



Rechner/Adressmaschine.tex

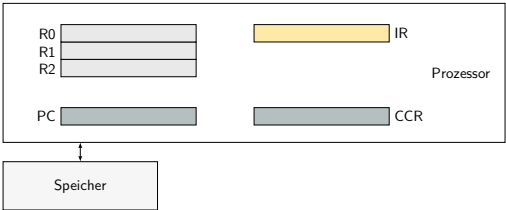


Rechner/GALPAL.tex

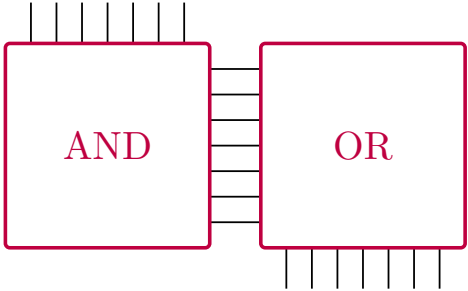


Rechner/ALU.tex

Rechner/Beispielprozessor.tex



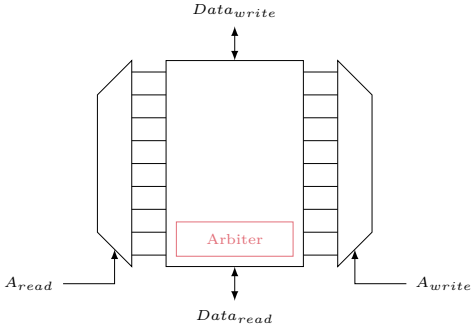
Rechner/PLA.tex



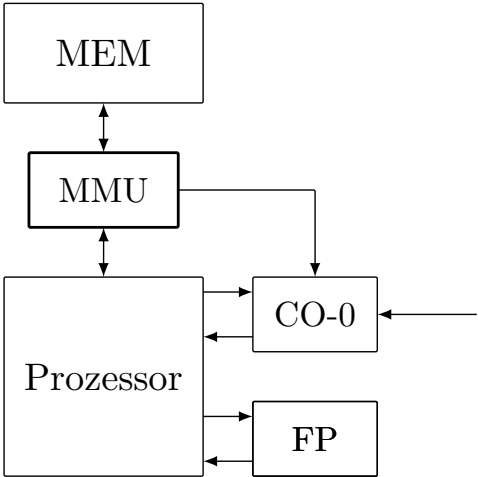
Rechner/AmpelPLA.tex

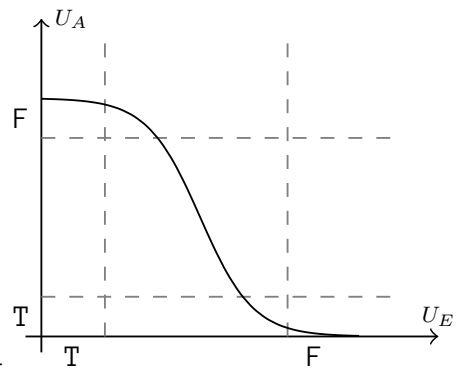
δ	Zustand			Folgez.		NS			WO			T
	h_0	z_0	z_1	z'_0	z'_1	\bullet	\circ	\circ	\bullet	\circ	\circ	
δ_0	0	0	0	0	1	\bullet	\circ	\circ	\circ	\circ	\bullet	$7 \Rightarrow h'_0 = 1$
δ_1	1	0	1	0	1	\bullet	\circ	\circ	\circ	\circ	\bullet	$T - 1$
δ_3	0	0	1	1	0	\circ	\bullet	\circ	\circ	\bullet	\bullet	$2 \Rightarrow h'_0 = 1$
δ_4	1	1	0	1	0	\circ	\bullet	\circ	\circ	\bullet	\bullet	$T - 1$
δ_6	0	1	0	1	1	\circ	\circ	\bullet	\bullet	\circ	\circ	$7 \Rightarrow h'_0 = 1$
δ_7	1	1	1	1	1	\circ	\circ	\bullet	\bullet	\circ	\circ	$T - 1$
δ_9	0	1	1	0	0	\circ	\bullet	\circ	\circ	\bullet	\bullet	$2 \Rightarrow h'_0 = 1$
δ_{10}	1	0	0	0	0	\circ	\bullet	\circ	\circ	\bullet	\bullet	$T - 1$

Rechner/Eintorspeicher.tex

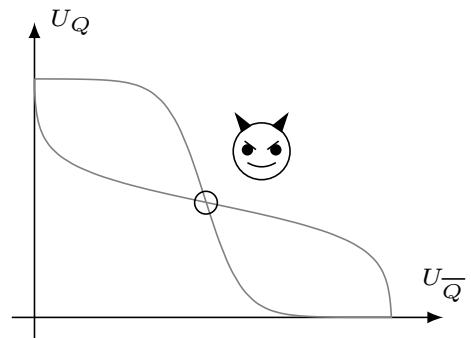


Rechner/MIPS.tex

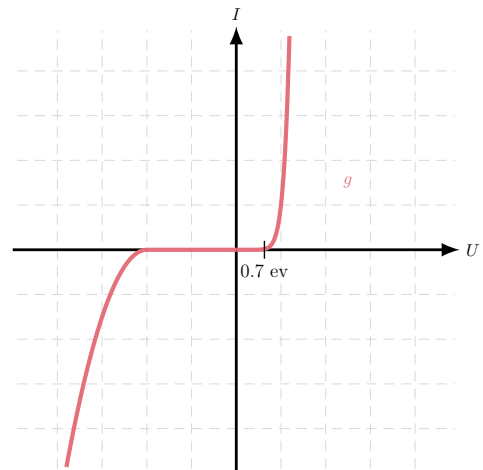




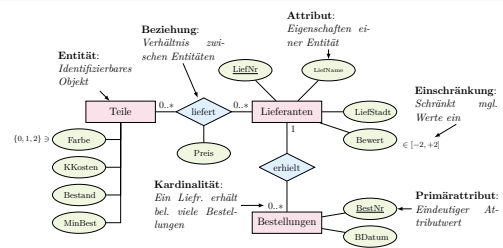
Rechner/Physik/TransistorStoertoleranz.tex



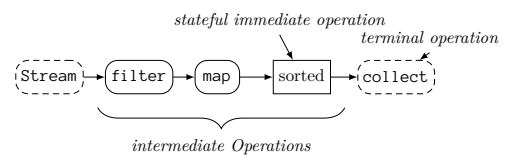
Rechner/Physik/Metastabil.tex



Rechner/Physik/DiodenStromstaerke.tex

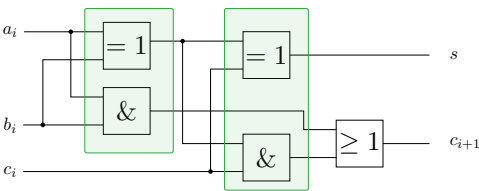


Datenbanken/ERExample.tex

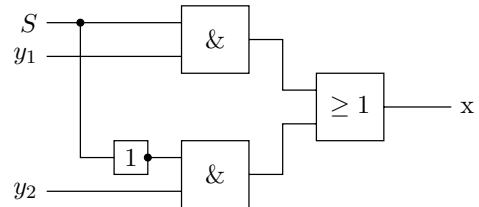


Java/StreamDemo.tex

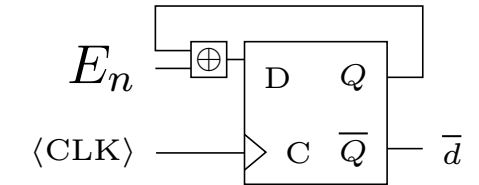
Schaltkreis/Volladdierer.tex



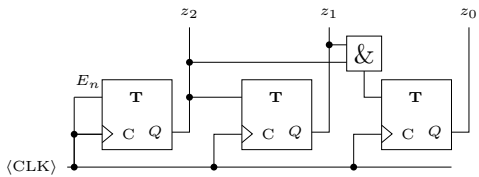
Schaltkreis/Demultiplexer.tex



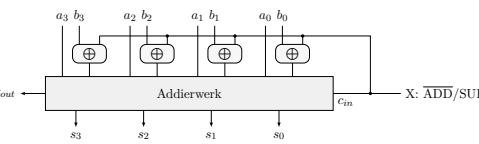
Schaltkreis/SynchroneaehlerDFF.tex



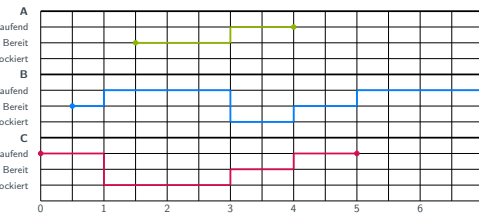
Schaltkreis/SynchroneaehlerTFF.tex



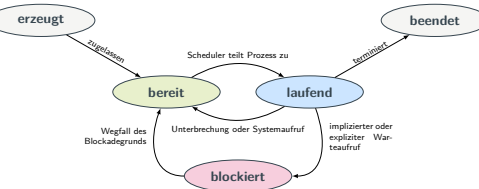
Schaltkreis/Addier-Subtrahierer.tex



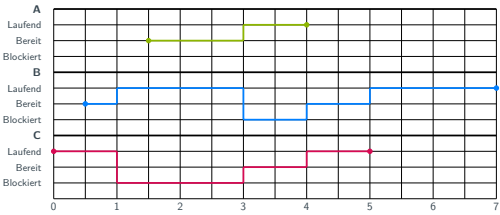
Prozesse/FCFS.tex



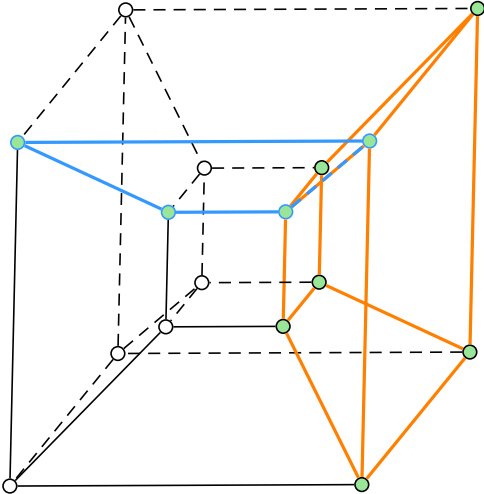
Prozesse/Prozesszustande.tex



Prozesse/FCFS-WorstCase.tex



Logik/KVWuerfel.tex



	\bar{a}	a	a	\bar{a}	
\bar{b}	0	0	0	0	\bar{d}
b	1	1	1	1	\bar{d}
b	1	1	1	1	d
\bar{b}	0	0	1	1	d
	\bar{c}	\bar{c}	c	c	

Logik/KVDiagramm.tex

Code für diese Datei:

```
1 \documentclass[PLAIN]{Lilly}
2 \begin{document}
3 \begin{latex}
4 %%Einbindung erfolgt über:
5 \getGraphics{(Pfad)}
6 \end{latex}
7 \begin{tabularx}{\linewidth}{^m{0.5\linewidth}^>{\centering\arraybackslash}X+}
8 \toprule\headerrow Pfad & Ergebnis\\
9 \midrule
10 \verb|Automat/Demo-2.tex| & \getGraphics{Automat/Demo-2.tex}\\
11 \midrule\verb|Automat/Demo-1.tex| & \getGraphics{Automat/Demo-1.tex}\\
12 \midrule\verb|Automat/Header.tex| & \getGraphics{Automat/Header.tex}\\
13 \midrule\verb|Automat/MooreAutomat.tex| & \getGraphics{Automat/MooreAutomat.tex}\\
14 \midrule\verb|Automat/MealyAutomat.tex| & \getGraphics{Automat/MealyAutomat.tex}\\
15 \midrule\verb|Automat/Demo.tex| & \getGraphics{Automat/Demo.tex}\\
16 \midrule\verb|Software/ModelViewController.tex| & \getGraphics{Software/ModelViewController.}
17 tex}\\
18 \midrule\verb|Software/DreiSchichtenArchitektur.tex| & \getGraphics[0.3\linewidth]{Software/}
19 DreiSchichtenArchitektur.tex}\\
20 \midrule\verb|Software/ThreadStates.tex| & \getGraphics{Software/ThreadStates.tex}\\
21 \midrule\verb|Software/RegexExample.tex| & \getGraphics{Software/RegexExample.tex}\\
22 \midrule\verb|Software/UMLObserverPattern.tex| & \getGraphics{Software/UMLObserverPattern.tex}
23 \\
24 \midrule\verb|Software/UMLSEQObserverPatternAdapted.tex| & \getGraphics{Software/}
25 UMLSEQObserverPatternAdapted.tex}\\
26 \midrule\verb|Software/UMLCompositePattern.tex| & \getGraphics{Software/UMLCompositePattern.}
27 tex}\\
28 \midrule\verb|Software/UMLSEQObserverPattern.tex| & \getGraphics{Software/}
29 UMLSEQObserverPattern.tex}\\
30 \midrule\verb|Software/UMLThread.tex| & \getGraphics{Software/UMLThread.tex}\\
31 \midrule\verb|Software/UMLExample.tex| & \getGraphics{Software/UMLExample.tex}\\
32 \midrule\verb|Software/UMLDecoratorPattern.tex| & \getGraphics{Software/UMLDecoratorPattern.}
33 tex}\\
34 \midrule\verb|Rechner/Stackmaschine.tex| & \getGraphics{Rechner/Stackmaschine.tex}\\
35 \midrule\verb|Rechner/DreiTorRegister.tex| & \getGraphics{Rechner/DreiTorRegister.tex}\\
36 \midrule\verb|Rechner/PROM.tex| & \getGraphics{Rechner/PROM.tex}\\
37 \midrule\verb|Rechner/PLAZuAmpel.tex| & \getGraphics{Rechner/PLAZuAmpel.tex}\\
38 \midrule\verb|Rechner/StackmaschineSimpler.tex| & \getGraphics{Rechner/StackmaschineSimpler.}
39 tex}\\
40 \midrule\verb|Rechner/LUT.tex| & \getGraphics{Rechner/LUT.tex}\\
41 \midrule\verb|Rechner/CSA.tex| & \getGraphics{Rechner/CSA.tex}\\
42 \midrule\verb|Rechner/ISA.tex| & \getGraphics{Rechner/ISA.tex}\\
43 \midrule\verb|Rechner/CPLD.tex| & \getGraphics{Rechner/CPLD.tex}\\
44 \midrule\verb|Rechner/Pages.tex| & \getGraphics{Rechner/Pages.tex}\\
45 \midrule\verb|Rechner/RegisterSeriell.tex| & \getGraphics{Rechner/RegisterSeriell.tex}\\
46 \midrule\verb|Rechner/BarrelShifter.tex| & \getGraphics{Rechner/BarrelShifter.tex}\\
47 \midrule\verb|Rechner/MuxDemuxKommunikation.tex| & \getGraphics{Rechner/MuxDemuxKommunikation.}
48 tex}\\
49 \midrule\verb|Rechner/Shiftregister.tex| & \getGraphics{Rechner/Shiftregister.tex}\\
50 \midrule\verb|Rechner/Geraeteverwaltung.tex| & \getGraphics{Rechner/Geraeteverwaltung.tex}\\
51 \midrule\verb|Rechner/LUTOrder.tex| & \getGraphics{Rechner/LUTOrder.tex}\\
52 \midrule\verb|Rechner/Stack.tex| & \getGraphics{Rechner/Stack.tex}\\
53 \midrule\verb|Rechner/HardwareSkizze.tex| & \getGraphics{Rechner/HardwareSkizze.tex}\\
54 \midrule\verb|Rechner/MuxShannon.tex| & \getGraphics{Rechner/MuxShannon.tex}\\
55 \midrule\verb|Rechner/CLA.tex| & \getGraphics{Rechner/CLA.tex}\\
56 \midrule\verb|Rechner/Speicherhierarchie.tex| & \getGraphics{Rechner/Speicherhierarchie.tex}\\
57 \midrule\verb|Rechner/RegisterParallel.tex| & \getGraphics{Rechner/RegisterParallel.tex}\\
58 \midrule\verb|Rechner/NAdressmaschine.tex| & \getGraphics{Rechner/NAdressmaschine.tex}\\
59 \midrule\verb|Rechner/GALPAL.tex| & \getGraphics{Rechner/GALPAL.tex}\\
60 \midrule\verb|Rechner/ALU.tex| & \getGraphics{Rechner/ALU.tex}\\
61 \midrule\verb|Rechner/Beispielprozessor.tex| & \getGraphics{Rechner/Beispielprozessor.tex}\\
62 \midrule\verb|Rechner/PLA.tex| & \getGraphics{Rechner/PLA.tex}\\
63 \midrule\verb|Rechner/AmpelPLA.tex| & \getGraphics{Rechner/AmpelPLA.tex}\\
64 \midrule\verb|Rechner/Eintorspeicher.tex| & \getGraphics{Rechner/Eintorspeicher.tex}\\
65 \midrule\verb|Rechner/MIPS.tex| & \getGraphics{Rechner/MIPS.tex}
```

```

57 \midrule\verb|Rechner/Physik/TransistorStoertoleranz.tex| & \getGraphics{Rechner/Physik/}
    TransistorStoertoleranz.tex}\\
58 \midrule\verb|Rechner/Physik/Metastabil.tex| & \getGraphics{Rechner/Physik/Metastabil.tex}\\
59 \midrule\verb|Rechner/Physik/DiodenStromstaerke.tex| & \getGraphics{Rechner/Physik/}
    DiodenStromstaerke.tex}\\
60 \midrule\verb|Datenbanken/ERExample.tex| & \getGraphics{Datenbanken/ERExample.tex}\\
61 \midrule\verb|Java/StreamDemo.tex| & \getGraphics{Java/StreamDemo.tex}\\
62 \midrule\verb|Schaltkreis/Volladdierer.tex| & \getGraphics{Schaltkreis/Volladdierer.tex}\\
63 \midrule\verb|Schaltkreis/Demultiplexer.tex| & \getGraphics{Schaltkreis/Demultiplexer.tex}\\
64 \midrule\verb|Schaltkreis/SynchronzaehlerDFF.tex| & \getGraphics{Schaltkreis/}
    SynchronzaehlerDFF.tex}\\
65 \midrule\verb|Schaltkreis/SynchronzaehlerTFF.tex| & \getGraphics{Schaltkreis/}
    SynchronzaehlerTFF.tex}\\
66 \midrule\verb|Schaltkreis/Addier-Subtrahierer.tex| & \getGraphics{Schaltkreis/Addier-}
    Subtrahierer.tex}\\
67 \midrule\verb|Prozesse/FCFS.tex| & \getGraphics{Prozesse/FCFS.tex}\\
68 \midrule\verb|Prozesse/Prozesszustaende.tex| & \getGraphics{Prozesse/Prozesszustaende.tex}\\
69 \midrule\verb|Prozesse/FCFS-WorstCase.tex| & \getGraphics{Prozesse/FCFS-WorstCase.tex}\\
70 \midrule\verb|Logik/KVWuerfel.tex| & \getGraphics{Logik/KVWuerfel.tex}\\
71 \midrule\verb|Logik/KVDiagramm.tex| & \getGraphics{Logik/KVDiagramm.tex}\\
72 \midrule\bottomrule
73 \end{tabularx}
74 \clearpage
75 Code für diese Datei:
76 \begin{group}\scriptsize\ilatex{./all.tex}\end{group}\end{document}

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