

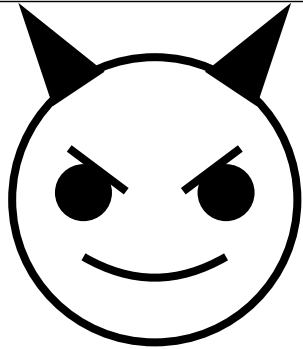
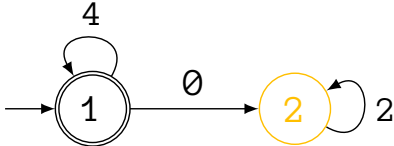
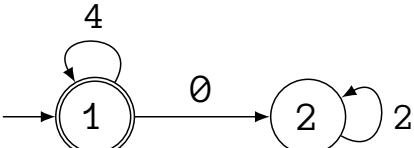
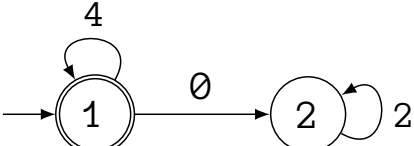
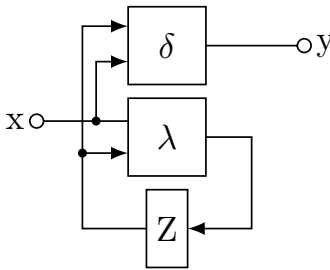
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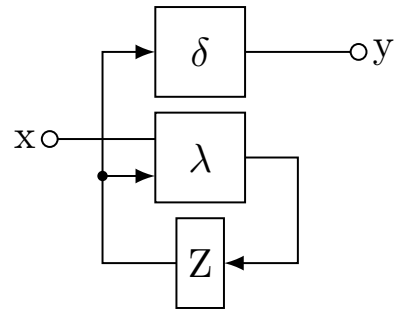
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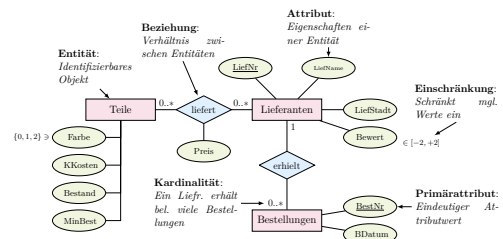
```
%%Einbindung erfolgt über:
\getGraphics{\Pfad}
```

Pfad	Ergebnis
Allerlei/Teufel	
Automat/Demo-1	
Automat/Demo-2	
Automat/Demo	
Automat/Header	Isch bin a Hädder!
Automat/MealyAutomat	 <p>Ausgabe von Zustand & Eingabe abhängig</p>

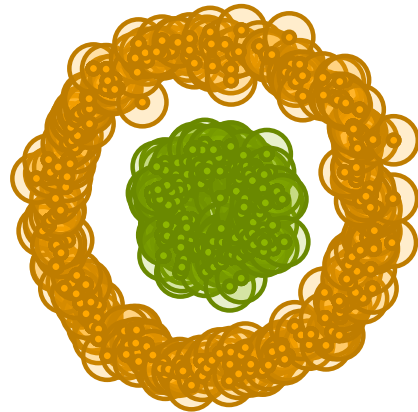


Automat/MooreAutomat

Ausgabe nur vom Zustand abhängig

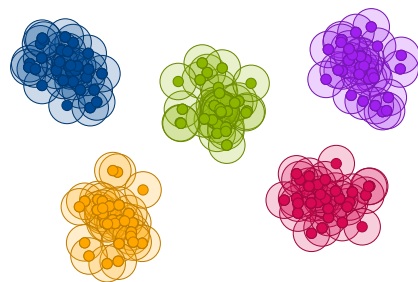


Datenbanken/ERExample



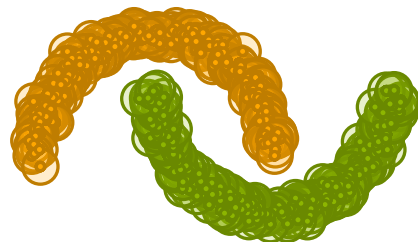
Eigene/Proseminar/Cluster/en-circles

pdf



Eigene/Proseminar/Cluster/en-clusters

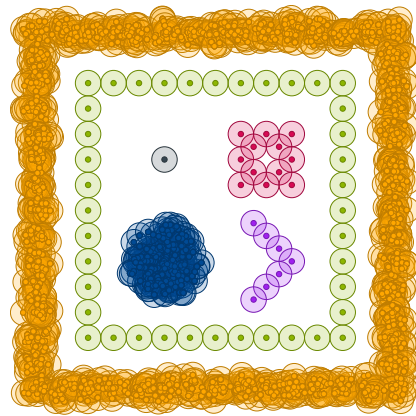
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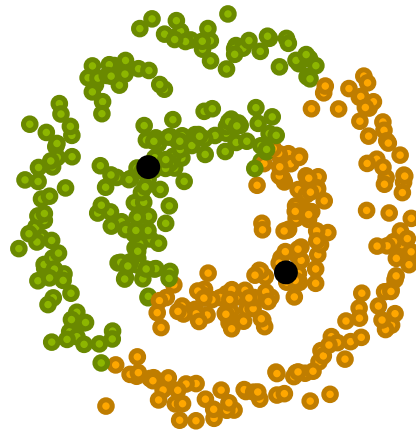
Eigene/Proseminar/Cluster/en-moons

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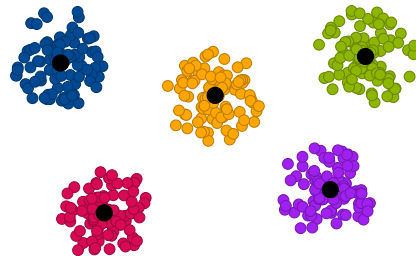
Eigene/Proseminar/Cluster/en-special
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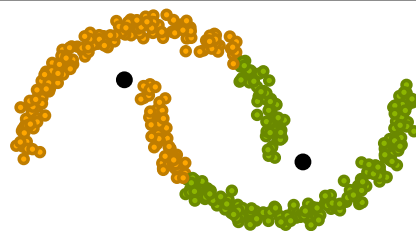
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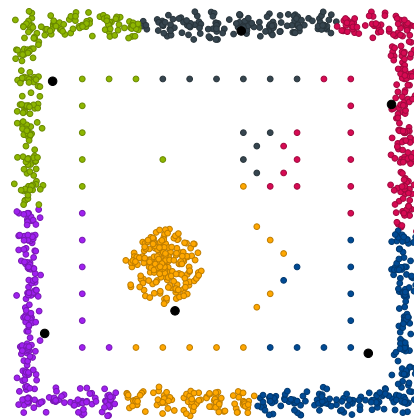
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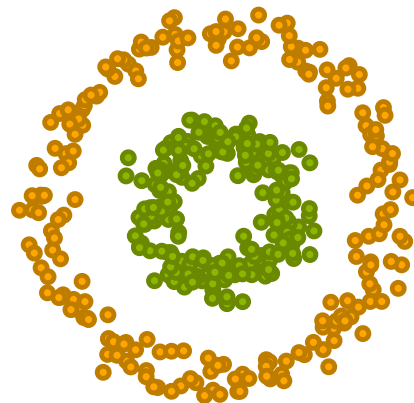
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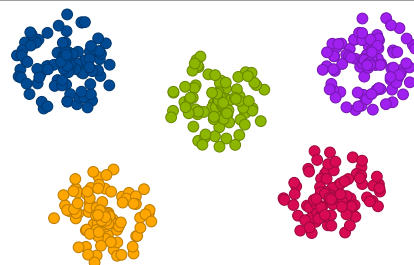
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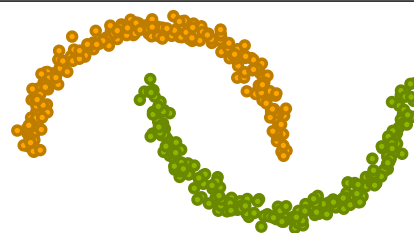
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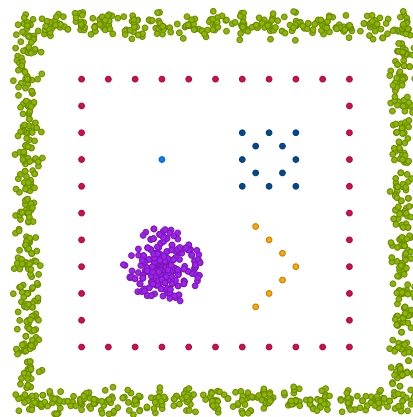
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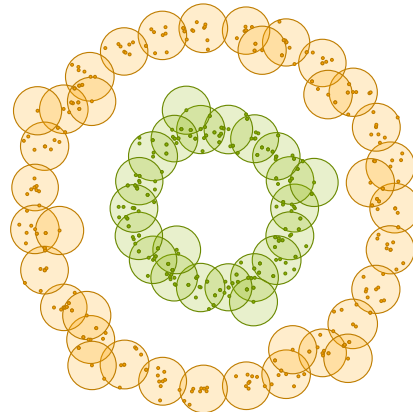
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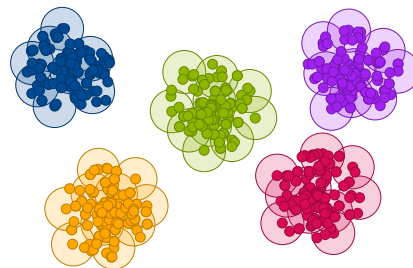
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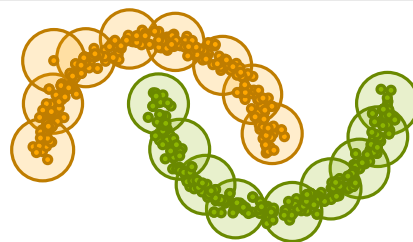
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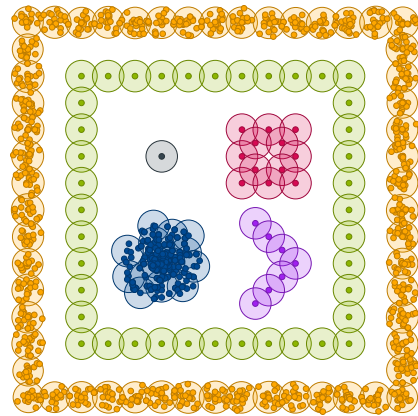


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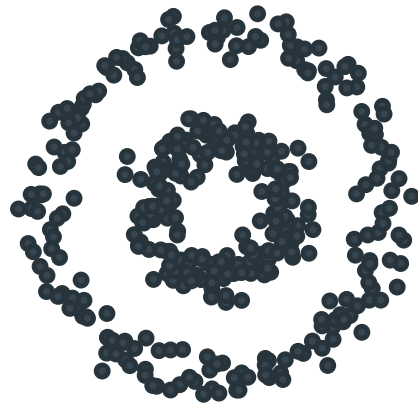


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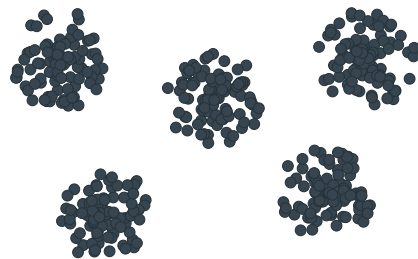




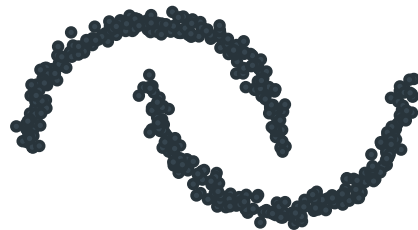
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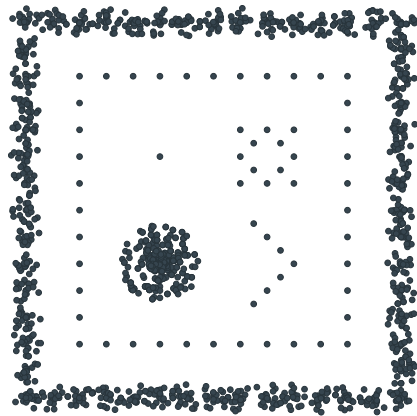
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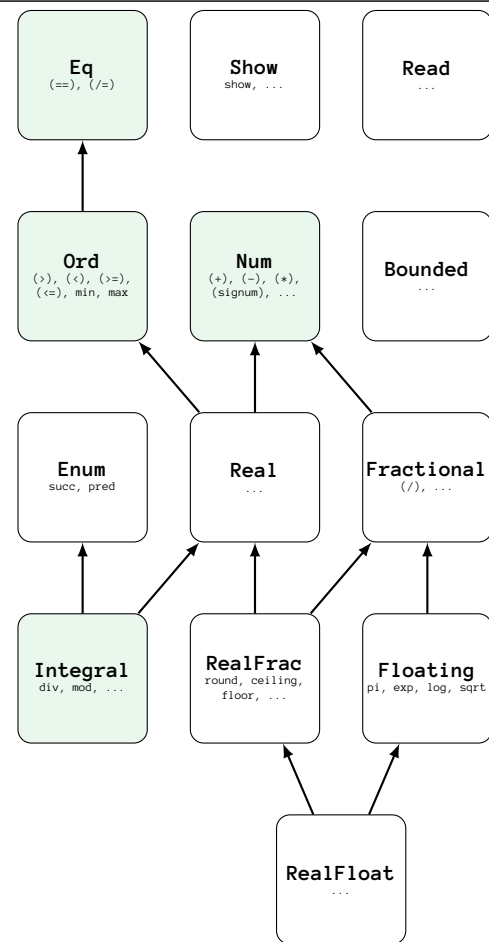
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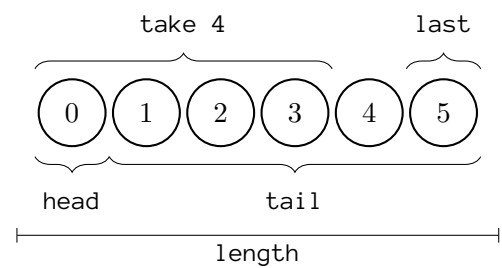
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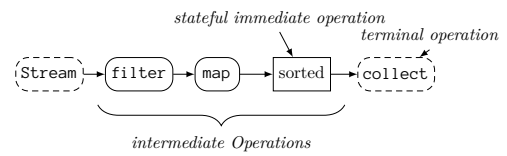
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Haskell/HaskellTypen



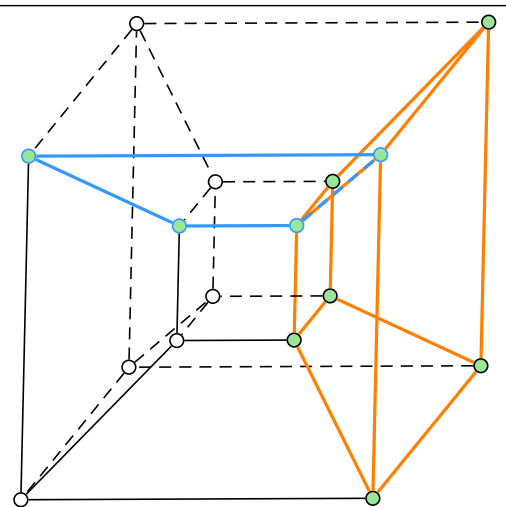
Haskell/Listenoperationen



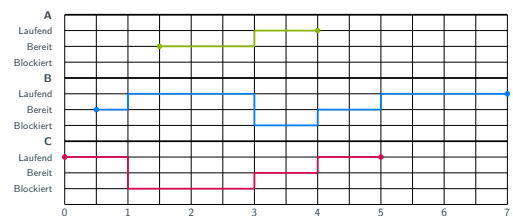
Java/StreamDemo

	\bar{a}	a	a	\bar{a}	
\bar{b}	0	0	0	0	\bar{d}
b	1	1	1	1	\bar{d}
b	1	1	1	1	d
\bar{b}	0	0	1	1	d
	\bar{c}	\bar{c}	c	c	

Logik/KVDiagramm

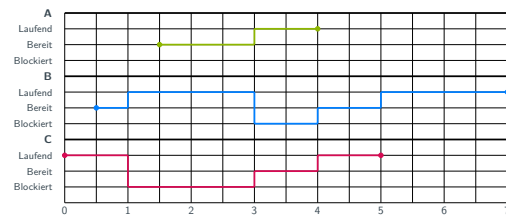


Logik/KWuerfel

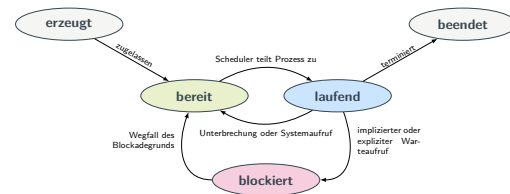


Prozesse/FCFS-WorstCase

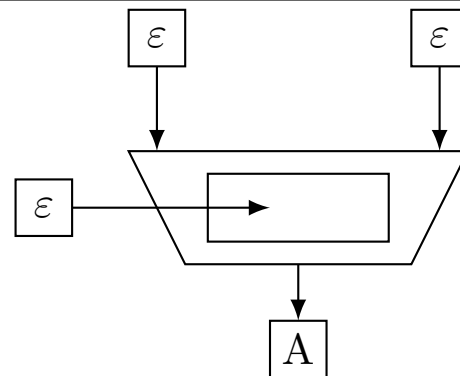
Prozesse/FCFS



Prozesse/Prozesszustände



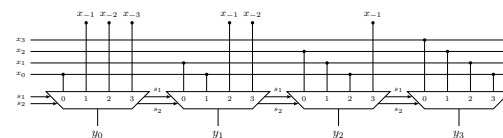
Rechner/ALU



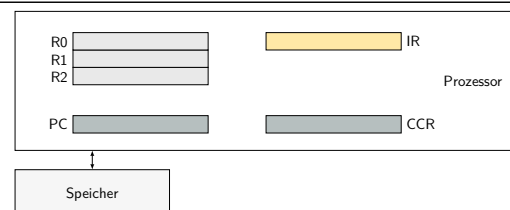
Rechner/AmpelPLA

δ	Zustand			Folgez.		NS				WO				T
	h_0	z_0	z_1	z'_0	z'_1	●	○	●	○	●	○	●	○	
δ_0	0	0	0	0	1	●	○	○	○	○	○	○	○	$7 \Rightarrow h'_0 = 1$
δ_1	1	0	1	0	1	●	○	○	○	○	○	○	○	$T - 1$
δ_3	0	0	1	1	0	○	○	○	○	○	○	○	○	$2 \Rightarrow h'_0 = 1$
δ_4	1	1	0	1	0	○	○	○	○	○	○	○	○	$T - 1$
δ_5	0	1	0	1	1	○	○	○	○	○	○	○	○	$7 \Rightarrow h'_0 = 1$
δ_7	1	1	1	1	1	○	○	○	○	○	○	○	○	$T - 1$
δ_9	0	1	1	0	0	○	○	○	○	○	○	○	○	$2 \Rightarrow h'_0 = 1$
δ_{10}	1	0	0	0	0	○	○	○	○	○	○	○	○	$T - 1$

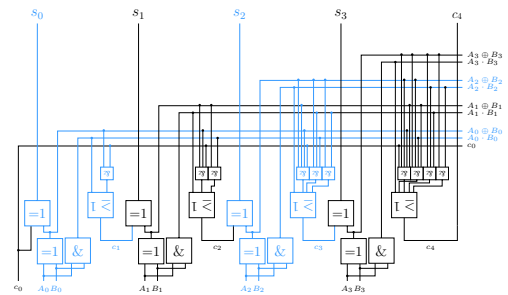
Rechner/BarrelShifter



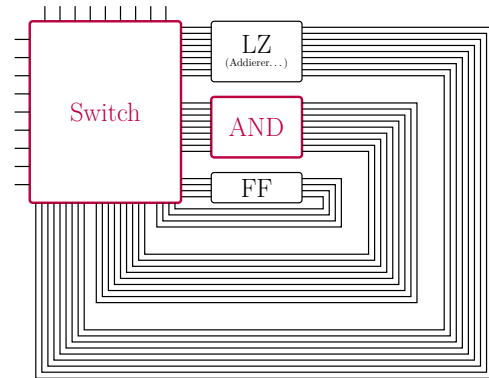
Rechner/Beispielprozessor



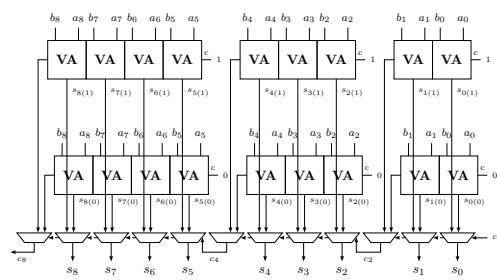
Rechner/CLA



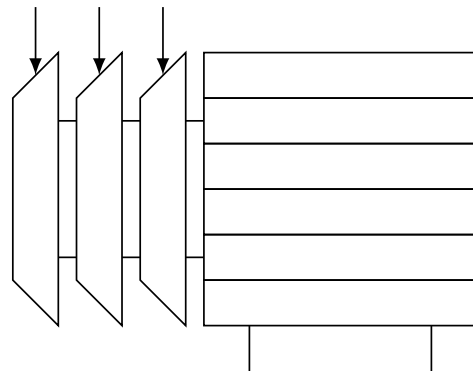
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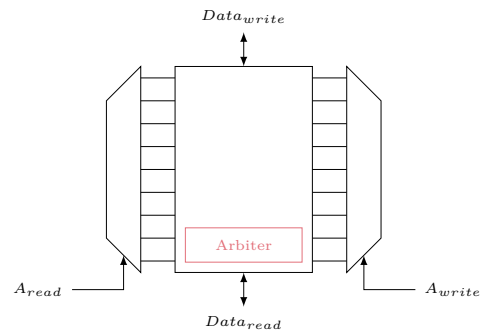
CSA - 9-bit Carry-Select-Addierer



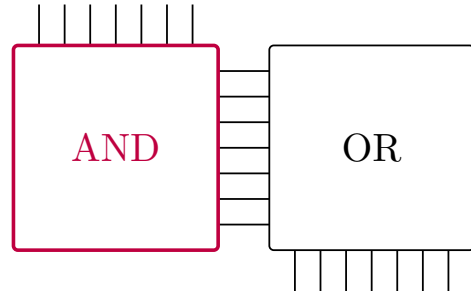
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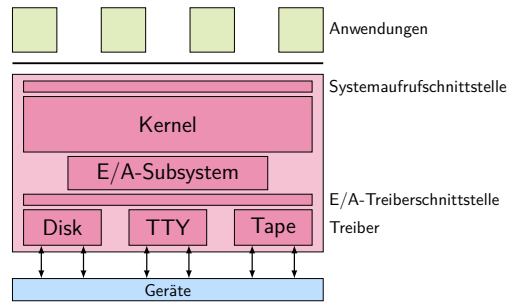
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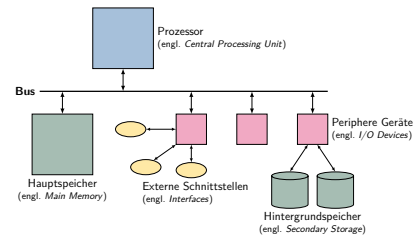
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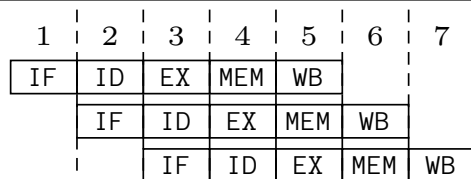
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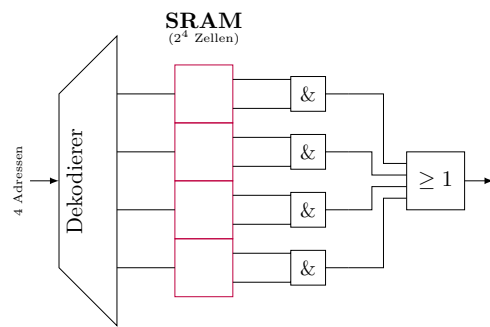
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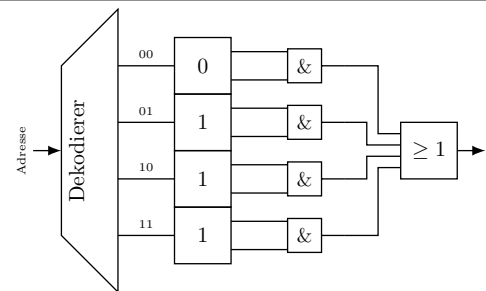
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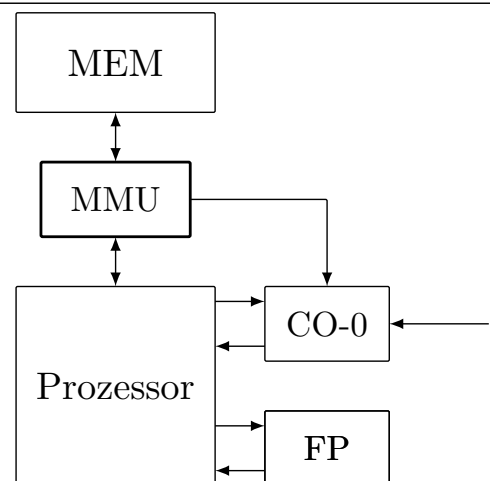
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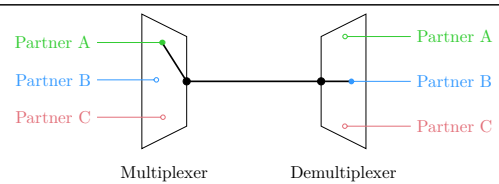
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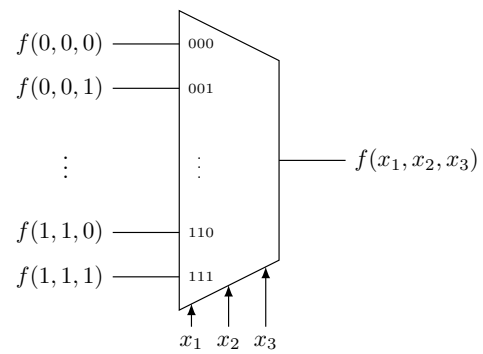
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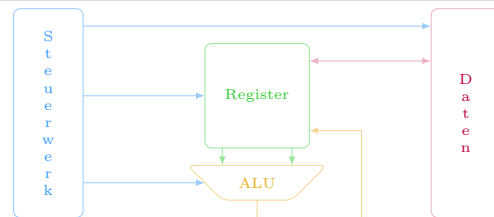
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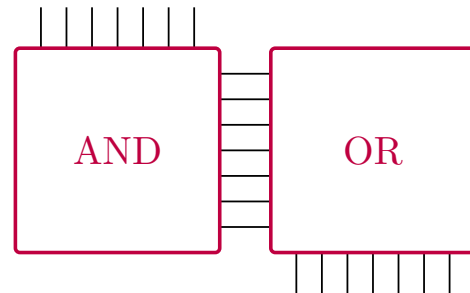
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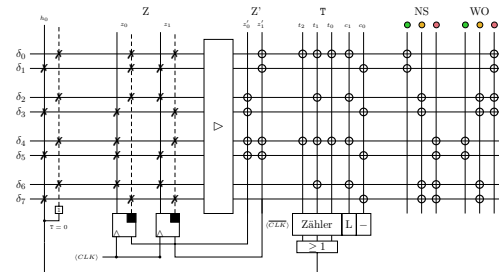
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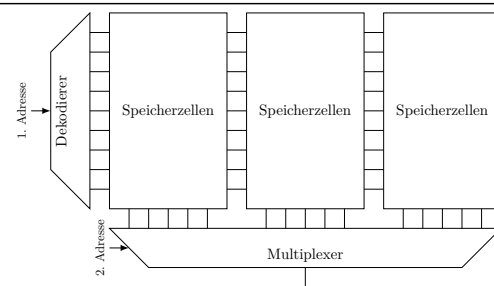
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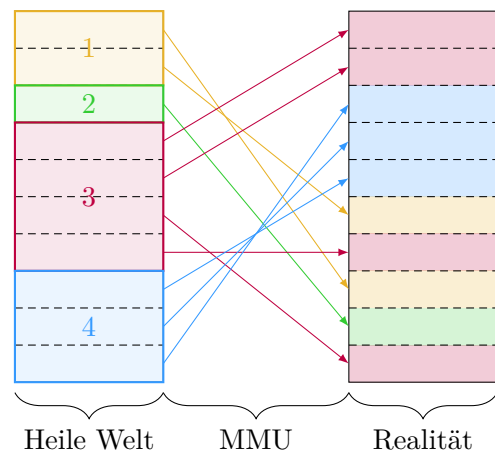
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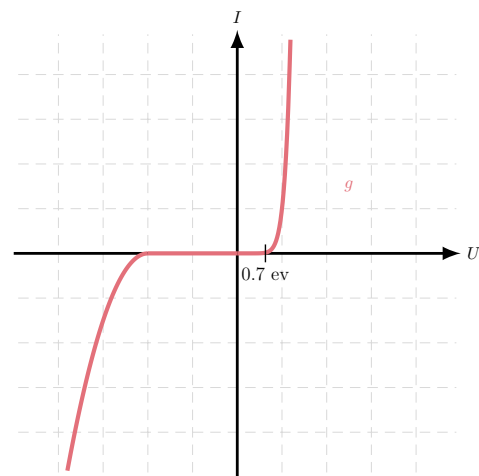
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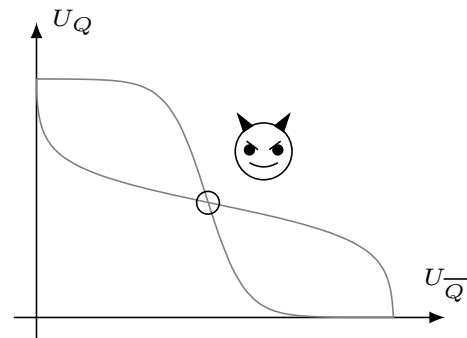
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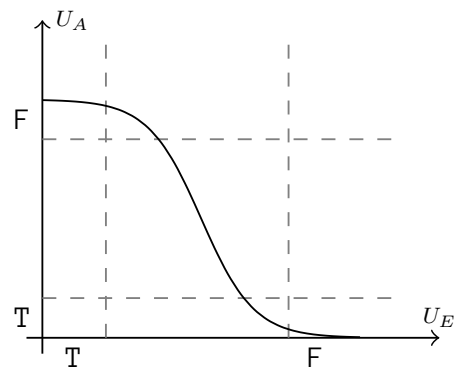
Rechner/Pages



Rechner/Physik/DiodenStromstaerke

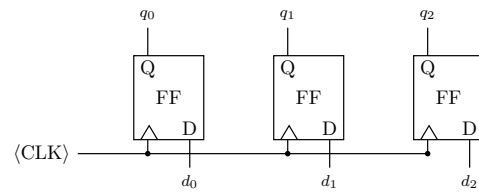


Rechner/Physik/Metastabil

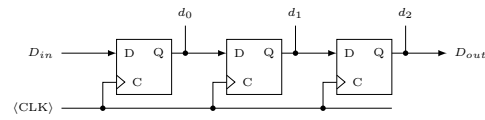


Rechner/Physik/TransistorStoertoleranz

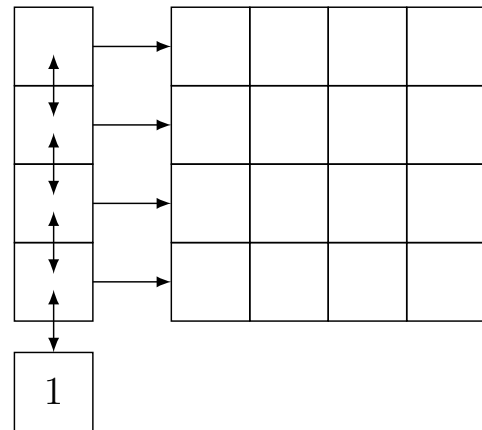
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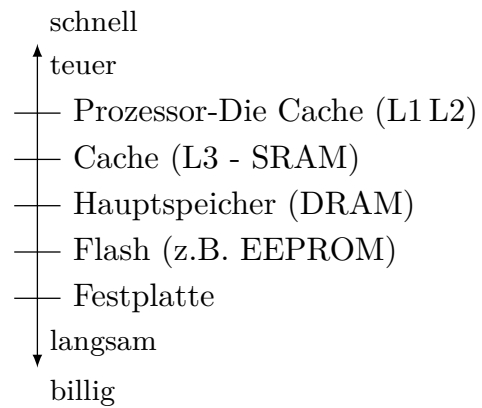
Rechner/RegisterSeriell



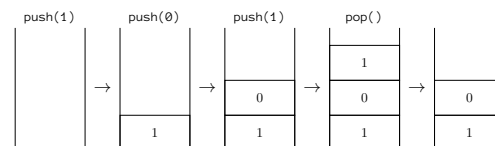
Rechner/Shiftregister



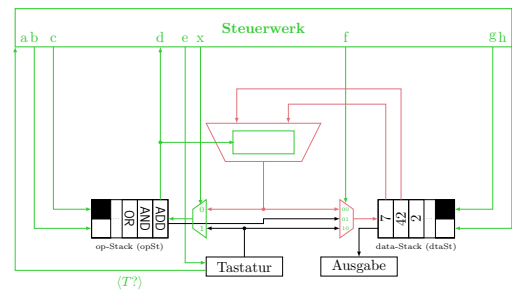
Rechner/Speicherhierarchie



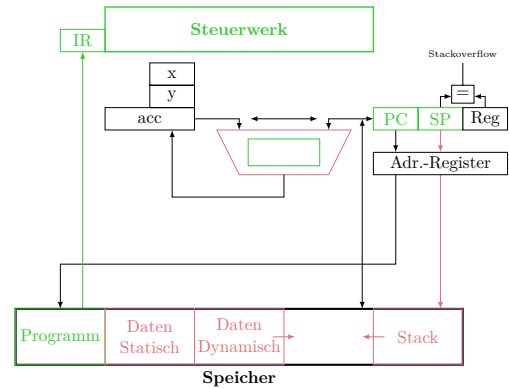
Rechner/StackExample



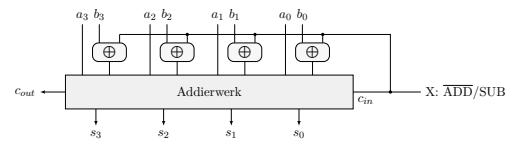
Rechner/Stackmaschiene



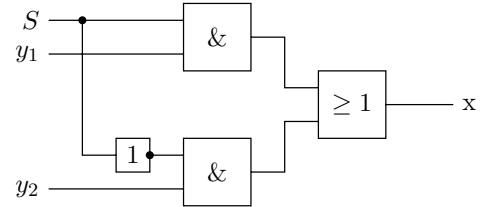
Rechner/StackmaschieneSimpler



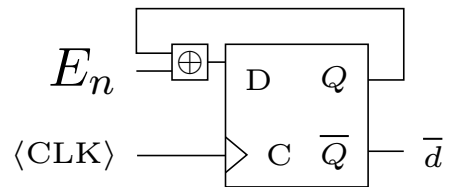
Schaltkreis/Addier-Subtrahierer



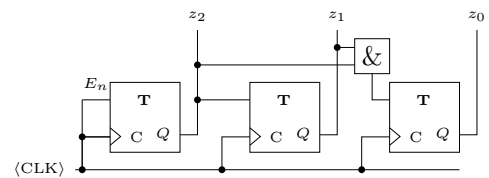
Schaltkreis/Demultiplexer



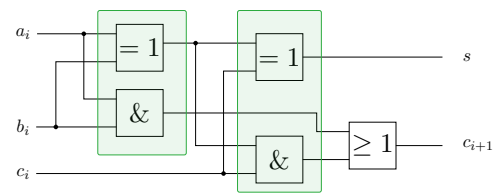
Schaltkreis/SynchronzaehlerDFF



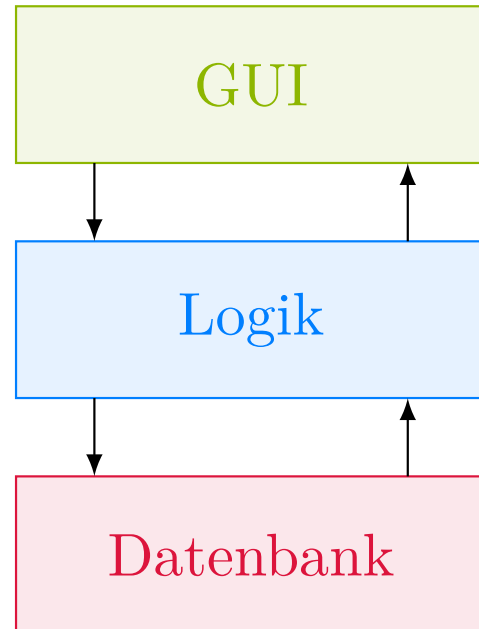
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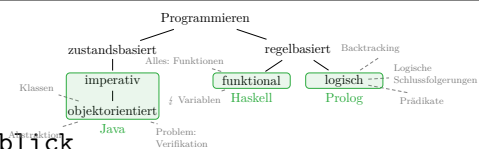
Schaltkreis/Volladdierer



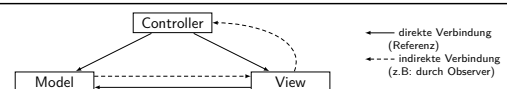
Software/DreiSchichtenArchitektur



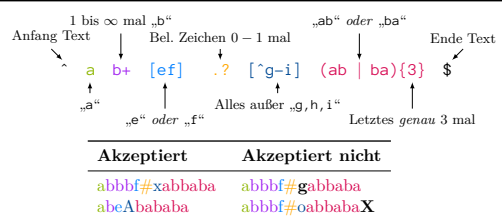
Software/Meta/ProgrammierparadigmenUeberblick



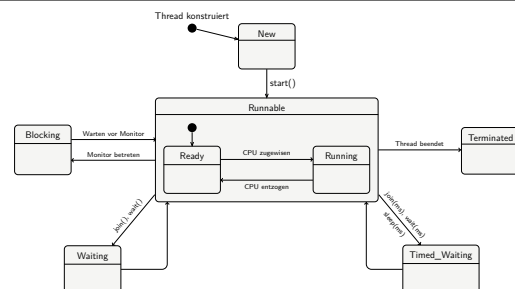
Software/ModelViewController



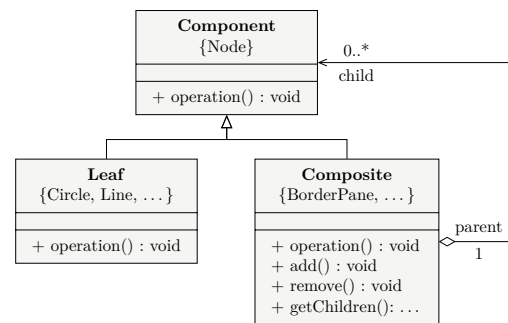
Software/RegexExample



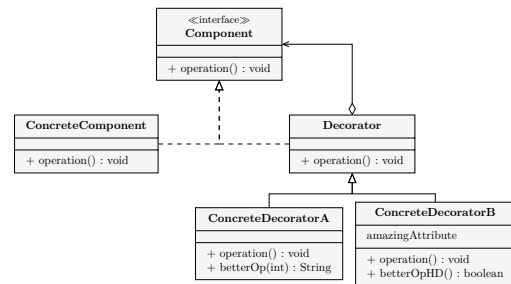
Software/ThreadStates



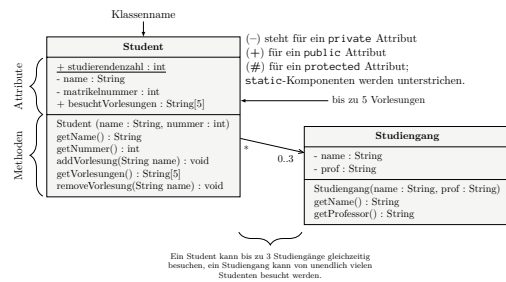
Software/UMLCompositePattern



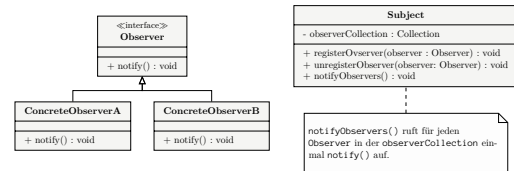
Software/UMLDecoratorPattern



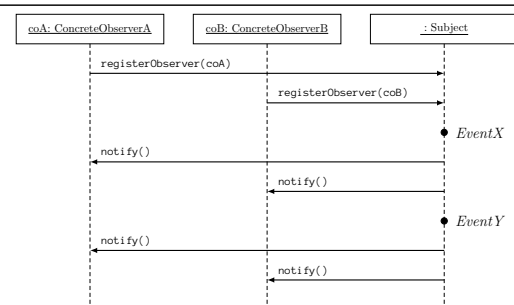
Software/UMLExample



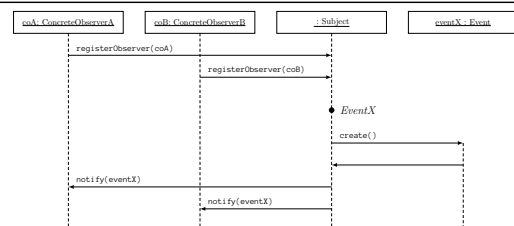
Software/UMLObserverPattern

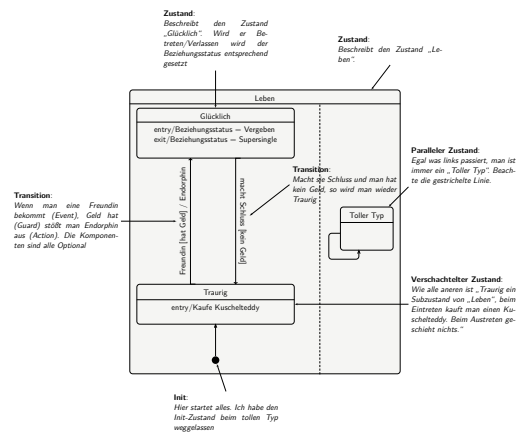


Software/UMLSEQObserverPattern

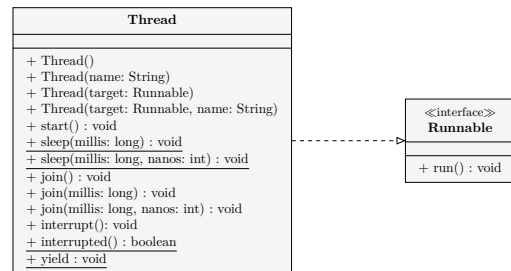


Software/UMLSEQObserverPatternAdapted





Software/UMLStateDiagramExample



Software/UMLThread