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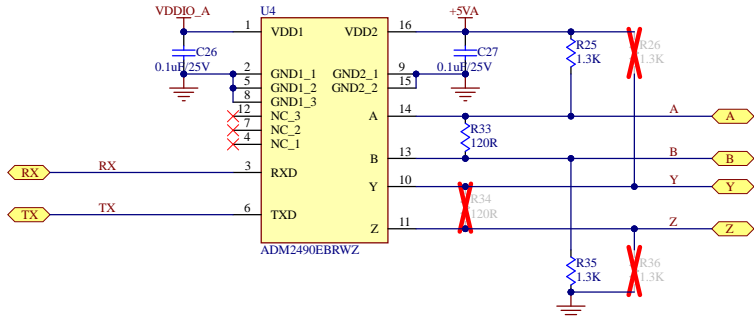
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Schematic Revision History

Revision	Modified descriptions / ECN List	Date				Product Title:	"					Checked By:	XXX
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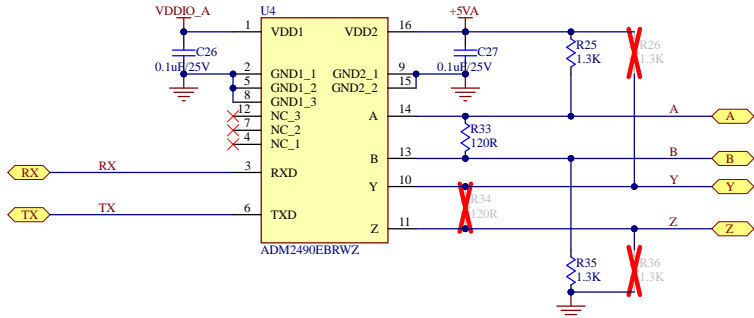
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Schematic Revision History

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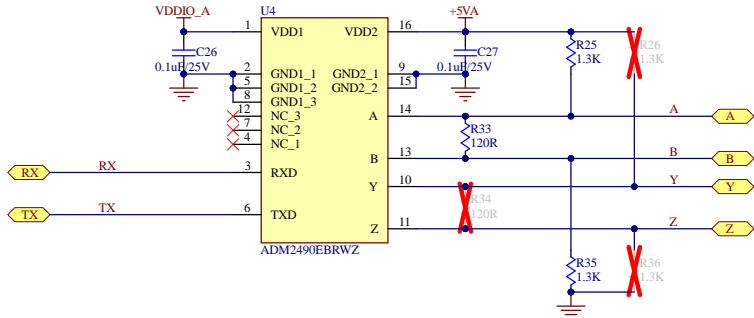
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Schematic Revision History

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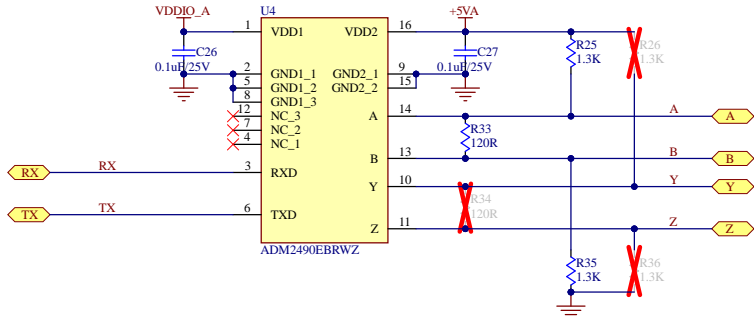
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Schematic Revision History

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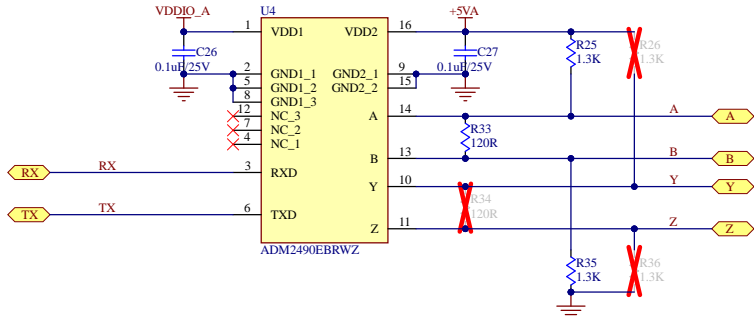
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Schematic Revision History

Revision	Modified descriptions / ECN List	Date


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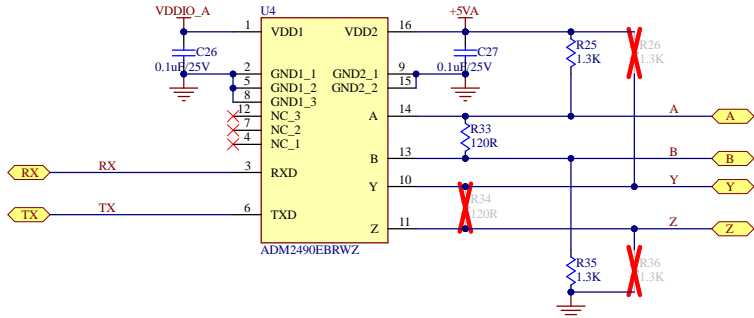
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Schematic Revision History

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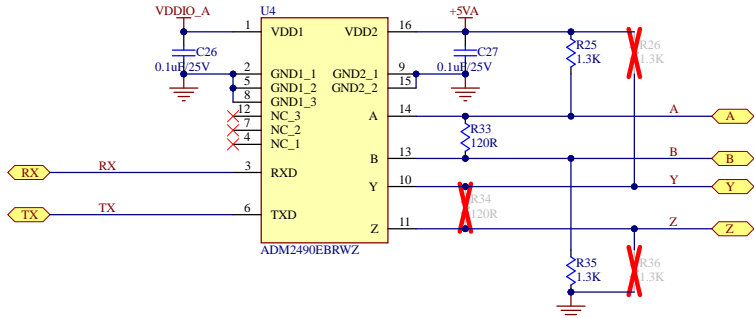
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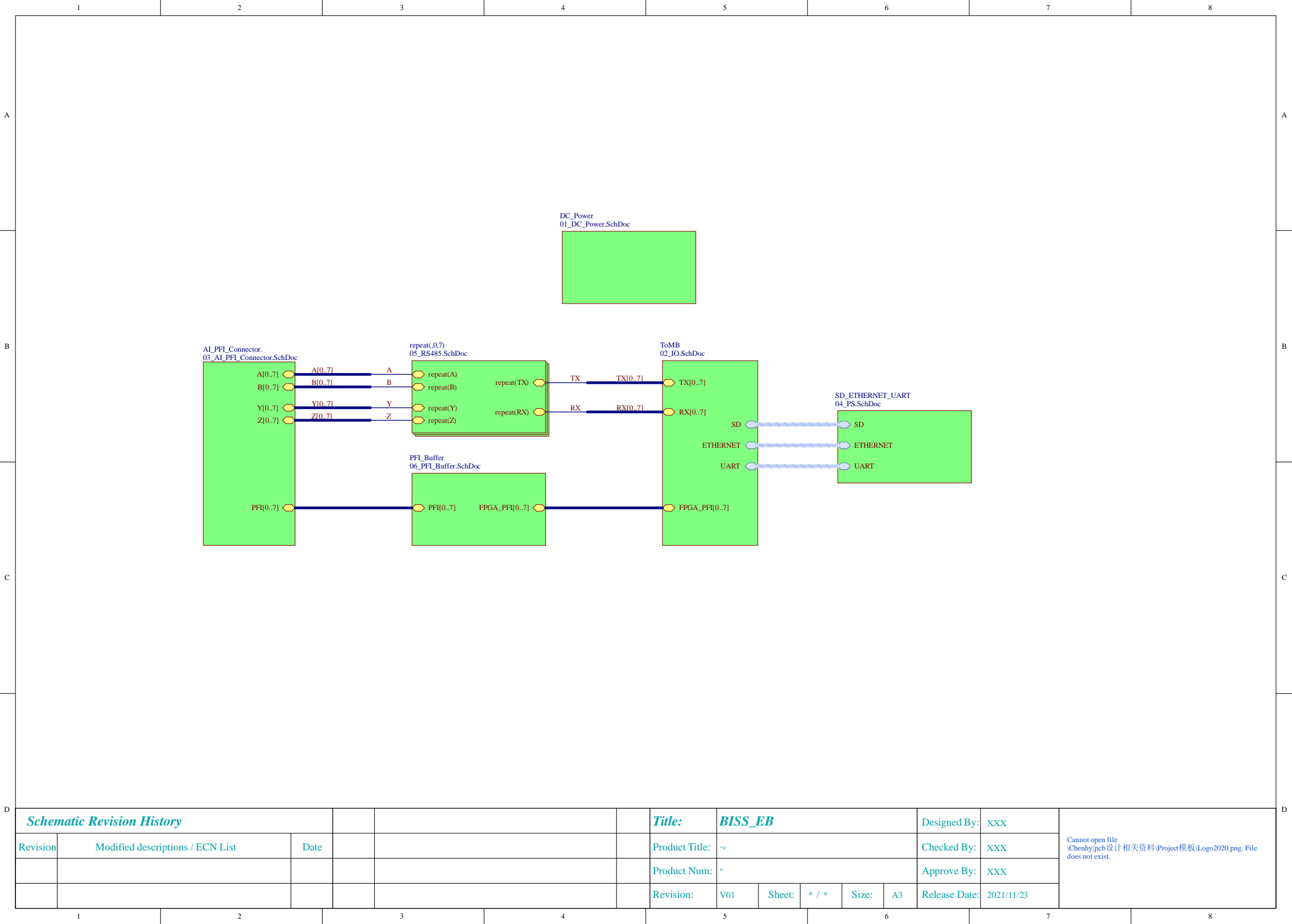


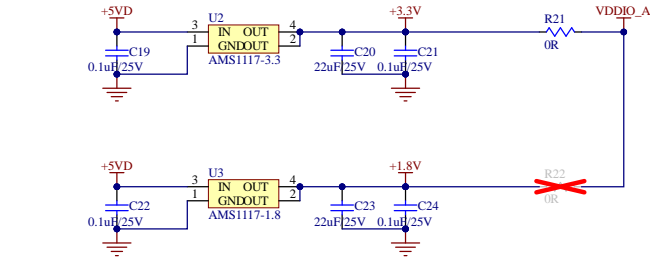
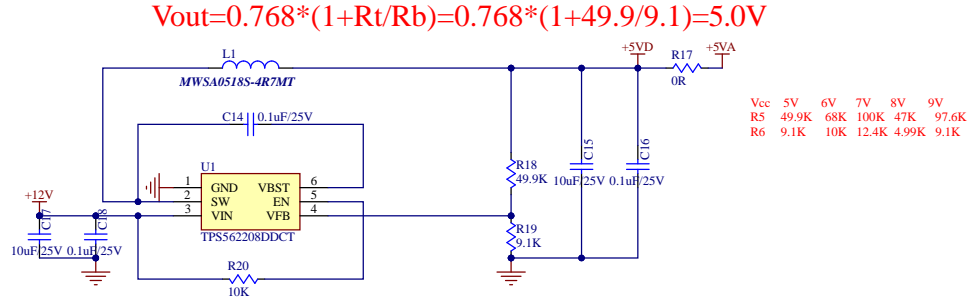
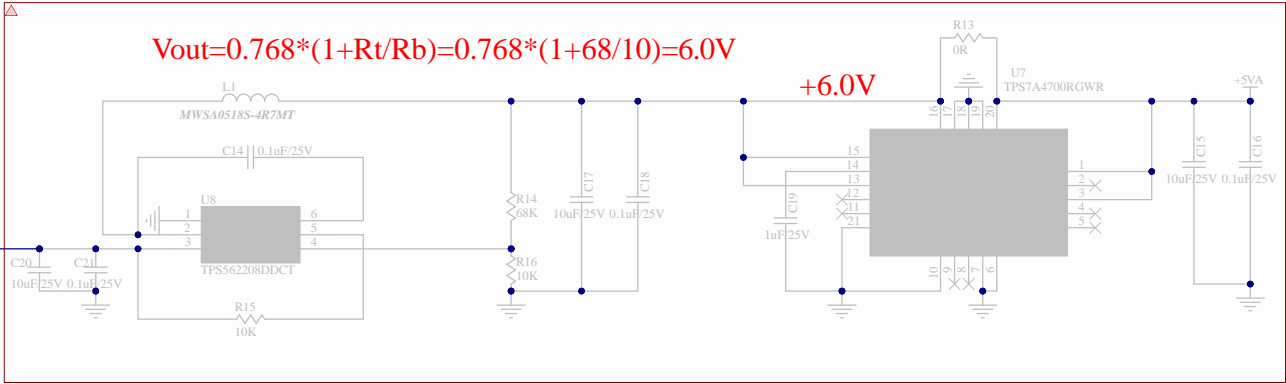
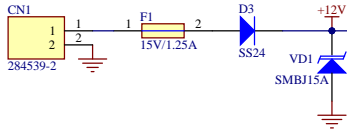
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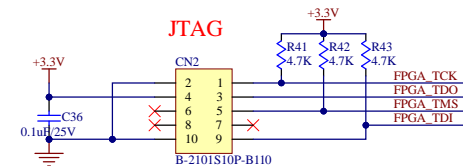
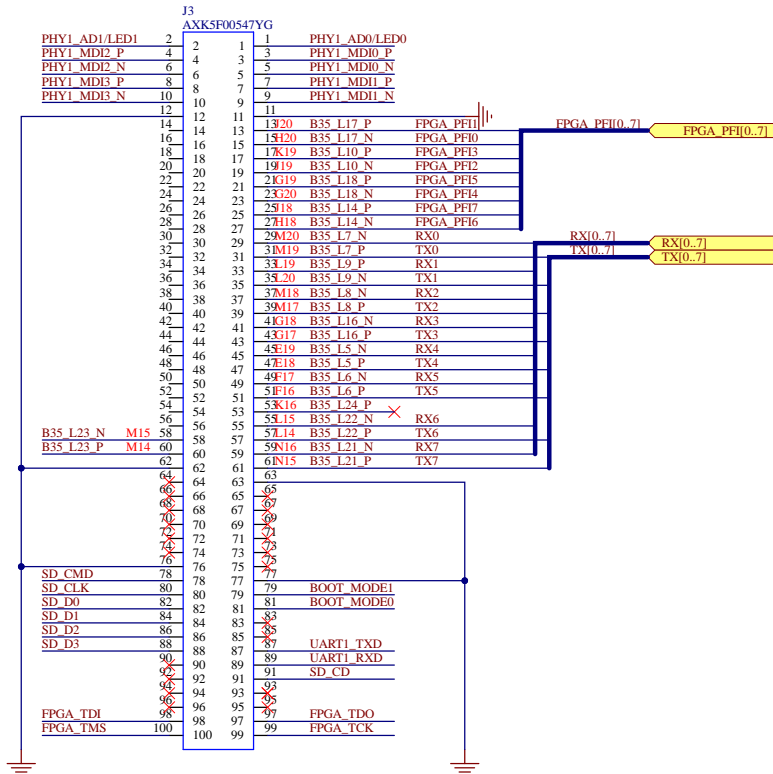
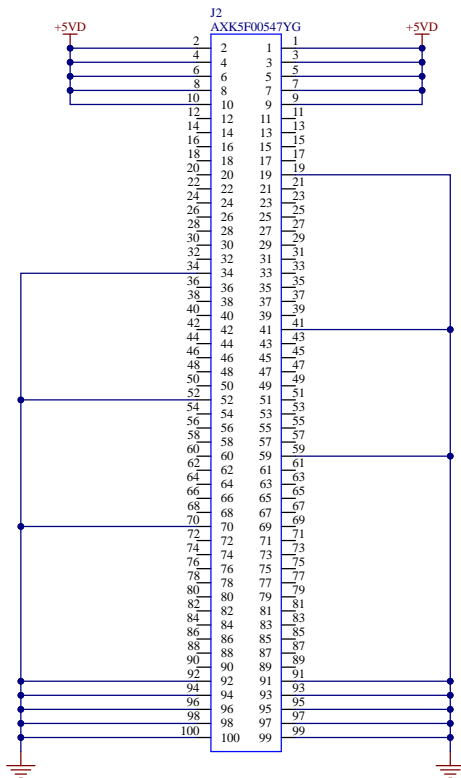


MARK1 MARK2 MARK3 MARK4 MARK5 MARK6

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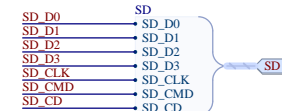
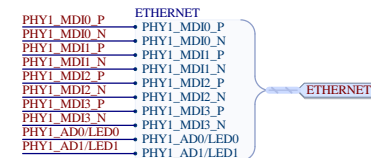
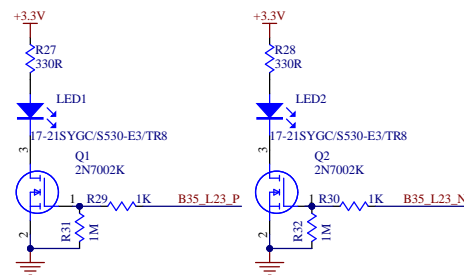
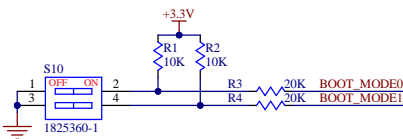
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# 核心板 IO

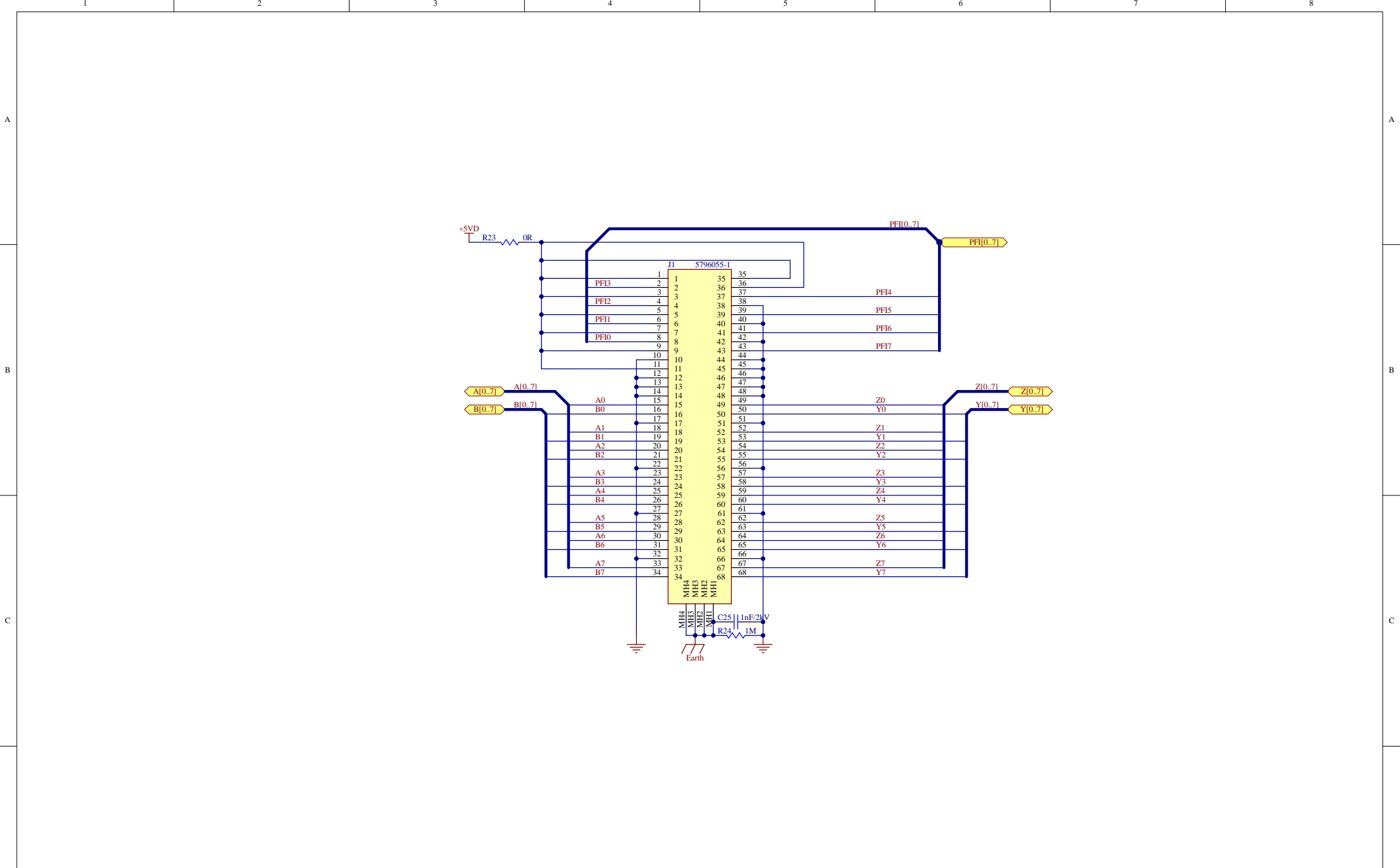


## BOOT OPTION

MIO4	MIO5	1	2
BOOT OTG		ON	ON
JTAG		OFF	ON
NAND		OFF	ON
QSPI		ON	OFF
SD CARD		OFF	OFF

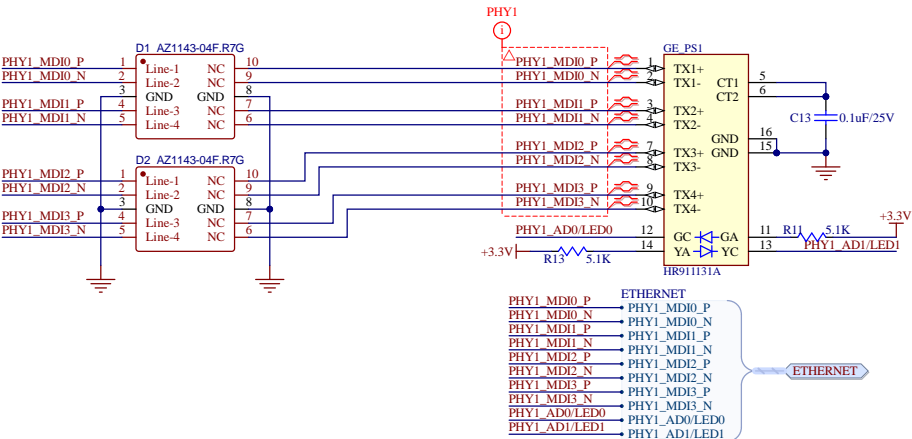


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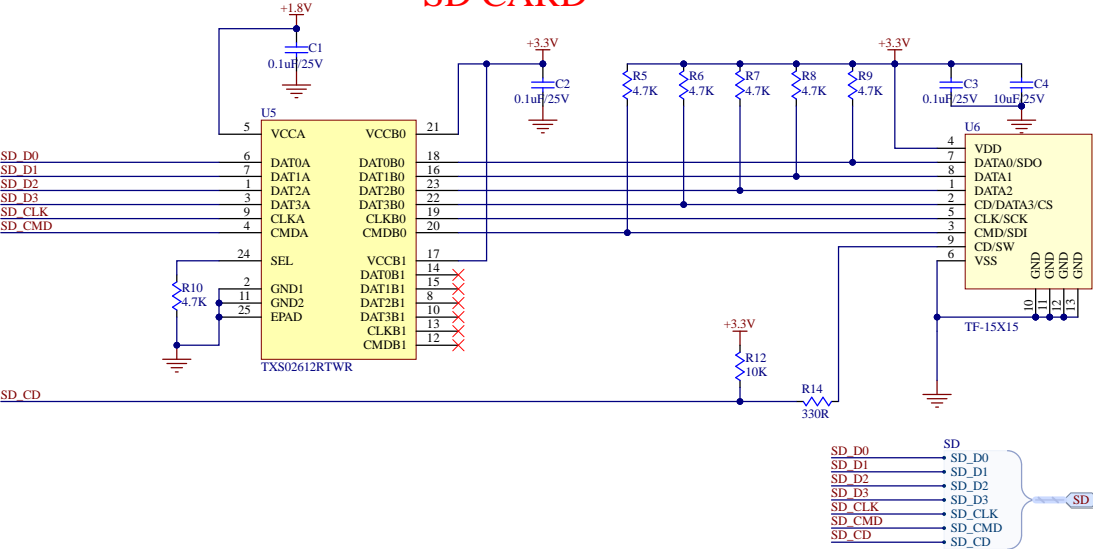


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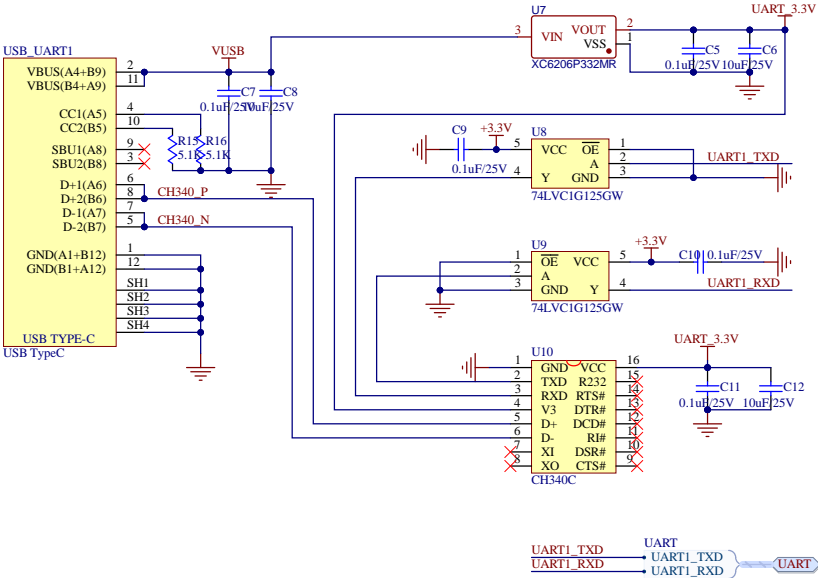
# ETHERNET(PS)



# SD CARD



# USB UART



## Schematic Revision History

Revision	Modified descriptions / ECN List	Date	Title:	BISS_EB	Designed By:	XXX	XXXX
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