

The diagram illustrates the pin configuration for the STM32H723VGT6 microcontroller. The pins are organized into several groups:

- Power and Reset:** NRST (14), BOOT0 (94), VREF+ (20), and VREF+ (12, 13).
- General Purpose I/O (GPIO):** PE0 (97), PE1 (98), PE2 (1), PE3 (2), PE4 (3), PE5 (4), PE6 (5), PD0 (81), PD1 (82), PD2 (83), PD3 (84), PD4 (85), PD5 (86), PD6 (87), PD7 (88), VCAP2 (48), and VCAP1 (73).
- Peripheral Function Pins:** PA0 (22), PA1 (23), PA2 (24), PA3 (25), PA4 (28), PA5 (29), PA6 (30), PA7 (31), PA8 (67), PA9 (68), PA10 (69), PA11 (70), PA12 (71), PA13 (JTMS), PA14 (JTCK), PA15 (JTDI), PB0 (34), PB1 (35), PB2 (36), PB3 (JTDO), PB4 (NJTRST), PB5 (91), PB6 (92), PB7 (93), PB8 (95), PB9 (96), PB10 (46), PB11 (47), PB12 (51), PB13 (52), PB14 (53), PB15 (54), PC0 (15), PC1 (16), PC2 (17), PC3 (18), PC4 (32), PC5 (33), PC6 (63), PC7 (64), PC8 (65), PC9 (66), PC10 (78), PC11 (79), PC12 (80), PC13 (7), PC14 (8), and PC15 (9).
- Power Supply and Ground:** VBAT (6), VDD33 (11), VDD33 (27), VDD33 (50), VDD33 (75), VDD33 (100), VDDA (21), VSS (10), and VSSA (19).
- Other Components:** A 20 MHz crystal (cr3) is connected to PE0 and PE1. A 3.3V supply is connected to PB4 and PB5. A 3kΩ resistor (R13) is connected to PB4 and PB5. A 3kΩ resistor (R14) is connected to PB13 and PB14.

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Pin connection diagram for the microcontroller:

- PA14 (Pin 1)
- +3.3V (Pin 3)
- CLK (Pin 2)
- DIO (Pin 4)
- VCC (Pin 5)
- GND (Pin 6)
- PA13 (Pin 7)
- GND (Pin 8)

PB10	1	TX
PB11	2	RX
+3.3V	3	VCC
GND	4	GND

← +3.3V	1	VCC
⏏ GND	2	GND
PE4	3	CS
PE2	4	SCK
PE5	5	MISO
PE6	6	MOSI

← +3.3V	1	VCC
⏏ GND	2	GND
PE3	3	PE3
PB8	4	SCL
PB9	5	SDA
×	6	NC

PD5	1	TX
PD6	2	RX
+5V	3	5v
GND	4	GND

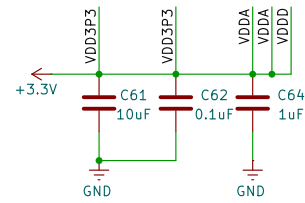
U0TXD	1	ERX
U0RXD	2	ETX
PD1	3	TX
PD0	4	RX

VIN	2	VIN
PA8	3	CH1
PA9	4	CH2
PA10	5	CH3
PA11	6	CH4
PC5	7	CUR
	8	NC

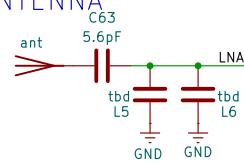
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POWER



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