



WebAssembly Specification

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WebAssembly Community Group

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1.1 Introduction

WebAssembly (abbreviated Wasm²) is a *safe, portable, low-level code format* designed for efficient execution and compact representation. Its main goal is to enable high performance applications on the Web, but it does not make any Web-specific assumptions or provide Web-specific features, so it can be employed in other environments as well.

WebAssembly is an open standard developed by a [W3C Community Group](https://www.w3.org/community/webassembly/)¹.

This document describes version 3.0 (Draft 2024-11-07) of the [core](#) WebAssembly standard. It is intended that it will be superseded by new incremental releases with additional features in the future.

1.1.1 Design Goals

The design goals of WebAssembly are the following:

- Fast, safe, and portable *semantics*:
 - **Fast**: executes with near native code performance, taking advantage of capabilities common to all contemporary hardware.
 - **Safe**: code is validated and executes in a memory-safe³, sandboxed environment preventing data corruption or security breaches.
 - **Well-defined**: fully and precisely defines valid programs and their behavior in a way that is easy to reason about informally and formally.
 - **Hardware-independent**: can be compiled on all modern architectures, desktop or mobile devices and embedded systems alike.
 - **Language-independent**: does not privilege any particular language, programming model, or object model.
 - **Platform-independent**: can be embedded in browsers, run as a stand-alone VM, or integrated in other environments.

² A contraction of “WebAssembly”, not an acronym, hence not using all-caps.

¹ <https://www.w3.org/community/webassembly/>

³ No program can break WebAssembly’s memory model. Of course, it cannot guarantee that an unsafe language compiling to WebAssembly does not corrupt its own memory layout, e.g. inside WebAssembly’s linear memory.

- **Open:** programs can interoperate with their environment in a simple and universal manner.
- Efficient and portable *representation*:
 - **Compact:** has a binary format that is fast to transmit by being smaller than typical text or native code formats.
 - **Modular:** programs can be split up in smaller parts that can be transmitted, cached, and consumed separately.
 - **Efficient:** can be decoded, validated, and compiled in a fast single pass, equally with either just-in-time (JIT) or ahead-of-time (AOT) compilation.
 - **Streamable:** allows decoding, validation, and compilation to begin as soon as possible, before all data has been seen.
 - **Parallelizable:** allows decoding, validation, and compilation to be split into many independent parallel tasks.
 - **Portable:** makes no architectural assumptions that are not broadly supported across modern hardware.

WebAssembly code is also intended to be easy to inspect and debug, especially in environments like web browsers, but such features are beyond the scope of this specification.

1.1.2 Scope

At its core, WebAssembly is a *virtual instruction set architecture (virtual ISA)*. As such, it has many use cases and can be embedded in many different environments. To encompass their variety and enable maximum reuse, the WebAssembly specification is split and layered into several documents.

This document is concerned with the core ISA layer of WebAssembly. It defines the instruction set, binary encoding, validation, and execution semantics, as well as a textual representation. It does not, however, define how WebAssembly programs can interact with a specific environment they execute in, nor how they are invoked from such an environment.

Instead, this specification is complemented by additional documents defining interfaces to specific embedding environments such as the Web. These will each define a WebAssembly *application programming interface (API)* suitable for a given environment.

1.1.3 Security Considerations

WebAssembly provides no ambient access to the computing environment in which code is executed. Any interaction with the environment, such as I/O, access to resources, or operating system calls, can only be performed by invoking [functions](#) provided by the [embedder](#) and imported into a WebAssembly [module](#). An embedder can establish security policies suitable for a respective environment by controlling or limiting which functional capabilities it makes available for import. Such considerations are an embedder's responsibility and the subject of [API definitions](#) for a specific environment.

Because WebAssembly is designed to be translated into machine code running directly on the host's hardware, it is potentially vulnerable to side channel attacks on the hardware level. In environments where this is a concern, an embedder may have to put suitable mitigations into place to isolate WebAssembly computations.

1.1.4 Dependencies

WebAssembly depends on two existing standards:

- [IEEE 754⁴](#), for the representation of [floating-point data](#) and the semantics of respective [numeric operations](#).
- [Unicode⁵](#), for the representation of [import/export names](#) and the [text format](#).

However, to make this specification self-contained, relevant aspects of the aforementioned standards are defined and formalized as part of this specification, such as the [binary representation](#) and [rounding](#) of floating-point values, and the [value range](#) and [UTF-8 encoding](#) of Unicode characters.

Note: The aforementioned standards are the authoritative source of all respective definitions. Formalizations given in this specification are intended to match these definitions. Any discrepancy in the syntax or semantics described is to be considered an error.

1.2 Overview

1.2.1 Concepts

WebAssembly encodes a low-level, assembly-like programming language. This language is structured around the following concepts.

Values

WebAssembly provides only four basic *number types*. These are integers and [IEEE 754⁶](#) numbers, each in 32 and 64 bit width. 32 bit integers also serve as Booleans and as memory addresses. The usual operations on these types are available, including the full matrix of conversions between them. There is no distinction between signed and unsigned integer types. Instead, integers are interpreted by respective operations as either unsigned or signed in two's complement representation.

In addition to these basic number types, there is a single 128 bit wide vector type representing different types of packed data. The supported representations are 4 32-bit, or 2 64-bit [IEEE 754⁷](#) numbers, or different widths of packed integer values, specifically 2 64-bit integers, 4 32-bit integers, 8 16-bit integers, or 16 8-bit integers.

Finally, values can consist of opaque *references* that represent pointers towards different sorts of entities. Unlike with other types, their size or representation is not observable.

Instructions

The computational model of WebAssembly is based on a *stack machine*. Code consists of sequences of *instructions* that are executed in order. Instructions manipulate values on an implicit *operand stack*⁸ and fall into two main categories. *Simple* instructions perform basic operations on data. They pop arguments from the operand stack and push results back to it. *Control* instructions alter control flow. Control flow is *structured*, meaning it is expressed with well-nested constructs such as blocks, loops, and conditionals. Branches can only target such constructs.

Traps

Under some conditions, certain instructions may produce a *trap*, which immediately aborts execution. Traps cannot be handled by WebAssembly code, but are reported to the outside environment, where they typically can be caught.

Functions

Code is organized into separate *functions*. Each function takes a sequence of values as parameters and returns

⁴ <https://ieeexplore.ieee.org/document/8766229>

⁵ <https://www.unicode.org/versions/latest/>

⁶ <https://ieeexplore.ieee.org/document/8766229>

⁷ <https://ieeexplore.ieee.org/document/8766229>

⁸ In practice, implementations need not maintain an actual operand stack. Instead, the stack can be viewed as a set of anonymous registers that are implicitly referenced by instructions. The [type system](#) ensures that the stack height, and thus any referenced register, is always known statically.

a sequence of values as results. Functions can call each other, including recursively, resulting in an implicit call stack that cannot be accessed directly. Functions may also declare mutable *local variables* that are usable as virtual registers.

Tables

A *table* is an array of opaque values of a particular *reference type*. It allows programs to select such values indirectly through a dynamic index operand. Thereby, for example, a program can call functions indirectly through a dynamic index into a table. This allows emulating function pointers by way of table indices.

Linear Memory

A *linear memory* is a contiguous, mutable array of raw bytes. Such a memory is created with an initial size but can be grown dynamically. A program can load and store values from/to a linear memory at any byte address (including unaligned). Integer loads and stores can specify a *storage size* which is smaller than the size of the respective value type. A trap occurs if an access is not within the bounds of the current memory size.

Modules

A WebAssembly binary takes the form of a *module* that contains definitions for functions, tables, and linear memories, as well as mutable or immutable *global variables*. Definitions can also be *imported*, specifying a module/name pair and a suitable type. Each definition can optionally be *exported* under one or more names. In addition to definitions, modules can define initialization data for their memories or tables that takes the form of *segments* copied to given offsets. They can also define a *start function* that is automatically executed.

Embedder

A WebAssembly implementation will typically be *embedded* into a *host* environment. This environment defines how loading of modules is initiated, how imports are provided (including host-side definitions), and how exports can be accessed. However, the details of any particular embedding are beyond the scope of this specification, and will instead be provided by complementary, environment-specific API definitions.

1.2.2 Semantic Phases

Conceptually, the semantics of WebAssembly is divided into three phases. For each part of the language, the specification specifies each of them.

Decoding

WebAssembly modules are distributed in a *binary format*. *Decoding* processes that format and converts it into an internal representation of a module. In this specification, this representation is modelled by *abstract syntax*, but a real implementation could compile directly to machine code instead.

Validation

A decoded module has to be *valid*. Validation checks a number of well-formedness conditions to guarantee that the module is meaningful and safe. In particular, it performs *type checking* of functions and the instruction sequences in their bodies, ensuring for example that the operand stack is used consistently.

Execution

Finally, a valid module can be *executed*. Execution can be further divided into two phases:

Instantiation. A module *instance* is the dynamic representation of a module, complete with its own state and execution stack. Instantiation executes the module body itself, given definitions for all its imports. It initializes globals, memories and tables and invokes the module's start function if defined. It returns the instances of the module's exports.

Invocation. Once instantiated, further WebAssembly computations can be initiated by *invoking* an exported function on a module instance. Given the required arguments, that executes the respective function and returns its results.

Instantiation and invocation are operations within the embedding environment.

2.1 Conventions

WebAssembly is a programming language that has multiple concrete representations (its [binary format](#) and the [text format](#)). Both map to a common structure. For conciseness, this structure is described in the form of an *abstract syntax*. All parts of this specification are defined in terms of this abstract syntax.

2.1.1 Grammar Notation

The following conventions are adopted in defining grammar rules for abstract syntax.

- Terminal symbols (atoms) are written in sans-serif font or in symbolic form: `i32`, `end`, `→`, `[`, `]`.
- Nonterminal symbols are written in italic font: *valtype*, *instr*.
- A^n is a sequence of $n \geq 0$ iterations of A .
- A^* is a possibly empty sequence of iterations of A . (This is a shorthand for A^n used where n is not relevant.)
- A^+ is a non-empty sequence of iterations of A . (This is a shorthand for A^n where $n \geq 1$.)
- $A^?$ is an optional occurrence of A . (This is a shorthand for A^n where $n \leq 1$.)
- Productions are written $sym ::= A_1 \mid \dots \mid A_n$.
- Large productions may be split into multiple definitions, indicated by ending the first one with explicit ellipses, $sym ::= A_1 \mid \dots$, and starting continuations with ellipses, $sym ::= \dots \mid A_2$.
- Some productions are augmented with side conditions in parentheses, “(if *condition*)”, that provide a shorthand for a combinatorial expansion of the production into many separate cases.
- If the same meta variable or non-terminal symbol appears multiple times in a production, then all those occurrences must have the same instantiation. (This is a shorthand for a side condition requiring multiple different variables to be equal.)

2.1.2 Auxiliary Notation

When dealing with syntactic constructs the following notation is also used:

- ϵ denotes the empty sequence.
- $|s|$ denotes the length of a sequence s .
- $s[i]$ denotes the i -th element of a sequence s , starting from 0.
- $s[i : n]$ denotes the sub-sequence $s[i] \dots s[i + n - 1]$ of a sequence s .
- $s \text{ with } [i] = A$ denotes the same sequence as s , except that the i -th element is replaced with A .
- $s \text{ with } [i : n] = A^n$ denotes the same sequence as s , except that the sub-sequence $s[i : n]$ is replaced with A^n .
- $\text{concat}(s^*)$ denotes the flat sequence formed by concatenating all sequences s_i in s^* .

Moreover, the following conventions are employed:

- The notation x^n , where x is a non-terminal symbol, is treated as a meta variable ranging over respective sequences of x (similarly for x^* , x^+ , $x^?$).
- When given a sequence x^n , then the occurrences of x in a sequence written $(A_1 x A_2)^n$ are assumed to be in point-wise correspondence with x^n (similarly for x^* , x^+ , $x^?$). This implicitly expresses a form of mapping syntactic constructions over a sequence.

Productions of the following form are interpreted as *records* that map a fixed set of fields field_i to “values” A_i , respectively:

$$r ::= \{ \text{field}_1 A_1, \text{field}_2 A_2, \dots \}$$

The following notation is adopted for manipulating such records:

- $r.\text{field}$ denotes the contents of the field component of r .
- $r \text{ with field} = A$ denotes the same record as r , except that the contents of the field component is replaced with A .
- $r_1 \oplus r_2$ denotes the composition of two records with the same fields of sequences by appending each sequence point-wise:

$$\{ \text{field}_1 A_1^*, \text{field}_2 A_2^*, \dots \} \oplus \{ \text{field}_1 B_1^*, \text{field}_2 B_2^*, \dots \} = \{ \text{field}_1 A_1^* B_1^*, \text{field}_2 A_2^* B_2^*, \dots \}$$

- $\bigoplus r^*$ denotes the composition of a sequence of records, respectively; if the sequence is empty, then all fields of the resulting record are empty.

The update notation for sequences and records generalizes recursively to nested components accessed by “paths” $pth ::= ([\dots] \mid \text{field})^+$:

- $s \text{ with } [i] pth = A$ is short for $s \text{ with } [i] = (s[i] \text{ with } pth = A)$,
- $r \text{ with field } pth = A$ is short for $r \text{ with field} = (r.\text{field} \text{ with } pth = A)$,

where $r \text{ with field} = A$ is shortened to $r \text{ with field} = A$.

2.1.3 Vectors

Vectors are bounded sequences of the form A^n (or A^*), where the A can either be values or complex constructions. A vector can have at most $2^{32} - 1$ elements.

$$\text{vec}(A) ::= A^n \quad (\text{if } n < 2^{32})$$

2.2 Values

WebAssembly programs operate on primitive numeric *values*. Moreover, in the definition of programs, immutable sequences of values occur to represent more complex data, such as text strings or other vectors.

2.2.1 Bytes

The simplest form of value are raw uninterpreted *bytes*. In the abstract syntax they are represented as hexadecimal literals.

$$\text{byte} ::= 0x00 \mid \dots \mid 0xFF$$

Conventions

- The meta variable b ranges over bytes.
- Bytes are sometimes interpreted as natural numbers $n < 256$.

2.2.2 Integers

Different classes of *integers* with different value ranges are distinguished by their *bit width* N and by whether they are *unsigned* or *signed*.

$$\begin{aligned} uN &::= 0 \mid 1 \mid \dots \mid 2^N - 1 \\ sN &::= -2^{N-1} \mid \dots \mid -1 \mid 0 \mid 1 \mid \dots \mid 2^{N-1} - 1 \\ iN &::= uN \end{aligned}$$

The class iN defines *uninterpreted* integers, whose signedness interpretation can vary depending on context. In the abstract syntax, they are represented as unsigned values. However, some operations *convert* them to signed based on a two's complement interpretation.

Note: The main integer types occurring in this specification are $u32$, $u64$, $s32$, $s64$, $i8$, $i16$, $i32$, $i64$. However, other sizes occur as auxiliary constructions, e.g., in the definition of *floating-point* numbers.

Conventions

- The meta variables m, n, i range over integers.
- Numbers may be denoted by simple arithmetics, as in the grammar above. In order to distinguish arithmetics like 2^N from sequences like $(1)^N$, the latter is distinguished with parentheses.

2.2.3 Floating-Point

Floating-point data represents 32 or 64 bit values that correspond to the respective binary formats of the [IEEE 754⁹](https://ieeexplore.ieee.org/document/8766229) standard (Section 3.3).

Every value has a *sign* and a *magnitude*. Magnitudes can either be expressed as *normal* numbers of the form $m_0.m_1m_2\dots m_M \cdot 2^e$, where e is the exponent and m is the *significand* whose most significant bit m_0 is 1, or as a *subnormal* number where the exponent is fixed to the smallest possible value and m_0 is 0; among the subnormals are positive and negative zero values. Since the significands are binary values, normals are represented in the form $(1 + m \cdot 2^{-M}) \cdot 2^e$, where M is the bit width of m ; similarly for subnormals.

⁹ <https://ieeexplore.ieee.org/document/8766229>

Possible magnitudes also include the special values ∞ (infinity) and `nan` (NaN, not a number). NaN values have a *payload* that describes the mantissa bits in the underlying [binary representation](#). No distinction is made between signalling and quiet NaNs.

$$\begin{aligned} fN &::= +fNmag \mid -fNmag \\ fNmag &::= \begin{array}{l} (1 + uM \cdot 2^{-M}) \cdot 2^e \quad (\text{if } -2^{E-1} + 2 \leq e \leq 2^{E-1} - 1) \\ (0 + uM \cdot 2^{-M}) \cdot 2^e \quad (\text{if } e = -2^{E-1} + 2) \\ \infty \\ \text{nan}(n) \quad (\text{if } 1 \leq n < 2^M) \end{array} \end{aligned}$$

where $M = \text{signif}(N)$ and $E = \text{expon}(N)$ with

$$\begin{array}{ll} \text{signif}(32) &= 23 & \text{expon}(32) &= 8 \\ \text{signif}(64) &= 52 & \text{expon}(64) &= 11 \end{array}$$

A *canonical NaN* is a floating-point value $\pm \text{nan}(\text{canon}_N)$ where canon_N is a payload whose most significant bit is 1 while all others are 0:

$$\text{canon}_N = 2^{\text{signif}(N)-1}$$

An *arithmetic NaN* is a floating-point value $\pm \text{nan}(n)$ with $n \geq \text{canon}_N$, such that the most significant bit is 1 while all others are arbitrary.

Note: In the abstract syntax, subnormals are distinguished by the leading 0 of the significand. The exponent of subnormals has the same value as the smallest possible exponent of a normal number. Only in the [binary representation](#) the exponent of a subnormal is encoded differently than the exponent of any normal number.

The notion of canonical NaN defined here is unrelated to the notion of canonical NaN that the [IEEE 754¹⁰](#) standard (Section 3.5.2) defines for decimal interchange formats.

Conventions

- The meta variable z ranges over floating-point values where clear from context.

2.2.4 Vectors

Numeric vectors are 128-bit values that are processed by vector instructions (also known as *SIMD* instructions, single instruction multiple data). They are represented in the abstract syntax using *i128*. The interpretation of lane types ([integer](#) or [floating-point](#) numbers) and lane sizes are determined by the specific instruction operating on them.

2.2.5 Names

Names are sequences of *characters*, which are *scalar values* as defined by [Unicode¹¹](#) (Section 2.4).

$$\begin{aligned} \text{name} &::= \text{char}^* \quad (\text{if } |\text{utf8}(\text{char}^*)| < 2^{32}) \\ \text{char} &::= \text{U+00} \mid \dots \mid \text{U+D7FF} \mid \text{U+E000} \mid \dots \mid \text{U+10FFFF} \end{aligned}$$

Due to the limitations of the [binary format](#), the length of a name is bounded by the length of its [UTF-8](#) encoding.

¹⁰ <https://ieeexplore.ieee.org/document/8766229>

¹¹ <https://www.unicode.org/versions/latest/>

Convention

- Characters (Unicode scalar values) are sometimes used interchangeably with natural numbers $n < 1114112$.

2.3 Types

Various entities in WebAssembly are classified by types. Types are checked during [validation](#), [instantiation](#), and possibly [execution](#).

2.3.1 Number Types

Number types classify numeric values.

$$\text{numtype} ::= \text{i32} \mid \text{i64} \mid \text{f32} \mid \text{f64}$$

The types [i32](#) and [i64](#) classify 32 and 64 bit integers, respectively. Integers are not inherently signed or unsigned, their interpretation is determined by individual operations.

The types [f32](#) and [f64](#) classify 32 and 64 bit floating-point data, respectively. They correspond to the respective binary floating-point representations, also known as *single* and *double* precision, as defined by the [IEEE 754](#)¹² standard (Section 3.3).

Number types are *transparent*, meaning that their bit patterns can be observed. Values of number type can be stored in [memories](#).

Conventions

- The notation $|t|$ denotes the *bit width* of a number type t . That is, $|\text{i32}| = |\text{f32}| = 32$ and $|\text{i64}| = |\text{f64}| = 64$.

2.3.2 Vector Types

Vector types classify vectors of [numeric](#) values processed by vector instructions (also known as *SIMD* instructions, single instruction multiple data).

$$\text{vectype} ::= \text{v128}$$

The type [v128](#) corresponds to a 128 bit vector of packed integer or floating-point data. The packed data can be interpreted as signed or unsigned integers, single or double precision floating-point values, or a single 128 bit type. The interpretation is determined by individual operations.

Vector types, like [number types](#) are *transparent*, meaning that their bit patterns can be observed. Values of vector type can be stored in [memories](#).

Conventions

- The notation $|t|$ for [bit width](#) extends to vector types as well, that is, $|\text{v128}| = 128$.

¹² <https://ieeexplore.ieee.org/document/8766229>

2.3.3 Heap Types

Heap types classify objects in the runtime store. There are three disjoint hierarchies of heap types:

- *function types* classify *functions*,
- *aggregate types* classify dynamically allocated *managed* data, such as *structures*, *arrays*, or *unboxed scalars*,
- *external types* classify *external* references possibly owned by the *embedder*.

The values from the latter two hierarchies are interconvertible by ways of the `extern.convert_any` and `any.convert_extern` instructions. That is, both type hierarchies are inhabited by an isomorphic set of values, but may have different, incompatible representations in practice.

```

absheaptype ::= func | nofunc
              | exn | noexn
              | extern | noextern
              | any | eq | i31 | struct | array | none
heaptype    ::= absheaptype | typeidx

```

A heap type is either *abstract* or *concrete*.

The abstract type `func` denotes the common supertype of all *function types*, regardless of their concrete definition. Dually, the type `nofunc` denotes the common subtype of all *function types*, regardless of their concrete definition. This type has no values.

The abstract type `exn` denotes the type of all *exception references*. Dually, the type `noexn` denotes the common subtype of all forms of exception references. This type has no values.

The abstract type `extern` denotes the common supertype of all external references received through the *embedder*. This type has no concrete subtypes. Dually, the type `noextern` denotes the common subtype of all forms of external references. This type has no values.

The abstract type `any` denotes the common supertype of all aggregate types, as well as possibly abstract values produced by *internalizing* an external reference of type `extern`. Dually, the type `none` denotes the common subtype of all forms of aggregate types. This type has no values.

The abstract type `eq` is a subtype of `any` that includes all types for which references can be compared, i.e., aggregate values and `i31`.

The abstract types `struct` and `array` denote the common supertypes of all *structure* and *array* aggregates, respectively.

The abstract type `i31` denotes *unboxed scalars*, that is, integers injected into references. Their observable value range is limited to 31 bits.

Note: An `i31` is not actually allocated in the store, but represented in a way that allows them to be mixed with actual references into the store without ambiguity. Engines need to perform some form of *pointer tagging* to achieve this, which is why 1 bit is reserved.

Although the types `none`, `nofunc`, `noexn`, and `noextern` are not inhabited by any values, they can be used to form the types of all null *references* in their respective hierarchy. For example, `(ref null nofunc)` is the generic type of a null reference compatible with all function reference types.

A concrete heap type consists of a *type index* and classifies an object of the respective *type* defined in a module.

The syntax of heap types is *extended* with additional forms for the purpose of specifying *validation* and *execution*.

2.3.4 Reference Types

Reference types classify *values* that are first-class references to objects in the runtime *store*.

$$reftype ::= \text{ref null}^? \text{ heaptype}$$

A reference type is characterised by the *heap type* it points to.

In addition, a reference type of the form *ref null ht* is *nullable*, meaning that it can either be a proper reference to *ht* or *null*. Other references are *non-null*.

Reference types are *opaque*, meaning that neither their size nor their bit pattern can be observed. Values of reference type can be stored in *tables*.

Conventions

- The reference type *anyref* is an abbreviation for *ref null any*.
- The reference type *eqref* is an abbreviation for *ref null eq*.
- The reference type *i31ref* is an abbreviation for *ref null i31*.
- The reference type *structref* is an abbreviation for *ref null struct*.
- The reference type *arrayref* is an abbreviation for *ref null array*.
- The reference type *funcref* is an abbreviation for *ref null func*.
- The reference type *exnref* is an abbreviation for *ref null exn*.
- The reference type *externref* is an abbreviation for *ref null extern*.
- The reference type *nullref* is an abbreviation for *ref null none*.
- The reference type *nullfuncref* is an abbreviation for *ref null nofunc*.
- The reference type *nullexnref* is an abbreviation for *ref null noexn*.
- The reference type *nullexternref* is an abbreviation for *ref null noextern*.

2.3.5 Value Types

Value types classify the individual values that WebAssembly code can compute with and the values that a variable accepts. They are either *number types*, *vector types*, or *reference types*.

$$valtype ::= \text{numtype} \mid \text{vectype} \mid \text{reftype}$$

The syntax of value types is *extended* with additional forms for the purpose of specifying *validation*.

Conventions

- The meta variable *t* ranges over value types or subclasses thereof where clear from context.

2.3.6 Result Types

Result types classify the result of [executing instructions](#) or [functions](#), which is a sequence of values, written with brackets.

$$resulttype ::= [vec(valtype)]$$

2.3.7 Function Types

Function types classify the signature of [functions](#), mapping a vector of parameters to a vector of results. They are also used to classify the inputs and outputs of [instructions](#).

$$functype ::= resulttype \rightarrow resulttype$$

2.3.8 Aggregate Types

Aggregate types describe compound objects consisting of multiple values. These are either *structures* or *arrays*, which both consist of a list of possibly mutable and possibly packed *fields*. Structures are heterogeneous, but require static indexing, while arrays need to be homogeneous, but allow dynamic indexing.

$$\begin{aligned} structtype &::= fieldtype^* \\ arraytype &::= fieldtype \\ fieldtype &::= mut\ storagetype \\ storagetype &::= valtype \mid packedtype \\ packedtype &::= i8 \mid i16 \end{aligned}$$

Conventions

- The notation $|t|$ for [bit width](#) extends to packed types as well, that is, $|i8| = 8$ and $|i16| = 16$.
- The auxiliary function [unpack](#) maps a storage type to the [value type](#) obtained when accessing a field:

$$\begin{aligned} \text{unpack}(valtype) &= valtype \\ \text{unpack}(packedtype) &= i32 \end{aligned}$$

2.3.9 Composite Types

Composite types are all types composed from simpler types, including [function types](#) and [aggregate types](#).

$$comptype ::= \text{func } functype \mid \text{struct } structtype \mid \text{array } arraytype$$

2.3.10 Recursive Types

Recursive types denote a group of mutually recursive [composite types](#), each of which can optionally declare a list of [type indices](#) of supertypes that it [matches](#). Each type can also be declared *final*, preventing further subtyping.

$$\begin{aligned} rectype &::= \text{rec } subtype^* \\ subtype &::= \text{sub final? } typeid^* \text{ } comptype \end{aligned}$$

In a [module](#), each member of a recursive type is assigned a separate [type index](#).

The syntax of sub types is [generalized](#) for the purpose of specifying [validation](#) and [execution](#).

2.3.11 Address Type

Address types are a subset of *number types* that classify the values that can be used as offsets into *memories* and *tables*.

$$\text{addrtype} ::= \text{i32} \mid \text{i64}$$

Conventions

The *minimum* of two address types is defined as the address type whose *bit width* is the minimum of the two.

$$\begin{aligned} \text{atmin}(at_1, at_2) &= at_1 && (\text{if } |at_1| \leq |at_2|) \\ \text{atmin}(at_1, at_2) &= at_2 && (\text{otherwise}) \end{aligned}$$

2.3.12 Limits

Limits classify the size range of resizeable storage associated with *memory types* and *table types*.

$$\text{limits} ::= \{\min \text{u64}, \max \text{u64}^?\}$$

If no maximum is given, the respective storage can grow to any size.

2.3.13 Memory Types

Memory types classify linear *memories* and their size range.

$$\text{memtype} ::= \text{addrtype limits}$$

The limits constrain the minimum and optionally the maximum size of a memory. The limits are given in units of *page size*.

2.3.14 Table Types

Table types classify *tables* over elements of *reference type* within a size range.

$$\text{tabletype} ::= \text{addrtype limits reftype}$$

Like memories, tables are constrained by limits for their minimum and optionally maximum size. The limits are given in numbers of entries.

2.3.15 Global Types

Global types classify *global* variables, which hold a value and can either be mutable or immutable.

$$\begin{aligned} \text{globaltype} &::= \text{mut valtype} \\ \text{mut} &::= \text{const} \mid \text{var} \end{aligned}$$

2.3.16 Tag Types

Tag types classify the signature of *tags* with a function type.

$$\text{tagtype} ::= \text{functype}$$

Currently tags are only used for categorizing exceptions. The parameters of *functype* define the list of values associated with the exception thrown with this tag. Furthermore, it is an invariant of the semantics that every *functype* in a *valid* tag type for an exception has an empty result type.

Note: Future versions of WebAssembly may have additional uses for tags, and may allow non-empty result types in the function types of tags.

2.3.17 External Types

External types classify *imports* and *external values* with their respective types.

$$\text{externtype} ::= \text{func deftype} \mid \text{table tabletype} \mid \text{mem memtype} \mid \text{global globaltype} \mid \text{tag tagtype}$$

Conventions

The following auxiliary notation is defined for sequences of external types. It filters out entries of a specific kind in an order-preserving fashion:

- $\text{funcs}(\text{externtype}^*) = [\text{deftype} \mid (\text{func deftype}) \in \text{externtype}^*]$
- $\text{tables}(\text{externtype}^*) = [\text{tabletype} \mid (\text{table tabletype}) \in \text{externtype}^*]$
- $\text{mems}(\text{externtype}^*) = [\text{memtype} \mid (\text{mem memtype}) \in \text{externtype}^*]$
- $\text{globals}(\text{externtype}^*) = [\text{globaltype} \mid (\text{global globaltype}) \in \text{externtype}^*]$
- $\text{tags}(\text{externtype}^*) = [\text{tagtype} \mid (\text{tag tagtype}) \in \text{externtype}^*]$

2.4 Instructions

WebAssembly code consists of sequences of *instructions*. Its computational model is based on a *stack machine* in that instructions manipulate values on an implicit *operand stack*, consuming (popping) argument values and producing or returning (pushing) result values.

In addition to dynamic operands from the stack, some instructions also have static *immediate* arguments, typically *indices* or type annotations, which are part of the instruction itself.

Some instructions are *structured* in that they bracket nested sequences of instructions.

The following sections group instructions into a number of different categories.

2.4.1 Numeric Instructions

Numeric instructions provide basic operations over numeric [values](#) of specific [type](#). These operations closely match respective operations available in hardware.

```

nn, mm ::= 32 | 64
sx      ::= u | s
instr   ::= inn.const unn | fnn.const fnn
           | inn.iunop | fnn.funop
           | inn.ibinop | fnn.fbinop
           | inn.itestop
           | inn.irelop | fnn.frelop
           | inn.extend8_s | inn.extend16_s | i64.extend32_s
           | i32.wrap_i64 | i64.extend_i32_sx | inn.trunc_fmm_sx
           | inn.trunc_sat_fmm_sx
           | f32.demote_f64 | f64.promote_f32 | fnn.convert_imm_sx
           | inn.reinterpret_fnn | fnn.reinterpret_inn
           | ...
iunop   ::= clz | ctz | popcnt
ibinop  ::= add | sub | mul | div_sx | rem_sx
           | and | or | xor | shl | shr_sx | rotl | rotr
funop   ::= abs | neg | sqrt | ceil | floor | trunc | nearest
fbinop  ::= add | sub | mul | div | min | max | copysign
itestop ::= eqz
irelop  ::= eq | ne | lt_sx | gt_sx | le_sx | ge_sx
frelop  ::= eq | ne | lt | gt | le | ge

```

Numeric instructions are divided by [number type](#). For each type, several subcategories can be distinguished:

- *Constants*: return a static constant.
- *Unary Operations*: consume one operand and produce one result of the respective type.
- *Binary Operations*: consume two operands and produce one result of the respective type.
- *Tests*: consume one operand of the respective type and produce a Boolean integer result.
- *Comparisons*: consume two operands of the respective type and produce a Boolean integer result.
- *Conversions*: consume a value of one type and produce a result of another (the source type of the conversion is the one after the “_”).

Some integer instructions come in two flavors, where a signedness annotation *sx* distinguishes whether the operands are to be [interpreted](#) as [unsigned](#) or [signed](#) integers. For the other integer instructions, the use of two’s complement for the signed interpretation means that they behave the same regardless of signedness.

Conventions

Occasionally, it is convenient to group operators together according to the following grammar shorthands:

```

unop    ::= iunop | funop | extendN_s
binop   ::= ibinop | fbinop
testop  ::= itestop
relop   ::= irelop | frelop
cvtop   ::= wrap | extend | trunc | trunc_sat | convert | demote | promote | reinterpret

```

2.4.2 Vector Instructions

Vector instructions (also known as *SIMD* instructions, *single instruction multiple data*) provide basic operations over values of vector type.

```

ishape ::= i8x16 | i16x8 | i32x4 | i64x2
fshape ::= f32x4 | f64x2
shape  ::= ishape | fshape
half   ::= low | high
laneidx ::= u8

```

```

instr ::= ...
| v128.const i128
| v128.vvunop
| v128.vvbinop
| v128.vvternop
| v128.vvtestop
| i8x16.shuffle laneidx16
| i8x16.swizzle
| shape.splat
| i8x16.extract_lane_sx laneidx | i16x8.extract_lane_sx laneidx
| i32x4.extract_lane laneidx | i64x2.extract_lane laneidx
| fshape.extract_lane laneidx
| shape.replace_lane laneidx
| i8x16.virelop | i16x8.virelop | i32x4.virelop
| i64x2.eq | i64x2.ne | i64x2.lt_s | i64x2.gt_s | i64x2.le_s | i64x2.ge_s
| fshape.vfrelop
| ishape.viunop | i8x16.popcnt
| i16x8.q15mulr_sat_s
| i32x4.dot_i16x8_s
| fshape.vfunop
| ishape.vitestop
| ishape.bitmask
| i8x16.narrow_i16x8_sx | i16x8.narrow_i32x4_sx
| i16x8.extend_half_i8x16_sx | i32x4.extend_half_i16x8_sx
| i64x2.extend_half_i32x4_sx
| ishape.vishiftop
| ishape.vibinop
| i8x16.viminmaxop | i16x8.viminmaxop | i32x4.viminmaxop
| i8x16.visatbinop | i16x8.visatbinop
| i16x8.mul | i32x4.mul | i64x2.mul
| i8x16.avgr_u | i16x8.avgr_u
| i16x8.extmul_half_i8x16_sx | i32x4.extmul_half_i16x8_sx | i64x2.extmul_half_i32x4_sx
| i16x8.extadd_pairwise_i8x16_sx | i32x4.extadd_pairwise_i16x8_sx
| fshape.vfbinoop
| i32x4.trunc_sat_f32x4_sx | i32x4.trunc_sat_f64x2_sx_zero
| f32x4.convert_i32x4_sx | f32x4.demote_f64x2_zero
| f64x2.convert_low_i32x4_sx | f64x2.promote_low_f32x4
| i8x16.relaxed_swizzle
| i16x8.relaxed_q15mulr_s
| i32x4.relaxed_trunc_f32x4_sx
| i16x8.relaxed_dot_i8x16_i7x16_s
| i32x4.relaxed_dot_i8x16_i7x16_add_s
| ishape.relaxed_laneselect
| fshape.rvfternop
| fshape.rvfbinoop
| ...

```

<i>vvunop</i>	::=	not
<i>vbinop</i>	::=	and andnot or xor
<i>vternop</i>	::=	bitselect
<i>vtestop</i>	::=	any_true
<i>vitestop</i>	::=	all_true
<i>virelop</i>	::=	eq ne lt_ <i>sx</i> gt_ <i>sx</i> le_ <i>sx</i> ge_ <i>sx</i>
<i>vfrellop</i>	::=	eq ne lt gt le ge
<i>viunop</i>	::=	abs neg
<i>vbinop</i>	::=	add sub
<i>vminmaxop</i>	::=	min_ <i>sx</i> max_ <i>sx</i>
<i>visatbinop</i>	::=	add_sat_ <i>sx</i> sub_sat_ <i>sx</i>
<i>vshiftop</i>	::=	shl shr_ <i>sx</i>
<i>vfunop</i>	::=	abs neg sqrt ceil floor trunc nearest
<i>vfbino</i>	::=	add sub mul div min max pmin pmax
<i>rufbino</i>	::=	relaxed_min relaxed_max
<i>rufternop</i>	::=	relaxed_madd relaxed_nmadd

Vector instructions have a naming convention involving a prefix that determines how their operands will be interpreted. This prefix describes the *shape* of the operand, written *txN*, and consisting of a packed [numeric type](#) *t* and the number of *lanes* *N* of that type. Operations are performed point-wise on the values of each lane.

Note: For example, the shape *i32x4* interprets the operand as four *i32* values, packed into an *i128*. The bit width of the numeric type *t* times *N* always is 128.

Instructions prefixed with *v128* do not involve a specific interpretation, and treat the *v128* as an *i128* value or a vector of 128 individual bits.

Vector instructions can be grouped into several subcategories:

- *Constants*: return a static constant.
- *Unary Operations*: consume one *v128* operand and produce one *v128* result.
- *Binary Operations*: consume two *v128* operands and produce one *v128* result.
- *Ternary Operations*: consume three *v128* operands and produce one *v128* result.
- *Tests*: consume one *v128* operand and produce a Boolean integer result.
- *Shifts*: consume a *v128* operand and a *i32* operand, producing one *v128* result.
- *Splats*: consume a value of numeric type and produce a *v128* result of a specified shape.
- *Extract lanes*: consume a *v128* operand and return the numeric value in a given lane.
- *Replace lanes*: consume a *v128* operand and a numeric value for a given lane, and produce a *v128* result.

Some vector instructions have a signedness annotation *sx* which distinguishes whether the elements in the operands are to be [interpreted](#) as [unsigned](#) or [signed](#) integers. For the other vector instructions, the use of two's complement for the signed interpretation means that they behave the same regardless of signedness.

Conventions

Occasionally, it is convenient to group operators together according to the following grammar shorthands:

```

vunop    ::= viunop | vfunop | popcnt
vinop    ::= vibinop | vfbbinop
            | viminmaxop | visatbinop
            | rvfbbinop
            | mul | avgr_u | q15mulr_sat_s
            | relaxed_q15mulr_s_s
vternop  ::= vtternop | rvftrternop
vtstop   ::= vitestop
vreltop  ::= virelop | vfreltop
vcvtop   ::= extend | trunc_sat | convert | demote | promote | relaxed_trunc

```

2.4.3 Reference Instructions

Instructions in this group are concerned with accessing [references](#).

```

instr    ::= ...
            | ref.null heapttype
            | ref.func funcidx
            | ref.is_null
            | ref.as_non_null
            | ref.eq
            | ref.test reftype
            | ref.cast reftype

```

The [ref.null](#) and [ref.func](#) instructions produce a [null](#) value or a reference to a given function, respectively.

The instruction [ref.is_null](#) checks for null, while [ref.as_non_null](#) converts a [nullable](#) to a non-null one, and [traps](#) if it encounters null.

The [ref.eq](#) compares two references.

The instructions [ref.test](#) and [ref.cast](#) test the [dynamic type](#) of a reference operand. The former merely returns the result of the test, while the latter performs a downcast and [traps](#) if the operand's type does not match.

Note: The [br_on_cast](#) and [br_on_cast_fail](#) instructions provides versions of the latter that branch depending on the success of the downcast instead of trapping.

2.4.4 Aggregate Instructions

Instructions in this group are concerned with creating and accessing references to aggregate types.

```

instr ::= ...
        | struct.new typeidx
        | struct.new_default typeidx
        | struct.get typeidx fieldidx
        | struct.get_sx typeidx fieldidx
        | struct.set typeidx fieldidx
        | array.new typeidx
        | array.new_fixed typeidx u32
        | array.new_default typeidx
        | array.new_data typeidx dataidx
        | array.new_elem typeidx elemidx
        | array.get typeidx
        | array.get_sx typeidx
        | array.set typeidx
        | array.len
        | array.fill typeidx
        | array.copy typeidx typeidx
        | array.init_data typeidx dataidx
        | array.init_elem typeidx elemidx
        | ref.i31
        | i31.get_sx
        | any.convert_extrn
        | extrn.convert_any

```

The instructions `struct.new` and `struct.new_default` allocate a new `structure`, initializing them either with operands or with default values. The remaining instructions on structs access individual fields, allowing for different sign extension modes in the case of `packed` storage types.

Similarly, `arrays` can be allocated either with an explicit initialization operand or a default value. Furthermore, `array.new_fixed` allocates an array with statically fixed size, and `array.new_data` and `array.new_elem` allocate an array and initialize it from a `data` or `element` segment, respectively. `array.get`, `array.get_s`, `array.get_u`, and `array.set` access individual slots, again allowing for different sign extension modes in the case of a `packed` storage type. `array.len` produces the length of an array. `array.fill` fills a specified slice of an array with a given value and `array.copy`, `array.init_data`, and `array.init_elem` copy elements to a specified slice of an array from a given array, data segment, or element segment, respectively.

The instructions `ref.i31` and `i31.get_sx` convert between type `i31` and an unboxed `scalar`.

The instructions `any.convert_extrn` and `extrn.convert_any` allow lossless conversion between references represented as type (`ref null extrn`).

2.4.5 Parametric Instructions

Instructions in this group can operate on operands of any `value` type.

```

instr ::= ...
        | drop
        | select (valtype*)?

```

The `drop` instruction simply throws away a single operand.

The `select` instruction selects one of its first two operands based on whether its third operand is zero or not. It may include a `value` type determining the type of these operands. If missing, the operands must be of `numeric` type.

Note: In future versions of WebAssembly, the type annotation on `select` may allow for more than a single value being selected at the same time.

2.4.6 Variable Instructions

Variable instructions are concerned with access to [local](#) or [global](#) variables.

```
instr ::= ...  
        | local.get localidx  
        | local.set localidx  
        | local.tee localidx  
        | global.get globalidx  
        | global.set globalidx
```

These instructions get or set the values of variables, respectively. The [local.tee](#) instruction is like [local.set](#) but also returns its argument.

2.4.7 Table Instructions

Instructions in this group are concerned with tables [table](#).

```
instr ::= ...  
        | table.get tableidx  
        | table.set tableidx  
        | table.size tableidx  
        | table.grow tableidx  
        | table.fill tableidx  
        | table.copy tableidx tableidx  
        | table.init tableidx elemidx  
        | elem.drop elemidx
```

The [table.get](#) and [table.set](#) instructions load or store an element in a table, respectively.

The [table.size](#) instruction returns the current size of a table. The [table.grow](#) instruction grows table by a given delta and returns the previous size, or -1 if enough space cannot be allocated. It also takes an initialization value for the newly allocated entries.

The [table.fill](#) instruction sets all entries in a range to a given value.

The [table.copy](#) instruction copies elements from a source table region to a possibly overlapping destination region; the first index denotes the destination. The [table.init](#) instruction copies elements from a [passive element segment](#) into a table. The [elem.drop](#) instruction prevents further use of a passive element segment. This instruction is intended to be used as an optimization hint. After an element segment is dropped its elements can no longer be retrieved, so the memory used by this segment may be freed.

An additional instruction that accesses a table is the [control instruction](#) [call_indirect](#).

2.4.8 Memory Instructions

Instructions in this group are concerned with linear [memory](#).

```

memarg ::= {offset u64, align u32}
ww      ::= 8 | 16 | 32 | 64
instr    ::= ...
            | inn.load memidx memarg | fnn.load memidx memarg
            | v128.load memidx memarg
            | inn.store memidx memarg | fnn.store memidx memarg
            | v128.store memidx memarg
            | inn.load8_sx memidx memarg | inn.load16_sx memidx memarg | i64.load32_sx memidx memarg
            | v128.load8x8_sx memidx memarg | v128.load16x4_sx memidx memarg | v128.load32x2_sx memidx memarg
            | v128.load32_zero memidx memarg | v128.load64_zero memidx memarg
            | v128.loadww_splat memidx memarg
            | v128.loadww_lane memidx memarg laneidx | inn.store8 memidx memarg | inn.store16 memidx memarg
            | v128.storeww_lane memidx memarg laneidx
            | memory.size memidx
            | memory.grow memidx
            | memory.fill memidx
            | memory.copy memidx memidx
            | memory.init memidx dataidx
            | data.drop dataidx

```

Memory is accessed with [load](#) and [store](#) instructions for the different [number types](#) and [vector types](#). They all take a [memory index](#) and a [memory immediate](#) *memarg* that contains an address *offset* and the expected *alignment* (expressed as the exponent of a power of 2).

Integer loads and stores can optionally specify a *storage size* that is smaller than the [bit width](#) of the respective value type. In the case of loads, a sign extension mode *sx* is then required to select appropriate behavior.

Vector loads can specify a shape that is half the [bit width](#) of *v128*. Each lane is half its usual size, and the sign extension mode *sx* then specifies how the smaller lane is extended to the larger lane. Alternatively, vector loads can perform a *splat*, such that only a single lane of the specified storage size is loaded, and the result is duplicated to all lanes.

The static address offset is added to the dynamic address operand, yielding a 33-bit or 65-bit *effective address* that is the zero-based index at which the memory is accessed. All values are read and written in [little endian](#)¹³ byte order. A [trap](#) results if any of the accessed memory bytes lies outside the address range implied by the memory's current size.

The [memory.size](#) instruction returns the current size of a memory. The [memory.grow](#) instruction grows a memory by a given delta and returns the previous size, or -1 if enough memory cannot be allocated. Both instructions operate in units of [page size](#). The [memory.fill](#) instruction sets all values in a region of a memory to a given byte. The [memory.copy](#) instruction copies data from a source memory region to a possibly overlapping destination region in another or the same memory; the first index denotes the destination. The [memory.init](#) instruction copies data from a [passive data segment](#) into a memory. The [data.drop](#) instruction prevents further use of a passive data segment. This instruction is intended to be used as an optimization hint. After a data segment is dropped its data can no longer be retrieved, so the memory used by this segment may be freed.

¹³ <https://en.wikipedia.org/wiki/Endianness#Little-endian>

2.4.9 Control Instructions

Instructions in this group affect the flow of control.

```

blocktype ::= typeidx | valtype?
instr      ::= ...
              | nop
              | unreachable
              | block blocktype instr* end
              | loop blocktype instr* end
              | if blocktype instr* else instr* end
              | br labelidx
              | br_if labelidx
              | br_table vec(labelidx) labelidx
              | br_on_null labelidx
              | br_on_non_null labelidx
              | br_on_cast labelidx reftype reftype
              | br_on_cast_fail labelidx reftype reftype
              | return
              | call funcidx
              | call_ref typeidx
              | call_indirect tableidx typeidx
              | return_call funcidx
              | return_call_ref typeidx
              | return_call_indirect tableidx typeidx
              | throw tagidx
              | throw_ref
              | try_table blocktype catch* instr* end
catch      ::= catch tagidx labelidx
              | catch_ref tagidx labelidx
              | catch_all labelidx
              | catch_all_ref labelidx

```

The `nop` instruction does nothing.

The `unreachable` instruction causes an unconditional `trap`.

The `block`, `loop`, `if`, and `try_table` instructions are *structured* instructions. They bracket nested sequences of instructions, called *blocks*, separated by the `else` pseudo-instruction, and terminated with an `end` pseudo-instruction. As the grammar prescribes, they must be well-nested.

The instructions `throw`, `throw_ref`, and `try_table` are concerned with *exceptions*. The `try_table` instruction installs an exception *handler* that handles exceptions as specified by its catch clauses.. The `throw` and `throw_ref` instructions raise and reraise an exception, respectively, and transfers control to the innermost enclosing exception handler that has a matching catch clause.

A structured instruction can consume *input* and produce *output* on the operand stack according to its annotated *block type*. It is given either as a *type index* that refers to a suitable *function type* reinterpreted as an *instruction type*, or as an optional *value type* inline, which is a shorthand for the instruction type $[] \rightarrow [\textit{valtype}]$.

Each structured control instruction introduces an implicit *label*. Labels are targets for branch instructions that reference them with *label indices*. Unlike with other *index spaces*, indexing of labels is relative by nesting depth, that is, label 0 refers to the innermost structured control instruction enclosing the referring branch instruction, while increasing indices refer to those farther out. Consequently, labels can only be referenced from *within* the associated structured control instruction. This also implies that branches can only be directed outwards, “breaking” from the block of the control construct they target. The exact effect depends on that control construct. In case of `block` or `if` it is a *forward jump*, resuming execution after the matching `end`. In case of `loop` it is a *backward jump* to the beginning of the loop.

Note: This enforces *structured control flow*. Intuitively, a branch targeting a `block` or `if` behaves like a break

statement in most C-like languages, while a branch targeting a `loop` behaves like a `continue` statement.

Branch instructions come in several flavors: `br` performs an unconditional branch, `br_if` performs a conditional branch, and `br_table` performs an indirect branch through an operand indexing into the label vector that is an immediate to the instruction, or to a default target if the operand is out of bounds. The `br_on_null` and `br_on_non_null` instructions check whether a reference operand is `null` and branch if that is the case or not the case, respectively. Similarly, `br_on_cast` and `br_on_cast_fail` attempt a downcast on a reference operand and branch if that succeeds, or fails, respectively.

The `return` instruction is a shortcut for an unconditional branch to the outermost block, which implicitly is the body of the current function. Taking a branch *unwinds* the operand stack up to the height where the targeted structured control instruction was entered. However, branches may additionally consume operands themselves, which they push back on the operand stack after unwinding. Forward branches require operands according to the output of the targeted block's type, i.e., represent the values produced by the terminated block. Backward branches require operands according to the input of the targeted block's type, i.e., represent the values consumed by the restarted block.

The `call` instruction invokes another `function`, consuming the necessary arguments from the stack and returning the result values of the call. The `call_ref` instruction invokes a function indirectly through a `function reference` operand. The `call_indirect` instruction calls a function indirectly through an operand indexing into a `table` that is denoted by a `table index` and must contain `function references`. Since it may contain functions of heterogeneous type, the callee is dynamically checked against the `function type` indexed by the instruction's second immediate, and the call is aborted with a `trap` if it does not match.

The `return_call`, `return_call_ref`, and `return_call_indirect` instructions are *tail-call* variants of the previous ones. That is, they first return from the current function before actually performing the respective call. It is guaranteed that no sequence of nested calls using only these instructions can cause resource exhaustion due to hitting an *implementation's limit* on the number of active calls.

2.4.10 Expressions

`Function` bodies, initialization values for `globals`, elements and offsets of `element` segments, and offsets of `data` segments are given as expressions, which are sequences of `instructions` terminated by an `end` marker.

$$expr ::= instr^* end$$

In some places, validation *restricts* expressions to be *constant*, which limits the set of allowable instructions.

2.5 Modules

WebAssembly programs are organized into *modules*, which are the unit of deployment, loading, and compilation. A module collects definitions for `types`, `functions`, `tables`, `memories`, `tags`, and `globals`. In addition, it can declare `imports` and `exports` and provide initialization in the form of `data` and `element` segments, or a `start function`.

$$module ::= \{ \begin{array}{l} \text{types } vec(rectype), \\ \text{funcs } vec(func), \\ \text{tables } vec(table), \\ \text{mems } vec(mem), \\ \text{globals } vec(global), \\ \text{tags } vec(tag), \\ \text{elems } vec(elem), \\ \text{datas } vec(data), \\ \text{start } start^?, \\ \text{imports } vec(import), \\ \text{exports } vec(export) \end{array} \}$$

Each of the vectors – and thus the entire module – may be empty.

2.5.1 Indices

Definitions are referenced with zero-based *indices*. Each class of definition has its own *index space*, as distinguished by the following classes.

<i>typeid</i>	::=	<i>u32</i>
<i>funcidx</i>	::=	<i>u32</i>
<i>tableidx</i>	::=	<i>u32</i>
<i>memidx</i>	::=	<i>u32</i>
<i>globalidx</i>	::=	<i>u32</i>
<i>tagidx</i>	::=	<i>u32</i>
<i>elemidx</i>	::=	<i>u32</i>
<i>dataidx</i>	::=	<i>u32</i>
<i>localidx</i>	::=	<i>u32</i>
<i>labelidx</i>	::=	<i>u32</i>
<i>fieldidx</i>	::=	<i>u32</i>

The index space for **functions**, **tables**, **memories**, **globals**, and **tags** includes respective **imports** declared in the same module. The indices of these imports precede the indices of other definitions in the same index space.

Element indices reference **element segments** and data indices reference **data segments**.

The index space for **locals** is only accessible inside a **function** and includes the parameters of that function, which precede the local variables.

Label indices reference **structured control instructions** inside an instruction sequence.

Each **aggregate type** provides an index space for its **fields**.

Conventions

- The meta variable *l* ranges over label indices.
- The meta variables *x*, *y* range over indices in any of the other index spaces.
- The notation $\text{idx}(A)$ denotes the set of indices from index space *idx* occurring free in *A*. Sometimes this set is reinterpreted as the **vector** of its elements.

Note: For example, if *instr** is `(data.drop x)(memory.init y)`, then $\text{dataidx}(\text{instr}^*) = \{x, y\}$, or equivalently, the vector *x y*.

2.5.2 Types

The **types** component of a module defines a vector of **recursive types**, each of consisting of a list of **sub types** referenced by individual **type indices**. All **function** or **aggregate** types used in a module must be defined in this component.

2.5.3 Functions

The **funcs** component of a module defines a vector of **functions** with the following structure:

<i>func</i>	::=	{ type <i>typeid</i> , locals <i>vec(local)</i> , body <i>expr</i> }
<i>local</i>	::=	{ type <i>valtype</i> }

The **type** of a function declares its signature by reference to a **type** defined in the module. The parameters of the function are referenced through 0-based **local indices** in the function's body; they are mutable.

The **locals** declare a vector of mutable local variables and their types. These variables are referenced through **local indices** in the function's body. The index of the first local is the smallest index not referencing a parameter.

The **body** is an **instruction** sequence that upon termination must produce a stack matching the function type's **result type**.

Functions are referenced through **function indices**, starting with the smallest index not referencing a function **import**.

2.5.4 Tables

The **tables** component of a module defines a vector of *tables* described by their **table type**:

$$table ::= \{type\ tabletype, init\ expr\}$$

A table is an array of opaque values of a particular **reference type**. Moreover, each table slot is initialized with the **init** value given by a **constant** initializer **expression**. Tables can further be initialized through **element segments**.

The **min** size in the **limits** of the table type specifies the initial size of that table, while its **max**, if present, restricts the size to which it can grow later.

Tables are referenced through **table indices**, starting with the smallest index not referencing a table **import**. Most constructs implicitly reference table index 0.

2.5.5 Memories

The **mems** component of a module defines a vector of *linear memories* (or *memories* for short) as described by their **memory type**:

$$mem ::= \{type\ memtype\}$$

A memory is a vector of raw uninterpreted bytes. The **min** size in the **limits** of the memory type specifies the initial size of that memory, while its **max**, if present, restricts the size to which it can grow later. Both are in units of **page size**.

Memories can be initialized through **data segments**.

Memories are referenced through **memory indices**, starting with the smallest index not referencing a memory **import**. Most constructs implicitly reference memory index 0.

2.5.6 Globals

The **globals** component of a module defines a vector of *global variables* (or *globals* for short):

$$global ::= \{type\ globaltype, init\ expr\}$$

Each global stores a single value of the given **global type**. Its **type** also specifies whether a global is immutable or mutable. Moreover, each global is initialized with an **init** value given by a **constant** initializer **expression**.

Globals are referenced through **global indices**, starting with the smallest index not referencing a global **import**.

2.5.7 Tags

The **tags** component of a module defines a vector of *tags* with the following structure.

$$tag ::= \{type\ typeid\}$$

The result type of the function signature with type index *typeid* must be empty.

Tags are referenced through **tag indices**, starting with the smallest index not referencing a tag **import**.

2.5.8 Element Segments

The initial contents of a table is uninitialized. *Element segments* can be used to initialize a subrange of a table from a static [vector](#) of elements.

The [elems](#) component of a module defines a vector of element segments. Each element segment defines a [reference type](#) and a corresponding list of [constant element expressions](#).

Element segments have a mode that identifies them as either *passive*, *active*, or *declarative*. A passive element segment's elements can be copied to a table using the [table.init](#) instruction. An active element segment copies its elements into a table during [instantiation](#), as specified by a [table index](#) and a [constant expression](#) defining an offset into that table. A declarative element segment is not available at runtime but merely serves to forward-declare references that are formed in code with instructions like [ref.func](#).

```
elem      ::= {type reftype, init vec(expr), mode elemmode}
elemmode  ::= passive
           | active {table tableidx, offset expr}
           | declarative
```

The [offset](#) is given by a [constant expression](#).

Element segments are referenced through [element indices](#).

2.5.9 Data Segments

The initial contents of a [memory](#) are zero bytes. *Data segments* can be used to initialize a range of memory from a static [vector](#) of bytes.

The [datas](#) component of a module defines a vector of data segments.

Like element segments, data segments have a mode that identifies them as either *passive* or *active*. A passive data segment's contents can be copied into a memory using the [memory.init](#) instruction. An active data segment copies its contents into a memory during [instantiation](#), as specified by a [memory index](#) and a [constant expression](#) defining an offset into that memory.

```
data      ::= {init vec(byte), mode datamode}
datamode  ::= passive
           | active {memory memidx, offset expr}
```

Data segments are referenced through [data indices](#).

Note: In the current version of WebAssembly, at most one memory is allowed in a module. Consequently, the only valid [memidx](#) is 0.

2.5.10 Start Function

The [start](#) component of a module declares the [function index](#) of a *start function* that is automatically invoked when the module is [instantiated](#), after [tables](#) and [memories](#) have been initialized.

```
start ::= {func funcidx}
```

Note: The start function is intended for initializing the state of a module. The module and its exports are not accessible externally before this initialization has completed.

2.5.11 Exports

The **exports** component of a module defines a set of *exports* that become accessible to the host environment once the module has been **instantiated**.

$$\begin{aligned} \text{export} & ::= \{\text{name } \text{name}, \text{desc } \text{exportdesc}\} \\ \text{exportdesc} & ::= \text{func } \text{funcidx} \\ & \quad | \text{table } \text{tableidx} \\ & \quad | \text{mem } \text{memidx} \\ & \quad | \text{global } \text{globalidx} \\ & \quad | \text{tag } \text{tagidx} \end{aligned}$$

Each export is labeled by a unique **name**. Exportable definitions are **functions**, **tables**, **memories**, **globals**, and **tags**, which are referenced through a respective descriptor.

Conventions

The following auxiliary notation is defined for sequences of exports, filtering out indices of a specific kind in an order-preserving fashion:

- $\text{funcs}(\text{export}^*) = [\text{funcidx} \mid \text{func } \text{funcidx} \in (\text{export}.\text{desc})^*]$
- $\text{tables}(\text{export}^*) = [\text{tableidx} \mid \text{table } \text{tableidx} \in (\text{export}.\text{desc})^*]$
- $\text{mems}(\text{export}^*) = [\text{memidx} \mid \text{mem } \text{memidx} \in (\text{export}.\text{desc})^*]$
- $\text{globals}(\text{export}^*) = [\text{globalidx} \mid \text{global } \text{globalidx} \in (\text{export}.\text{desc})^*]$
- $\text{tags}(\text{export}^*) = [\text{tagidx} \mid \text{tag } \text{tagidx} \in (\text{export}.\text{desc})^*]$

2.5.12 Imports

The **imports** component of a module defines a set of *imports* that are required for **instantiation**.

$$\begin{aligned} \text{import} & ::= \{\text{module } \text{name}, \text{name } \text{name}, \text{desc } \text{importdesc}\} \\ \text{importdesc} & ::= \text{func } \text{typeidx} \\ & \quad | \text{table } \text{tabletype} \\ & \quad | \text{mem } \text{memtype} \\ & \quad | \text{global } \text{globaltype} \\ & \quad | \text{tag } \text{typeidx} \end{aligned}$$

Each import is labeled by a two-level **name** space, consisting of a **module** name and a **name** for an entity within that module. Importable definitions are **functions**, **tables**, **memories**, **globals**, and **tags**. Each import is specified by a descriptor with a respective type that a definition provided during instantiation is required to match.

Every import defines an index in the respective **index space**. In each index space, the indices of imports go before the first index of any definition contained in the module itself.

Note: Unlike export names, import names are not necessarily unique. It is possible to import the same **module/name** pair multiple times; such imports may even have different type descriptions, including different kinds of entities. A module with such imports can still be instantiated depending on the specifics of how an **embedder** allows resolving and supplying imports. However, embedders are not required to support such overloading, and a WebAssembly module itself cannot implement an overloaded name.

3.1 Conventions

Validation checks that a WebAssembly module is well-formed. Only valid modules can be *instantiated*.

Validity is defined by a *type system* over the *abstract syntax* of a *module* and its contents. For each piece of abstract syntax, there is a typing rule that specifies the constraints that apply to it. All rules are given in two *equivalent* forms:

1. In *prose*, describing the meaning in intuitive form.
2. In *formal notation*, describing the rule in mathematical form.¹⁴

Note: The prose and formal rules are equivalent, so that understanding of the formal notation is *not* required to read this specification. The formalism offers a more concise description in notation that is used widely in programming languages semantics and is readily amenable to mathematical proof.

In both cases, the rules are formulated in a *declarative* manner. That is, they only formulate the constraints, they do not define an algorithm. The skeleton of a sound and complete algorithm for type-checking instruction sequences according to this specification is provided in the *appendix*.

3.1.1 Types

To define the semantics, the definition of some sorts of types is extended to include additional forms. By virtue of not being representable in either the *binary format* or the *text format*, these forms cannot be used in a program; they only occur during *validation* or *execution*.

$$\begin{array}{ll}
 \textit{valtype} & ::= \dots \mid \textit{bot} \\
 \textit{absheaptypes} & ::= \dots \mid \textit{bot} \\
 \textit{heaptypes} & ::= \dots \mid \textit{deftypes} \mid \textit{rec } i \\
 \textit{subtypes} & ::= \textit{sub final}^? \textit{heaptypes}^* \textit{comptypes}
 \end{array}$$

¹⁴ The semantics is derived from the following article: Andreas Haas, Andreas Rossberg, Derek Schuff, Ben Titzer, Dan Gohman, Luke Wagner, Alon Zakai, JF Bastien, Michael Holman. *Bringing the Web up to Speed with WebAssembly*¹⁵. Proceedings of the 38th ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI 2017). ACM 2017.

¹⁵ <https://dl.acm.org/citation.cfm?doid=3062341.3062363>

The unique *value type* `bot` is a *bottom type* that *matches* all value types. Similarly, `bot` is also used as a bottom type of all *heap types*.

Note: No validation rule uses bottom types explicitly, but various rules can pick any value or heap type, including bottom. This ensures the existence of *principal types*, and thus a *validation algorithm* without back tracking.

A *concrete heap type* can consist of a *defined type* directly. this occurs as the result of *substituting* a *type index* with its definition.

A concrete heap type may also be a *recursive type index*. Such an index refers to the *i*-th component of a surrounding *recursive type*. It occurs as the result of *rolling up* the definition of a *recursive type*.

Finally, the representation of supertypes in a *sub type* is generalized from mere *type indices* to *heap types*. They occur as *defined types* or *recursive type indices* after *substituting* type indices or *rolling up recursive types*.

Note: It is an invariant of the semantics that sub types occur only in one of two forms: either as “syntactic” types as in a source module, where all supertypes are type indices, or as “semantic” types, where all supertypes are resolved to either defined types or recursive type indices.

A type of any form is *closed* when it does not contain a heap type that is a *type index* or a recursive type index without a surrounding *recursive type*, i.e., all *type indices* have been *substituted* with their *defined type* and all free recursive type indices have been *unrolled*.

Note: Recursive type indices are internal to a recursive type. They are distinguished from regular type indices and represented such that two closed types are syntactically equal if and only if they have the same recursive structure.

Convention

- The *difference* $rt_1 \setminus rt_2$ between two *reference types* is defined as follows:

$$\begin{aligned} (\text{ref null}_1^? ht_1) \setminus (\text{ref null } ht_2) &= (\text{ref } ht_1) \\ (\text{ref null}_1^? ht_1) \setminus (\text{ref } ht_2) &= (\text{ref null}_1^? ht_1) \end{aligned}$$

Note: This definition computes an approximation of the reference type that is inhabited by all values from rt_1 except those from rt_2 . Since the type system does not have general union types, the definition only affects the presence of null and cannot express the absence of other values.

3.1.2 Defined Types

Defined types denote the individual types defined in a *module*. Each such type is represented as a projection from the *recursive type* group it originates from, indexed by its position in that group.

$$deftype ::= rectype.i$$

Defined types do not occur in the *binary* or *text* format, but are formed by *rolling up* the *recursive types* defined in a module.

It is hence an invariant of the semantics that all *recursive types* occurring in defined types are *rolled up*.

Conventions

- $t[x^* := dt^*]$ denotes the parallel *substitution* of *type indices* x^* with *defined types* dt^* in type t , provided $|x^*| = |dt^*|$.
- $t[(\text{rec } i)^* := dt^*]$ denotes the parallel *substitution* of *recursive type indices* $(\text{rec } i)^*$ with *defined types* dt^* in type t , provided $|(\text{rec } i)^*| = |dt^*|$.
- $t[:= dt^*]$ is shorthand for the substitution $t[x^* := dt^*]$, where $x^* = 0 \dots (|dt^*| - 1)$.

3.1.3 Rolling and Unrolling

In order to allow comparing *recursive types* for *equivalence*, their representation is changed such that all *type indices* internal to the same recursive type are replaced by *recursive type indices*.

Note: This representation is independent of the type index space, so that it is meaningful across module boundaries. Moreover, this representation ensures that types with equivalent recursive structure are also syntactically equal, hence allowing a simple equality check on (closed) types. It gives rise to an *iso-recursive* interpretation of types.

The representation change is performed by two auxiliary operations on the syntax of *recursive types*:

- *Rolling up* a recursive type *substitutes* its internal *type indices* with corresponding *recursive type indices*.
- *Unrolling* a recursive type *substitutes* its *recursive type indices* with the corresponding *defined types*.

These operations are extended to *defined types* and defined as follows:

$$\begin{aligned}
 \text{roll}_x(\text{rec subtype}^*) &= \text{rec } (\text{subtype}[(x + i)^* := (\text{rec } i)^*])^* && (\text{if } i^* = 0 \dots (|\text{subtype}^*| - 1)) \\
 \text{unroll}(\text{rec subtype}^*) &= \text{rec } (\text{subtype}[(\text{rec } i)^* := ((\text{rec subtype}^*).i)^*])^* && (\text{if } i^* = 0 \dots (|\text{subtype}^*| - 1)) \\
 \text{roll}_x(\text{rectype}) &= ((\text{rec subtype}^*).i)^* && (\text{if } i^* = 0 \dots (|\text{subtype}^*| - 1)) \\
 &\quad \wedge \text{roll}_x(\text{rectype}) = \text{rec subtype}^* && \\
 \text{unroll}(\text{rectype}.i) &= \text{subtype}^*[i] && (\text{if } \text{unroll}(\text{rectype}) = \text{rec subtype}^*)
 \end{aligned}$$

In addition, the following auxiliary function denotes the *expansion* of a *defined type*:

$$\text{expand}(\text{deftype}) = \text{comptype} \quad (\text{if } \text{unroll}(\text{deftype}) = \text{sub final}^? \text{ ht}^* \text{ comptype})$$

3.1.4 Instruction Types

Instruction types classify the behaviour of *instructions* or instruction sequences, by describing how they manipulate the *operand stack* and the initialization status of *locals*:

$$\text{instrtype} ::= \text{resulttype} \rightarrow_{\text{localidx}^*} \text{resulttype}$$

An instruction type $[t_1^*] \rightarrow_{x^*} [t_2^*]$ describes the required input stack with argument values of types t_1^* that an instruction pops off and the provided output stack with result values of types t_2^* that it pushes back. Moreover, it enumerates the *indices* x^* of locals that have been set by the instruction or sequence.

Note: Instruction types are only used for *validation*, they do not occur in programs.

3.1.5 Local Types

Local types classify *locals*, by describing their *value type* as well as their *initialization status*:

$$\begin{aligned} \textit{init} &::= \text{set} \mid \text{unset} \\ \textit{localtype} &::= \textit{init} \textit{valtype} \end{aligned}$$

Note: Local types are only used for *validation*, they do not occur in programs.

3.1.6 Contexts

Validity of an individual definition is specified relative to a *context*, which collects relevant information about the surrounding *module* and the definitions in scope:

- *Types*: the list of *types* defined in the current module.
- *Functions*: the list of *functions* declared in the current module, represented by a *defined type* that *expands* to their *function type*.
- *Tables*: the list of *tables* declared in the current module, represented by their *table type*.
- *Memories*: the list of *memories* declared in the current module, represented by their *memory type*.
- *Globals*: the list of *globals* declared in the current module, represented by their *global type*.
- *Tags*: the list of tags declared in the current module, represented by their *tag type*.
- *Element Segments*: the list of *element segments* declared in the current module, represented by the elements' *reference type*.
- *Data Segments*: the list of *data segments* declared in the current module, each represented by an *ok* entry.
- *Locals*: the list of *locals* declared in the current *function* (including parameters), represented by their *local type*.
- *Labels*: the stack of *labels* accessible from the current position, represented by their *result type*.
- *Return*: the return type of the current *function*, represented as an optional *result type* that is absent when no return is allowed, as in free-standing expressions.
- *References*: the list of *function indices* that occur in the module outside functions and can hence be used to form references inside them.

In other words, a context contains a sequence of suitable *types* for each *index space*, describing each defined entry in that space. Locals, labels and return type are only used for validating *instructions* in *function bodies*, and are left empty elsewhere. The label stack is the only part of the context that changes as validation of an instruction sequence proceeds.

More concretely, contexts are defined as *records* C with abstract syntax:

$$C ::= \{ \begin{array}{ll} \text{types} & \textit{deftype}^*, \\ \text{funcs} & \textit{deftype}^*, \\ \text{tables} & \textit{tabletype}^*, \\ \text{mems} & \textit{memtype}^*, \\ \text{globals} & \textit{globaltype}^*, \\ \text{tags} & \textit{tagtype}^*, \\ \text{elems} & \textit{reftype}^*, \\ \text{datas} & \textit{ok}^*, \\ \text{locals} & \textit{localtype}^*, \\ \text{labels} & \textit{resulttype}^*, \\ \text{return} & \textit{resulttype}^?, \\ \text{refs} & \textit{funcidx}^* \end{array} \}$$

In addition to field access written $C.\textit{field}$ the following notation is adopted for manipulating contexts:

- When spelling out a context, empty fields are omitted.
- C , field A^* denotes the same context as C but with the elements A^* prepended to its field component sequence.

Note: [Indexing notation](#) like $C.\text{labels}[i]$ is used to look up indices in their respective [index space](#) in the context. Context extension notation C , field A is primarily used to locally extend *relative* index spaces, such as [label indices](#). Accordingly, the notation is defined to append at the *front* of the respective sequence, introducing a new relative index 0 and shifting the existing ones.

Convention

Any form of [type](#) can be *closed* to bring it into [closed](#) form relative to a [context](#) it is [valid](#) in by [substituting](#) each [type index](#) x occurring in it with the corresponding [defined type](#) $C.\text{types}[x]$, after first closing the types in $C.\text{types}$ themselves.

$$\begin{aligned} \text{clos}_C(t) &= t[:= \text{clos}^*(C.\text{types})] \\ \text{clos}^*(\epsilon) &= \epsilon \\ \text{clos}^*(dt^* dt_N) &= dt'^* dt_N[:= dt'^*] \quad (\text{if } dt'^* = \text{clos}^*(dt^*)) \end{aligned}$$

3.1.7 Prose Notation

Validation is specified by stylised rules for each relevant part of the [abstract syntax](#). The rules not only state constraints defining when a phrase is valid, they also classify it with a type. The following conventions are adopted in stating these rules.

- A phrase A is said to be “valid with type T ” if and only if all constraints expressed by the respective rules are met. The form of T depends on what A is.

Note: For example, if A is a [function](#), then T is a [function type](#); for an A that is a [global](#), T is a [global type](#); and so on.

- The rules implicitly assume a given [context](#) C .
- In some places, this context is locally extended to a context C' with additional entries. The formulation “Under context C' , ... *statement* ...” is adopted to express that the following statement must apply under the assumptions embodied in the extended context.

3.1.8 Formal Notation

Note: This section gives a brief explanation of the notation for specifying typing rules formally. For the interested reader, a more thorough introduction can be found in respective text books.¹⁶

The proposition that a phrase A has a respective type T is written $A : T$. In general, however, typing is dependent on a context C . To express this explicitly, the complete form is a *judgement* $C \vdash A : T$, which says that $A : T$ holds under the assumptions encoded in C .

The formal typing rules use a standard approach for specifying type systems, rendering them into *deduction rules*. Every rule has the following general form:

$$\frac{\text{premise}_1 \quad \text{premise}_2 \quad \dots \quad \text{premise}_n}{\text{conclusion}}$$

¹⁶ For example: Benjamin Pierce. [Types and Programming Languages](#)^{Page 33, 17}. The MIT Press 2002

¹⁷ <https://www.cis.upenn.edu/~bcpierce/tapl/>

Such a rule is read as a big implication: if all premises hold, then the conclusion holds. Some rules have no premises; they are *axioms* whose conclusion holds unconditionally. The conclusion always is a judgment $C \vdash A : T$, and there is one respective rule for each relevant construct A of the abstract syntax.

Note: For example, the typing rule for the `i32.add` instruction can be given as an axiom:

$$\overline{C \vdash \text{i32.add} : [\text{i32 } \text{i32}] \rightarrow [\text{i32}]}$$

The instruction is always valid with type $[\text{i32 } \text{i32}] \rightarrow [\text{i32}]$ (saying that it consumes two `i32` values and produces one), independent of any side conditions.

An instruction like `local.get` can be typed as follows:

$$\frac{C.\text{globals}[x] = \text{mut } t}{C \vdash \text{global.get } x : [] \rightarrow [t]}$$

Here, the premise enforces that the immediate `global index` x exists in the context. The instruction produces a value of its respective type t (and does not consume any values). If $C.\text{globals}[x]$ does not exist then the premise does not hold, and the instruction is ill-typed.

Finally, a `structured` instruction requires a recursive rule, where the premise is itself a typing judgement:

$$\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{label } [t_2^*] \vdash \text{instr}^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{block } \text{blocktype } \text{instr}^* \text{ end} : [t_1^*] \rightarrow [t_2^*]}$$

A `block` instruction is only valid when the instruction sequence in its body is. Moreover, the result type must match the block's annotation `blocktype`. If so, then the `block` instruction has the same type as the body. Inside the body an additional label of the corresponding result type is available, which is expressed by extending the context C with the additional label information for the premise.

3.2 Types

Simple `types`, such as `number types` are universally valid. However, restrictions apply to most other types, such as `reference types`, `function types`, as well as the `limits` of `table types` and `memory types`, which must be checked during validation.

Moreover, `block types` are converted to plain `function types` for ease of processing.

3.2.1 Number Types

`Number types` are always valid.

$$\overline{C \vdash \text{numtype } \text{ok}}$$

3.2.2 Vector Types

`Vector types` are always valid.

$$\overline{C \vdash \text{vectype } \text{ok}}$$

3.2.3 Heap Types

Concrete heap types are only valid when the type index is, while abstract ones are vacuously valid.

absheaptypes

- The heap type is valid.

$$\overline{C \vdash \text{absheaptypes ok}}$$

typeid

- The type $C.\text{types}[\text{typeid}]$ must be defined in the context.
- Then the heap type is valid.

$$\frac{C.\text{types}[\text{typeid}] = \text{deftype}}{C \vdash \text{typeid ok}}$$

3.2.4 Reference Types

Reference types are valid when the referenced heap type is.

ref null? heaptypes

- The heap type *heaptypes* must be valid.
- Then the reference type is valid.

$$\frac{C \vdash \text{heaptypes ok}}{C \vdash \text{ref null? heaptypes ok}}$$

3.2.5 Value Types

Valid value types are either valid number types, valid vector types, or valid reference types.

3.2.6 Block Types

Block types may be expressed in one of two forms, both of which are converted to instruction types by the following rules.

typeid

- The type $C.\text{types}[\text{typeid}]$ must be defined in the context.
- The expansion of $C.\text{funcs}[\text{typeid}]$ must be a function type $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- Then the block type is valid as instruction type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{\text{expand}(C.\text{types}[\text{typeid}]) = \text{func } [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{typeid} : [t_1^*] \rightarrow [t_2^*]}$$

$[valtype?]$

- The value type $valtype$ must either be absent, or valid.
- Then the block type is valid as instruction type $[] \rightarrow [valtype?]$.

$$\frac{(C \vdash valtype \text{ ok})^?}{C \vdash [valtype?] : [] \rightarrow [valtype?]}$$

3.2.7 Result Types

$[t^*]$

- Each value type t_i in the type sequence t^* must be valid.
- Then the result type is valid.

$$\frac{(C \vdash t \text{ ok})^*}{C \vdash [t^*] \text{ ok}}$$

3.2.8 Instruction Types

$[t_1^*] \rightarrow_{x^*} [t_2^*]$

- The result type $[t_1^*]$ must be valid.
- The result type $[t_2^*]$ must be valid.
- Each local index x_i in x^* must be defined in the context.
- Then the instruction type is valid.

$$\frac{C \vdash [t_1^*] \text{ ok} \quad C \vdash [t_2^*] \text{ ok} \quad (C.\text{locals}[x] = \text{localtype})^*}{C \vdash [t_1^*] \rightarrow_{x^*} [t_2^*] \text{ ok}}$$

3.2.9 Function Types

$[t_1^*] \rightarrow [t_2^*]$

- The result type $[t_1^*]$ must be valid.
- The result type $[t_2^*]$ must be valid.
- Then the function type is valid.

$$\frac{C \vdash [t_1^*] \text{ ok} \quad C \vdash [t_2^*] \text{ ok}}{C \vdash [t_1^*] \rightarrow [t_2^*] \text{ ok}}$$

3.2.10 Composite Types

func functype

- The function type *functype* must be valid.
- Then the composite type is valid.

$$\frac{C \vdash \text{functype ok}}{C \vdash \text{func functype ok}}$$

*struct fieldtype**

- For each field type *fieldtype_i* in *fieldtype**:
 - The field type *fieldtype_i* must be valid.
- Then the composite type is valid.

$$\frac{(C \vdash ft \text{ ok})^*}{C \vdash \text{struct } ft^* \text{ ok}}$$

array fieldtype

- The field type *fieldtype* must be valid.
- Then the composite type is valid.

$$\frac{C \vdash ft \text{ ok}}{C \vdash \text{array } ft \text{ ok}}$$

3.2.11 Field Types

mut storagetype

- The storage type *storagetype* must be valid.
- Then the field type is valid.

$$\frac{C \vdash st \text{ ok}}{C \vdash \text{mut } st \text{ ok}}$$

packedtype

- The packed type is valid.

$$\overline{C \vdash \text{packedtype ok}}$$

3.2.12 Recursive Types

Recursive types are validated for a specific *type index* that denotes the index of the type defined by the recursive group.

*rec subtype**

- Either the sequence *subtype** is empty.
- Or:
 - The first *sub type* of the sequence *subtype** must be *valid* for the *type index* x .
 - The remaining sequence *subtype** must be *valid* for the *type index* $x + 1$.
- Then the recursive type is valid for the *type index* x .

$$\frac{}{C \vdash \text{rec } \epsilon \text{ ok}(x)} \quad \frac{C \vdash \text{subtype ok}(x) \quad C \vdash \text{rec subtype}'^* \text{ ok}(x + 1)}{C \vdash \text{rec subtype subtype}'^* \text{ ok}(x)}$$

sub final? y comptime*

- The *composite type* *comptime* must be *valid*.
- The sequence y^* may be no longer than 1.
- For every *type index* y_i in y^* :
 - The *type index* y_i must be smaller than x .
 - The *type index* y_i must exist in the context C .
 - Let *subtype_i* be the *unrolling* of the *defined type* $C.\text{types}[y_i]$.
 - The *sub type* *subtype_i* must not contain *final*.
 - Let *comptime'_i* be the *composite type* in *subtype_i*.
 - The *composite type* *comptime* must *match* *comptime'_i*.
- Then the sub type is valid for the *type index* x .

$$\frac{|y^*| \leq 1 \quad (y < x)^* \quad (\text{unroll}(C.\text{types}[y]) = \text{sub } y'^* \text{ comptime}')^* \quad C \vdash \text{comptime ok} \quad (C \vdash \text{comptime} \leq \text{comptime}')^*}{C \vdash \text{sub final? } y^* \text{ comptime ok}(x)}$$

Note: The side condition on the index ensures that a declared supertype is a previously defined types, preventing cyclic subtype hierarchies.

Future versions of WebAssembly may allow more than one supertype.

3.2.13 Defined Types

rectype.i

- The *recursive type* *rectype* must be *valid* for some *type index* x .
- Let *rec subtype** be the *defined type* *rectype*.
- The number i must be smaller than the length of the sequence *subtype** of *sub types*.
- Then the defined type is *valid*.

$$\frac{C \vdash \text{rectype ok}(x) \quad \text{rectype} = \text{rec subtype}^n \quad i < n}{C \vdash \text{rectype}.i \text{ ok}}$$

3.2.14 Limits

Limits must have meaningful bounds that are within a given range.

$\{\min n, \max m^?\}$

- The value of n must not be larger than k .
- If the maximum $m^?$ is not empty, then:
 - Its value must not be larger than k .
 - Its value must not be smaller than n .
- Then the limit is valid within range k .

$$\frac{n \leq k \quad (m \leq k)^? \quad (n \leq m)^?}{C \vdash \{\min n, \max m^?\} : k}$$

3.2.15 Table Types

$\text{addrtype limits reftype}$

- The limits limits must be **valid** within range $2^{|\text{addrtype}|} - 1$.
- The reference type reftype must be **valid**.
- Then the table type is valid.

$$\frac{C \vdash \text{limits} : 2^{|\text{addrtype}|} - 1 \quad C \vdash \text{reftype ok}}{C \vdash \text{addrtype limits reftype ok}}$$

3.2.16 Memory Types

addrtype limits

- The limits limits must be **valid** within range $2^{|\text{addrtype}|-16}$.
- Then the memory type is valid.

$$\frac{C \vdash \text{limits} : 2^{|\text{addrtype}|-16}}{C \vdash \text{limits ok}}$$

3.2.17 Tag Types

$[t_1^n] \rightarrow [t_2^m]$

- The function type $[t_1^n] \rightarrow [t_2^m]$ must be **valid**.
- The type sequence t_2^m must be empty.
- Then the tag type is valid.

$$\overline{\vdash [t^*] \rightarrow [] \text{ ok}}$$

3.2.18 Global Types

mut valtype

- The value type *valtype* must be **valid**.
- Then the global type is valid.

$$\frac{C \vdash \text{valtype ok}}{C \vdash \text{mut valtype ok}}$$

3.2.19 External Types

func deftype

- The defined type *deftype* must be **valid**.
- The defined type *deftype* must be a function type.
- Then the external type is valid.

$$\frac{C \vdash \text{deftype ok} \quad \text{expand}(\text{deftype}) = \text{func func type}}{C \vdash \text{func deftype}}$$

table tabletype

- The table type *tabletype* must be **valid**.
- Then the external type is valid.

$$\frac{C \vdash \text{tabletype ok}}{C \vdash \text{table tabletype ok}}$$

mem memtype

- The memory type *memtype* must be **valid**.
- Then the external type is valid.

$$\frac{C \vdash \text{memtype ok}}{C \vdash \text{mem memtype ok}}$$

tag *tagtype*

- The tag type *tagtype* must be valid.
- Then the external type is valid.

$$\frac{\vdash \text{tagtype ok}}{\vdash \text{tag tagtype ok}}$$

global *globaltype*

- The global type *globaltype* must be valid.
- Then the external type is valid.

$$\frac{C \vdash \text{globaltype ok}}{C \vdash \text{global globaltype ok}}$$

3.2.20 Defaultable Types

A type is *defaultable* if it has a [default value](#) for initialization.

Value Types

- A defaultable value type *t* must be:
 - either a number type,
 - or a vector type,
 - or a nullable reference type.

$$\overline{C \vdash \text{numtype defaultable}}$$

$$\overline{C \vdash \text{vectype defaultable}}$$

$$\overline{C \vdash (\text{ref null heaptypes}) \text{ defaultable}}$$

3.3 Matching

On most types, a notion of *subtyping* is defined that is applicable in [validation](#) rules, during [module instantiation](#) when checking the types of imports, or during [execution](#), when performing casts.

3.3.1 Number Types

A number type *numtype*₁ matches a number type *numtype*₂ if and only if:

- Both *numtype*₁ and *numtype*₂ are the same.

$$\overline{C \vdash \text{numtype} \leq \text{numtype}}$$

3.3.2 Vector Types

A vector type $vectype_1$ matches a vector type $vectype_2$ if and only if:

- Both $vectype_1$ and $vectype_2$ are the same.

$$\overline{C \vdash vectype \leq vectype}$$

3.3.3 Heap Types

A heap type $heaptypes_1$ matches a heap type $heaptypes_2$ if and only if:

- Either both $heaptypes_1$ and $heaptypes_2$ are the same.
- Or there exists a valid heap type $heaptypes'$, such that $heaptypes_1$ matches $heaptypes'$ and $heaptypes'$ matches $heaptypes_2$.
- Or $heaptypes_1$ is eq and $heaptypes_2$ is any.
- Or $heaptypes_1$ is one of i31, struct, or array and $heaptypes_2$ is eq.
- Or $heaptypes_1$ is a defined type which expands to a structure type and $heaptypes_2$ is struct.
- Or $heaptypes_1$ is a defined type which expands to an array type and $heaptypes_2$ is array.
- Or $heaptypes_1$ is a defined type which expands to a function type and $heaptypes_2$ is func.
- Or $heaptypes_1$ is a defined type $deftype_1$ and $heaptypes_2$ is a defined type $deftype_2$, and $deftype_1$ matches $deftype_2$.
- Or $heaptypes_1$ is a type index x_1 , and the defined type $C.types[x_1]$ matches $heaptypes_2$.
- Or $heaptypes_2$ is a type index x_2 , and $heaptypes_1$ matches the defined type $C.types[x_2]$.
- Or $heaptypes_1$ is none and $heaptypes_2$ matches any.
- Or $heaptypes_1$ is nofunc and $heaptypes_2$ matches func.
- Or $heaptypes_1$ is noextern and $heaptypes_2$ matches extern.
- Or $heaptypes_1$ is bot.

$$\overline{C \vdash heaptypes \leq heaptypes} \quad \frac{C \vdash heaptypes' \text{ ok} \quad C \vdash heaptypes_1 \leq heaptypes' \quad C \vdash heaptypes' \leq heaptypes_2}{C \vdash heaptypes_1 \leq heaptypes_2}$$

$$\overline{C \vdash \text{eq} \leq \text{any}} \quad \overline{C \vdash \text{i31} \leq \text{eq}} \quad \overline{C \vdash \text{struct} \leq \text{eq}} \quad \overline{C \vdash \text{array} \leq \text{eq}}$$

$$\frac{\text{expand}(deftype) = \text{struct } st}{C \vdash deftype \leq \text{struct}} \quad \frac{\text{expand}(deftype) = \text{array } at}{C \vdash deftype \leq \text{array}} \quad \frac{\text{expand}(deftype) = \text{func } ft}{C \vdash deftype \leq \text{func}}$$

$$\frac{C \vdash C.types[typeidx_1] \leq heaptypes_2}{C \vdash typeidx_1 \leq heaptypes_2} \quad \frac{C \vdash heaptypes_1 \leq C.types[typeidx_2]}{C \vdash heaptypes_1 \leq typeidx_2}$$

$$\frac{C \vdash ht \leq \text{any}}{C \vdash \text{none} \leq ht} \quad \frac{C \vdash ht \leq \text{func}}{C \vdash \text{nofunc} \leq ht} \quad \frac{C \vdash ht \leq \text{extern}}{C \vdash \text{noextern} \leq ht}$$

$$\overline{C \vdash \text{bot} \leq heaptypes}$$

3.3.4 Reference Types

A reference type $\text{ref null}_1^? \text{heaptype}_1$ matches a reference type $\text{ref null}_2^? \text{heaptype}_2$ if and only if:

- The heap type heaptype_1 matches heaptype_2 .
- null_1 is absent or null_2 is present.

$$\frac{C \vdash \text{heaptype}_1 \leq \text{heaptype}_2}{C \vdash \text{ref heaptype}_1 \leq \text{ref heaptype}_2} \quad \frac{C \vdash \text{heaptype}_1 \leq \text{heaptype}_2}{C \vdash \text{ref null}^? \text{heaptype}_1 \leq \text{ref null} \text{heaptype}_2}$$

3.3.5 Value Types

A value type valtype_1 matches a value type valtype_2 if and only if:

- Either both valtype_1 and valtype_2 are number types and valtype_1 matches valtype_2 .
- Or both valtype_1 and valtype_2 are reference types and valtype_1 matches valtype_2 .
- Or valtype_1 is `bot`.

$$\overline{C \vdash \text{bot} \leq \text{valtype}}$$

3.3.6 Result Types

Subtyping is lifted to **result types** in a pointwise manner. That is, a **result type** $[t_1^*]$ matches a **result type** $[t_2^*]$ if and only if:

- Every value type t_1 in $[t_1^*]$ matches the corresponding value type t_2 in $[t_2^*]$.

$$\frac{(C \vdash t_1 \leq t_2)^*}{C \vdash [t_1^*] \leq [t_2^*]}$$

3.3.7 Instruction Types

Subtyping is further lifted to **instruction types**. An **instruction type** $[t_{11}^*] \rightarrow_{x_1^*} [t_{12}^*]$ matches a type $[t_{21}^a st] \rightarrow_{x_2^*} [t_{22}^*]$ if and only if:

- There is a common sequence of **value types** t^* such that t_{21}^* equals $t^* t_{21}'^*$ and t_{22}^* equals $t^* t_{22}'^*$.
- The **result type** $[t_{21}'^*]$ matches $[t_{11}^*]$.
- The **result type** $[t_{22}'^*]$ matches $[t_{12}^*]$.
- For every **local index** x that is in x_2^* but not in x_1^* , the **local type** $C.\text{locals}[x]$ is **set** t_x for some **value type** t_x .

$$\frac{\begin{array}{l} C \vdash [t_{21}^*] \leq [t_{11}^*] \quad \{x^*\} = \{x_2^*\} \setminus \{x_1^*\} \\ C \vdash [t_{12}^*] \leq [t_{22}^*] \quad (C.\text{locals}[x] = \text{set } t_x)^* \end{array}}{C \vdash [t_{11}^*] \rightarrow_{x_1^*} [t_{12}^*] \leq [t^* t_{21}'^*] \rightarrow_{x_2^*} [t^* t_{22}'^*]}$$

Note: Instruction types are contravariant in their input and covariant in their output. Subtyping also incorporates a sort of “frame” condition, which allows adding arbitrary invariant stack elements on both sides in the super type.

Finally, the supertype may ignore variables from the init set x_1^* . It may also *add* variables to the init set, provided these are already set in the context, i.e., are vacuously initialized.

3.3.8 Function Types

A function type $[t_{11}^*] \rightarrow [t_{12}^*]$ matches a type $[t_{21}^a st] \rightarrow [t_{22}^*]$ if and only if:

- The result type $[t_{21}^*]$ matches $[t_{11}^*]$.
- The result type $[t_{12}^*]$ matches $[t_{22}^*]$.

$$\frac{C \vdash [t_{21}^*] \leq [t_{11}^*] \quad C \vdash [t_{12}^*] \leq [t_{22}^*]}{C \vdash [t_{11}^*] \rightarrow [t_{12}^*] \leq [t_{21}^*] \rightarrow [t_{22}^*]}$$

3.3.9 Composite Types

A composite type $comptype_1$ matches a type $comptype_2$ if and only if:

- Either the composite type $comptype_1$ is `func` $func_{type_1}$ and $comptype_2$ is `func` $func_{type_2}$ and:
 - The function type $func_{type_1}$ matches $func_{type_2}$.
- Or the composite type $comptype_1$ is `struct` $fieldtype_1^{n_1}$ and $comptype_2$ is `struct` $fieldtype_2$ and:
 - The arity n_1 is greater than or equal to n_2 .
 - For every field type $fieldtype_{2i}$ in $fieldtype_2^{n_2}$ and corresponding $fieldtype_{1i}$ in $fieldtype_1^{n_1}$
 - * The field type $fieldtype_{1i}$ matches $fieldtype_{2i}$.
- Or the composite type $comptype_1$ is `array` $fieldtype_1$ and $comptype_2$ is `array` $fieldtype_2$ and:
 - The field type $fieldtype_1$ matches $fieldtype_2$.

$$\frac{C \vdash func_{type_1} \leq func_{type_2}}{C \vdash func func_{type_1} \leq func func_{type_2}}$$

$$\frac{(C \vdash fieldtype_1 \leq fieldtype_2)^*}{C \vdash struct fieldtype_1^* fieldtype_1'^* \leq struct fieldtype_2^*}$$

$$\frac{C \vdash fieldtype_1 \leq fieldtype_2}{C \vdash array fieldtype_1 \leq array fieldtype_2}$$

3.3.10 Field Types

A field type $mut_1 storagetype_1$ matches a type $mut_2 storagetype_2$ if and only if:

- Storage type $storagetype_1$ matches $storagetype_2$.
- Either both mut_1 and mut_2 are `const`.
- Or both mut_1 and mut_2 are `var` and $storagetype_2$ matches $storagetype_1$ as well.

$$\frac{C \vdash storagetype_1 \leq storagetype_2}{C \vdash const storagetype_1 \leq const storagetype_2} \quad \frac{C \vdash storagetype_1 \leq storagetype_2 \quad C \vdash storagetype_2 \leq storagetype_1}{C \vdash var storagetype_1 \leq var storagetype_2}$$

A storage type $storagetype_1$ matches a type $storagetype_2$ if and only if:

- Either $storagetype_1$ is a value type $valtype_1$ and $storagetype_2$ is a value type $valtype_2$ and $valtype_1$ matches $valtype_2$.
- Or $storagetype_1$ is a packed type $packedtype_1$ and $storagetype_2$ is a packed type $packedtype_2$ and $packedtype_1$ matches $packedtype_2$.

A packed type packedtype_1 matches a type packedtype_2 if and only if:

- The packed type packedtype_1 is the same as packedtype_2 .

$$\frac{}{C \vdash \text{packedtype} \leq \text{packedtype}}$$

3.3.11 Defined Types

A defined type deftype_1 matches a type deftype_2 if and only if:

- Either deftype_1 and deftype_2 are equal when closed under context C .
- Or:
 - Let the sub type sub final[?] $\text{heaptypes}^* \text{comptypes}$ be the result of unrolling deftype_1 .
 - Then there must exist a heap type heaptypes_i in heaptypes^* that matches deftype_2 .

$$\frac{\text{clos}_C(\text{deftype}_1) = \text{clos}_C(\text{deftype}_2)}{C \vdash \text{deftype}_1 \leq \text{deftype}_2}$$

$$\frac{\text{unroll}(\text{deftype}_1) = \text{sub final}^? \text{heaptypes}^* \text{comptypes} \quad C \vdash \text{heaptypes}^*[i] \leq \text{deftype}_2}{C \vdash \text{deftype}_1 \leq \text{deftype}_2}$$

Note: Note that there is no explicit definition of type `_equivalence_`, since it coincides with syntactic equality, as used in the premise of the former rule above.

3.3.12 Limits

Limits $\{\min n_1, \max m_1^?\}$ match limits $\{\min n_2, \max m_2^?\}$ if and only if:

- n_1 is larger than or equal to n_2 .
- Either:
 - $m_2^?$ is empty.
- Or:
 - Both $m_1^?$ and $m_2^?$ are non-empty.
 - m_1 is smaller than or equal to m_2 .

$$\frac{n_1 \geq n_2}{C \vdash \{\min n_1, \max m_1^?\} \leq \{\min n_2, \max m_2^?\}} \quad \frac{n_1 \geq n_2 \quad m_1 \leq m_2}{C \vdash \{\min n_1, \max m_1\} \leq \{\min n_2, \max m_2\}}$$

3.3.13 Table Types

A table type $(\text{addrtype}_1 \text{limits}_1 \text{reftype}_1)$ matches $(\text{addrtype}_2 \text{limits}_2 \text{reftype}_2)$ if and only if:

- Address types addrtype_1 and addrtype_2 are the same.
- Limits limits_1 match limits_2 .
- The reference type reftype_1 matches reftype_2 , and vice versa.

$$\frac{C \vdash \text{limits}_1 \leq \text{limits}_2 \quad C \vdash \text{reftype}_1 \leq \text{reftype}_2 \quad C \vdash \text{reftype}_2 \leq \text{reftype}_1}{C \vdash \text{addrtype } \text{limits}_1 \text{ reftype}_1 \leq \text{addrtype } \text{limits}_2 \text{ reftype}_2}$$

3.3.14 Memory Types

A memory type $(\text{addrtype}_1 \text{ limits}_1)$ matches $(\text{addrtype}_2 \text{ limits}_2)$ if and only if:

- Address types addrtype_1 and addrtype_2 are the same.
- Limits limits_1 match limits_2 .

$$\frac{C \vdash \text{limits}_1 \leq \text{limits}_2}{C \vdash \text{addrtype } \text{limits}_1 \leq \text{addrtype } \text{limits}_2}$$

3.3.15 Global Types

A global type $(\text{mut}_1 t_1)$ matches $(\text{mut}_2 t_2)$ if and only if:

- Either both mut_1 and mut_2 are `var` and t_1 matches t_2 and vice versa.
- Or both mut_1 and mut_2 are `const` and t_1 matches t_2 .

$$\frac{C \vdash t_1 \leq t_2 \quad C \vdash t_2 \leq t_1}{C \vdash \text{var } t_1 \leq \text{var } t_2} \quad \frac{C \vdash t_1 \leq t_2}{C \vdash \text{const } t_1 \leq \text{const } t_2}$$

3.3.16 Tag Types

A tag type tagtype_1 matches tagtype_2 if and only if they are the same.

$$\overline{C \vdash \text{tagtype} \leq \text{tagtype}}$$

3.3.17 External Types

Functions

An external type $\text{func } \text{deftype}_1$ matches $\text{func } \text{deftype}_2$ if and only if:

- The defined type deftype_1 matches deftype_2 .

$$\frac{C \vdash \text{deftype}_1 \leq \text{deftype}_2}{C \vdash \text{func } \text{deftype}_1 \leq \text{func } \text{deftype}_2}$$

Tables

An external type table $tabletype_1$ matches table $tabletype_2$ if and only if:

- Table type $tabletype_1$ matches $tabletype_2$.

$$\frac{C \vdash tabletype_1 \leq tabletype_2}{C \vdash \text{table } tabletype_1 \leq \text{table } tabletype_2}$$

Memories

An external type mem $memtype_1$ matches mem $memtype_2$ if and only if:

- Memory type $memtype_1$ matches $memtype_2$.

$$\frac{C \vdash memtype_1 \leq memtype_2}{C \vdash \text{mem } memtype_1 \leq \text{mem } memtype_2}$$

Globals

An external type global $globaltype_1$ matches global $globaltype_2$ if and only if:

- Global type $globaltype_1$ matches $globaltype_2$.

$$\frac{C \vdash globaltype_1 \leq globaltype_2}{C \vdash \text{global } globaltype_1 \leq \text{global } globaltype_2}$$

Tags

An external type tag $tagtype_1$ matches tag $tagtype_2$ if and only if:

- Tag type $tagtype_1$ matches $tagtype_2$.

$$\frac{C \vdash tagtype_1 \leq tagtype_2}{C \vdash \text{tag } tagtype_1 \leq \text{tag } tagtype_2}$$

3.4 Instructions

Instructions are classified by **instruction types** that describe how they manipulate the **operand stack** and initialize **locals**: A type $[t_1^*] \rightarrow_{x^*} [t_2^*]$ describes the required input stack with argument values of types t_1^* that an instruction pops off and the provided output stack with result values of types t_2^* that it pushes back. Moreover, it enumerates the **indices** x^* of locals that have been set by the instruction. In most cases, this is empty.

Note: For example, the instruction `i32.add` has type $[i32\ i32] \rightarrow [i32]$, consuming two `i32` values and producing one. The instruction `local.set x` has type $[t] \rightarrow_x []$, provided t is the type declared for the local x .

Typing extends to **instruction sequences** $instr^*$. Such a sequence has an instruction type $[t_1^*] \rightarrow_{x^*} [t_2^*]$ if the accumulative effect of executing the instructions is consuming values of types t_1^* off the operand stack, pushing new values of types t_2^* , and setting all locals x^* .

For some instructions, the typing rules do not fully constrain the type, and therefore allow for multiple types. Such instructions are called *polymorphic*. Two degrees of polymorphism can be distinguished:

- *value-polymorphic*: the **value type** t of one or several individual operands is unconstrained. That is the case for all **parametric instructions** like `drop` and `select`.

- *stack-polymorphic*: the entire (or most of the) instruction type $[t_1^*] \rightarrow [t_2^*]$ of the instruction is unconstrained. That is the case for all control instructions that perform an *unconditional control transfer*, such as `unreachable`, `br`, `br_table`, and `return`.

In both cases, the unconstrained types or type sequences can be chosen arbitrarily, as long as they meet the constraints imposed for the surrounding parts of the program.

Note: For example, the `select` instruction is valid with type $[t \ t \ i32] \rightarrow [t]$, for any possible number type t . Consequently, both instruction sequences

(i32.const 1) (i32.const 2) (i32.const 3) `select`

and

(f64.const 1.0) (f64.const 2.0) (i32.const 3) `select`

are valid, with t in the typing of `select` being instantiated to `i32` or `f64`, respectively.

The `unreachable` instruction is stack-polymorphic, and hence valid with type $[t_1^*] \rightarrow [t_2^*]$ for any possible sequences of value types t_1^* and t_2^* . Consequently,

`unreachable i32.add`

is valid by assuming type $[] \rightarrow [i32]$ for the `unreachable` instruction. In contrast,

`unreachable (i64.const 0) i32.add`

is invalid, because there is no possible type to pick for the `unreachable` instruction that would make the sequence well-typed.

The [Appendix](#) describes a type checking algorithm that efficiently implements validation of instruction sequences as prescribed by the rules given here.

3.4.1 Numeric Instructions

t.const c

- The instruction is valid with type $[] \rightarrow [t]$.

$$\overline{C \vdash t.\text{const } c : [] \rightarrow [t]}$$

t.unop

- The instruction is valid with type $[t] \rightarrow [t]$.

$$\overline{C \vdash t.\text{unop} : [t] \rightarrow [t]}$$

t.binop

- The instruction is valid with type $[t \ t] \rightarrow [t]$.

$$\overline{C \vdash t.\text{binop} : [t \ t] \rightarrow [t]}$$

t.testop

- The instruction is valid with type $[t] \rightarrow [i32]$.

$$\overline{C \vdash t.testop : [t] \rightarrow [i32]}$$

t.relop

- The instruction is valid with type $[t\ t] \rightarrow [i32]$.

$$\overline{C \vdash t.relop : [t\ t] \rightarrow [i32]}$$

t₂.cvtop_t₁_sx[?]

- The instruction is valid with type $[t_1] \rightarrow [t_2]$.

$$\overline{C \vdash t_2.cvtop_t_1_sx^? : [t_1] \rightarrow [t_2]}$$

3.4.2 Reference Instructions

ref.null ht

- The heap type *ht* must be valid.
- Then the instruction is valid with type $[] \rightarrow [(\text{ref null } ht)]$.

$$\frac{C \vdash ht \text{ ok}}{C \vdash \text{ref.null } ht : [] \rightarrow [(\text{ref null } ht)]}$$

ref.func x

- The function $C.\text{funcs}[x]$ must be defined in the context.
- Let *dt* be the defined type $C.\text{funcs}[x]$.
- The function index *x* must be contained in $C.\text{refs}$.
- The instruction is valid with type $[] \rightarrow [(\text{ref } dt)]$.

$$\frac{C.\text{funcs}[x] = dt \quad x \in C.\text{refs}}{C \vdash \text{ref.func } x : [] \rightarrow [(\text{ref } dt)]}$$

ref.is_null

- The instruction is valid with type $[(\text{ref null } ht)] \rightarrow [i32]$, for any valid heap type *ht*.

$$\frac{C \vdash ht \text{ ok}}{C \vdash \text{ref.is_null} : [(\text{ref null } ht)] \rightarrow [i32]}$$

ref.as_non_null

- The instruction is valid with type $[(\text{ref null } ht)] \rightarrow [(\text{ref } ht)]$, for any valid heap type ht .

$$\frac{C \vdash ht \text{ ok}}{C \vdash \text{ref.as_non_null} : [(\text{ref null } ht)] \rightarrow [(\text{ref } ht)]}$$

ref.eq

- The instruction is valid with type $[(\text{ref null eq})(\text{ref null eq})] \rightarrow [\text{i32}]$.

$$C \vdash \text{ref.eq} : [(\text{ref null eq}) (\text{ref null eq})] \rightarrow [\text{i32}]$$

ref.test rt

- The reference type rt must be valid.
- Then the instruction is valid with type $[rt'] \rightarrow [\text{i32}]$ for any valid reference type rt' for which rt matches rt' .

$$\frac{C \vdash rt \text{ ok} \quad C \vdash rt' \text{ ok} \quad C \vdash rt \leq rt'}{C \vdash \text{ref.test } rt : [rt'] \rightarrow [\text{i32}]}$$

Note: The liberty to pick a supertype rt' allows typing the instruction with the least precise super type of rt as input, that is, the top type in the corresponding heap subtyping hierarchy.

ref.cast rt

- The reference type rt must be valid.
- Then the instruction is valid with type $[rt'] \rightarrow [rt]$ for any valid reference type rt' for which rt matches rt' .

$$\frac{C \vdash rt \text{ ok} \quad C \vdash rt' \text{ ok} \quad C \vdash rt \leq rt'}{C \vdash \text{ref.cast } rt : [rt'] \rightarrow [rt]}$$

Note: The liberty to pick a supertype rt' allows typing the instruction with the least precise super type of rt as input, that is, the top type in the corresponding heap subtyping hierarchy.

3.4.3 Aggregate Reference Instructions

struct.new x

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be a structure type $\text{struct } \text{fieldtype}^*$.
- For each field type fieldtype_i in fieldtype^* :
 - Let fieldtype_i be mut storagetype_i .
 - Let t_i be the value type $\text{unpack}(\text{storagetype}_i)$.
- Let t^* be the concatenation of all t_i .
- Then the instruction is valid with type $[t^*] \rightarrow [(\text{ref } x)]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{struct } (\text{mut } st)^*}{C \vdash \text{struct.new } x : [(\text{unpack}(st))^*] \rightarrow [(\text{ref } x)]}$$

`struct.new_default x`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be a structure type $\text{struct } \text{fieldtype}^*$.
- For each field type fieldtype_i in fieldtype^* :
 - Let fieldtype_i be mut storagetype_i .
 - Let t_i be the value type $\text{unpack}(\text{storagetype}_i)$.
 - The type t_i must be defaultable.
- Let t^* be the concatenation of all t_i .
- Then the instruction is valid with type $[] \rightarrow [(\text{ref } x)]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{struct } (\text{mut } st)^* \quad (C \vdash \text{unpack}(st) \text{ defaultable})^*}{C \vdash \text{struct.new_default } x : [] \rightarrow [(\text{ref } x)]}$$

`struct.getsx? x y`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be a structure type $\text{struct } \text{fieldtype}^*$.
- Let the field type mut storagetype be $\text{fieldtype}^*[y]$.
- Let t be the value type $\text{unpack}(\text{storagetype})$.
- The extension sx must be present if and only if storagetype is a packed type.
- Then the instruction is valid with type $[(\text{ref null } x)] \rightarrow [t]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{struct } ft^* \quad ft^*[y] = \text{mut } st \quad \text{sx}^? = \epsilon \Leftrightarrow st = \text{unpack}(st)}{C \vdash \text{struct.get_sx}^? x y : [(\text{ref null } x)] \rightarrow [\text{unpack}(st)]}$$

`struct.set x y`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be a structure type $\text{struct } \text{fieldtype}^*$.
- Let the field type mut storagetype be $\text{fieldtype}^*[y]$.
- The prefix mut must be var .
- Let t be the value type $\text{unpack}(\text{storagetype})$.
- Then the instruction is valid with type $[(\text{ref null } x) t] \rightarrow []$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{struct } ft^* \quad ft^*[y] = \text{var } st}{C \vdash \text{struct.set } x y : [(\text{ref null } x) \text{unpack}(st)] \rightarrow []}$$

`array.new x`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be an array type $\text{array } \text{fieldtype}$.
- Let fieldtype be mut storagetype .
- Let t be the value type $\text{unpack}(\text{storagetype})$.
- Then the instruction is valid with type $[t \text{ i32}] \rightarrow [(\text{ref } x)]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{array } (\text{mut } st)}{C \vdash \text{array.new } x : [\text{unpack}(st) \text{ i32}] \rightarrow [(\text{ref } x)]}$$

`array.new_default x`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let $fieldtype$ be $mut\ storagetype$.
- Let t be the value type $unpack(storagetype)$.
- The type t must be defaultable.
- Then the instruction is valid with type $[i32] \rightarrow [(ref\ x)]$.

$$\frac{\text{expand}(C.types[x]) = array\ (mut\ st) \quad C \vdash \text{unpack}(st)\ \text{defaultable}}{C \vdash \text{array.new } x : [i32] \rightarrow [(ref\ x)]}$$

`array.new_fixed x n`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let $fieldtype$ be $mut\ storagetype$.
- Let t be the value type $unpack(storagetype)$.
- Then the instruction is valid with type $[t^n] \rightarrow [(ref\ x)]$.

$$\frac{\text{expand}(C.types[x]) = array\ (mut\ st)}{C \vdash \text{array.new_fixed } x\ n : [unpack(st)^n] \rightarrow [(ref\ x)]}$$

`array.new_elem x y`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let $fieldtype$ be $mut\ storagetype$.
- The storage type $storagetype$ must be a reference type rt .
- The element segment $C.elems[y]$ must exist.
- Let rt' be the reference type $C.elems[y]$.
- The reference type rt' must match rt .
- Then the instruction is valid with type $[i32\ i32] \rightarrow [(ref\ x)]$.

$$\frac{\text{expand}(C.types[x]) = array\ (mut\ rt) \quad C \vdash C.elems[y] \leq rt}{C \vdash \text{array.new_elem } x\ y : [i32\ i32] \rightarrow [(ref\ x)]}$$

`array.new_data x y`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let $fieldtype$ be $mut\ storagetype$.
- Let t be the value type $unpack(storagetype)$.
- The type t must be a numeric type or a vector type.
- The data segment $C.datas[y]$ must exist.
- Then the instruction is valid with type $[i32\ i32] \rightarrow [(ref\ x)]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{array } (mut \ st) \quad \text{unpack}(st) = numtype \vee \text{unpack}(st) = vectype \quad C.\text{datas}[y] = \text{ok}}{C \vdash \text{array.new_data } x \ y : [i32 \ i32] \rightarrow [(\text{ref } x)]}$$

`array.getst? x`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be an array type $\text{array } fieldtype$.
- Let the field type $mut \ storagetype$ be $fieldtype$.
- Let t be the value type $\text{unpack}(storagetype)$.
- The extension st must be present if and only if $storagetype$ is a packed type.
- Then the instruction is valid with type $[(\text{ref } null \ x) \ i32] \rightarrow [t]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{array } (mut \ st) \quad st? = \epsilon \Leftrightarrow st = \text{unpack}(st)}{C \vdash \text{array.get}_{st}? \ x : [(\text{ref } null \ x) \ i32] \rightarrow [\text{unpack}(st)]}$$

`array.set x`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be an array type $\text{array } fieldtype$.
- Let the field type $mut \ storagetype$ be $fieldtype$.
- The prefix mut must be `var`.
- Let t be the value type $\text{unpack}(storagetype)$.
- Then the instruction is valid with type $[(\text{ref } null \ x) \ i32 \ t] \rightarrow []$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{array } (var \ st)}{C \vdash \text{array.set } x : [(\text{ref } null \ x) \ i32 \ \text{unpack}(st)] \rightarrow []}$$

`array.len`

- The the instruction is valid with type $[(\text{ref } null \ \text{array})] \rightarrow [i32]$.

$$C \vdash \text{array.len} : [(\text{ref } null \ \text{array})] \rightarrow [i32]$$

`array.fill x`

- The defined type $C.\text{types}[x]$ must exist.
- The expansion of $C.\text{types}[x]$ must be an array type $\text{array } fieldtype$.
- Let the field type $mut \ storagetype$ be $fieldtype$.
- The prefix mut must be `var`.
- Let t be the value type $\text{unpack}(storagetype)$.
- Then the instruction is valid with type $[(\text{ref } null \ x) \ i32 \ t \ i32] \rightarrow []$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{array } (var \ st)}{C \vdash \text{array.fill } x : [(\text{ref } null \ x) \ i32 \ \text{unpack}(st) \ i32] \rightarrow []}$$

`array.copy x y`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype_1$.
- Let the field type $mut_1\ storagetype_1$ be $fieldtype_1$.
- The prefix mut_1 must be `var`.
- The defined type $C.types[y]$ must exist.
- The expansion of $C.types[y]$ must be an array type $array\ fieldtype_2$.
- Let the field type $mut_2\ storagetype_2$ be $fieldtype_2$.
- The storage type $storagetype_2$ must match $storagetype_1$.
- Then the instruction is valid with type $[(ref\ null\ x)\ i32\ (ref\ null\ y)\ i32\ i32] \rightarrow []$.

$$\frac{\text{expand}(C.types[x]) = array\ (var\ st_1) \quad \text{expand}(C.types[y]) = array\ (mut\ st_2) \quad C \vdash st_2 \leq st_1}{C \vdash array.copy\ x\ y : [(ref\ null\ x)\ i32\ (ref\ null\ y)\ i32\ i32] \rightarrow []}$$

`array.init_data x y`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let the field type $mut\ storagetype$ be $fieldtype$.
- The prefix mut must be `var`.
- Let t be the value type $unpack(storagetype)$.
- The value type t must be a numeric type or a vector type.
- The data segment $C.datas[y]$ must exist.
- Then the instruction is valid with type $[(ref\ null\ x)\ i32\ i32\ i32] \rightarrow []$.

$$\frac{\text{expand}(C.types[x]) = array\ (var\ st) \quad unpack(st) = numtype \vee unpack(st) = vectype \quad C.datas[y] = ok}{C \vdash array.init_data\ x\ y : [(ref\ null\ x)\ i32\ i32\ i32] \rightarrow []}$$

`array.init_elem x y`

- The defined type $C.types[x]$ must exist.
- The expansion of $C.types[x]$ must be an array type $array\ fieldtype$.
- Let the field type $mut\ storagetype$ be $fieldtype$.
- The prefix mut must be `var`.
- The storage type $storagetype$ must be a reference type rt .
- The element segment $C.elems[y]$ must exist.
- Let rt' be the reference type $C.elems[y]$.
- The reference type rt' must match rt .
- Then the instruction is valid with type $[(ref\ null\ x)\ i32\ i32\ i32] \rightarrow []$.

$$\frac{\text{expand}(C.types[x]) = array\ (var\ rt) \quad C \vdash C.elems[y] \leq rt}{C \vdash array.init_elem\ x\ y : [(ref\ null\ x)\ i32\ i32\ i32] \rightarrow []}$$

3.4.4 Scalar Reference Instructions

`ref.i31`

- The instruction is valid with type $[i32] \rightarrow [(ref\ i31)]$.

$$\overline{C \vdash ref.i31 : [i32] \rightarrow [(ref\ i31)]}$$

`i31.get_sx`

- The instruction is valid with type $[(ref\ null\ i31)] \rightarrow [i32]$.

$$\overline{C \vdash i31.get_sx : [(ref\ null\ i31)] \rightarrow [i32]}$$

3.4.5 External Reference Instructions

`any.convert_extern`

- The instruction is valid with type $[(ref\ null_1^? \text{extern})] \rightarrow [(ref\ null_2^? \text{any})]$ for any $null_1^?$ that equals $null_2^?$.

$$\frac{null_1^? = null_2^?}{\overline{C \vdash any.convert_extern : [(ref\ null_1^? \text{extern})] \rightarrow [(ref\ null_2^? \text{any})]}}$$

`extern.convert_any`

- The instruction is valid with type $[(ref\ null_1^? \text{any})] \rightarrow [(ref\ null_2^? \text{extern})]$ for any $null_1^?$ that equals $null_2^?$.

$$\frac{null_1^? = null_2^?}{\overline{C \vdash extern.convert_any : [(ref\ null_1^? \text{any})] \rightarrow [(ref\ null_2^? \text{extern})]}}$$

3.4.6 Vector Instructions

Vector instructions can have a prefix to describe the [shape](#) of the operand. Packed numeric types, `i8` and `i16`, are not [value types](#). An auxiliary function maps such packed type shapes to value types:

$$\text{unpack}(txN) = \text{unpack}(t)$$

The following auxiliary function denotes the number of lanes in a vector shape, i.e., its *dimension*:

$$\text{dim}(txN) = N$$

`v128.const c`

- The instruction is valid with type $[] \rightarrow [v128]$.

$$\overline{C \vdash v128.const\ c : [] \rightarrow [v128]}$$

v128.vvunop

- The instruction is valid with type $[v128] \rightarrow [v128]$.

$$\overline{C \vdash v128.vvunop : [v128] \rightarrow [v128]}$$

v128.vvbinop

- The instruction is valid with type $[v128 \ v128] \rightarrow [v128]$.

$$\overline{C \vdash v128.vvbinop : [v128 \ v128] \rightarrow [v128]}$$

v128.vvternop

- The instruction is valid with type $[v128 \ v128 \ v128] \rightarrow [v128]$.

$$\overline{C \vdash v128.vvternop : [v128 \ v128 \ v128] \rightarrow [v128]}$$

v128.vvtestop

- The instruction is valid with type $[v128] \rightarrow [i32]$.

$$\overline{C \vdash v128.vvtestop : [v128] \rightarrow [i32]}$$

i8x16.swizzle

- The instruction is valid with type $[v128 \ v128] \rightarrow [v128]$.

$$\overline{C \vdash i8x16.swizzle : [v128 \ v128] \rightarrow [v128]}$$

i8x16.relaxed_swizzle

- The instruction is valid with type $[v128 \ v128] \rightarrow [v128]$.

$$\overline{C \vdash i8x16.relaxed_swizzle : [v128 \ v128] \rightarrow [v128]}$$

*i8x16.shuffle laneidx*¹⁶

- For all $laneidx_i$, in $laneidx$ ¹⁶, $laneidx_i$ must be smaller than 32.
- The instruction is valid with type $[v128 \ v128] \rightarrow [v128]$.

$$\overline{(laneidx < 32)^{16} \quad C \vdash i8x16.shuffle \ laneidx^{16} : [v128 \ v128] \rightarrow [v128]}$$

shape.splat

- Let t be $\text{unpack}(\text{shape})$.
- The instruction is valid with type $[t] \rightarrow [\text{v128}]$.

$$\overline{C \vdash \text{shape.splat} : [\text{unpack}(\text{shape})] \rightarrow [\text{v128}]}$$

shape.extract_lane_sx[?] laneidx

- The lane index laneidx must be smaller than $\text{dim}(\text{shape})$.
- The instruction is valid with type $[\text{v128}] \rightarrow [\text{unpack}(\text{shape})]$.

$$\frac{\text{laneidx} < \text{dim}(\text{shape})}{\overline{C \vdash \text{shape.extract_lane_sx}^? \text{ laneidx} : [\text{v128}] \rightarrow [\text{unpack}(\text{shape})]}}$$

shape.replace_lane laneidx

- The lane index laneidx must be smaller than $\text{dim}(\text{shape})$.
- Let t be $\text{unpack}(\text{shape})$.
- The instruction is valid with type $[\text{v128 } t] \rightarrow [\text{v128}]$.

$$\frac{\text{laneidx} < \text{dim}(\text{shape})}{\overline{C \vdash \text{shape.replace_lane } \text{laneidx} : [\text{v128 } \text{unpack}(\text{shape})] \rightarrow [\text{v128}]}}$$

shape.vunop

- The instruction is valid with type $[\text{v128}] \rightarrow [\text{v128}]$.

$$\overline{C \vdash \text{shape.vunop} : [\text{v128}] \rightarrow [\text{v128}]}$$

shape.vbinop

- The instruction is valid with type $[\text{v128 } \text{v128}] \rightarrow [\text{v128}]$.

$$\overline{C \vdash \text{shape.vbinop} : [\text{v128 } \text{v128}] \rightarrow [\text{v128}]}$$

shape.vternop

- The instruction is valid with type $[\text{v128 } \text{v128 } \text{v128}] \rightarrow [\text{v128}]$.

$$\overline{C \vdash \text{shape.vternop} : [\text{v128 } \text{v128 } \text{v128}] \rightarrow [\text{v128}]}$$

shape.relaxed_laneselect

- The instruction is valid with type $[\text{v128 } \text{v128 } \text{v128}] \rightarrow [\text{v128}]$.

$$\overline{C \vdash \text{shape.relaxed_laneselect} : [\text{v128 } \text{v128 } \text{v128}] \rightarrow [\text{v128}]}$$

shape.vrelop

- The instruction is valid with type $[v128\ v128] \rightarrow [v128]$.

$$\overline{C \vdash \text{shape.vrelop} : [v128\ v128] \rightarrow [v128]}$$

ishape.vshiftop

- The instruction is valid with type $[v128\ i32] \rightarrow [v128]$.

$$\overline{C \vdash \text{ishape.vshiftop} : [v128\ i32] \rightarrow [v128]}$$

shape.vtestop

- The instruction is valid with type $[v128] \rightarrow [i32]$.

$$\overline{C \vdash \text{shape.vtestop} : [v128] \rightarrow [i32]}$$

shape.vcvtop_half?_shape_sx?_zero?

- The instruction is valid with type $[v128] \rightarrow [v128]$.

$$\overline{C \vdash \text{shape.vcvtop_half?_shape_sx?_zero?} : [v128] \rightarrow [v128]}$$

ishape₁.narrow_ishape₂_sx

- The instruction is valid with type $[v128\ v128] \rightarrow [v128]$.

$$\overline{C \vdash \text{ishape}_1.\text{narrow_ishape}_2.\text{sx} : [v128\ v128] \rightarrow [v128]}$$

ishape.bitmask

- The instruction is valid with type $[v128] \rightarrow [i32]$.

$$\overline{C \vdash \text{ishape.bitmask} : [v128] \rightarrow [i32]}$$

ishape₁.dot_ishape₂_s

- The instruction is valid with type $[v128\ v128] \rightarrow [v128]$.

$$\overline{C \vdash \text{ishape}_1.\text{dot_ishape}_2.\text{s} : [v128\ v128] \rightarrow [v128]}$$

ishape₁.dot_ishape₂_i7x16_s

- The instruction is valid with type $[v128\ v128] \rightarrow [v128]$.

$$\overline{C \vdash \text{ishape}_1.\text{dot_ishape}_2.\text{i7x16_s} : [v128\ v128] \rightarrow [v128]}$$

ishape₁.dot_ishape₂_i7x16_add__s

- The instruction is valid with type $[v128\ v128\ v128] \rightarrow [v128]$.

$$\frac{}{C \vdash \text{ishape}_1.\text{dot_ishape}_2.\text{i7x16_add_s} : [v128\ v128\ v128] \rightarrow [v128]}$$

ishape₁.extmul_half_ishape₂_sx

- The instruction is valid with type $[v128\ v128] \rightarrow [v128]$.

$$\frac{}{C \vdash \text{ishape}_1.\text{extmul_half_ishape}_2.\text{sx} : [v128\ v128] \rightarrow [v128]}$$

ishape₁.extadd_pairwise_ishape₂_sx

- The instruction is valid with type $[v128] \rightarrow [v128]$.

$$\frac{}{C \vdash \text{ishape}_1.\text{extadd_pairwise_ishape}_2.\text{sx} : [v128] \rightarrow [v128]}$$

3.4.7 Parametric Instructions

drop

- The instruction is valid with type $[t] \rightarrow []$, for any valid value type t .

$$\frac{C \vdash t \text{ ok}}{C \vdash \text{drop} : [t] \rightarrow []}$$

Note: Both *drop* and *select* without annotation are value-polymorphic instructions.

select (t^)?*

- If t^* is present, then:
 - The result type $[t^*]$ must be valid.
 - The length of t^* must be 1.
 - Then the instruction is valid with type $[t^*\ t^*\ i32] \rightarrow [t^*]$.
- Else:
 - The instruction is valid with type $[t\ t\ i32] \rightarrow [t]$, for any valid value type t that matches some number type or vector type.

$$\frac{C \vdash [t] \text{ ok}}{C \vdash \text{select } t : [t\ t\ i32] \rightarrow [t]} \quad \frac{C \vdash [t] \text{ ok} \quad C \vdash [t] \leq [\text{numtype}]}{C \vdash \text{select} : [t\ t\ i32] \rightarrow [t]} \quad \frac{C \vdash [t] \text{ ok} \quad C \vdash t \leq [\text{vectype}]}{C \vdash \text{select} : [t\ t\ i32] \rightarrow [t]}$$

Note: In future versions of WebAssembly, *select* may allow more than one value per choice.

3.4.8 Variable Instructions

`local.get` x

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $\text{init } t$ be the local type $C.\text{locals}[x]$.
- The initialization status init must be set.
- Then the instruction is valid with type $[] \rightarrow [t]$.

$$\frac{C.\text{locals}[x] = \text{set } t}{C \vdash \text{local.get } x : [] \rightarrow [t]}$$

`local.set` x

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $\text{init } t$ be the local type $C.\text{locals}[x]$.
- Then the instruction is valid with type $[t] \rightarrow_x []$.

$$\frac{C.\text{locals}[x] = \text{init } t}{C \vdash \text{local.set } x : [t] \rightarrow_x []}$$

`local.tee` x

- The local $C.\text{locals}[x]$ must be defined in the context.
- Let $\text{init } t$ be the local type $C.\text{locals}[x]$.
- Then the instruction is valid with type $[t] \rightarrow_x [t]$.

$$\frac{C.\text{locals}[x] = \text{init } t}{C \vdash \text{local.tee } x : [t] \rightarrow_x [t]}$$

`global.get` x

- The global $C.\text{globals}[x]$ must be defined in the context.
- Let $\text{mut } t$ be the global type $C.\text{globals}[x]$.
- Then the instruction is valid with type $[] \rightarrow [t]$.

$$\frac{C.\text{globals}[x] = \text{mut } t}{C \vdash \text{global.get } x : [] \rightarrow [t]}$$

`global.set` x

- The global $C.\text{globals}[x]$ must be defined in the context.
- Let $\text{mut } t$ be the global type $C.\text{globals}[x]$.
- The mutability mut must be `var`.
- Then the instruction is valid with type $[t] \rightarrow []$.

$$\frac{C.\text{globals}[x] = \text{var } t}{C \vdash \text{global.set } x : [t] \rightarrow []}$$

3.4.9 Table Instructions

`table.get x`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t be the **table type** $C.tables[x]$.
- Then the instruction is valid with type $[at] \rightarrow [t]$.

$$\frac{C.tables[x] = at \text{ limits } t}{C \vdash \text{table.get } x : [at] \rightarrow [t]}$$

`table.set x`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t be the **table type** $C.tables[x]$.
- Then the instruction is valid with type $[at \ t] \rightarrow []$.

$$\frac{C.tables[x] = at \text{ limits } t}{C \vdash \text{table.set } x : [at \ t] \rightarrow []}$$

`table.size x`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t be the **table type** $C.tables[x]$.
- Then the instruction is valid with type $[] \rightarrow [at]$.

$$\frac{C.tables[x] = at \text{ limits } t}{C \vdash \text{table.size } x : [] \rightarrow [at]}$$

`table.grow x`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t be the **table type** $C.tables[x]$.
- Then the instruction is valid with type $[t \ at] \rightarrow [at]$.

$$\frac{C.tables[x] = at \text{ limits } t}{C \vdash \text{table.grow } x : [t \ at] \rightarrow [at]}$$

`table.fill x`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t be the **table type** $C.tables[x]$.
- Then the instruction is valid with type $[at \ t \ at] \rightarrow []$.

$$\frac{C.tables[x] = at \text{ limits } t}{C \vdash \text{table.fill } x : [at \ t \ at] \rightarrow []}$$

`table.copy x y`

- The table $C.tables[x]$ must be defined in the context.
- Let at_1 *limits* t_1 be the table type $C.tables[x]$.
- The table $C.tables[y]$ must be defined in the context.
- Let at_2 *limits* t_2 be the table type $C.tables[y]$.
- The reference type t_2 must match t_1 .
- Let at be the minimum of at_1 and at_2
- Then the instruction is valid with type $[at_1\ at_2\ at] \rightarrow []$.

$$\frac{C.tables[x] = at\ limits\ t_1 \quad C.tables[y] = at\ limits\ t_2 \quad C \vdash t_2 \leq t_1}{C \vdash \text{table.copy } x\ y : [at_1\ at_2\ \text{atmin}(at_1, at_2)] \rightarrow []}$$

`table.init x y`

- The table $C.tables[x]$ must be defined in the context.
- Let at *limits* t_1 be the table type $C.tables[x]$.
- The element segment $C.elems[y]$ must be defined in the context.
- Let t_2 be the reference type $C.elems[y]$.
- The reference type t_2 must match t_1 .
- Then the instruction is valid with type $[at\ i32\ i32] \rightarrow []$.

$$\frac{C.tables[x] = at\ limits\ t_1 \quad C.elems[y] = t_2 \quad C \vdash t_2 \leq t_1}{C \vdash \text{table.init } x\ y : [at\ i32\ i32] \rightarrow []}$$

`elem.drop x`

- The element segment $C.elems[x]$ must be defined in the context.
- Then the instruction is valid with type $[] \rightarrow []$.

$$\frac{C.elems[x] = t}{C \vdash \text{elem.drop } x : [] \rightarrow []}$$

3.4.10 Memory Instructions

`t.load x memarg`

- The memory $C.mems[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.mems[x]$.
- The offset $memarg.offset$ must be less than $2^{|at|}$.
- The alignment $2^{memarg.align}$ must not be larger than the bit width of t divided by 8.
- Then the instruction is valid with type $[at] \rightarrow [t]$.

$$\frac{C.mems[x] = at\ limits \quad memarg.offset < 2^{|at|} \quad 2^{memarg.align} \leq |t|/8}{C \vdash \text{t.load } x\ memarg : [at] \rightarrow [t]}$$

t.loadN_{sx} x memarg

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset memarg.offset must be less than $2^{|at|}$.
- The alignment $2^{\text{memarg.align}}$ must not be larger than $N/8$.
- Then the instruction is valid with type $[at] \rightarrow [t]$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad \text{memarg.offset} < 2^{|at|} \quad 2^{\text{memarg.align}} \leq N/8}{C \vdash t.\text{loadN}_{sx} x \text{ memarg} : [at] \rightarrow [t]}$$

t.store x memarg

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset memarg.offset must be less than $2^{|at|}$.
- The alignment $2^{\text{memarg.align}}$ must not be larger than the bit width of t divided by 8.
- Then the instruction is valid with type $[at t] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad \text{memarg.offset} < 2^{|at|} \quad 2^{\text{memarg.align}} \leq |t|/8}{C \vdash t.\text{store } x \text{ memarg} : [at t] \rightarrow []}$$

t.storeN x memarg

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset memarg.offset must be less than $2^{|at|}$.
- The alignment $2^{\text{memarg.align}}$ must not be larger than $N/8$.
- Then the instruction is valid with type $[at t] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad \text{memarg.offset} < 2^{|at|} \quad 2^{\text{memarg.align}} \leq N/8}{C \vdash t.\text{storeN } x \text{ memarg} : [at t] \rightarrow []}$$

v128.loadN_{xM}_{sx} x memarg

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset memarg.offset must be less than $2^{|at|}$.
- The alignment $2^{\text{memarg.align}}$ must not be larger than $N/8 \cdot M$.
- Then the instruction is valid with type $[at] \rightarrow [v128]$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad \text{memarg.offset} < 2^{|at|} \quad 2^{\text{memarg.align}} \leq N/8 \cdot M}{C \vdash v128.\text{loadN}_{xM}_{sx} x \text{ memarg} : [at] \rightarrow [v128]}$$

`v128.loadN_splat x memarg`

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset $memarg.offset$ must be less than $2^{|at|}$.
- The alignment $2^{memarg.align}$ must not be larger than $N/8$.
- Then the instruction is valid with type $[at] \rightarrow [v128]$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad memarg.offset < 2^{|at|} \quad 2^{memarg.align} \leq N/8}{C \vdash v128.loadN_splat\ x\ memarg : [at] \rightarrow [v128]}$$

`v128.loadN_zero x memarg`

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset $memarg.offset$ must be less than $2^{|at|}$.
- The alignment $2^{memarg.align}$ must not be larger than $N/8$.
- Then the instruction is valid with type $[at] \rightarrow [v128]$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad memarg.offset < 2^{|at|} \quad 2^{memarg.align} \leq N/8}{C \vdash v128.loadN_zero\ x\ memarg : [at] \rightarrow [v128]}$$

`v128.loadN_lane x memarg laneidx`

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset $memarg.offset$ must be less than $2^{|at|}$.
- The alignment $2^{memarg.align}$ must not be larger than $N/8$.
- The lane index $laneidx$ must be smaller than $128/N$.
- Then the instruction is valid with type $[at\ v128] \rightarrow [v128]$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad memarg.offset < 2^{|at|} \quad 2^{memarg.align} \leq N/8 \quad laneidx < 128/N}{C \vdash v128.loadN_lane\ x\ memarg\ laneidx : [at\ v128] \rightarrow [v128]}$$

`v128.storeN_lane x memarg laneidx`

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let at *limits* be the memory type $C.\text{mems}[x]$.
- The offset $memarg.offset$ must be less than $2^{|at|}$.
- The alignment $2^{memarg.align}$ must not be larger than $N/8$.
- The lane index $laneidx$ must be smaller than $128/N$.
- Then the instruction is valid with type $[at\ v128] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at \text{ limits} \quad memarg.offset < 2^{|at|} \quad 2^{memarg.align} \leq N/8 \quad laneidx < 128/N}{C \vdash v128.storeN_lane\ x\ memarg\ laneidx : [at\ v128] \rightarrow []}$$

memory.size x

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let $at\ limits$ be the memory type $C.\text{mems}[x]$.
- Then the instruction is valid with type $[] \rightarrow [at]$.

$$\frac{C.\text{mems}[x] = at\ limits}{C \vdash \text{memory.size } x : [] \rightarrow [at]}$$

memory.grow x

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let $at\ limits$ be the memory type $C.\text{mems}[x]$.
- Then the instruction is valid with type $[at] \rightarrow [at]$.

$$\frac{C.\text{mems}[x] = at\ limits}{C \vdash \text{memory.grow } x : [at] \rightarrow [at]}$$

memory.fill x

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let $at\ limits$ be the memory type $C.\text{mems}[x]$.
- Then the instruction is valid with type $[at\ i32\ at] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at\ limits}{C \vdash \text{memory.fill } x : [at\ i32\ at] \rightarrow []}$$

memory.copy $x\ y$

- The memory $C.\text{mems}[x]$ must be defined in the context.
- The memory $C.\text{mems}[y]$ must be defined in the context.
- Let $at_x\ limits_x$ be the memory type $C.\text{mems}[x]$.
- Let $at_y\ limits_y$ be the memory type $C.\text{mems}[y]$.
- Let at be the minimum of at_x and at_y .
- Then the instruction is valid with type $[at_x\ at_y\ at] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at_x\ limits_x \quad C.\text{mems}[y] = at_y\ limits_y}{C \vdash \text{memory.copy } x\ y : [at_x\ at_y\ \text{atmin}(at_x, at_y)] \rightarrow []}$$

memory.init $x\ y$

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Let $at\ limits$ be the memory type $C.\text{mems}[x]$.
- The data segment $C.\text{datas}[y]$ must be defined in the context.
- Then the instruction is valid with type $[at\ i32\ i32] \rightarrow []$.

$$\frac{C.\text{mems}[x] = at\ limits \quad C.\text{datas}[y] = \text{ok}}{C \vdash \text{memory.init } x\ y : [at\ i32\ i32] \rightarrow []}$$

`data.drop x`

- The data segment $C.\text{datas}[x]$ must be defined in the context.
- Then the instruction is valid with type $[] \rightarrow []$.

$$\frac{C.\text{datas}[x] = \text{ok}}{C \vdash \text{data.drop } x : [] \rightarrow []}$$

3.4.11 Control Instructions

`nop`

- The instruction is valid with type $[] \rightarrow []$.

$$\overline{C \vdash \text{nop} : [] \rightarrow []}$$

`unreachable`

- The instruction is valid with any [valid type](#) of the form $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{C \vdash [t_1^*] \rightarrow [t_2^*] \text{ ok}}{C \vdash \text{unreachable} : [t_1^*] \rightarrow [t_2^*]}$$

Note: The `unreachable` instruction is [stack-polymorphic](#).

`block blocktype instr* end`

- The `block type` must be [valid](#) as some [instruction type](#) $[t_1^*] \rightarrow [t_2^*]$.
- Let C' be the same [context](#) as C , but with the [result type](#) $[t_2^*]$ prepended to the [labels](#) vector.
- Under context C' , the instruction sequence instr^* must be [valid](#) with type $[t_1^*] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels } [t_2^*] \vdash \text{instr}^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{block blocktype instr}^* \text{ end} : [t_1^*] \rightarrow [t_2^*]}$$

Note: The notation $C, \text{labels } [t^*]$ inserts the new label type at index 0, shifting all others.

`loop blocktype instr* end`

- The `block type` must be [valid](#) as some [instruction type](#) $[t_1^*] \rightarrow_{x^*} [t_2^*]$.
- Let C' be the same [context](#) as C , but with the [result type](#) $[t_1^*]$ prepended to the [labels](#) vector.
- Under context C' , the instruction sequence instr^* must be [valid](#) with type $[t_1^*] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels } [t_1^*] \vdash \text{instr}^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{loop blocktype instr}^* \text{ end} : [t_1^*] \rightarrow [t_2^*]}$$

Note: The notation $C, \text{labels } [t^*]$ inserts the new label type at index 0, shifting all others.

if *blocktype* *instr*₁^{*} else *instr*₂^{*} end

- The **block type** must be **valid** as some **instruction type** $[t_1^*] \rightarrow [t_2^*]$.
- Let C' be the same **context** as C , but with the **result type** $[t_2^*]$ prepended to the **labels** vector.
- Under context C' , the instruction sequence *instr*₁^{*} must be **valid** with type $[t_1^*] \rightarrow [t_2^*]$.
- Under context C' , the instruction sequence *instr*₂^{*} must be **valid** with type $[t_1^*] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[t_1^* \text{ i32}] \rightarrow [t_2^*]$.

$$\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels}[t_2^*] \vdash \text{instr}_1^* : [t_1^*] \rightarrow [t_2^*] \quad C, \text{labels}[t_2^*] \vdash \text{instr}_2^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{if } \text{blocktype } \text{instr}_1^* \text{ else } \text{instr}_2^* \text{ end} : [t_1^* \text{ i32}] \rightarrow [t_2^*]}$$

Note: The notation $C, \text{labels}[t^*]$ inserts the new label type at index 0, shifting all others.

try_table *blocktype* *catch*^{*} *instr*^{*} end

- The **block type** must be **valid** as some **function type** $[t_1^*] \rightarrow [t_2^*]$.
- For every **catch clause** *catch*_{*i*} in *catch*^{*}, *catch*_{*i*} must be **valid**.
- Let C' be the same **context** as C , but with the **result type** $[t_2^*]$ prepended to the **labels** vector.
- Under context C' , the instruction sequence *instr*^{*} must be **valid** with type $[t_1^*] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{C \vdash \text{blocktype} : [t_1^*] \rightarrow [t_2^*] \quad (C \vdash \text{catch ok})^* \quad C, \text{labels}[t_2^*] \vdash \text{instr}^* : [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{try_table } \text{blocktype } \text{catch}^* \text{ instr}^* \text{ end} : [t_1^*] \rightarrow [t_2^*]}$$

Note: The notation $C, \text{labels}[t^*]$ inserts the new label type at index 0, shifting all others.

catch *x* *l*

- The tag $C.\text{tags}[x]$ must be defined in the context.
- Let $[t^*] \rightarrow [t'^*]$ be the **expansion** of the **tag type** $C.\text{tags}[x]$.
- The **result type** $[t'^*]$ must be empty.
- The label $C.\text{labels}[l]$ must be defined in the context.
- The **result type** $[t^*]$ must be the same as $C.\text{labels}[l]$.
- Then the catch clause is valid.

$$\frac{\text{expand}(C.\text{tags}[x]) = [t^*] \rightarrow [] \quad C.\text{labels}[l] = [t^*]}{C \vdash \text{catch } x \text{ } l \text{ ok}}$$

`catch_ref x l`

- The tag $C.\text{tags}[x]$ must be defined in the context.
- Let $[t^*] \rightarrow [t'^*]$ be the [expansion](#) of the tag type $C.\text{tags}[x]$.
- The result type $[t'^*]$ must be empty.
- The label $C.\text{labels}[l]$ must be defined in the context.
- The result type $[t^* (\text{ref exn})]$ must [match](#) $C.\text{labels}[l]$.
- Then the catch clause is valid.

$$\frac{\text{expand}(C.\text{tags}[x]) = [t^*] \rightarrow [] \quad C \vdash [t^* (\text{ref exn})] \leq C.\text{labels}[l]}{C \vdash \text{catch_ref } x \ l \text{ ok}}$$

`catch_all l`

- The label $C.\text{labels}[l]$ must be defined in the context.
- The result type $C.\text{labels}[l]$ must be empty.
- Then the catch clause is valid.

$$\frac{C.\text{labels}[l] = []}{C \vdash \text{catch_all } l \text{ ok}}$$

`catch_all_ref l`

- The label $C.\text{labels}[l]$ must be defined in the context.
- The result type $[(\text{ref exn})]$ must [match](#) $C.\text{labels}[l]$.
- Then the catch clause is valid.

$$\frac{C \vdash [(\text{ref exn})] \leq C.\text{labels}[l]}{C \vdash \text{catch_all_ref } l \text{ ok}}$$

`br l`

- The label $C.\text{labels}[l]$ must be defined in the context.
- Let $[t^*]$ be the result type $C.\text{labels}[l]$.
- Then the instruction is valid with any [valid](#) type of the form $[t_1^* t^*] \rightarrow [t_2^*]$.

$$\frac{C.\text{labels}[l] = [t^*] \quad C \vdash [t_1^* t^*] \rightarrow [t_2^*] \text{ ok}}{C \vdash \text{br } l : [t_1^* t^*] \rightarrow [t_2^*]}$$

Note: The label index space in the context C contains the most recent label type first, so that $C.\text{labels}[l]$ performs a relative lookup as expected.

The `br` instruction is [stack-polymorphic](#).

`br_if l`

- The label $C.\text{labels}[l]$ must be defined in the context.
- Let $[t^*]$ be the [result type](#) $C.\text{labels}[l]$.
- Then the instruction is valid with type $[t^* \text{ i32}] \rightarrow [t^*]$.

$$\frac{C.\text{labels}[l] = [t^*]}{C \vdash \text{br_if } l : [t^* \text{ i32}] \rightarrow [t^*]}$$

Note: The [label index](#) space in the [context](#) C contains the most recent label type first, so that $C.\text{labels}[l]$ performs a relative lookup as expected.

`br_table l* lN`

- The label $C.\text{labels}[l_N]$ must be defined in the context.
- For each label l_i in l^* , the label $C.\text{labels}[l_i]$ must be defined in the context.
- There must be a sequence t^* of [value types](#), such that:
 - The result type $[t^*]$ [matches](#) $C.\text{labels}[l_N]$.
 - For all l_i in l^* , the result type $[t^*]$ [matches](#) $C.\text{labels}[l_i]$.
- Then the instruction is valid with any [valid type](#) of the form $[t_1^* t^* \text{ i32}] \rightarrow [t_2^*]$.

$$\frac{(C \vdash [t^*] \leq C.\text{labels}[l])^* \quad C \vdash [t^*] \leq C.\text{labels}[l_N] \quad C \vdash [t_1^* t^* \text{ i32}] \rightarrow [t_2^*] \text{ ok}}{C \vdash \text{br_table } l^* l_N : [t_1^* t^* \text{ i32}] \rightarrow [t_2^*]}$$

Note: The [label index](#) space in the [context](#) C contains the most recent label first, so that $C.\text{labels}[l_i]$ performs a relative lookup as expected.

The `br_table` instruction is [stack-polymorphic](#).

Furthermore, the [result type](#) $[t^*]$ is also chosen non-deterministically in this rule. Although it may seem necessary to compute $[t^*]$ as the greatest lower bound of all label types in practice, a simple [linear algorithm](#) does not require this.

`br_on_null l`

- The label $C.\text{labels}[l]$ must be defined in the context.
- Let $[t^*]$ be the [result type](#) $C.\text{labels}[l]$.
- Then the instruction is valid with type $[t^* (\text{ref null } ht)] \rightarrow [t^* (\text{ref } ht)]$ for any [valid heap type](#) ht .

$$\frac{C.\text{labels}[l] = [t^*] \quad C \vdash ht \text{ ok}}{C \vdash \text{br_on_null } l : [t^* (\text{ref null } ht)] \rightarrow [t^* (\text{ref } ht)]}$$

`br_on_non_null l`

- The label $C.labels[l]$ must be defined in the context.
- Let $[t'^*]$ be the **result type** $C.labels[l]$.
- The result type $[t'^*]$ must contain at least one type.
- Let the **value type** t_l be the last element in the sequence t'^* , and $[t^*]$ the remainder of the sequence preceding it.
- The value type t_l must be a reference type of the form **ref null**² ht .
- Then the instruction is valid with type $[t^* (\text{ref null } ht)] \rightarrow [t'^*]$.

$$\frac{C.labels[l] = [t^* (\text{ref } ht)]}{C \vdash \text{br_on_non_null } l : [t^* (\text{ref null } ht)] \rightarrow [t'^*]}$$

`br_on_cast l rt1 rt2`

- The label $C.labels[l]$ must be defined in the context.
- Let $[t_l^*]$ be the **result type** $C.labels[l]$.
- The type sequence t_l^* must be of the form $t^* rt'$.
- The **reference type** rt_1 must be **valid**.
- The **reference type** rt_2 must be **valid**.
- The **reference type** rt_2 must **match** rt_1 .
- The **reference type** rt_2 must **match** rt' .
- Let rt'_1 be the **type difference** between rt_1 and rt_2 .
- Then the instruction is valid with type $[t^* rt_1] \rightarrow [t^* rt'_1]$.

$$\frac{C.labels[l] = [t^* rt] \quad C \vdash rt_1 \text{ ok} \quad C \vdash rt_2 \text{ ok} \quad C \vdash rt_2 \leq rt_1 \quad C \vdash rt_2 \leq rt}{C \vdash \text{br_on_cast } l \text{ } rt_1 \text{ } rt_2 : [t^* rt_1] \rightarrow [t^* rt_1 \setminus rt_2]}$$

`br_on_cast_fail l rt1 rt2`

- The label $C.labels[l]$ must be defined in the context.
- Let $[t_l^*]$ be the **result type** $C.labels[l]$.
- The type sequence t_l^* must be of the form $t^* rt'$.
- The **reference type** rt_1 must be **valid**.
- The **reference type** rt_2 must be **valid**.
- The **reference type** rt_2 must **match** rt_1 .
- Let rt'_1 be the **type difference** between rt_1 and rt_2 .
- The **reference type** rt'_1 must **match** rt' .
- Then the instruction is valid with type $[t^* rt_1] \rightarrow [t^* rt_2]$.

$$\frac{C.labels[l] = [t^* rt] \quad C \vdash rt_1 \text{ ok} \quad C \vdash rt_2 \text{ ok} \quad C \vdash rt_2 \leq rt_1 \quad C \vdash rt_1 \setminus rt_2 \leq rt}{C \vdash \text{br_on_cast_fail } l \text{ } rt_1 \text{ } rt_2 : [t^* rt_1] \rightarrow [t^* rt_2]}$$

return

- The return type $C.\text{return}$ must not be absent in the context.
- Let $[t^*]$ be the **result type** of $C.\text{return}$.
- Then the instruction is valid with any **valid** type of the form $[t_1^* t^*] \rightarrow [t_2^*]$.

$$\frac{C.\text{return} = [t^*] \quad C \vdash [t_1^* t^*] \rightarrow [t_2^*] \text{ ok}}{C \vdash \text{return} : [t_1^* t^*] \rightarrow [t_2^*]}$$

Note: The **return** instruction is **stack-polymorphic**.

$C.\text{return}$ is absent (set to ϵ) when validating an **expression** that is not a function body. This differs from it being set to the empty result type ($[\epsilon]$), which is the case for functions not returning anything.

call x

- The function $C.\text{funcs}[x]$ must be defined in the context.
- The **expansion** of $C.\text{funcs}[x]$ must be a **function type** $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- Then the instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{\text{expand}(C.\text{funcs}[x]) = \text{func } [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{call } x : [t_1^*] \rightarrow [t_2^*]}$$

call_ref x

- The type $C.\text{types}[x]$ must be defined in the context.
- The **expansion** of $C.\text{funcs}[x]$ must be a **function type** $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- Then the instruction is valid with type $[t_1^* (\text{ref null } x)] \rightarrow [t_2^*]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{func } [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{call_ref } x : [t_1^* (\text{ref null } x)] \rightarrow [t_2^*]}$$

call_indirect $x y$

- The table $C.\text{tables}[x]$ must be defined in the context.
- Let *at limits* t be the **table type** $C.\text{tables}[x]$.
- The **reference type** t must **match type** **ref null func**.
- The type $C.\text{types}[y]$ must be defined in the context.
- The **expansion** of $C.\text{types}[y]$ must be a **function type** $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- Then the instruction is valid with type $[t_1^* \text{ at}] \rightarrow [t_2^*]$.

$$\frac{C.\text{tables}[x] = \text{at limits } t \quad C \vdash t \leq \text{ref null func} \quad \text{expand}(C.\text{types}[y]) = \text{func } [t_1^*] \rightarrow [t_2^*]}{C \vdash \text{call_indirect } x y : [t_1^* \text{ at}] \rightarrow [t_2^*]}$$

`return_call` x

- The return type $C.\text{return}$ must not be absent in the context.
- The function $C.\text{funcs}[x]$ must be defined in the context.
- The expansion of $C.\text{funcs}[x]$ must be a function type $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- The result type $[t_2^*]$ must match $C.\text{return}$.
- Then the instruction is valid with any valid type $[t_3^* t_1^*] \rightarrow [t_4^*]$.

$$\frac{\text{expand}(C.\text{funcs}[x]) = \text{func } [t_1^*] \rightarrow [t_2^*] \quad C \vdash [t_2^*] \leq C.\text{return} \quad C \vdash [t_3^* t_1^*] \rightarrow [t_4^*] \text{ ok}}{C \vdash \text{return_call } x : [t_3^* t_1^*] \rightarrow [t_4^*]}$$

Note: The `return_call` instruction is stack-polymorphic.

`return_call_ref` x

- The type $C.\text{types}[x]$ must be defined in the context.
- The expansion of $C.\text{types}[x]$ must be a function type $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- The result type $[t_2^*]$ must match $C.\text{return}$.
- Then the instruction is valid with any valid type $[t_3^* t_1^* (\text{ref null } x)] \rightarrow [t_4^*]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{func } [t_1^*] \rightarrow [t_2^*] \quad C \vdash [t_2^*] \leq C.\text{return} \quad C \vdash [t_3^* t_1^* (\text{ref null } x)] \rightarrow [t_4^*] \text{ ok}}{C \vdash \text{call_ref } x : [t_3^* t_1^* (\text{ref null } x)] \rightarrow [t_4^*]}$$

Note: The `return_call_ref` instruction is stack-polymorphic.

`return_call_indirect` $x y$

- The return type $C.\text{return}$ must not be empty in the context.
- The table $C.\text{tables}[x]$ must be defined in the context.
- Let at limits t be the table type $C.\text{tables}[x]$.
- The reference type t must match type `ref null func`.
- The type $C.\text{types}[y]$ must be defined in the context.
- The expansion of $C.\text{types}[y]$ must be a function type $\text{func } [t_1^*] \rightarrow [t_2^*]$.
- The result type $[t_2^*]$ must match $C.\text{return}$.
- Then the instruction is valid with type $[t_3^* t_1^* at] \rightarrow [t_4^*]$, for any sequences of value types t_3^* and t_4^* .

$$\frac{C.\text{tables}[x] = at \text{ limits } t \quad C \vdash t \leq \text{ref null func} \quad \text{expand}(C.\text{types}[y]) = \text{func } [t_1^*] \rightarrow [t_2^*] \quad C \vdash [t_2^*] \leq C.\text{return} \quad C \vdash [t_3^* t_1^* at] \rightarrow [t_4^*] \text{ ok}}{C \vdash \text{return_call_indirect } x y : [t_3^* t_1^* at] \rightarrow [t_4^*]}$$

Note: The `return_call_indirect` instruction is stack-polymorphic.

`throw x`

- The tag $C.\text{tags}[x]$ must be defined in the context.
- Let $[t^*] \rightarrow [t'^*]$ be the tag type $C.\text{tags}[x]$.
- The result type $[t'^*]$ must be empty.
- Then the instruction is valid with type $[t_1^* t^*] \rightarrow [t_2^*]$, for any sequences of value types t_1^* and t_2^* .

$$\frac{C.\text{tags}[x] = [t^*] \rightarrow []}{C \vdash \text{throw } x : [t_1^* t^*] \rightarrow [t_2^*]}$$

Note: The `throw` instruction is stack-polymorphic.

`throw_ref`

- The instruction is valid with type $[t_1^* \text{exnref}] \rightarrow [t_2^*]$, for any sequences of value types t_1^* and t_2^* .

$$C \vdash \text{throw_ref} : [t_1^* \text{exnref}] \rightarrow [t_2^*]$$

Note: The `throw_ref` instruction is stack-polymorphic.

3.4.12 Instruction Sequences

Typing of instruction sequences is defined recursively.

Empty Instruction Sequence: ϵ

- The empty instruction sequence is valid with type $[] \rightarrow []$.

$$C \vdash \epsilon : [] \rightarrow []$$

Non-empty Instruction Sequence: $\text{instr } \text{instr}'^*$

- The instruction instr must be valid with some type $[t_1^*] \rightarrow_{x_1^*} [t_2^*]$.
- Let C' be the same context as C , but with:
 - **locals** the same as in C , except that for every local index x in x_1^* , the local type $\text{locals}[x]$ has been updated to **initialization status set**.
- Under the context C' , the instruction sequence instr'^* must be valid with some type $[t_2^*] \rightarrow_{x_2^*} [t_3^*]$.
- Then the combined instruction sequence is valid with type $[t_1^*] \rightarrow_{x_1^* x_2^*} [t_3^*]$.

$$\frac{C \vdash \text{instr} : [t_1^*] \rightarrow_{x_1^*} [t_2^*] \quad (C.\text{locals}[x_1] = \text{init } t)^* \quad C' \vdash \text{instr}'^* : [t_2^*] \rightarrow_{x_2^*} [t_3^*] \quad C' = C \text{ (with } C.\text{locals}[x_1] = \text{set } t)^*}{C \vdash \text{instr } \text{instr}'^* : [t_1^*] \rightarrow_{x_1^* x_2^*} [t_3^*]}$$

Subsumption for $instr^*$

- The instruction sequence $instr^*$ must be valid with some type $instrtype$.
- The instruction type $instrtype'$: must be a **valid**
- The instruction type $instrtype$ must **match** the type $instrtype'$.
- Then the instruction sequence $instr^*$ is also valid with type $instrtype'$.

$$\frac{C \vdash instr : instrtype \quad C \vdash instrtype' \text{ ok} \quad C \vdash instrtype \leq instrtype'}{C \vdash instr^* : instrtype'}$$

Note: In combination with the previous rule, subsumption allows to compose instructions whose types would not directly fit otherwise. For example, consider the instruction sequence

(i32.const 1) (i32.const 1) i32.add

To type this sequence, its subsequence (i32.const 1) i32.add needs to be valid with an intermediate type. But the direct type of (i32.const 1) is $[] \rightarrow [i32]$, not matching the two inputs expected by i32.add. The subsumption rule allows to weaken the type of (i32.const 1) to the supertype $[i32] \rightarrow [i32 \ i32]$, such that it can be composed with i32.add and yields the intermediate type $[i32] \rightarrow [i32]$ for the subsequence. That can in turn be composed with the first constant.

Furthermore, subsumption allows to drop init variables x^* from the instruction type in a context where they are not needed, for example, at the end of the body of a **block**.

3.4.13 Expressions

Expressions $expr$ are classified by **result types** of the form $[t^*]$.

$instr^* \text{ end}$

- The instruction sequence $instr^*$ must be **valid** with type $[] \rightarrow [t^*]$.
- Then the expression is valid with **result type** $[t^*]$.

$$\frac{C \vdash instr^* : [] \rightarrow [t^*]}{C \vdash instr^* \text{ end} : [t^*]}$$

Constant Expressions

- In a *constant* expression $instr^* \text{ end}$ all instructions in $instr^*$ must be constant.
- A constant instruction $instr$ must be:
 - either of the form $t.\text{const } c$,
 - or of the form $inn.\text{ibinop}$, where ibinop is limited to **add**, **sub**, or **mul**.
 - or of the form **ref.null**,
 - or of the form **ref.i31**,
 - or of the form **ref.func** x ,
 - or of the form **struct.new** x ,
 - or of the form **struct.new_default** x ,
 - or of the form **array.new** x ,

- or of the form `array.new_default x`,
- or of the form `array.new_fixed x`,
- or of the form `any.convert_extern`,
- or of the form `extern.convert_any`,
- or of the form `global.get x`, in which case $C.\text{globals}[x]$ must be a **global type** of the form `const t`.

$$\begin{array}{c}
\frac{(C \vdash \text{instr const})^*}{C \vdash \text{instr}^* \text{ end const}} \\
\frac{C \vdash t.\text{const } c \text{ const} \quad \text{ibinop} \in \{\text{add}, \text{sub}, \text{mul}\}}{C \vdash \text{inn.ibinop const}} \\
\frac{}{C \vdash \text{ref.null } t \text{ const}} \quad \frac{}{C \vdash \text{ref.i31 const}} \quad \frac{}{C \vdash \text{ref.func } x \text{ const}} \\
\frac{}{C \vdash \text{struct.new } x \text{ const}} \quad \frac{}{C \vdash \text{struct.new_default } x \text{ const}} \\
\frac{}{C \vdash \text{array.new } x \text{ const}} \quad \frac{}{C \vdash \text{array.new_default } x \text{ const}} \quad \frac{}{C \vdash \text{array.new_fixed } x \text{ const}} \\
\frac{}{C \vdash \text{any.convert_extern const}} \quad \frac{}{C \vdash \text{extern.convert_any const}} \\
\frac{C.\text{globals}[x] = \text{const } t}{C \vdash \text{global.get } x \text{ const}}
\end{array}$$

Note: Currently, constant expressions occurring in **globals** are further constrained in that contained `global.get` instructions are only allowed to refer to *imported* or *previously defined* globals. Constant expressions occurring in **tables** may only have `global.get` instructions that refer to *imported* globals. This is enforced in the **validation rule for modules** by constraining the context C accordingly.

The definition of constant expression may be extended in future versions of WebAssembly.

3.5 Modules

Modules are valid when all the components they contain are valid. Furthermore, most definitions are themselves classified with a suitable type.

3.5.1 Types

The sequence of **types** defined in a module is validated incrementally, yielding a suitable **context**.

*type**

- If the sequence is empty, then:
 - The **context** C must be empty.
 - Then the type sequence is valid.
- Otherwise:
 - Let the **recursive type** *rectype* be the last element in the sequence.
 - The sequence without *rectype* must be valid for some context C' .
 - Let the **type index** x be the length of $C'.\text{types}$, i.e., the first type index free in C' .
 - Let the sequence of **defined types** *deftype** be the result $\text{roll}_x^*(\text{rectype})$ of **rolling up** into its sequence of defined types.

- The recursive type *rectype* must be **valid** under the context *C* for type index *x*.
- The current context *C* be the same as *C'*, but with *deftype*^{*} appended to *types*.
- Then the type sequence is valid.

$$\frac{\overline{\{\} \vdash \epsilon \text{ ok}} \quad C' \vdash \text{type}^* \text{ ok} \quad C = C' \text{ with } \text{types} = C'.\text{types} \text{ roll}_{|C'.\text{types}|}^*(\text{rectype}) \quad C \vdash \text{rectype ok}(|C'.\text{types}|)}{C \vdash \text{type}^* \text{ rectype ok}}$$

Note: Despite the appearance, the context *C* is effectively an `_output_` of this judgement.

3.5.2 Functions

Functions *func* are classified by **defined types** that **expand** to **function types** of the form $\text{func } [t_1^*] \rightarrow [t_2^*]$.

$\{\text{type } x, \text{locals } t^*, \text{body } \text{expr}\}$

- The defined type $C.\text{types}[x]$ must be a function type.
- Let $\text{func } [t_1^*] \rightarrow [t_2^*]$ be the **expansion** of the defined type $C.\text{types}[x]$.
- For each local declared by a value type *t* in *t*^{*}:
 - The local for type *t* must be **valid** with local type *localtype*_{*i*}.
- Let *localtype*^{*} be the concatenation of all *localtype*_{*i*}.
- Let *C'* be the same **context** as *C*, but with:
 - **locals** set to the sequence of **value types** $(\text{set } t_1)^* \text{ localtype}^*$, concatenating parameters and locals,
 - **labels** set to the singular sequence containing only **result type** $[t_2^*]$.
 - **return** set to the **result type** $[t_2^*]$.
- Under the context *C'*, the expression *expr* must be valid with type $[t_2^*]$.
- Then the function definition is valid with type $C.\text{types}[x]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{func } [t_1^*] \rightarrow [t_2^*] \quad (C \vdash \{\text{type } t\} : \text{init } t)^* \quad C, \text{locals } (\text{set } t_1)^* (\text{init } t)^*, \text{labels } [t_2^*], \text{return } [t_2^*] \vdash \text{expr}}{C \vdash \{\text{type } x, \text{locals } \{\text{type } t\}^*, \text{body } \text{expr}\} : C.\text{types}[x]}$$

3.5.3 Locals

Locals are classified with **local types**.

$\{\text{type } \text{valtype}\}$

- The value type *valtype* must be **valid**.
- If *valtype* is **defaultable**, then:
 - The local is valid with **local type set** *valtype*.
- Else:
 - The local is valid with **local type unset** *valtype*.

$$\frac{C \vdash t \text{ ok} \quad C \vdash t \text{ defaulttable}}{C \vdash \{\text{type } t\} : \text{set } t}$$

$$\frac{C \vdash t \text{ ok}}{C \vdash \{\text{type } t\} : \text{unset } t}$$

Note: For cases where both rules are applicable, the former yields the more permissable type.

3.5.4 Tables

Tables *table* are classified by *table types*.

$\{\text{type } \textit{tabletype}, \text{init } \textit{expr}\}$

- The table type *tabletype* must be valid.
- Let *t* be the element reference type of *tabletype*.
- The expression *expr* must be valid with result type $[t]$.
- The expression *expr* must be constant.
- Then the table definition is valid with type *tabletype*.

$$\frac{C \vdash \textit{tabletype} \text{ ok} \quad \textit{tabletype} = \textit{limits } t \quad C \vdash \textit{expr} : [t] \quad C \vdash \textit{expr} \text{ const}}{C \vdash \{\text{type } \textit{tabletype}, \text{init } \textit{expr}\} : \textit{tabletype}}$$

3.5.5 Memories

Memories *mem* are classified by *memory types*.

$\{\text{type } \textit{memtype}\}$

- The memory type *memtype* must be valid.
- Then the memory definition is valid with type *memtype*.

$$\frac{C \vdash \textit{memtype} \text{ ok}}{C \vdash \{\text{type } \textit{memtype}\} : \textit{memtype}}$$

3.5.6 Globals

Globals *global* are classified by *global types* of the form *mut t*.

Sequences of globals are handled incrementally, such that each definition has access to previous definitions.

$\{\text{type } \textit{mut } t, \text{init } \textit{expr}\}$

- The global type $\textit{mut } t$ must be valid.
- The expression \textit{expr} must be valid with result type $[t]$.
- The expression \textit{expr} must be constant.
- Then the global definition is valid with type $\textit{mut } t$.

$$\frac{C \vdash \textit{mut } t \text{ ok} \quad C \vdash \textit{expr} : [t] \quad C \vdash \textit{expr} \text{ const}}{C \vdash \{\text{type } \textit{mut } t, \text{init } \textit{expr}\} : \textit{mut } t}$$

\textit{global}^*

- If the sequence is empty, then it is valid with the empty sequence of global types.
- Else:
 - The first global definition must be valid with some type global type gt_1 .
 - Let C' be the same context as C , but with the global type gt_1 appended to the globals vector.
 - Under context C' , the remainder of the sequence must be valid with some sequence gt^* of global types.
 - Then the sequence is valid with the sequence of global types consisting of gt_1 prepended to gt^* .

$$\frac{C \vdash \textit{global}_1 : gt_1 \quad C \oplus \{\text{globals } gt_1\} \vdash \textit{global}^* : gt^*}{C \vdash \textit{global}_1 \textit{ global}^* : gt_1 \textit{ gt}^*}$$

3.5.7 Tags

Tags \textit{tag} are classified by their tag type, each containing an index to a function type with empty result.

$\{\text{type } x\}$

- The type $C.\text{types}[x]$ must be defined in the context.
- Let $[t^*] \rightarrow [t'^*]$ be the function type $C.\text{types}[x]$.
- The sequence t'^* must be empty.
- Then the tag definition is valid with tag type $[t^*] \rightarrow []$.

$$\frac{C.\text{types}[x] = [t^*] \rightarrow []}{C \vdash \{\text{type } x\} : [t^*] \rightarrow []}$$

Note: Future versions of WebAssembly might allow non-empty return types for tags.

3.5.8 Element Segments

Element segments *elem* are classified by the [reference type](#) of their elements.

$\{\text{type } t, \text{init } e^*, \text{mode } \textit{elemmode}\}$

- The [reference type](#) t must be [valid](#).
- For each e_i in e^* :
 - The expression e_i must be [valid](#) with some [result type](#) $[t]$.
 - The expression e_i must be [constant](#).
- The element mode *elemmode* must be [valid](#) with some [reference type](#) t' .
- The [reference type](#) t must [match](#) the [reference type](#) t' .
- Then the element segment is [valid](#) with [reference type](#) t .

$$\frac{C \vdash t \text{ ok} \quad (C \vdash e : [t])^* \quad (C \vdash e \text{ const})^* \quad C \vdash \textit{elemmode} : t' \quad C \vdash t \leq t'}{C \vdash \{\text{type } t, \text{init } e^*, \text{mode } \textit{elemmode}\} : t}$$

[passive](#)

- The element mode is [valid](#) with any [valid reference type](#).

$$\frac{C \vdash \textit{reftype} \text{ ok}}{C \vdash \textit{passive} : \textit{reftype}}$$

[active](#) $\{\text{table } x, \text{offset } \textit{expr}\}$

- The table $C.\text{tables}[x]$ must be defined in the context.
- Let *limits* t be the [table type](#) $C.\text{tables}[x]$.
- The expression *expr* must be [valid](#) with [result type](#) $[i32]$.
- The expression *expr* must be [constant](#).
- Then the element mode is [valid](#) with [reference type](#) t .

$$\frac{C.\text{tables}[x] = \textit{limits } t \quad C \vdash \textit{expr} : [i32] \quad C \vdash \textit{expr} \text{ const}}{C \vdash \text{active } \{\text{table } x, \text{offset } \textit{expr}\} : t}$$

[declarative](#)

- The element mode is [valid](#) with any [valid reference type](#).

$$\frac{C \vdash \textit{reftype} \text{ ok}}{C \vdash \text{declarative} : \textit{reftype}}$$

3.5.9 Data Segments

Data segments *data* are not classified by any type but merely checked for well-formedness.

$\{\text{init } b^*, \text{mode } \textit{datamode}\}$

- The data mode *datamode* must be valid.
- Then the data segment is valid.

$$\frac{C \vdash \textit{datamode} \text{ ok}}{C \vdash \{\text{init } b^*, \text{mode } \textit{datamode}\} \text{ ok}}$$

passive

- The data mode is valid.

$$\frac{}{C \vdash \textit{passive} \text{ ok}}$$

active $\{\text{memory } x, \text{offset } \textit{expr}\}$

- The memory $C.\text{mems}[x]$ must be defined in the context.
- The expression *expr* must be *valid* with *result type* $[i32]$.
- The expression *expr* must be *constant*.
- Then the data mode is valid.

$$\frac{C.\text{mems}[x] = \textit{limits} \quad C \vdash \textit{expr} : [i32] \quad C \vdash \textit{expr} \text{ const}}{C \vdash \text{active } \{\text{memory } x, \text{offset } \textit{expr}\} \text{ ok}}$$

3.5.10 Start Function

Start function declarations *start* are not classified by any type.

$\{\text{func } x\}$

- The function $C.\text{funcs}[x]$ must be defined in the context.
- The *expansion* of $C.\text{funcs}[x]$ must be a *function type* $\text{func } [] \rightarrow []$.
- Then the start function is valid.

$$\frac{\text{expand}(C.\text{funcs}[x]) = \text{func } [] \rightarrow []}{C \vdash \{\text{func } x\} \text{ ok}}$$

3.5.11 Exports

Exports *export* and export descriptions *exportdesc* are classified by their external type.

$\{\text{name } name, \text{desc } exportdesc\}$

- The export description *exportdesc* must be valid with external type *externtype*.
- Then the export is valid with external type *externtype*.

$$\frac{C \vdash exportdesc : externtype}{C \vdash \{\text{name } name, \text{desc } exportdesc\} : externtype}$$

func *x*

- The function $C.\text{funcs}[x]$ must be defined in the context.
- Let *dt* be the defined type $C.\text{funcs}[x]$.
- Then the export description is valid with external type *func* *dt*.

$$\frac{C.\text{funcs}[x] = dt}{C \vdash \text{func } x : \text{func } dt}$$

table *x*

- The table $C.\text{tables}[x]$ must be defined in the context.
- Then the export description is valid with external type *table* $C.\text{tables}[x]$.

$$\frac{C.\text{tables}[x] = tabletype}{C \vdash \text{table } x : \text{table } tabletype}$$

mem *x*

- The memory $C.\text{mems}[x]$ must be defined in the context.
- Then the export description is valid with external type *mem* $C.\text{mems}[x]$.

$$\frac{C.\text{mems}[x] = memtype}{C \vdash \text{mem } x : \text{mem } memtype}$$

global *x*

- The global $C.\text{globals}[x]$ must be defined in the context.
- Then the export description is valid with external type *global* $C.\text{globals}[x]$.

$$\frac{C.\text{globals}[x] = globaltype}{C \vdash \text{global } x : \text{global } globaltype}$$

tag x

- The tag $C.\text{tags}[x]$ must be defined in the context.
- Then the export description is valid with external type tag $C.\text{tags}[x]$.

$$\frac{C.\text{tags}[x] = \text{tagtype}}{C \vdash \text{tag } x : \text{tag } \text{tagtype}}$$

3.5.12 Imports

Imports *import* and import descriptions *importdesc* are classified by external types.

{module name_1 , name name_2 , desc *importdesc*}

- The import description *importdesc* must be valid with type *externtype*.
- Then the import is valid with type *externtype*.

$$\frac{C \vdash \text{importdesc} : \text{externtype}}{C \vdash \{\text{module } \text{name}_1, \text{name } \text{name}_2, \text{desc } \text{importdesc}\} : \text{externtype}}$$

func x

- The defined type $C.\text{types}[x]$ must be a function type.
- Then the import description is valid with type func $C.\text{types}[x]$.

$$\frac{\text{expand}(C.\text{types}[x]) = \text{func } \text{func type}}{C \vdash \text{func } x : \text{func } C.\text{types}[x]}$$

table *tabletype*

- The table type *tabletype* must be valid.
- Then the import description is valid with type table *tabletype*.

$$\frac{C \vdash \text{tabletype } \text{ok}}{C \vdash \text{table } \text{tabletype} : \text{table } \text{tabletype}}$$

mem *memtype*

- The memory type *memtype* must be valid.
- Then the import description is valid with type mem *memtype*.

$$\frac{C \vdash \text{memtype } \text{ok}}{C \vdash \text{mem } \text{memtype} : \text{mem } \text{memtype}}$$

global *globaltype*

- The global type *globaltype* must be valid.
- Then the import description is valid with type global *globaltype*.

$$\frac{C \vdash \text{globaltype} \text{ ok}}{C \vdash \text{global } \text{globaltype} : \text{global } \text{globaltype}}$$

tag *tag*

- Let $\{\text{type } x\}$ be the tag *tag*.
- The type $C.\text{types}[x]$ must be defined in the context.
- The tag type $C.\text{types}[x]$ must be a valid tag type.
- Then the import description is valid with type tag $C.\text{types}[x]$.

$$\frac{\vdash C.\text{types}[x] \text{ ok}}{C \vdash \text{tag } \{\text{type } x\} : \text{tag } C.\text{types}[x]}$$

3.5.13 Modules

Modules are classified by their mapping from the external types of their imports to those of their exports.

A module is entirely *closed*, that is, its components can only refer to definitions that appear in the module itself. Consequently, no initial context is required. Instead, the context C for validation of the module's content is constructed from the definitions in the module.

The external types classifying a module may contain free type indices that refer to types defined within the module.

- Let *module* be the module to validate.
- The $\text{types } \text{module.types}$ must be valid yielding a context C_0 .
- Let C be a context where:
 - $C.\text{types}$ is $C_0.\text{types}$,
 - $C.\text{funcs}$ is $\text{funcs}(it^*)$ concatenated with dt^* , with the import's external types it^* and the internal defined types dt^* as determined below,
 - $C.\text{tables}$ is $\text{tables}(it^*)$ concatenated with tt^* , with the import's external types it^* and the internal table types tt^* as determined below,
 - $C.\text{mems}$ is $\text{mems}(it^*)$ concatenated with mt^* , with the import's external types it^* and the internal memory types mt^* as determined below,
 - $C.\text{globals}$ is $\text{globals}(it^*)$ concatenated with gt^* , with the import's external types it^* and the internal global types gt^* as determined below,
 - $C.\text{tags}$ is $\text{tags}(it^*)$ concatenated with ht^* , with the import's external types it^* and the internal tag types ht^* as determined below,
 - $C.\text{elems}$ is rt^* as determined below,
 - $C.\text{datas}$ is ok^n , where n is the length of the vector module.datas ,
 - $C.\text{locals}$ is empty,
 - $C.\text{labels}$ is empty,
 - $C.\text{return}$ is empty.
 - $C.\text{refs}$ is the set $\text{funcidx}(\text{module with funcs} = \epsilon \text{ with start} = \epsilon)$, i.e., the set of function indices occurring in the module, except in its functions or start function.

- Let C' be the **context** where:
 - $C'.\text{globals}$ is the sequence $\text{globals}(it^*)$,
 - $C'.\text{types}$ is the same as $C.\text{types}$,
 - $C'.\text{funcs}$ is the same as $C.\text{funcs}$,
 - $C'.\text{tables}$ is the same as $C.\text{tables}$,
 - $C'.\text{mems}$ is the same as $C.\text{mems}$,
 - $C'.\text{refs}$ is the same as $C.\text{refs}$,
 - all other fields are empty.
- Under the context C' :
 - The sequence module.globals of **globals** must be **valid** with a sequence gt^* of **global types**.
 - For each table_i in module.tables , the definition table_i must be **valid** with a **table type** tt_i .
 - For each mem_i in module.mems , the definition mem_i must be **valid** with a **memory type** mt_i .
- Under the context C :
 - For each func_i in module.funcs , the definition func_i must be **valid** with a **defined type** dt_i .
 - For each tag_i in module.tags , the definition tag_i must be **valid** with a **tag type** ht_i .
 - For each elem_i in module.elems , the segment elem_i must be **valid** with **reference type** rt_i .
 - For each data_i in module.datas , the segment data_i must be **valid**.
 - If module.start is non-empty, then module.start must be **valid**.
 - For each import_i in module.imports , the segment import_i must be **valid** with an **external type** it_i .
 - For each export_i in module.exports , the segment export_i must be **valid** with **external type** et_i .
- Let dt^* be the concatenation of the internal **function types** dt_i , in index order.
- Let tt^* be the concatenation of the internal **table types** tt_i , in index order.
- Let mt^* be the concatenation of the internal **memory types** mt_i , in index order.
- Let gt^* be the concatenation of the internal **global types** gt_i , in index order.
- Let ht^* be the concatenation of the internal **tag types** ht_i , in index order.
- Let rt^* be the concatenation of the **reference types** rt_i , in index order.
- Let it^* be the concatenation of **external types** it_i of the imports, in index order.
- Let et^* be the concatenation of **external types** et_i of the exports, in index order.
- The length of $C.\text{mems}$ must not be larger than 1.
- All export names $\text{export}_i.\text{name}$ must be different.
- Then the module is **valid** with **external types** $it^* \rightarrow et^*$.

$$\begin{array}{c}
 C_0 \vdash \text{type}^* \text{ ok} \quad C' \vdash \text{global}^* : gt^* \quad (C' \vdash \text{table} : tt)^* \quad (C' \vdash \text{mem} : mt)^* \quad (C \vdash \text{func} : dt)^* \quad (C \vdash \text{tag} : ht)^* \\
 (C \vdash \text{elem} : rt)^* \quad (C \vdash \text{data ok})^n \quad (C \vdash \text{start ok})^? \quad (C \vdash \text{import} : it)^* \\
 idt^* = \text{funcs}(it^*) \quad itt^* = \text{tables}(it^*) \quad imt^* = \text{mems}(it^*) \\
 igt^* = \text{globals}(it^*) \quad iht^* = \text{tags}(it^*) \\
 x^* = \text{funcidx}(\text{module with funcs} = \epsilon \text{ with start} = \epsilon) \\
 C = \{\text{types } C_0.\text{types}, \text{funcs } idt^* dt^*, \text{tables } itt^* tt^*, \text{mems } imt^* mt^*, \text{globals } igt^* gt^*, \text{tags } iht^* ht^*, \text{elems } rt^*, \text{datas ok}^n, \text{refs } x^*\} \\
 C' = \{\text{types } C_0.\text{types}, \text{globals } igt^*, \text{funcs } (C.\text{funcs}), \text{tables } (C.\text{tables}), \text{mems } (C.\text{mems}), \text{refs } (C.\text{refs})\} \quad (\text{export.name})^* \text{ disjoint} \\
 \text{module} = \{\text{types } \text{type}^*, \text{funcs } \text{func}^*, \text{tables } \text{table}^*, \text{mems } \text{mem}^*, \text{globals } \text{global}^*, \text{tags } \text{tag}^*, \\
 \text{elems } \text{elem}^*, \text{datas } \text{data}^n, \text{start } \text{start}^?, \text{imports } \text{import}^*, \text{exports } \text{export}^*\} \\
 \hline
 \vdash \text{module} : it^* \rightarrow et^*
 \end{array}$$

Note: All functions in a module are mutually recursive. Consequently, the definition of the [context](#) C in this rule is recursive: it depends on the outcome of validation of the function, table, memory, and global definitions contained in the module, which itself depends on C . However, this recursion is just a specification device. All types needed to construct C can easily be determined from a simple pre-pass over the module that does not perform any actual validation.

Globals, however, are not recursive but evaluated sequentially, such that each [constant expressions](#) only has access to imported or previously defined globals.

4.1 Conventions

WebAssembly code is *executed* when *instantiating* a module or *invoking* an *exported* function on the resulting module *instance*.

Execution behavior is defined in terms of an *abstract machine* that models the *program state*. It includes a *stack*, which records operand values and control constructs, and an abstract *store* containing global state.

For each instruction, there is a rule that specifies the effect of its execution on the program state. Furthermore, there are rules describing the instantiation of a module. As with *validation*, all rules are given in two *equivalent* forms:

1. In *prose*, describing the execution in intuitive form.
2. In *formal notation*, describing the rule in mathematical form.¹⁸

Note: As with validation, the prose and formal rules are equivalent, so that understanding of the formal notation is *not* required to read this specification. The formalism offers a more concise description in notation that is used widely in programming languages semantics and is readily amenable to mathematical proof.

4.1.1 Prose Notation

Execution is specified by stylised, step-wise rules for each *instruction* of the *abstract syntax*. The following conventions are adopted in stating these rules.

- The execution rules implicitly assume a given *store* *S*.
- The execution rules also assume the presence of an implicit *stack* that is modified by *pushing* or *popping* values, labels, and frames.
- Certain rules require the stack to contain at least one frame. The most recent frame is referred to as the *current* frame.

¹⁸ The semantics is derived from the following article: Andreas Haas, Andreas Rossberg, Derek Schuff, Ben Titze, Dan Gohman, Luke Wagner, Alon Zakai, JF Bastien, Michael Holman. *Bringing the Web up to Speed with WebAssembly*^{Page 87, 19}. Proceedings of the 38th ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI 2017). ACM 2017.

¹⁹ <https://dl.acm.org/citation.cfm?doid=3062341.3062363>

- Both the store and the current frame are mutated by *replacing* some of their components. Such replacement is assumed to apply globally.
- The execution of an instruction may *trap*, in which case the entire computation is aborted and no further modifications to the store are performed by it. (Other computations can still be initiated afterwards.)
- The execution of an instruction may also end in a *jump* to a designated target, which defines the next instruction to execute.
- Execution can *enter* and *exit* [instruction sequences](#) that form [blocks](#).
- [Instruction sequences](#) are implicitly executed in order, unless a trap or jump occurs, or an exception is thrown.
- In various places the rules contain *assertions* expressing crucial invariants about the program state.

4.1.2 Formal Notation

Note: This section gives a brief explanation of the notation for specifying execution formally. For the interested reader, a more thorough introduction can be found in respective text books.²⁰

The formal execution rules use a standard approach for specifying operational semantics, rendering them into *reduction rules*. Every rule has the following general form:

$$\text{configuration} \hookrightarrow \text{configuration}$$

A *configuration* is a syntactic description of a program state. Each rule specifies one *step* of execution. As long as there is at most one reduction rule applicable to a given configuration, reduction – and thereby execution – is *deterministic*. WebAssembly has only very few exceptions to this, which are noted explicitly in this specification.

For WebAssembly, a configuration typically is a tuple $(S; F; \text{instr}^*)$ consisting of the current [store](#) S , the [call frame](#) F of the current function, and the sequence of [instructions](#) that is to be executed. (A more precise definition is given [later](#).)

To avoid unnecessary clutter, the store S and the frame F are omitted from reduction rules that do not touch them.

There is no separate representation of the [stack](#). Instead, it is conveniently represented as part of the configuration’s instruction sequence. In particular, [values](#) are defined to coincide with [const](#) instructions, and a sequence of [const](#) instructions can be interpreted as an operand “stack” that grows to the right.

Note: For example, the [reduction rule](#) for the [i32.add](#) instruction can be given as follows:

$$(\text{i32.const } n_1) (\text{i32.const } n_2) \text{i32.add} \hookrightarrow (\text{i32.const } (n_1 + n_2) \bmod 2^{32})$$

Per this rule, two [const](#) instructions and the [add](#) instruction itself are removed from the instruction stream and replaced with one new [const](#) instruction. This can be interpreted as popping two values off the stack and pushing the result.

When no result is produced, an instruction reduces to the empty sequence:

$$\text{nop} \hookrightarrow \epsilon$$

[Labels](#) and [frames](#) are similarly [defined](#) to be part of an instruction sequence.

The order of reduction is determined by the definition of an appropriate [evaluation context](#).

Reduction *terminates* when no more reduction rules are applicable. [Soundness](#) of the WebAssembly [type system](#) guarantees that this is only the case when the original instruction sequence has either been reduced to a sequence of

²⁰ For example: Benjamin Pierce. [Types and Programming Languages](#)^{Page 88, 21}. The MIT Press 2002

²¹ <https://www.cis.upenn.edu/~bcpierce/tapl/>

`const` instructions, which can be interpreted as the *values* of the resulting operand stack, or if a *trap* or an uncaught exception occurred.

Note: For example, the following instruction sequence,

$$(f64.const\ x_1)\ (f64.const\ x_2)\ f64.neg\ (f64.const\ x_3)\ f64.add\ f64.mul$$

terminates after three steps:

$$\begin{aligned} & (f64.const\ x_1)\ (f64.const\ x_2)\ f64.neg\ (f64.const\ x_3)\ f64.add\ f64.mul \\ \hookrightarrow & (f64.const\ x_1)\ (f64.const\ x_4)\ (f64.const\ x_3)\ f64.add\ f64.mul \\ \hookrightarrow & (f64.const\ x_1)\ (f64.const\ x_5)\ f64.mul \\ \hookrightarrow & (f64.const\ x_6) \end{aligned}$$

where $x_4 = -x_2$ and $x_5 = -x_2 + x_3$ and $x_6 = x_1 \cdot (-x_2 + x_3)$.

4.2 Runtime Structure

Store, *stack*, and other *runtime structure* forming the WebAssembly abstract machine, such as *values* or *module instances*, are made precise in terms of additional auxiliary syntax.

4.2.1 Values

WebAssembly computations manipulate *values* of either the four basic *number types*, i.e., *integers* and *floating-point data* of 32 or 64 bit width each, or *vectors* of 128 bit width, or of *reference type*.

In most places of the semantics, values of different types can occur. In order to avoid ambiguities, values are therefore represented with an abstract syntax that makes their type explicit. It is convenient to reuse the same notation as for the *const instructions* and *ref.null* producing them.

References other than null are represented with additional *administrative instructions*. They either are *scalar references*, containing a 31-bit *integer*, *structure references*, pointing to a specific *structure address*, *array references*, pointing to a specific *array address*, *function references*, pointing to a specific *function address*, *exception references*, pointing to a specific *exception address*, or *host references* pointing to an uninterpreted form of *host address* defined by the *embedder*. Any of the aforementioned references can furthermore be wrapped up as an *external reference*.

$$\begin{aligned} num & ::= i32.const\ i32 \\ & \quad | i64.const\ i64 \\ & \quad | f32.const\ f32 \\ & \quad | f64.const\ f64 \\ vec & ::= v128.const\ i128 \\ ref & ::= ref.null\ (absheapttype\ |\ deftype) \\ & \quad | ref.i31\ u31 \\ & \quad | ref.struct\ structaddr \\ & \quad | ref.array\ arrayaddr \\ & \quad | ref.func\ funcaddr \\ & \quad | ref.exn\ exnaddr \\ & \quad | ref.host\ hostaddr \\ & \quad | ref.extern\ ref \\ val & ::= num\ |\ vec\ |\ ref \end{aligned}$$

Note: Future versions of WebAssembly may add additional forms of reference.

Value types can have an associated *default value*; it is the respective value 0 for **number types**, 0 for **vector types**, and null for nullable **reference types**. For other references, no default value is defined, default_t hence is an optional value *val*²².

$$\begin{aligned} \text{default}_t &= t.\text{const } 0 && (\text{if } t = \text{numtype}) \\ \text{default}_t &= t.\text{const } 0 && (\text{if } t = \text{vectype}) \\ \text{default}_t &= \text{ref.null } t && (\text{if } t = (\text{ref null heapttype})) \\ \text{default}_t &= \epsilon && (\text{if } t = (\text{ref heapttype})) \end{aligned}$$

Convention

- The meta variable r ranges over reference values where clear from context.

4.2.2 Results

A *result* is the outcome of a computation. It is either a sequence of **values**, an uncaught **exception**, or a **trap**.

$$\begin{aligned} \text{result} &::= \text{val}^* \\ &| T[(\text{ref.exn } \text{exnaddr}) \text{ throw_ref}] \\ &| \text{trap} \end{aligned}$$

4.2.3 Store

The *store* represents all global state that can be manipulated by WebAssembly programs. It consists of the runtime representation of all *instances* of **functions**, **tables**, **memories**, **globals**, **tags**, **element segments**, **data segments**, and **structures**, **arrays** or **exceptions** that have been **allocated** during the life time of the abstract machine.²²

It is an invariant of the semantics that no element or data instance is **addressed** from anywhere else but the owning module instances.

Syntactically, the store is defined as a **record** listing the existing instances of each category:

$$\text{store} ::= \{ \begin{array}{ll} \text{funcs} & \text{funcinst}^*, \\ \text{tables} & \text{tableinst}^*, \\ \text{mems} & \text{meminst}^*, \\ \text{globals} & \text{globalinst}^*, \\ \text{tags} & \text{taginst}^*, \\ \text{elems} & \text{eleminst}^*, \\ \text{datas} & \text{datainst}^*, \\ \text{structs} & \text{structinst}^*, \\ \text{arrays} & \text{arrayinst}^*, \\ \text{exns} & \text{exninst}^* \end{array} \}$$

Convention

- The meta variable S ranges over stores where clear from context.

²² In practice, implementations may apply techniques like garbage collection or reference counting to remove objects from the store that are no longer referenced. However, such techniques are not semantically observable, and hence outside the scope of this specification.

4.2.4 Addresses

Function instances, table instances, memory instances, global instances, tag instances, element instances, data instances and structure, array instances or exception instances in the store are referenced with abstract *addresses*. These are simply indices into the respective store component. In addition, an *embedder* may supply an uninterpreted set of *host addresses*.

$$\begin{aligned}
 \text{addr} &::= 0 \mid 1 \mid 2 \mid \dots \\
 \text{funcaddr} &::= \text{addr} \\
 \text{tableaddr} &::= \text{addr} \\
 \text{memaddr} &::= \text{addr} \\
 \text{globaladdr} &::= \text{addr} \\
 \text{tagaddr} &::= \text{addr} \\
 \text{elemaddr} &::= \text{addr} \\
 \text{dataaddr} &::= \text{addr} \\
 \text{structaddr} &::= \text{addr} \\
 \text{arrayaddr} &::= \text{addr} \\
 \text{exnaddr} &::= \text{addr} \\
 \text{hostaddr} &::= \text{addr}
 \end{aligned}$$

An *embedder* may assign identity to *exported* store objects corresponding to their addresses, even where this identity is not observable from within WebAssembly code itself (such as for *function instances* or immutable *globals*).

Note: Addresses are *dynamic*, globally unique references to runtime objects, in contrast to *indices*, which are *static*, module-local references to their original definitions. A *memory address* *memaddr* denotes the abstract address of a memory *instance* in the store, not an offset *inside* a memory instance.

There is no specific limit on the number of allocations of store objects, hence logical addresses can be arbitrarily large natural numbers.

Conventions

- The notation $\text{addr}(A)$ denotes the set of addresses from address space *addr* occurring free in *A*. We sometimes reinterpret this set as the *vector* of its elements.

4.2.5 Module Instances

A *module instance* is the runtime representation of a *module*. It is created by *instantiating* a module, and collects runtime representations of all entities that are imported, defined, or exported by the module.

$$\text{moduleinst} ::= \{ \begin{array}{ll} \text{types} & \text{deftype}^*, \\ \text{funcaddrs} & \text{funcaddr}^*, \\ \text{tableaddrs} & \text{tableaddr}^*, \\ \text{memaddrs} & \text{memaddr}^*, \\ \text{globaladdrs} & \text{globaladdr}^*, \\ \text{tagaddrs} & \text{tagaddr}^*, \\ \text{elemaddrs} & \text{elemaddr}^*, \\ \text{dataaddrs} & \text{dataaddr}^*, \\ \text{exports} & \text{exportinst}^* \end{array} \}$$

Each component references runtime instances corresponding to respective declarations from the original module – whether imported or defined – in the order of their static *indices*. *Function instances*, *table instances*, *memory instances*, *global instances*, and *tag instances* are referenced with an indirection through their respective *addresses* in the *store*.

It is an invariant of the semantics that all *export instances* in a given module instance have different *names*.

4.2.6 Function Instances

A *function instance* is the runtime representation of a [function](#). It effectively is a *closure* of the original function over the runtime [module instance](#) of its originating [module](#). The module instance is used to resolve references to other definitions during execution of the function.

$$\begin{aligned} \text{funcinst} &::= \{\text{type } \text{deftype}, \text{module } \text{moduleinst}, \text{code } \text{func}\} \\ &\quad | \quad \{\text{type } \text{deftype}, \text{hostcode } \text{hostfunc}\} \\ \text{hostfunc} &::= \dots \end{aligned}$$

A *host function* is a function expressed outside WebAssembly but passed to a [module](#) as an [import](#). The definition and behavior of host functions are outside the scope of this specification. For the purpose of this specification, it is assumed that when [invoked](#), a host function behaves non-deterministically, but within certain [constraints](#) that ensure the integrity of the runtime.

Note: Function instances are immutable, and their identity is not observable by WebAssembly code. However, the [embedder](#) might provide implicit or explicit means for distinguishing their [addresses](#).

4.2.7 Table Instances

A *table instance* is the runtime representation of a [table](#). It records its [type](#) and holds a sequence of [reference values](#).

$$\text{tableinst} ::= \{\text{type } \text{tabletype}, \text{elem } \text{ref}^*\}$$

Table elements can be mutated through [table instructions](#), the execution of an active [element segment](#), or by external means provided by the [embedder](#).

It is an invariant of the semantics that all table elements have a type [matching](#) the element type of [tabletype](#). It also is an invariant that the length of the element sequence never exceeds the maximum size of [tabletype](#), if present.

4.2.8 Memory Instances

A *memory instance* is the runtime representation of a linear [memory](#). It records its [type](#) and holds a sequence of [bytes](#).

$$\text{meminst} ::= \{\text{type } \text{memtype}, \text{data } \text{byte}^*\}$$

The length of the sequence always is a multiple of the WebAssembly *page size*, which is defined to be the constant 65536 – abbreviated 64 Ki.

The bytes can be mutated through [memory instructions](#), the execution of an active [data segment](#), or by external means provided by the [embedder](#).

It is an invariant of the semantics that the length of the byte vector, divided by page size, never exceeds the maximum size of [memtype](#), if present.

4.2.9 Global Instances

A *global instance* is the runtime representation of a [global variable](#). It records its [type](#) and holds an individual [value](#).

$$\text{globalinst} ::= \{\text{type } \text{globaltype}, \text{value } \text{val}\}$$

The value of mutable globals can be mutated through [variable instructions](#) or by external means provided by the [embedder](#).

It is an invariant of the semantics that the value has a type [matching](#) the [value type](#) of [globaltype](#).

4.2.10 Tag Instances

A *tag instance* is the runtime representation of a [tag](#) definition. It records the [type](#) of the tag.

$$taginst ::= \{type\ tagtype\}$$

4.2.11 Element Instances

An *element instance* is the runtime representation of an [element segment](#). It holds a vector of references and their common [type](#).

$$eleminst ::= \{type\ reftype, elem\ vec(ref)\}$$

4.2.12 Data Instances

An *data instance* is the runtime representation of a [data segment](#). It holds a vector of [bytes](#).

$$datainst ::= \{data\ vec(byte)\}$$

4.2.13 Export Instances

An *export instance* is the runtime representation of an [export](#). It defines the export's [name](#) and the associated [external value](#).

$$exportinst ::= \{name\ name, value\ externval\}$$

4.2.14 External Values

An *external value* is the runtime representation of an entity that can be imported or exported. It is an [address](#) denoting either a [function instance](#), [table instance](#), [memory instance](#), [tag instances](#), or [global instances](#) in the shared store.

$$externval ::= \begin{array}{l} \text{func } funcaddr \\ | \\ \text{table } tableaddr \\ | \\ \text{mem } memaddr \\ | \\ \text{global } globaladdr \\ | \\ \text{tag } tagaddr \end{array}$$

Conventions

The following auxiliary notation is defined for sequences of external values. It filters out entries of a specific kind in an order-preserving fashion:

- $funcs(externval^*) = [funcaddr \mid (func\ funcaddr) \in externval^*]$
- $tables(externval^*) = [tableaddr \mid (table\ tableaddr) \in externval^*]$
- $mems(externval^*) = [memaddr \mid (mem\ memaddr) \in externval^*]$
- $globals(externval^*) = [globaladdr \mid (global\ globaladdr) \in externval^*]$
- $tags(externval^*) = [tagaddr \mid (tag\ tagaddr) \in externval^*]$

4.2.15 Aggregate Instances

A *structure instance* is the runtime representation of a heap object allocated from a [structure type](#). Likewise, an *array instance* is the runtime representation of a heap object allocated from an [array type](#). Both record their respective [defined type](#) and hold a vector of the values of their *fields*.

$$\begin{aligned} \text{structinst} &::= \{\text{type } \text{deftype}, \text{fields } \text{vec}(\text{fieldval})\} \\ \text{arrayinst} &::= \{\text{type } \text{deftype}, \text{fields } \text{vec}(\text{fieldval})\} \\ \text{fieldval} &::= \text{val} \mid \text{packedval} \\ \text{packedval} &::= \text{i8.pack } u8 \mid \text{i16.pack } u16 \end{aligned}$$

Conventions

- Conversion of a regular [value](#) to a [field value](#) is defined as follows:

$$\begin{aligned} \text{pack}_{\text{valtype}}(\text{val}) &= \text{val} \\ \text{pack}_{\text{packedtype}}(\text{i32.const } i) &= \text{packedtype.pack}(\text{wrap}_{32,|\text{packedtype}|}(i)) \end{aligned}$$

- The inverse conversion of a [field value](#) to a regular [value](#) is defined as follows:

$$\begin{aligned} \text{unpack}_{\text{valtype}}(\text{val}) &= \text{val} \\ \text{unpack}_{\text{packedtype}}^{\text{sx}}(\text{packedtype.pack } i) &= \text{i32.const}(\text{extend}_{|\text{packedtype}|,32}^{\text{sx}}(i)) \end{aligned}$$

4.2.16 Exception Instances

An *exception instance* is the runtime representation of an `_exception_` produced by a [throw](#) instruction. It holds the [address](#) of the respective [tag](#) and the argument [values](#).

$$\text{exninst} ::= \{\text{tag } \text{tagaddr}, \text{fields } \text{vec}(\text{val})\}$$

4.2.17 Stack

Besides the [store](#), most [instructions](#) interact with an implicit *stack*. The stack contains the following kinds of entries:

- *Values*: the *operands* of instructions.
- *Labels*: active [structured control instructions](#) that can be targeted by branches.
- *Activations*: the *call frames* of active [function](#) calls.
- *Handlers*: active exception handlers.

These entries can occur on the stack in any order during the execution of a program. Stack entries are described by abstract syntax as follows.

Note: It is possible to model the WebAssembly semantics using separate stacks for operands, control constructs, and calls. However, because the stacks are interdependent, additional book keeping about associated stack heights would be required. For the purpose of this specification, an interleaved representation is simpler.

Values

Values are represented by [themselves](#).

Labels

Labels carry an argument arity n and their associated branch *target*, which is expressed syntactically as an [instruction sequence](#):

$$label ::= label_n\{instr^*\}$$

Intuitively, $instr^*$ is the *continuation* to execute when the branch is taken, in place of the original control construct.

Note: For example, a loop label has the form

$$label_n\{\text{loop} \dots \text{end}\}$$

When performing a branch to this label, this executes the loop, effectively restarting it from the beginning. Conversely, a simple block label has the form

$$label_n\{\epsilon\}$$

When branching, the empty continuation ends the targeted block, such that execution can proceed with consecutive instructions.

Activation Frames

Activation frames carry the return arity n of the respective function, hold the values of its [locals](#) (including arguments) in the order corresponding to their static [local indices](#), and a reference to the function's own [module instance](#):

$$\begin{aligned} frame & ::= frame_n\{framestate\} \\ framestate & ::= \{\text{locals } (val^?)*, \text{module } moduleinst\} \end{aligned}$$

Locals may be uninitialized, in which case they are empty. Locals are mutated by respective [variable instructions](#).

Exception Handlers

Exception handlers are installed by [try_table](#) instructions and record the corresponding list of [catch clauses](#):

$$handler ::= handler_n\{catch^*\}$$

The handlers on the stack are searched when an exception is [thrown](#).

Conventions

- The meta variable L ranges over labels where clear from context.
- The meta variable F ranges over frame states where clear from context.
- The meta variable H ranges over exception handlers where clear from context.
- The following auxiliary definition takes a [block type](#) and looks up the [instruction type](#) that it denotes in the current frame:

$$\begin{aligned} instrtype_{S;F}(typeid_x) &= func\text{type} && (\text{if } \text{expand}(F.\text{module.types}[typeid_x]) = \text{func } func\text{type}) \\ instrtype_{S;F}([valtype^?]) &= [] \rightarrow [valtype^?] \end{aligned}$$

4.2.18 Administrative Instructions

Note: This section is only relevant for the [formal notation](#).

In order to express the reduction of [traps](#), [calls](#), [exception handling](#), and [control instructions](#), the syntax of instructions is extended to include the following *administrative instructions*:

$$\begin{array}{lcl}
 \text{instr} & ::= & \dots \\
 & | & \text{trap} \\
 & | & \text{ref.i31 } u31 \\
 & | & \text{ref.struct } \text{structaddr} \\
 & | & \text{ref.array } \text{arrayaddr} \\
 & | & \text{ref.func } \text{funcaddr} \\
 & | & \text{ref.exn } \text{exnaddr} \\
 & | & \text{ref.host } \text{hostaddr} \\
 & | & \text{ref.extern } \text{ref} \\
 & | & \text{invoke } \text{funcaddr} \\
 & | & \text{return_invoke } \text{funcaddr} \\
 & | & \text{label}_n\{\text{instr}^*\} \text{ instr}^* \text{ end} \\
 & | & \text{handler}_n\{\text{catch}^*\} \text{ instr}^* \text{ end} \\
 & | & \text{frame}_n\{\text{framestate}\} \text{ instr}^* \text{ end}
 \end{array}$$

The [trap](#) instruction represents the occurrence of a trap. Traps are bubbled up through nested instruction sequences, ultimately reducing the entire program to a single [trap](#) instruction, signalling abrupt termination.

The [ref.i31](#) instruction represents [unboxed scalar](#) reference values, [ref.struct](#) and [ref.array](#) represent [structure](#) and [array](#) references, respectively, [ref.func](#) represents [function references](#), and [ref.exn](#) represents [exception references](#). Similarly, [ref.host](#) represents [host references](#) and [ref.extern](#) represents any externalized reference.

The [invoke](#) instruction represents the imminent invocation of a [function instance](#), identified by its [address](#). It unifies the handling of different forms of calls. Analogously, [return_invoke](#) represents the imminent tail invocation of a function instance.

The [label](#), [frame](#), and [handler](#) instructions model [labels](#), [frames](#), and active [exception handlers](#), respectively, “on the stack”. Moreover, the administrative syntax maintains the nesting structure of the original [structured control instruction](#) or [function body](#) and their [instruction sequences](#) with an [end](#) marker. That way, the end of the inner instruction sequence is known when part of an outer sequence.

Note: For example, the [reduction rule](#) for [block](#) is:

$$\text{block } [t^n] \text{ instr}^* \text{ end} \quad \hookrightarrow \quad \text{label}_n\{\epsilon\} \text{ instr}^* \text{ end}$$

This replaces the block with a label instruction, which can be interpreted as “pushing” the label on the stack. When [end](#) is reached, i.e., the inner instruction sequence has been reduced to the empty sequence – or rather, a sequence of n [const](#) instructions representing the resulting values – then the [label](#) instruction is eliminated courtesy of its own [reduction rule](#):

$$\text{label}_m\{\text{instr}^*\} \text{ val}^n \text{ end} \quad \hookrightarrow \quad \text{val}^n$$

This can be interpreted as removing the label from the stack and only leaving the locally accumulated operand values.

Block Contexts

In order to specify the reduction of **branches**, the following syntax of *block contexts* is defined, indexed by the count k of labels surrounding a *hole* $[_]$ that marks the place where the next step of computation is taking place:

$$\begin{aligned} B^k &::= \text{val } B^k \mid B^k \text{ instr} \mid \text{handler}_n\{\text{catch}^*\} B^k \text{ end} \mid C^k \\ C^0 &::= [_] \\ C^{k+1} &::= \text{label}_n\{\text{instr}^*\} B^k \text{ end} \end{aligned}$$

This definition allows to index active labels surrounding a **branch** or **return** instruction.

Note: For example, the **reduction** of a simple branch can be defined as follows:

$$\text{label}_0\{\text{instr}^*\} B^l[\text{br } l] \text{ end} \hookrightarrow \text{instr}^*$$

Here, the hole $[_]$ of the context is instantiated with a branch instruction. When a branch occurs, this rule replaces the target label and associated instruction sequence with the label's continuation. The selected label is identified through the **label index** l , which corresponds to the number of surrounding **label** instructions that must be hopped over – which is exactly the count encoded in the index of a block context.

Throw Contexts

In order to specify the reduction of **try_table** blocks, the following syntax of *throw contexts* is defined, as well as associated structural rules:

$$\begin{aligned} T &::= [_] \\ &\mid \text{val}^* T \text{ instr}^* \\ &\mid \text{label}_n\{\text{instr}^*\} T \text{ end} \\ &\mid \text{frame}_n\{F\} T \text{ end} \end{aligned}$$

Throw contexts allow matching the program context around a throw instruction up to the innermost enclosing **exception handler**, if one exists.

Note: Contrary to block contexts, throw contexts do not skip over handlers.

Configurations

A *configuration* consists of the current **store** and an executing *thread*.

A thread is a computation over **instructions** that operates relative to the state of a current **frame** referring to the **module instance** in which the computation runs, i.e., where the current function originates from.

$$\begin{aligned} \text{config} &::= \text{store}; \text{thread} \\ \text{thread} &::= \text{framestate}; \text{instr}^* \end{aligned}$$

Note: The current version of WebAssembly is single-threaded, but configurations with multiple threads may be supported in the future.

Evaluation Contexts

Finally, the following definition of *evaluation context* and associated structural rules enable reduction inside instruction sequences and administrative forms as well as the propagation of traps:

$$\begin{aligned}
 E &::= [_] \mid \text{val}^* E \text{ instr}^* \mid \text{label}_n\{\text{instr}^*\} E \text{ end} \mid \text{handler}_n\{\text{catch}^*\} E \text{ end} \\
 S; F; E[\text{instr}^*] &\hookrightarrow S'; F'; E[\text{instr}'^*] && (\text{if } S; F; \text{instr}^* \hookrightarrow S'; F'; \text{instr}'^*) \\
 S; F; \text{frame}_n\{F'\} \text{ instr}^* \text{ end} &\hookrightarrow S'; F'; \text{frame}_n\{F''\} \text{ instr}'^* \text{ end} && (\text{if } S; F'; \text{instr}^* \hookrightarrow S'; F''; \text{instr}'^*) \\
 S; F; E[\text{trap}] &\hookrightarrow S; F; \text{trap} && (\text{if } E \neq [_]) \\
 S; F; \text{frame}_n\{F'\} \text{ trap end} &\hookrightarrow S; F; \text{trap}
 \end{aligned}$$

Reduction terminates when a thread's instruction sequence has been reduced to a **result**, that is, either a sequence of **values**, to an uncaught **exception**, or to a **trap**.

Note: The restriction on evaluation contexts rules out contexts like $[_]$ and $\epsilon [_] \epsilon$ for which $E[\text{trap}] = \text{trap}$.

For an example of reduction under evaluation contexts, consider the following instruction sequence.

(f64.const x_1) (f64.const x_2) f64.neg (f64.const x_3) f64.add f64.mul

This can be decomposed into $E[(\text{f64.const } x_2) \text{ f64.neg}]$ where

$$E = (\text{f64.const } x_1) [_] (\text{f64.const } x_3) \text{ f64.add f64.mul}$$

Moreover, this is the *only* possible choice of evaluation context where the contents of the hole matches the left-hand side of a reduction rule.

4.3 Numerics

Numeric primitives are defined in a generic manner, by operators indexed over a bit width N .

Some operators are *non-deterministic*, because they can return one of several possible results (such as different NaN values). Technically, each operator thus returns a *set* of allowed values. For convenience, deterministic results are expressed as plain values, which are assumed to be identified with a respective singleton set.

Some operators are *partial*, because they are not defined on certain inputs. Technically, an empty set of results is returned for these inputs.

In formal notation, each operator is defined by equational clauses that apply in decreasing order of precedence. That is, the first clause that is applicable to the given arguments defines the result. In some cases, similar clauses are combined into one by using the notation \pm or \mp . When several of these placeholders occur in a single clause, then they must be resolved consistently: either the upper sign is chosen for all of them or the lower sign.

Note: For example, the `fcopysign` operator is defined as follows:

$$\begin{aligned}
 \text{fcopysign}_N(\pm p_1, \pm p_2) &= \pm p_1 \\
 \text{fcopysign}_N(\pm p_1, \mp p_2) &= \mp p_1
 \end{aligned}$$

This definition is to be read as a shorthand for the following expansion of each clause into two separate ones:

$$\begin{aligned}
 \text{fcopysign}_N(+p_1, +p_2) &= +p_1 \\
 \text{fcopysign}_N(-p_1, -p_2) &= -p_1 \\
 \text{fcopysign}_N(+p_1, -p_2) &= -p_1 \\
 \text{fcopysign}_N(-p_1, +p_2) &= +p_1
 \end{aligned}$$

Numeric operators are lifted to input sequences by applying the operator element-wise, returning a sequence of results. When there are multiple inputs, they must be of equal length.

$$op(c_1^n, \dots, c_k^n) = op(c_1^n[0], \dots, c_k^n[0]) \dots op(c_1^n[n-1], \dots, c_k^n[n-1])$$

Note: For example, the unary operator `fabs`, when given a sequence of floating-point values, return a sequence of floating-point results:

$$\text{fabs}_N(z^n) = \text{fabs}_N(z[0]) \dots \text{fabs}_N(z[n])$$

The binary operator `iadd`, when given two sequences of integers of the same length, n , return a sequence of integer results:

$$\text{iadd}_N(i_1^n, i_2^n) = \text{iadd}_N(i_1[0], i_2[0]) \dots \text{iadd}_N(i_1[n], i_2[n])$$

Conventions:

- The meta variable d is used to range over single bits.
- The meta variable p is used to range over (signless) **magnitudes** of floating-point values, including `nan` and ∞ .
- The meta variable q is used to range over (signless) **rational magnitudes**, excluding `nan` or ∞ .
- The notation f^{-1} denotes the inverse of a bijective function f .
- Truncation of rational values is written `trunc($\pm q$)`, with the usual mathematical definition:

$$\text{trunc}(\pm q) = \pm i \quad (\text{if } i \in \mathbb{N} \wedge +q - 1 < i \leq +q)$$

- Saturation of integers is written `sat_uN(i)` and `sat_sN(i)`. The arguments to these two functions range over arbitrary signed integers.

- Unsigned saturation, `sat_uN(i)` clamps i to between 0 and $2^N - 1$:

$$\begin{aligned} \text{sat_u}_N(i) &= 0 && (\text{if } i < 0) \\ \text{sat_u}_N(i) &= 2^N - 1 && (\text{if } i > 2^N - 1) \\ \text{sat_u}_N(i) &= i && (\text{otherwise}) \end{aligned}$$

- Signed saturation, `sat_sN(i)` clamps i to between -2^{N-1} and $2^{N-1} - 1$:

$$\begin{aligned} \text{sat_s}_N(i) &= -2^{N-1} && (\text{if } i < -2^{N-1}) \\ \text{sat_s}_N(i) &= 2^{N-1} - 1 && (\text{if } i > 2^{N-1} - 1) \\ \text{sat_s}_N(i) &= i && (\text{otherwise}) \end{aligned}$$

4.3.1 Representations

Numbers and numeric vectors have an underlying binary representation as a sequence of bits:

$$\begin{aligned} \text{bits}_{iN}(i) &= \text{ibits}_N(i) \\ \text{bits}_{fN}(z) &= \text{fbits}_N(z) \\ \text{bits}_{vN}(i) &= \text{ibits}_N(i) \end{aligned}$$

The first case of these applies to representations of both integer **value types** and **packed types**.

Each of these functions is a bijection, hence they are invertible.

Integers

Integers are represented as base two unsigned numbers:

$$\text{ibits}_N(i) = d_{N-1} \dots d_0 \quad (i = 2^{N-1} \cdot d_{N-1} + \dots + 2^0 \cdot d_0)$$

Boolean operators like \wedge , \vee , or \neg are lifted to bit sequences of equal length by applying them pointwise.

Floating-Point

Floating-point values are represented in the respective binary format defined by IEEE 754²³ (Section 3.4):

$$\begin{aligned} \text{fbits}_N(\pm(1 + m \cdot 2^{-M}) \cdot 2^e) &= \text{fsign}(\pm) \text{ibits}_E(e + \text{fbias}_N) \text{ibits}_M(m) \\ \text{fbits}_N(\pm(0 + m \cdot 2^{-M}) \cdot 2^e) &= \text{fsign}(\pm) (0)^E \text{ibits}_M(m) \\ \text{fbits}_N(\pm\infty) &= \text{fsign}(\pm) (1)^E (0)^M \\ \text{fbits}_N(\pm\text{nan}(n)) &= \text{fsign}(\pm) (1)^E \text{ibits}_M(n) \\ \text{fbias}_N &= 2^{E-1} - 1 \\ \text{fsign}(+) &= 0 \\ \text{fsign}(-) &= 1 \end{aligned}$$

where $M = \text{signif}(N)$ and $E = \text{expon}(N)$.

Vectors

Numeric vectors of type $\text{v}N$ have the same underlying representation as an iN . They can also be interpreted as a sequence of numeric values packed into a $\text{v}N$ with a particular *shape* txM , provided that $N = |t| \cdot M$.

$$\begin{aligned} \text{lanes}_{txM}(c) &= c_0 \dots c_{M-1} \\ (\text{where } w &= |t|/8 \\ &\wedge b^* = \text{bytes}_{iN}(c) \\ &\wedge c_i = \text{bytes}_t^{-1}(b^*[i \cdot w : w])) \end{aligned}$$

This function is a bijection on iN , hence it is invertible.

Storage

When a number is stored into *memory*, it is converted into a sequence of *bytes* in *little endian*²⁴ byte order:

$$\begin{aligned} \text{bytes}_t(i) &= \text{littleendian}(\text{bits}_t(i)) \\ \text{littleendian}(\epsilon) &= \epsilon \\ \text{littleendian}(d^8 d'^*) &= \text{littleendian}(d'^*) \text{ibits}_8^{-1}(d^8) \end{aligned}$$

Again these functions are invertible bijections.

4.3.2 Integer Operations

Sign Interpretation

Integer operators are defined on iN values. Operators that use a signed interpretation convert the value using the following definition, which takes the two's complement when the value lies in the upper half of the value range (i.e., its most significant bit is 1):

$$\begin{aligned} \text{signed}_N(i) &= i & (0 \leq i < 2^{N-1}) \\ \text{signed}_N(i) &= i - 2^N & (2^{N-1} \leq i < 2^N) \end{aligned}$$

This function is bijective, and hence invertible.

²³ <https://ieeexplore.ieee.org/document/8766229>

²⁴ <https://en.wikipedia.org/wiki/Endianness#Little-endian>

Boolean Interpretation

The integer result of predicates – i.e., `tests` and `relational` operators – is defined with the help of the following auxiliary function producing the value 1 or 0 depending on a condition.

$$\begin{aligned}\text{bool}(C) &= 1 && (\text{if } C) \\ \text{bool}(C) &= 0 && (\text{otherwise})\end{aligned}$$

$\text{iadd}_N(i_1, i_2)$

- Return the result of adding i_1 and i_2 modulo 2^N .

$$\text{iadd}_N(i_1, i_2) = (i_1 + i_2) \bmod 2^N$$

$\text{isub}_N(i_1, i_2)$

- Return the result of subtracting i_2 from i_1 modulo 2^N .

$$\text{isub}_N(i_1, i_2) = (i_1 - i_2 + 2^N) \bmod 2^N$$

$\text{imul}_N(i_1, i_2)$

- Return the result of multiplying i_1 and i_2 modulo 2^N .

$$\text{imul}_N(i_1, i_2) = (i_1 \cdot i_2) \bmod 2^N$$

$\text{idiv}_u(i_1, i_2)$

- If i_2 is 0, then the result is undefined.
- Else, return the result of dividing i_1 by i_2 , truncated toward zero.

$$\begin{aligned}\text{idiv}_u(i_1, 0) &= \{\} \\ \text{idiv}_u(i_1, i_2) &= \text{trunc}(i_1/i_2)\end{aligned}$$

Note: This operator is `partial`.

$\text{idiv}_s(i_1, i_2)$

- Let j_1 be the `signed interpretation` of i_1 .
- Let j_2 be the `signed interpretation` of i_2 .
- If j_2 is 0, then the result is undefined.
- Else if j_1 divided by j_2 is 2^{N-1} , then the result is undefined.
- Else, return the result of dividing j_1 by j_2 , truncated toward zero.

$$\begin{aligned}\text{idiv}_s(i_1, 0) &= \{\} \\ \text{idiv}_s(i_1, i_2) &= \{\} && (\text{if } \text{signed}_N(i_1)/\text{signed}_N(i_2) = 2^{N-1}) \\ \text{idiv}_s(i_1, i_2) &= \text{signed}_N^{-1}(\text{trunc}(\text{signed}_N(i_1)/\text{signed}_N(i_2)))\end{aligned}$$

Note: This operator is `partial`. Besides division by 0, the result of $(-2^{N-1})/(-1) = +2^{N-1}$ is not representable as an N -bit signed integer.

$\text{irem_u}_N(i_1, i_2)$

- If i_2 is 0, then the result is undefined.
- Else, return the remainder of dividing i_1 by i_2 .

$$\begin{aligned}\text{irem_u}_N(i_1, 0) &= \{\} \\ \text{irem_u}_N(i_1, i_2) &= i_1 - i_2 \cdot \text{trunc}(i_1/i_2)\end{aligned}$$

Note: This operator is [partial](#).As long as both operators are defined, it holds that $i_1 = i_2 \cdot \text{idiv_u}(i_1, i_2) + \text{irem_u}(i_1, i_2)$.

 $\text{irem_s}_N(i_1, i_2)$

- Let j_1 be the [signed interpretation](#) of i_1 .
- Let j_2 be the [signed interpretation](#) of i_2 .
- If i_2 is 0, then the result is undefined.
- Else, return the remainder of dividing j_1 by j_2 , with the sign of the dividend j_1 .

$$\begin{aligned}\text{irem_s}_N(i_1, 0) &= \{\} \\ \text{irem_s}_N(i_1, i_2) &= \text{signed}_N^{-1}(j_1 - j_2 \cdot \text{trunc}(j_1/j_2)) \\ &\quad (\text{where } j_1 = \text{signed}_N(i_1) \wedge j_2 = \text{signed}_N(i_2))\end{aligned}$$

Note: This operator is [partial](#).As long as both operators are defined, it holds that $i_1 = i_2 \cdot \text{idiv_s}(i_1, i_2) + \text{irem_s}(i_1, i_2)$.

 $\text{inot}_N(i)$

- Return the bitwise negation of i .

$$\text{inot}_N(i) = \text{ibits}_N^{-1}(\text{ibits}_N(i) \vee \text{ibits}_N(2^N - 1))$$

 $\text{iand}_N(i_1, i_2)$

- Return the bitwise conjunction of i_1 and i_2 .

$$\text{iand}_N(i_1, i_2) = \text{ibits}_N^{-1}(\text{ibits}_N(i_1) \wedge \text{ibits}_N(i_2))$$

 $\text{iandnot}_N(i_1, i_2)$

- Return the bitwise conjunction of i_1 and the bitwise negation of i_2 .

$$\text{iandnot}_N(i_1, i_2) = \text{iand}_N(i_1, \text{inot}_N(i_2))$$

$\text{ior}_N(i_1, i_2)$

- Return the bitwise disjunction of i_1 and i_2 .

$$\text{ior}_N(i_1, i_2) = \text{ibits}_N^{-1}(\text{ibits}_N(i_1) \vee \text{ibits}_N(i_2))$$

 $\text{ixor}_N(i_1, i_2)$

- Return the bitwise exclusive disjunction of i_1 and i_2 .

$$\text{ixor}_N(i_1, i_2) = \text{ibits}_N^{-1}(\text{ibits}_N(i_1) \vee \text{ibits}_N(i_2))$$

 $\text{ishl}_N(i_1, i_2)$

- Let k be i_2 modulo N .
- Return the result of shifting i_1 left by k bits, modulo 2^N .

$$\text{ishl}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^{N-k} 0^k) \quad (\text{if } \text{ibits}_N(i_1) = d_1^k d_2^{N-k} \wedge k = i_2 \bmod N)$$

 $\text{ishr_u}_N(i_1, i_2)$

- Let k be i_2 modulo N .
- Return the result of shifting i_1 right by k bits, extended with 0 bits.

$$\text{ishr_u}_N(i_1, i_2) = \text{ibits}_N^{-1}(0^k d_1^{N-k}) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} d_2^k \wedge k = i_2 \bmod N)$$

 $\text{ishr_s}_N(i_1, i_2)$

- Let k be i_2 modulo N .
- Return the result of shifting i_1 right by k bits, extended with the most significant bit of the original value.

$$\text{ishr_s}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_0^{k+1} d_1^{N-k-1}) \quad (\text{if } \text{ibits}_N(i_1) = d_0 d_1^{N-k-1} d_2^k \wedge k = i_2 \bmod N)$$

 $\text{irotl}_N(i_1, i_2)$

- Let k be i_2 modulo N .
- Return the result of rotating i_1 left by k bits.

$$\text{irotl}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^{N-k} d_1^k) \quad (\text{if } \text{ibits}_N(i_1) = d_1^k d_2^{N-k} \wedge k = i_2 \bmod N)$$

 $\text{irotr}_N(i_1, i_2)$

- Let k be i_2 modulo N .
- Return the result of rotating i_1 right by k bits.

$$\text{irotr}_N(i_1, i_2) = \text{ibits}_N^{-1}(d_2^k d_1^{N-k}) \quad (\text{if } \text{ibits}_N(i_1) = d_1^{N-k} d_2^k \wedge k = i_2 \bmod N)$$

$\text{iclz}_N(i)$

- Return the count of leading zero bits in i ; all bits are considered leading zeros if i is 0.

$$\text{iclz}_N(i) = k \quad (\text{if } \text{ibits}_N(i) = 0^k (1 \ d^*)^?)$$

 $\text{ictz}_N(i)$

- Return the count of trailing zero bits in i ; all bits are considered trailing zeros if i is 0.

$$\text{ictz}_N(i) = k \quad (\text{if } \text{ibits}_N(i) = (d^* 1)^? 0^k)$$

 $\text{ipopcnt}_N(i)$

- Return the count of non-zero bits in i .

$$\text{ipopcnt}_N(i) = k \quad (\text{if } \text{ibits}_N(i) = (0^* 1)^k 0^*)$$

 $\text{ieqz}_N(i)$

- Return 1 if i is zero, 0 otherwise.

$$\text{ieqz}_N(i) = \text{bool}(i = 0)$$

 $\text{ieq}_N(i_1, i_2)$

- Return 1 if i_1 equals i_2 , 0 otherwise.

$$\text{ieq}_N(i_1, i_2) = \text{bool}(i_1 = i_2)$$

 $\text{ine}_N(i_1, i_2)$

- Return 1 if i_1 does not equal i_2 , 0 otherwise.

$$\text{ine}_N(i_1, i_2) = \text{bool}(i_1 \neq i_2)$$

 $\text{ilt_u}_N(i_1, i_2)$

- Return 1 if i_1 is less than i_2 , 0 otherwise.

$$\text{ilt_u}_N(i_1, i_2) = \text{bool}(i_1 < i_2)$$

 $\text{ilt_s}_N(i_1, i_2)$

- Let j_1 be the [signed interpretation](#) of i_1 .
- Let j_2 be the [signed interpretation](#) of i_2 .
- Return 1 if j_1 is less than j_2 , 0 otherwise.

$$\text{ilt_s}_N(i_1, i_2) = \text{bool}(\text{signed}_N(i_1) < \text{signed}_N(i_2))$$

$\text{igt_u}_N(i_1, i_2)$

- Return 1 if i_1 is greater than i_2 , 0 otherwise.

$$\text{igt_u}_N(i_1, i_2) = \text{bool}(i_1 > i_2)$$

$\text{igt_s}_N(i_1, i_2)$

- Let j_1 be the [signed interpretation](#) of i_1 .
- Let j_2 be the [signed interpretation](#) of i_2 .
- Return 1 if j_1 is greater than j_2 , 0 otherwise.

$$\text{igt_s}_N(i_1, i_2) = \text{bool}(\text{signed}_N(i_1) > \text{signed}_N(i_2))$$

$\text{ile_u}_N(i_1, i_2)$

- Return 1 if i_1 is less than or equal to i_2 , 0 otherwise.

$$\text{ile_u}_N(i_1, i_2) = \text{bool}(i_1 \leq i_2)$$

$\text{ile_s}_N(i_1, i_2)$

- Let j_1 be the [signed interpretation](#) of i_1 .
- Let j_2 be the [signed interpretation](#) of i_2 .
- Return 1 if j_1 is less than or equal to j_2 , 0 otherwise.

$$\text{ile_s}_N(i_1, i_2) = \text{bool}(\text{signed}_N(i_1) \leq \text{signed}_N(i_2))$$

$\text{ige_u}_N(i_1, i_2)$

- Return 1 if i_1 is greater than or equal to i_2 , 0 otherwise.

$$\text{ige_u}_N(i_1, i_2) = \text{bool}(i_1 \geq i_2)$$

$\text{ige_s}_N(i_1, i_2)$

- Let j_1 be the [signed interpretation](#) of i_1 .
- Let j_2 be the [signed interpretation](#) of i_2 .
- Return 1 if j_1 is greater than or equal to j_2 , 0 otherwise.

$$\text{ige_s}_N(i_1, i_2) = \text{bool}(\text{signed}_N(i_1) \geq \text{signed}_N(i_2))$$

$\text{iextend}_{M_sN}(i)$

- Let j be the result of computing $\text{wrap}_{N,M}(i)$.
- Return $\text{extend}_{M,N}^s(j)$.

$$\text{iextend}_{M_sN}(i) = \text{extend}_{M,N}^s(\text{wrap}_{N,M}(i))$$

$\text{ibitselect}_N(i_1, i_2, i_3)$

- Let j_1 be the bitwise conjunction of i_1 and i_3 .
- Let j'_3 be the bitwise negation of i_3 .
- Let j_2 be the bitwise conjunction of i_2 and j'_3 .
- Return the bitwise disjunction of j_1 and j_2 .

$$\text{ibitselect}_N(i_1, i_2, i_3) = \text{ior}_N(\text{iand}_N(i_1, i_3), \text{iand}_N(i_2, \text{inot}_N(i_3)))$$

$\text{iabs}_N(i)$

- Let j be the [signed interpretation](#) of i .
- If j is greater than or equal to 0, then return i .
- Else return the negation of j , modulo 2^N .

$$\begin{aligned} \text{iabs}_N(i) &= i && (\text{if } \text{signed}_N(i) \geq 0) \\ \text{iabs}_N(i) &= -\text{signed}_N(i) \bmod 2^N && (\text{otherwise}) \end{aligned}$$

$\text{ineg}_N(i)$

- Return the result of negating i , modulo 2^N .

$$\text{ineg}_N(i) = (2^N - i) \bmod 2^N$$

$\text{imin_u}_N(i_1, i_2)$

- Return i_1 if $\text{ilt_u}_N(i_1, i_2)$ is 1, return i_2 otherwise.

$$\begin{aligned} \text{imin_u}_N(i_1, i_2) &= i_1 && (\text{if } \text{ilt_u}_N(i_1, i_2) = 1) \\ \text{imin_u}_N(i_1, i_2) &= i_2 && (\text{otherwise}) \end{aligned}$$

$\text{imin_s}_N(i_1, i_2)$

- Return i_1 if $\text{ilt_s}_N(i_1, i_2)$ is 1, return i_2 otherwise.

$$\begin{aligned} \text{imin_s}_N(i_1, i_2) &= i_1 && (\text{if } \text{ilt_s}_N(i_1, i_2) = 1) \\ \text{imin_s}_N(i_1, i_2) &= i_2 && (\text{otherwise}) \end{aligned}$$

$\text{imax_u}_N(i_1, i_2)$

- Return i_1 if $\text{igt_u}_N(i_1, i_2)$ is 1, return i_2 otherwise.

$$\begin{aligned}\text{imax_u}_N(i_1, i_2) &= i_1 && (\text{if } \text{igt_u}_N(i_1, i_2) = 1) \\ \text{imax_u}_N(i_1, i_2) &= i_2 && (\text{otherwise})\end{aligned}$$

$\text{imax_s}_N(i_1, i_2)$

- Return i_1 if $\text{igt_s}_N(i_1, i_2)$ is 1, return i_2 otherwise.

$$\begin{aligned}\text{imax_s}_N(i_1, i_2) &= i_1 && (\text{if } \text{igt_s}_N(i_1, i_2) = 1) \\ \text{imax_s}_N(i_1, i_2) &= i_2 && (\text{otherwise})\end{aligned}$$

$\text{iadd_sat_u}_N(i_1, i_2)$

- Let i be the result of adding i_1 and i_2 .
- Return $\text{sat_u}_N(i)$.

$$\text{iadd_sat_u}_N(i_1, i_2) = \text{sat_u}_N(i_1 + i_2)$$

$\text{iadd_sat_s}_N(i_1, i_2)$

- Let j_1 be the signed interpretation of i_1
- Let j_2 be the signed interpretation of i_2
- Let j be the result of adding j_1 and j_2 .
- Return the value whose signed interpretation is $\text{sat_s}_N(j)$.

$$\text{iadd_sat_s}_N(i_1, i_2) = \text{signed}_N^{-1}(\text{sat_s}_N(\text{signed}_N(i_1) + \text{signed}_N(i_2)))$$

$\text{isub_sat_u}_N(i_1, i_2)$

- Let i be the result of subtracting i_2 from i_1 .
- Return $\text{sat_u}_N(i)$.

$$\text{isub_sat_u}_N(i_1, i_2) = \text{sat_u}_N(i_1 - i_2)$$

$\text{isub_sat_s}_N(i_1, i_2)$

- Let j_1 be the signed interpretation of i_1
- Let j_2 be the signed interpretation of i_2
- Let j be the result of subtracting j_2 from j_1 .
- Return the value whose signed interpretation is $\text{sat_s}_N(j)$.

$$\text{isub_sat_s}_N(i_1, i_2) = \text{signed}_N^{-1}(\text{sat_s}_N(\text{signed}_N(i_1) - \text{signed}_N(i_2)))$$

$\text{iavgr_u}_N(i_1, i_2)$

- Let j be the result of adding i_1 , i_2 , and 1.
- Return the result of dividing j by 2, truncated toward zero.

$$\text{iavgr_u}_N(i_1, i_2) = \text{trunc}((i_1 + i_2 + 1)/2)$$

$\text{iq15mulrsat_s}_N(i_1, i_2)$

- Return the whose signed interpretation is the result of $\text{sat_s}_N(\text{ishr_s}_N(i_1 \cdot i_2 + 2^{14}, 15))$.

$$\text{iq15mulrsat_s}_N(i_1, i_2) = \text{signed}_N^{-1}(\text{sat_s}_N(\text{ishr_s}_N(i_1 \cdot i_2 + 2^{14}, 15)))$$

4.3.3 Floating-Point Operations

Floating-point arithmetic follows the [IEEE 754²⁵](#) standard, with the following qualifications:

- All operators use round-to-nearest ties-to-even, except where otherwise specified. Non-default directed rounding attributes are not supported.
- Following the recommendation that operators propagate NaN payloads from their operands is permitted but not required.
- All operators use “non-stop” mode, and floating-point exceptions are not otherwise observable. In particular, neither alternate floating-point exception handling attributes nor operators on status flags are supported. There is no observable difference between quiet and signalling NaNs.

Note: Some of these limitations may be lifted in future versions of WebAssembly.

Rounding

Rounding always is round-to-nearest ties-to-even, in correspondence with [IEEE 754²⁶](#) (Section 4.3.1).

An *exact* floating-point number is a rational number that is exactly representable as a floating-point number of given bit width N .

A *limit* number for a given floating-point bit width N is a positive or negative number whose magnitude is the smallest power of 2 that is not exactly representable as a floating-point number of width N (that magnitude is 2^{128} for $N = 32$ and 2^{1024} for $N = 64$).

A *candidate* number is either an exact floating-point number or a positive or negative limit number for the given bit width N .

A *candidate pair* is a pair z_1, z_2 of candidate numbers, such that no candidate number exists that lies between the two.

A real number r is converted to a floating-point value of bit width N as follows:

- If r is 0, then return $+0$.
- Else if r is an exact floating-point number, then return r .
- Else if r greater than or equal to the positive limit, then return $+\infty$.
- Else if r is less than or equal to the negative limit, then return $-\infty$.
- Else if z_1 and z_2 are a candidate pair such that $z_1 < r < z_2$, then:

²⁵ <https://ieeexplore.ieee.org/document/8766229>

²⁶ <https://ieeexplore.ieee.org/document/8766229>

- If $|r - z_1| < |r - z_2|$, then let z be z_1 .
- Else if $|r - z_1| > |r - z_2|$, then let z be z_2 .
- Else if $|r - z_1| = |r - z_2|$ and the **significand** of z_1 is even, then let z be z_1 .
- Else, let z be z_2 .
- If z is 0, then:
 - If $r < 0$, then return -0 .
 - Else, return $+0$.
- Else if z is a limit number, then:
 - If $r < 0$, then return $-\infty$.
 - Else, return $+\infty$.

- Else, return z .

$\text{float}_N(0)$	$=$	$+0$	
$\text{float}_N(r)$	$=$	r	(if $r \in \text{exact}_N$)
$\text{float}_N(r)$	$=$	$+\infty$	(if $r \geq +\text{limit}_N$)
$\text{float}_N(r)$	$=$	$-\infty$	(if $r \leq -\text{limit}_N$)
$\text{float}_N(r)$	$=$	$\text{closest}_N(r, z_1, z_2)$	(if $z_1 < r < z_2 \wedge (z_1, z_2) \in \text{candidatepair}_N$)
$\text{closest}_N(r, z_1, z_2)$	$=$	$\text{rectify}_N(r, z_1)$	(if $ r - z_1 < r - z_2 $)
$\text{closest}_N(r, z_1, z_2)$	$=$	$\text{rectify}_N(r, z_2)$	(if $ r - z_1 > r - z_2 $)
$\text{closest}_N(r, z_1, z_2)$	$=$	$\text{rectify}_N(r, z_1)$	(if $ r - z_1 = r - z_2 \wedge \text{even}_N(z_1)$)
$\text{closest}_N(r, z_1, z_2)$	$=$	$\text{rectify}_N(r, z_2)$	(if $ r - z_1 = r - z_2 \wedge \text{even}_N(z_2)$)
$\text{rectify}_N(r, \pm\text{limit}_N)$	$=$	$\pm\infty$	
$\text{rectify}_N(r, 0)$	$=$	$+0$	($r \geq 0$)
$\text{rectify}_N(r, 0)$	$=$	-0	($r < 0$)
$\text{rectify}_N(r, z)$	$=$	z	

where:

exact_N	$=$	$fN \cap \mathbb{Q}$
limit_N	$=$	$2^{2^{\text{expon}(N)} - 1}$
candidate_N	$=$	$\text{exact}_N \cup \{+\text{limit}_N, -\text{limit}_N\}$
candidatepair_N	$=$	$\{(z_1, z_2) \in \text{candidate}_N^2 \mid z_1 < z_2 \wedge \forall z \in \text{candidate}_N, z \leq z_1 \vee z \geq z_2\}$
$\text{even}_N((d + m \cdot 2^{-M}) \cdot 2^e)$	\Leftrightarrow	$m \bmod 2 = 0$
$\text{even}_N(\pm\text{limit}_N)$	\Leftrightarrow	true

NaN Propagation

When the result of a floating-point operator other than **fneg**, **fabs**, or **fcopysign** is a NaN, then its sign is non-deterministic and the **payload** is computed as follows:

- If the payload of all NaN inputs to the operator is **canonical** (including the case that there are no NaN inputs), then the payload of the output is canonical as well.
- Otherwise the payload is picked non-deterministically among all **arithmetic NaNs**; that is, its most significant bit is 1 and all others are unspecified.
- In the **deterministic profile**, however, a positive canonical NaNs is reliably produced in the latter case.

The non-deterministic result is expressed by the following auxiliary function producing a set of allowed outputs from a set of inputs:

	$\text{nan}_N\{z^*\}$	$=$	$\{+\text{nan}(\text{canon}_N)\}$	
[!DET]	$\text{nan}_N\{z^*\}$	$=$	$\{+\text{nan}(n), -\text{nan}(n) \mid n = \text{canon}_N\}$	(if $\{z^*\} \subseteq \{+\text{nan}(\text{canon}_N), -\text{nan}(\text{canon}_N)\}$)
[!DET]	$\text{nan}_N\{z^*\}$	$=$	$\{+\text{nan}(n), -\text{nan}(n) \mid n \geq \text{canon}_N\}$	(if $\{z^*\} \not\subseteq \{+\text{nan}(\text{canon}_N), -\text{nan}(\text{canon}_N)\}$)

$\text{fadd}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if both z_1 and z_2 are infinities of opposite signs, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 and z_2 are infinities of equal sign, then return that infinity.
- Else if either z_1 or z_2 is an infinity, then return that infinity.
- Else if both z_1 and z_2 are zeroes of opposite sign, then return positive zero.
- Else if both z_1 and z_2 are zeroes of equal sign, then return that zero.
- Else if either z_1 or z_2 is a zero, then return the other operand.
- Else if both z_1 and z_2 are values with the same magnitude but opposite signs, then return positive zero.
- Else return the result of adding z_1 and z_2 , **rounded** to the nearest representable value.

$$\begin{aligned}
 \text{fadd}_N(\pm\text{nan}(n), z_2) &= \text{nans}_N\{\pm\text{nan}(n), z_2\} \\
 \text{fadd}_N(z_1, \pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n), z_1\} \\
 \text{fadd}_N(\pm\infty, \mp\infty) &= \text{nans}_N\{\} \\
 \text{fadd}_N(\pm\infty, \pm\infty) &= \pm\infty \\
 \text{fadd}_N(z_1, \pm\infty) &= \pm\infty \\
 \text{fadd}_N(\pm\infty, z_2) &= \pm\infty \\
 \text{fadd}_N(\pm 0, \mp 0) &= +0 \\
 \text{fadd}_N(\pm 0, \pm 0) &= \pm 0 \\
 \text{fadd}_N(z_1, \pm 0) &= z_1 \\
 \text{fadd}_N(\pm 0, z_2) &= z_2 \\
 \text{fadd}_N(\pm q, \mp q) &= +0 \\
 \text{fadd}_N(z_1, z_2) &= \text{float}_N(z_1 + z_2)
 \end{aligned}$$

$\text{fsub}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if both z_1 and z_2 are infinities of equal signs, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 and z_2 are infinities of opposite sign, then return z_1 .
- Else if z_1 is an infinity, then return that infinity.
- Else if z_2 is an infinity, then return that infinity negated.
- Else if both z_1 and z_2 are zeroes of equal sign, then return positive zero.
- Else if both z_1 and z_2 are zeroes of opposite sign, then return z_1 .
- Else if z_2 is a zero, then return z_1 .
- Else if z_1 is a zero, then return z_2 negated.
- Else if both z_1 and z_2 are the same value, then return positive zero.
- Else return the result of subtracting z_2 from z_1 , **rounded** to the nearest representable value.

$\text{fsub}_N(\pm\text{nan}(n), z_2)$	$= \text{nans}_N\{\pm\text{nan}(n), z_2\}$
$\text{fsub}_N(z_1, \pm\text{nan}(n))$	$= \text{nans}_N\{\pm\text{nan}(n), z_1\}$
$\text{fsub}_N(\pm\infty, \pm\infty)$	$= \text{nans}_N\{\}$
$\text{fsub}_N(\pm\infty, \mp\infty)$	$= \pm\infty$
$\text{fsub}_N(z_1, \pm\infty)$	$= \mp\infty$
$\text{fsub}_N(\pm\infty, z_2)$	$= \pm\infty$
$\text{fsub}_N(\pm 0, \pm 0)$	$= +0$
$\text{fsub}_N(\pm 0, \mp 0)$	$= \pm 0$
$\text{fsub}_N(z_1, \pm 0)$	$= z_1$
$\text{fsub}_N(\pm 0, \pm q_2)$	$= \mp q_2$
$\text{fsub}_N(\pm q, \pm q)$	$= +0$
$\text{fsub}_N(z_1, z_2)$	$= \text{float}_N(z_1 - z_2)$

Note: Up to the non-determinism regarding NaNs, it always holds that $\text{fsub}_N(z_1, z_2) = \text{fadd}_N(z_1, \text{fneg}_N(z_2))$.

$\text{fmul}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if one of z_1 and z_2 is a zero and the other an infinity, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 and z_2 are infinities of equal sign, then return positive infinity.
- Else if both z_1 and z_2 are infinities of opposite sign, then return negative infinity.
- Else if either z_1 or z_2 is an infinity and the other a value with equal sign, then return positive infinity.
- Else if either z_1 or z_2 is an infinity and the other a value with opposite sign, then return negative infinity.
- Else if both z_1 and z_2 are zeroes of equal sign, then return positive zero.
- Else if both z_1 and z_2 are zeroes of opposite sign, then return negative zero.
- Else return the result of multiplying z_1 and z_2 , rounded to the nearest representable value.

$\text{fmul}_N(\pm\text{nan}(n), z_2)$	$= \text{nans}_N\{\pm\text{nan}(n), z_2\}$
$\text{fmul}_N(z_1, \pm\text{nan}(n))$	$= \text{nans}_N\{\pm\text{nan}(n), z_1\}$
$\text{fmul}_N(\pm\infty, \pm 0)$	$= \text{nans}_N\{\}$
$\text{fmul}_N(\pm\infty, \mp 0)$	$= \text{nans}_N\{\}$
$\text{fmul}_N(\pm 0, \pm\infty)$	$= \text{nans}_N\{\}$
$\text{fmul}_N(\pm 0, \mp\infty)$	$= \text{nans}_N\{\}$
$\text{fmul}_N(\pm\infty, \pm\infty)$	$= +\infty$
$\text{fmul}_N(\pm\infty, \mp\infty)$	$= -\infty$
$\text{fmul}_N(\pm q_1, \pm\infty)$	$= +\infty$
$\text{fmul}_N(\pm q_1, \mp\infty)$	$= -\infty$
$\text{fmul}_N(\pm\infty, \pm q_2)$	$= +\infty$
$\text{fmul}_N(\pm\infty, \mp q_2)$	$= -\infty$
$\text{fmul}_N(\pm 0, \pm 0)$	$= +0$
$\text{fmul}_N(\pm 0, \mp 0)$	$= -0$
$\text{fmul}_N(z_1, z_2)$	$= \text{float}_N(z_1 \cdot z_2)$

$\text{fdiv}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if both z_1 and z_2 are infinities, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 and z_2 are zeroes, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if z_1 is an infinity and z_2 a value with equal sign, then return positive infinity.
- Else if z_1 is an infinity and z_2 a value with opposite sign, then return negative infinity.
- Else if z_2 is an infinity and z_1 a value with equal sign, then return positive zero.
- Else if z_2 is an infinity and z_1 a value with opposite sign, then return negative zero.
- Else if z_1 is a zero and z_2 a value with equal sign, then return positive zero.
- Else if z_1 is a zero and z_2 a value with opposite sign, then return negative zero.
- Else if z_2 is a zero and z_1 a value with equal sign, then return positive infinity.
- Else if z_2 is a zero and z_1 a value with opposite sign, then return negative infinity.
- Else return the result of dividing z_1 by z_2 , **rounded** to the nearest representable value.

$\text{fdiv}_N(\pm\text{nan}(n), z_2)$	$= \text{nans}_N\{\pm\text{nan}(n), z_2\}$
$\text{fdiv}_N(z_1, \pm\text{nan}(n))$	$= \text{nans}_N\{z_1, \pm\text{nan}(n)\}$
$\text{fdiv}_N(\pm\infty, \pm\infty)$	$= \text{nans}_N\{\}$
$\text{fdiv}_N(\pm\infty, \mp\infty)$	$= \text{nans}_N\{\}$
$\text{fdiv}_N(\pm 0, \pm 0)$	$= \text{nans}_N\{\}$
$\text{fdiv}_N(\pm 0, \mp 0)$	$= \text{nans}_N\{\}$
$\text{fdiv}_N(\pm\infty, \pm q_2)$	$= +\infty$
$\text{fdiv}_N(\pm\infty, \mp q_2)$	$= -\infty$
$\text{fdiv}_N(\pm q_1, \pm\infty)$	$= +0$
$\text{fdiv}_N(\pm q_1, \mp\infty)$	$= -0$
$\text{fdiv}_N(\pm 0, \pm q_2)$	$= +0$
$\text{fdiv}_N(\pm 0, \mp q_2)$	$= -0$
$\text{fdiv}_N(\pm q_1, \pm 0)$	$= +\infty$
$\text{fdiv}_N(\pm q_1, \mp 0)$	$= -\infty$
$\text{fdiv}_N(z_1, z_2)$	$= \text{float}_N(z_1/z_2)$

 $\text{fma}_N(z_1, z_2, z_3)$

The function `fma` is the same as *fusedMultiplyAdd* defined by IEEE 754²⁷ (Section 5.4.1). It computes $(z_1 \cdot z_2) + z_3$ as if with unbounded range and precision, rounding only once for the final result.

- If either z_1 or z_2 or z_3 is a NaN, return an element of $\text{nans}_N\{z_1, z_2, z_3\}$.
- Else if either z_1 or z_2 is a zero and the other is an infinity, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 or z_2 are infinities of equal sign, and z_3 is a negative infinity, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 or z_2 are infinities of opposite sign, and z_3 is a positive infinity, then return an element of $\text{nans}_N\{\}$.
- Else if either z_1 or z_2 is an infinity and the other is a value of the same sign, and z_3 is a negative infinity, then return an element of $\text{nans}_N\{\}$.
- Else if either z_1 or z_2 is an infinity and the other is a value of the opposite sign, and z_3 is a positive infinity, then return an element of $\text{nans}_N\{\}$.
- Else if both z_1 and z_2 are zeroes of the same sign and z_3 is a zero, then return positive zero.
- Else if both z_1 and z_2 are zeroes of the opposite sign and z_3 is a positive zero, then return positive zero.

²⁷ <https://ieeexplore.ieee.org/document/8766229>

- Else if both z_1 and z_2 are zeroes of the opposite sign and z_3 is a negative zero, then return negative zero.
- Else return the result of multiplying z_1 and z_2 , adding z_3 to the intermediate, and the final result ref:rounded <aux-ieee> to the nearest representable value.

$$\begin{aligned}
\text{fma}_N(\pm\text{nan}(n), z_2, z_3) &= \text{nans}_N\{\pm\text{nan}(n), z_2, z_3\} \\
\text{fma}_N(z_1, \pm\text{nan}(n), z_3) &= \text{nans}_N\{\pm\text{nan}(n), z_1, z_3\} \\
\text{fma}_N(z_1, z_2, \pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n), z_1, z_2\} \\
\text{fma}_N(\pm\infty, \pm 0, z_3) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm\infty, \mp 0, z_3) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm\infty, \pm\infty, -\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm\infty, \mp\infty, +\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm q_1, \pm\infty, -\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm q_1, \mp\infty, +\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm\infty, \pm q_1, -\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\mp\infty, \pm q_1, +\infty) &= \text{nans}_N\{\} \\
\text{fma}_N(\pm 0, \pm 0, \mp 0) &= +0 \\
\text{fma}_N(\pm 0, \pm 0, \pm 0) &= +0 \\
\text{fma}_N(\pm 0, \mp 0, +0) &= +0 \\
\text{fma}_N(\pm 0, \mp 0, -0) &= -0 \\
\text{fma}_N(z_1, z_2, z_3) &= \text{float}_N(z_1 \cdot z_2 + z_3)
\end{aligned}$$

$\text{fmin}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if either z_1 or z_2 is a negative infinity, then return negative infinity.
- Else if either z_1 or z_2 is a positive infinity, then return the other value.
- Else if both z_1 and z_2 are zeroes of opposite signs, then return negative zero.
- Else return the smaller value of z_1 and z_2 .

$$\begin{aligned}
\text{fmin}_N(\pm\text{nan}(n), z_2) &= \text{nans}_N\{\pm\text{nan}(n), z_2\} \\
\text{fmin}_N(z_1, \pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n), z_1\} \\
\text{fmin}_N(+\infty, z_2) &= z_2 \\
\text{fmin}_N(-\infty, z_2) &= -\infty \\
\text{fmin}_N(z_1, +\infty) &= z_1 \\
\text{fmin}_N(z_1, -\infty) &= -\infty \\
\text{fmin}_N(\pm 0, \mp 0) &= -0 \\
\text{fmin}_N(z_1, z_2) &= z_1 && (\text{if } z_1 \leq z_2) \\
\text{fmin}_N(z_1, z_2) &= z_2 && (\text{if } z_2 \leq z_1)
\end{aligned}$$

$\text{fmax}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return an element of $\text{nans}_N\{z_1, z_2\}$.
- Else if either z_1 or z_2 is a positive infinity, then return positive infinity.
- Else if either z_1 or z_2 is a negative infinity, then return the other value.
- Else if both z_1 and z_2 are zeroes of opposite signs, then return positive zero.
- Else return the larger value of z_1 and z_2 .

$$\begin{aligned}
\text{fmax}_N(\pm\text{nan}(n), z_2) &= \text{nan}_N\{\pm\text{nan}(n), z_2\} \\
\text{fmax}_N(z_1, \pm\text{nan}(n)) &= \text{nan}_N\{\pm\text{nan}(n), z_1\} \\
\text{fmax}_N(+\infty, z_2) &= +\infty \\
\text{fmax}_N(-\infty, z_2) &= z_2 \\
\text{fmax}_N(z_1, +\infty) &= +\infty \\
\text{fmax}_N(z_1, -\infty) &= z_1 \\
\text{fmax}_N(\pm 0, \mp 0) &= +0 \\
\text{fmax}_N(z_1, z_2) &= z_1 && (\text{if } z_1 \geq z_2) \\
\text{fmax}_N(z_1, z_2) &= z_2 && (\text{if } z_2 \geq z_1)
\end{aligned}$$

$\text{fcopysign}_N(z_1, z_2)$

- If z_1 and z_2 have the same sign, then return z_1 .
- Else return z_1 with negated sign.

$$\begin{aligned}
\text{fcopysign}_N(\pm p_1, \pm p_2) &= \pm p_1 \\
\text{fcopysign}_N(\pm p_1, \mp p_2) &= \mp p_1
\end{aligned}$$

$\text{fabs}_N(z)$

- If z is a NaN, then return z with positive sign.
- Else if z is an infinity, then return positive infinity.
- Else if z is a zero, then return positive zero.
- Else if z is a positive value, then z .
- Else return z negated.

$$\begin{aligned}
\text{fabs}_N(\pm\text{nan}(n)) &= +\text{nan}(n) \\
\text{fabs}_N(\pm\infty) &= +\infty \\
\text{fabs}_N(\pm 0) &= +0 \\
\text{fabs}_N(\pm q) &= +q
\end{aligned}$$

$\text{fneg}_N(z)$

- If z is a NaN, then return z with negated sign.
- Else if z is an infinity, then return that infinity negated.
- Else if z is a zero, then return that zero negated.
- Else return z negated.

$$\begin{aligned}
\text{fneg}_N(\pm\text{nan}(n)) &= \mp\text{nan}(n) \\
\text{fneg}_N(\pm\infty) &= \mp\infty \\
\text{fneg}_N(\pm 0) &= \mp 0 \\
\text{fneg}_N(\pm q) &= \mp q
\end{aligned}$$

$\text{fsqrt}_N(z)$

- If z is a NaN, then return an element of $\text{nan}_N\{z\}$.
- Else if z is negative infinity, then return an element of $\text{nan}_N\{\}$.
- Else if z is positive infinity, then return positive infinity.
- Else if z is a zero, then return that zero.
- Else if z has a negative sign, then return an element of $\text{nan}_N\{\}$.
- Else return the square root of z .

$$\begin{aligned}
 \text{fsqrt}_N(\pm\text{nan}(n)) &= \text{nan}_N\{\pm\text{nan}(n)\} \\
 \text{fsqrt}_N(-\infty) &= \text{nan}_N\{\} \\
 \text{fsqrt}_N(+\infty) &= +\infty \\
 \text{fsqrt}_N(\pm 0) &= \pm 0 \\
 \text{fsqrt}_N(-q) &= \text{nan}_N\{\} \\
 \text{fsqrt}_N(+q) &= \text{float}_N(\sqrt{q})
 \end{aligned}$$

$\text{fceil}_N(z)$

- If z is a NaN, then return an element of $\text{nan}_N\{z\}$.
- Else if z is an infinity, then return z .
- Else if z is a zero, then return z .
- Else if z is smaller than 0 but greater than -1 , then return negative zero.
- Else return the smallest integral value that is not smaller than z .

$$\begin{aligned}
 \text{fceil}_N(\pm\text{nan}(n)) &= \text{nan}_N\{\pm\text{nan}(n)\} \\
 \text{fceil}_N(\pm\infty) &= \pm\infty \\
 \text{fceil}_N(\pm 0) &= \pm 0 \\
 \text{fceil}_N(-q) &= -0 && (\text{if } -1 < -q < 0) \\
 \text{fceil}_N(\pm q) &= \text{float}_N(i) && (\text{if } \pm q \leq i < \pm q + 1)
 \end{aligned}$$

$\text{ffloor}_N(z)$

- If z is a NaN, then return an element of $\text{nan}_N\{z\}$.
- Else if z is an infinity, then return z .
- Else if z is a zero, then return z .
- Else if z is greater than 0 but smaller than 1, then return positive zero.
- Else return the largest integral value that is not larger than z .

$$\begin{aligned}
 \text{ffloor}_N(\pm\text{nan}(n)) &= \text{nan}_N\{\pm\text{nan}(n)\} \\
 \text{ffloor}_N(\pm\infty) &= \pm\infty \\
 \text{ffloor}_N(\pm 0) &= \pm 0 \\
 \text{ffloor}_N(+q) &= +0 && (\text{if } 0 < +q < 1) \\
 \text{ffloor}_N(\pm q) &= \text{float}_N(i) && (\text{if } \pm q - 1 < i \leq \pm q)
 \end{aligned}$$

$\text{ftrunc}_N(z)$

- If z is a NaN, then return an element of $\text{nans}_N\{z\}$.
- Else if z is an infinity, then return z .
- Else if z is a zero, then return z .
- Else if z is greater than 0 but smaller than 1, then return positive zero.
- Else if z is smaller than 0 but greater than -1 , then return negative zero.
- Else return the integral value with the same sign as z and the largest magnitude that is not larger than the magnitude of z .

$$\begin{aligned}
\text{ftrunc}_N(\pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n)\} \\
\text{ftrunc}_N(\pm\infty) &= \pm\infty \\
\text{ftrunc}_N(\pm 0) &= \pm 0 \\
\text{ftrunc}_N(+q) &= +0 && (\text{if } 0 < +q < 1) \\
\text{ftrunc}_N(-q) &= -0 && (\text{if } -1 < -q < 0) \\
\text{ftrunc}_N(\pm q) &= \text{float}_N(\pm i) && (\text{if } +q - 1 < i \leq +q)
\end{aligned}$$

 $\text{fnearest}_N(z)$

- If z is a NaN, then return an element of $\text{nans}_N\{z\}$.
- Else if z is an infinity, then return z .
- Else if z is a zero, then return z .
- Else if z is greater than 0 but smaller than or equal to 0.5, then return positive zero.
- Else if z is smaller than 0 but greater than or equal to -0.5 , then return negative zero.
- Else return the integral value that is nearest to z ; if two values are equally near, return the even one.

$$\begin{aligned}
\text{fnearest}_N(\pm\text{nan}(n)) &= \text{nans}_N\{\pm\text{nan}(n)\} \\
\text{fnearest}_N(\pm\infty) &= \pm\infty \\
\text{fnearest}_N(\pm 0) &= \pm 0 \\
\text{fnearest}_N(+q) &= +0 && (\text{if } 0 < +q \leq 0.5) \\
\text{fnearest}_N(-q) &= -0 && (\text{if } -0.5 \leq -q < 0) \\
\text{fnearest}_N(\pm q) &= \text{float}_N(\pm i) && (\text{if } |i - q| < 0.5) \\
\text{fnearest}_N(\pm q) &= \text{float}_N(\pm i) && (\text{if } |i - q| = 0.5 \wedge i \text{ even})
\end{aligned}$$

 $\text{feq}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 0.
- Else if both z_1 and z_2 are zeroes, then return 1.
- Else if both z_1 and z_2 are the same value, then return 1.
- Else return 0.

$$\begin{aligned}
\text{feq}_N(\pm\text{nan}(n), z_2) &= 0 \\
\text{feq}_N(z_1, \pm\text{nan}(n)) &= 0 \\
\text{feq}_N(\pm 0, \mp 0) &= 1 \\
\text{feq}_N(z_1, z_2) &= \text{bool}(z_1 = z_2)
\end{aligned}$$

$\text{fne}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 1.
- Else if both z_1 and z_2 are zeroes, then return 0.
- Else if both z_1 and z_2 are the same value, then return 0.
- Else return 1.

$$\begin{aligned}\text{fne}_N(\pm\text{nan}(n), z_2) &= 1 \\ \text{fne}_N(z_1, \pm\text{nan}(n)) &= 1 \\ \text{fne}_N(\pm 0, \mp 0) &= 0 \\ \text{fne}_N(z_1, z_2) &= \text{bool}(z_1 \neq z_2)\end{aligned}$$

 $\text{flt}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 0.
- Else if z_1 and z_2 are the same value, then return 0.
- Else if z_1 is positive infinity, then return 0.
- Else if z_1 is negative infinity, then return 1.
- Else if z_2 is positive infinity, then return 1.
- Else if z_2 is negative infinity, then return 0.
- Else if both z_1 and z_2 are zeroes, then return 0.
- Else if z_1 is smaller than z_2 , then return 1.
- Else return 0.

$$\begin{aligned}\text{flt}_N(\pm\text{nan}(n), z_2) &= 0 \\ \text{flt}_N(z_1, \pm\text{nan}(n)) &= 0 \\ \text{flt}_N(z, z) &= 0 \\ \text{flt}_N(+\infty, z_2) &= 0 \\ \text{flt}_N(-\infty, z_2) &= 1 \\ \text{flt}_N(z_1, +\infty) &= 1 \\ \text{flt}_N(z_1, -\infty) &= 0 \\ \text{flt}_N(\pm 0, \mp 0) &= 0 \\ \text{flt}_N(z_1, z_2) &= \text{bool}(z_1 < z_2)\end{aligned}$$

 $\text{fgt}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 0.
- Else if z_1 and z_2 are the same value, then return 0.
- Else if z_1 is positive infinity, then return 1.
- Else if z_1 is negative infinity, then return 0.
- Else if z_2 is positive infinity, then return 0.
- Else if z_2 is negative infinity, then return 1.
- Else if both z_1 and z_2 are zeroes, then return 0.
- Else if z_1 is larger than z_2 , then return 1.
- Else return 0.

$\text{fgt}_N(\pm\text{nan}(n), z_2)$	$=$	0
$\text{fgt}_N(z_1, \pm\text{nan}(n))$	$=$	0
$\text{fgt}_N(z, z)$	$=$	0
$\text{fgt}_N(+\infty, z_2)$	$=$	1
$\text{fgt}_N(-\infty, z_2)$	$=$	0
$\text{fgt}_N(z_1, +\infty)$	$=$	0
$\text{fgt}_N(z_1, -\infty)$	$=$	1
$\text{fgt}_N(\pm 0, \mp 0)$	$=$	0
$\text{fgt}_N(z_1, z_2)$	$=$	$\text{bool}(z_1 > z_2)$

$\text{fle}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 0.
- Else if z_1 and z_2 are the same value, then return 1.
- Else if z_1 is positive infinity, then return 0.
- Else if z_1 is negative infinity, then return 1.
- Else if z_2 is positive infinity, then return 1.
- Else if z_2 is negative infinity, then return 0.
- Else if both z_1 and z_2 are zeroes, then return 1.
- Else if z_1 is smaller than or equal to z_2 , then return 1.
- Else return 0.

$\text{fle}_N(\pm\text{nan}(n), z_2)$	$=$	0
$\text{fle}_N(z_1, \pm\text{nan}(n))$	$=$	0
$\text{fle}_N(z, z)$	$=$	1
$\text{fle}_N(+\infty, z_2)$	$=$	0
$\text{fle}_N(-\infty, z_2)$	$=$	1
$\text{fle}_N(z_1, +\infty)$	$=$	1
$\text{fle}_N(z_1, -\infty)$	$=$	0
$\text{fle}_N(\pm 0, \mp 0)$	$=$	1
$\text{fle}_N(z_1, z_2)$	$=$	$\text{bool}(z_1 \leq z_2)$

$\text{fge}_N(z_1, z_2)$

- If either z_1 or z_2 is a NaN, then return 0.
- Else if z_1 and z_2 are the same value, then return 1.
- Else if z_1 is positive infinity, then return 1.
- Else if z_1 is negative infinity, then return 0.
- Else if z_2 is positive infinity, then return 0.
- Else if z_2 is negative infinity, then return 1.
- Else if both z_1 and z_2 are zeroes, then return 1.
- Else if z_1 is smaller than or equal to z_2 , then return 1.
- Else return 0.

$$\begin{aligned}
\text{fge}_N(\pm\text{nan}(n), z_2) &= 0 \\
\text{fge}_N(z_1, \pm\text{nan}(n)) &= 0 \\
\text{fge}_N(z, z) &= 1 \\
\text{fge}_N(+\infty, z_2) &= 1 \\
\text{fge}_N(-\infty, z_2) &= 0 \\
\text{fge}_N(z_1, +\infty) &= 0 \\
\text{fge}_N(z_1, -\infty) &= 1 \\
\text{fge}_N(\pm 0, \mp 0) &= 1 \\
\text{fge}_N(z_1, z_2) &= \text{bool}(z_1 \geq z_2)
\end{aligned}$$

$\text{fpmin}_N(z_1, z_2)$

- If z_2 is less than z_1 then return z_2 .
- Else return z_1 .

$$\begin{aligned}
\text{fpmin}_N(z_1, z_2) &= z_2 \quad (\text{if } \text{flt}_N(z_2, z_1) = 1) \\
\text{fpmin}_N(z_1, z_2) &= z_1 \quad (\text{otherwise})
\end{aligned}$$

$\text{fpmax}_N(z_1, z_2)$

- If z_1 is less than z_2 then return z_2 .
- Else return z_1 .

$$\begin{aligned}
\text{fpmax}_N(z_1, z_2) &= z_2 \quad (\text{if } \text{flt}_N(z_1, z_2) = 1) \\
\text{fpmax}_N(z_1, z_2) &= z_1 \quad (\text{otherwise})
\end{aligned}$$

4.3.4 Conversions

$\text{extend}^u_{M,N}(i)$

- Return i .

$$\text{extend}^u_{M,N}(i) = i$$

Note: In the abstract syntax, unsigned extension just reinterprets the same value.

$\text{extend}^s_{M,N}(i)$

- Let j be the signed interpretation of i of size M .
- Return the two's complement of j relative to size N .

$$\text{extend}^s_{M,N}(i) = \text{signed}_N^{-1}(\text{signed}_M(i))$$

$\text{wrap}_{M,N}(i)$

- Return i modulo 2^N .

$$\text{wrap}_{M,N}(i) = i \bmod 2^N$$

$\text{trunc}^u_{M,N}(z)$

- If z is a NaN, then the result is undefined.
- Else if z is an infinity, then the result is undefined.
- Else if z is a number and $\text{trunc}(z)$ is a value within range of the target type, then return that value.
- Else the result is undefined.

$$\begin{aligned} \text{trunc}^u_{M,N}(\pm\text{nan}(n)) &= \{\} \\ \text{trunc}^u_{M,N}(\pm\infty) &= \{\} \\ \text{trunc}^u_{M,N}(\pm q) &= \text{trunc}(\pm q) && (\text{if } -1 < \text{trunc}(\pm q) < 2^N) \\ \text{trunc}^u_{M,N}(\pm q) &= \{\} && (\text{otherwise}) \end{aligned}$$

Note: This operator is [partial](#). It is not defined for NaNs, infinities, or values for which the result is out of range.

$\text{trunc}^s_{M,N}(z)$

- If z is a NaN, then the result is undefined.
- Else if z is an infinity, then the result is undefined.
- If z is a number and $\text{trunc}(z)$ is a value within range of the target type, then return that value.
- Else the result is undefined.

$$\begin{aligned} \text{trunc}^s_{M,N}(\pm\text{nan}(n)) &= \{\} \\ \text{trunc}^s_{M,N}(\pm\infty) &= \{\} \\ \text{trunc}^s_{M,N}(\pm q) &= \text{trunc}(\pm q) && (\text{if } -2^{N-1} - 1 < \text{trunc}(\pm q) < 2^{N-1}) \\ \text{trunc}^s_{M,N}(\pm q) &= \{\} && (\text{otherwise}) \end{aligned}$$

Note: This operator is [partial](#). It is not defined for NaNs, infinities, or values for which the result is out of range.

$\text{trunc_sat_u}_{M,N}(z)$

- If z is a NaN, then return 0.
- Else if z is negative infinity, then return 0.
- Else if z is positive infinity, then return $2^N - 1$.
- Else, return $\text{sat_u}_N(\text{trunc}(z))$.

$$\begin{aligned} \text{trunc_sat_u}_{M,N}(\pm\text{nan}(n)) &= 0 \\ \text{trunc_sat_u}_{M,N}(-\infty) &= 0 \\ \text{trunc_sat_u}_{M,N}(+\infty) &= 2^N - 1 \\ \text{trunc_sat_u}_{M,N}(z) &= \text{sat_u}_N(\text{trunc}(z)) \end{aligned}$$

$\text{trunc_sat}_{S_{M,N}}(z)$

- If z is a NaN, then return 0.
- Else if z is negative infinity, then return -2^{N-1} .
- Else if z is positive infinity, then return $2^{N-1} - 1$.
- Else, return the value whose signed interpretation is $\text{sat}_{S_N}(\text{trunc}(z))$.

$$\begin{aligned}\text{trunc_sat}_{S_{M,N}}(\pm\text{nan}(n)) &= 0 \\ \text{trunc_sat}_{S_{M,N}}(-\infty) &= -2^{N-1} \\ \text{trunc_sat}_{S_{M,N}}(+\infty) &= 2^{N-1} - 1 \\ \text{trunc_sat}_{S_{M,N}}(z) &= \text{signed}_N^{-1}(\text{sat}_{S_N}(\text{trunc}(z)))\end{aligned}$$

$\text{promote}_{M,N}(z)$

- If z is a canonical NaN, then return an element of $\text{nans}_N\{\}$ (i.e., a canonical NaN of size N).
- Else if z is a NaN, then return an element of $\text{nans}_N\{\pm\text{nan}(1)\}$ (i.e., any arithmetic NaN of size N).
- Else, return z .

$$\begin{aligned}\text{promote}_{M,N}(\pm\text{nan}(n)) &= \text{nans}_N\{\} && (\text{if } n = \text{canon}_N) \\ \text{promote}_{M,N}(\pm\text{nan}(n)) &= \text{nans}_N\{+\text{nan}(1)\} && (\text{otherwise}) \\ \text{promote}_{M,N}(z) &= z\end{aligned}$$

$\text{demote}_{M,N}(z)$

- If z is a canonical NaN, then return an element of $\text{nans}_N\{\}$ (i.e., a canonical NaN of size N).
- Else if z is a NaN, then return an element of $\text{nans}_N\{\pm\text{nan}(1)\}$ (i.e., any NaN of size N).
- Else if z is an infinity, then return that infinity.
- Else if z is a zero, then return that zero.
- Else, return $\text{float}_N(z)$.

$$\begin{aligned}\text{demote}_{M,N}(\pm\text{nan}(n)) &= \text{nans}_N\{\} && (\text{if } n = \text{canon}_N) \\ \text{demote}_{M,N}(\pm\text{nan}(n)) &= \text{nans}_N\{+\text{nan}(1)\} && (\text{otherwise}) \\ \text{demote}_{M,N}(\pm\infty) &= \pm\infty \\ \text{demote}_{M,N}(\pm 0) &= \pm 0 \\ \text{demote}_{M,N}(\pm q) &= \text{float}_N(\pm q)\end{aligned}$$

$\text{convert}^u_{M,N}(i)$

- Return $\text{float}_N(i)$.

$$\text{convert}^u_{M,N}(i) = \text{float}_N(i)$$

$\text{convert}^s_{M,N}(i)$

- Let j be the signed interpretation of i .
- Return $\text{float}_N(j)$.

$$\text{convert}^s_{M,N}(i) = \text{float}_N(\text{signed}_M(i))$$

 $\text{reinterpret}_{t_1,t_2}(c)$

- Let d^* be the bit sequence $\text{bits}_{t_1}(c)$.
- Return the constant c' for which $\text{bits}_{t_2}(c') = d^*$.

$$\text{reinterpret}_{t_1,t_2}(c) = \text{bits}_{t_2}^{-1}(\text{bits}_{t_1}(c))$$

 $\text{narrow}^s_{M,N}(i)$

- Let j be the signed interpretation of i of size M .
- Return the value whose signed interpretation is $\text{sat}_{sN}(j)$.

$$\text{narrow}^s_{M,N}(i) = \text{signed}_N^{-1}(\text{sat}_{sN}(\text{signed}_M(i)))$$

 $\text{narrow}^u_{M,N}(i)$

- Let j be the signed interpretation of i of size M .
- Return $\text{sat}_{uN}(j)$.

$$\text{narrow}^u_{M,N}(i) = \text{sat}_{uN}(\text{signed}_M(i))$$

4.3.5 Relaxed Operations

The result of *relaxed* operators are *implementation-dependent*, because the set of possible results may depend on properties of the host environment, such as its hardware. Technically, their behaviour is controlled by a set of *global parameters* to the semantics that an implementation can instantiate in different ways. These choices are fixed, that is, parameters are constant during the execution of any given program.

Every such parameter is an index into a sequence of possible sets of results and must be instantiated to a defined index. In the *deterministic profile*, every parameter is prescribed to be 0. This behaviour is expressed by the following auxiliary function, where R is a global parameter selecting one of the allowed outcomes:

$$\begin{aligned} \text{[!DET]} \quad \text{relaxed}(R)[A_0, \dots, A_n] &= A_R \\ \text{relaxed}(R)[A_0, \dots, A_n] &= A_0 \end{aligned}$$

Note: Each parameter can be thought of as inducing a family of operations that is fixed to one particular choice by an implementation. The fixed operation itself can still be non-deterministic or partial.

Implementations are expected to either choose the behaviour that is the most efficient on the underlying hardware, or the behaviour of the deterministic profile.

$\text{frelaxed_madd}_N(z_1, z_2, z_3)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{fmadd}} \in \{0, 1\}$.

- Return $\text{relaxed}(R_{\text{fmadd}})[\text{fadd}_N(\text{fmul}_N(z_1, z_2), z_3)$ or $\text{fma}_N(z_1, z_2, z_3)]$.

$$\text{frelaxed_madd}_N(z_1, z_2, z_3) = \text{relaxed}(R_{\text{fmadd}})[\text{fadd}_N(\text{fmul}_N(z_1, z_2), z_3), \text{fma}_N(z_1, z_2, z_3)]$$

Note: Relaxed multiply-add allows for fused or unfused results, which leads to implementation-dependent rounding behaviour. In the [deterministic profile](#), the unfused behaviour is used.

$\text{frelaxed_nmadd}_N(z_1, z_2, z_3)$

- Return $\text{frelaxed_madd}(-z_1, z_2, z_3)$.

$$\text{frelaxed_nmadd}_N(z_1, z_2, z_3) = \text{frelaxed_madd}_N(-z_1, z_2, z_3)$$

Note: This operation is implementation-dependent because [frelaxed_madd](#) is implementation-dependent.

$\text{frelaxed_min}_N(z_1, z_2)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{fmin}} \in \{0, 1, 2, 3\}$.

- If z_1 is a NaN, then return $\text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(z_1, z_2), \text{NaN}(n), z_2, z_2]$.
- If z_2 is a NaN, then return $\text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(z_1, z_2), z_1, \text{NaN}(n), z_1]$.
- If both z_1 and z_2 are zeroes of opposite sign, then return $\text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(z_1, z_2), \text{pm } 0, \text{mp } 0, -0]$.
- Return $\text{fmin}_N(z_1, z_2)$.

$$\begin{aligned} \text{frelaxed_min}_N(\pm \text{nan}(n), z_2) &= \text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(\pm \text{nan}(n), z_2), \text{nan}(n), z_2, z_2] \\ \text{frelaxed_min}_N(z_1, \pm \text{nan}(n)) &= \text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(z_1, \pm \text{nan}(n)), z_1, \text{nan}(n), z_1] \\ \text{frelaxed_min}_N(\pm 0, \mp 0) &= \text{relaxed}(R_{\text{fmin}})[\text{fmin}_N(\pm 0, \mp 0), \pm 0, \mp 0, -0] \\ \text{frelaxed_min}_N(z_1, z_2) &= \text{fmin}_N(z_1, z_2) \quad (\text{otherwise}) \end{aligned}$$

Note: Relaxed minimum is implementation-dependent for NaNs and for zeroes with different signs. In the [deterministic profile](#), it behaves like regular [fmin](#).

$\text{frelaxed_max}_N(z_1, z_2)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{fmax}} \in \{0, 1, 2, 3\}$.

- If z_1 is a NaN, then return $\text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(z_1, z_2), \text{NaN}(n), z_2, z_2]$.
- If z_2 is a NaN, then return $\text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(z_1, z_2), z_1, \text{NaN}(n), z_1]$.
- If both z_1 and z_2 are zeroes of opposite sign, then return $\text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(z_1, z_2), \text{pm } 0, \text{mp } 0, +0]$.
- Return $\text{fmax}_N(z_1, z_2)$.

$$\begin{aligned}
 \text{frelaxed_max}_N(\pm \text{nan}(n), z_2) &= \text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(\pm \text{nan}(n), z_2), \text{nan}(n), z_2, z_2] \\
 \text{frelaxed_max}_N(z_1, \pm \text{nan}(n)) &= \text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(z_1, \pm \text{nan}(n)), z_1, \text{nan}(n), z_1] \\
 \text{frelaxed_max}_N(\pm 0, \mp 0) &= \text{relaxed}(R_{\text{fmax}})[\text{fmax}_N(\pm 0, \mp 0), \pm 0, \mp 0, +0] \\
 \text{frelaxed_max}_N(z_1, z_2) &= \text{fmax}_N(z_1, z_2) \quad (\text{otherwise})
 \end{aligned}$$

Note: Relaxed maximum is implementation-dependent for NaNs and for zeroes with different signs. In the [deterministic profile](#), it behaves like regular `fmax`.

`irelaxed_dot_mulM,N(i1, i2)`

This is an auxiliary operator for the specification of `relaxed_dot`.

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{idot}} \in \{0, 1\}$.

- Return `relaxed(Ridot)[imulN(extendsM,N(i1), extendsM,N(i2)), imulN(extendsM,N(i1), extenduM,N(i2))]`.

$$\text{irelaxed_dot_mul}_{M,N}(i_1, i_2) = \text{relaxed}(R_{\text{idot}})[\text{imul}_N(\text{extend}^s_{M,N}(i_1), \text{extend}^s_{M,N}(i_2)), \text{imul}_N(\text{extend}^s_{M,N}(i_1), \text{extend}^u_{M,N}(i_2))]$$

Note: Relaxed dot product is implementation-dependent when the second operand is negative in a signed interpretation. In the [deterministic profile](#), it behaves like signed dot product.

`irelaxed_q15mulr_sN(i1, i2)`

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{iq15mulr}} \in \{0, 1\}$.

- If both i_1 and i_2 equal $(\text{signed}_N^{-1}(-2^{N-1}))$, then return `relaxed(Riq15mulr)[2N-1 - 1, signedN-1(-2N-1)]`.
- Return `iq15mulrsat_s(i1, i2)`

$$\begin{aligned}
 \text{irelaxed_q15mulr_s}_N(\text{signed}_N^{-1}(-2^{N-1}), \text{signed}_N^{-1}(-2^{N-1})) &= \text{relaxed}(R_{\text{iq15mulr}})[2^{N-1} - 1, \text{signed}_N^{-1}(-2^{N-1})] \\
 \text{irelaxed_q15mulr_s}_N(i_1, i_2) &= \text{iq15mulrsat_s}(i_1, i_2)
 \end{aligned}$$

Note: Relaxed Q15 multiplication is implementation-dependent when the result overflows. In the [deterministic profile](#), it behaves like regular `iq15mulrsat_s`.

`relaxed_truncM,Nu(z)`

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{trunc_u}} \in \{0, 1, 2, 3\}$.

- If z is normal or subnormal and `trunc(z)` is non-negative and less than 2^N , then return `truncuM,N(z)`.
- Else, return `relaxed(Rtrunc_u)[trunc_sat_uM,N(z), 2N - 1, 2N - 2, 2(N-1)]`.

$$\begin{aligned}
 \text{relaxed_trunc}_{M,N}^u(\pm q) &= \text{trunc}_{M,N}^u(\pm q) && (\text{if } 0 \leq \text{trunc}(\pm q) < 2^N) \\
 \text{relaxed_trunc}_{M,N}^u(z) &= \text{relaxed}(R_{\text{trunc_u}})[\text{trunc_sat_u}_{M,N}(z), 2^N - 1, 2^N - 2, 2^{N-1}] && (\text{otherwise})
 \end{aligned}$$

Note: Relaxed unsigned truncation is implementation-dependent for NaNs and out-of-range values. In the [deterministic profile](#), it behaves like regular `trunc_sat_u`.

$\text{relaxed_trunc}_{M,N}^s(z)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{trunc_s}} \in \{0, 1\}$.

- If z is normal or subnormal and $\text{trunc}(z)$ is greater than or equal to -2^{N-1} and less than 2^{N-1} , then return $\text{trunc}_{M,N}^s(z)$.
- Else, return $\text{relaxed}(R_{\text{trunc_s}})[\text{trunc_sat}_{M,N}(z), 2^N - 1, 2^N - 2, 2(N - 1)]$.

$$\begin{aligned} \text{relaxed_trunc}_{M,N}^s(\pm q) &= \text{trunc}_{M,N}^s(\pm q) && (\text{if } -2^{N-1} \leq \text{trunc}(\pm q) < 2^{N-1}) \\ \text{relaxed_trunc}_{M,N}^s(z) &= \text{relaxed}(R_{\text{trunc_s}})[\text{trunc_sat}_{M,N}(z), \text{signed}_N^{-1}(-2^{N-1})] && (\text{otherwise}) \end{aligned}$$

Note: Relaxed signed truncation is implementation-dependent for NaNs and out-of-range values. In the [deterministic profile](#), it behaves like regular `trunc_sat_s`.

$\text{frelaxed_swizzle}(i^n, j^n)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{swizzle}} \in \{0, 1\}$.

- For each j_k in j^n , let r_k be the value $\text{frelaxed_swizzle_lane}(i^n, j_k)$.
- Let r^n be the concatenation of all r_k .
- Return r^n .

$$\text{frelaxed_swizzle}(i^n, j^n) = \text{frelaxed_swizzle_lane}(i^n, j)^n$$

where:

$$\begin{aligned} \text{frelaxed_swizzle_lane}(i^n, j) &= i[j] && (\text{if } j < 16) \\ \text{frelaxed_swizzle_lane}(i^n, j) &= 0 && (\text{if } \text{signed}_8(j) < 0) \\ \text{frelaxed_swizzle_lane}(i^n, j) &= \text{relaxed}(R_{\text{swizzle}})[0, i^n[j \bmod n]] && (\text{otherwise}) \end{aligned}$$

Note: Relaxed swizzle is implementation-dependent if the signed interpretation of any of the 8-bit indices in j^n is larger than or equal to 16. In the [deterministic profile](#), it behaves like regular `swizzle`.

$\text{irelaxed_laneselect}_N(i_1, i_2, i_3)$

The implementation-specific behaviour of this operation is determined by the global parameter $R_{\text{laneselect}} \in \{0, 1\}$.

- If i_3 is smaller than 2^{N-1} , then let i'_3 be the value 0, otherwise $2^N - 1$.
- Let i''_3 be $\text{relaxed}(R_{\text{laneselect}})[i_3, i'_3]$.
- Return $\text{ibitselect}_N(i_1, i_2, i''_3)$.

$$\text{irelaxed_laneselect}_N(i_1, i_2, i_3) = \text{ibitselect}_N(i_1, i_2, \text{relaxed}(R_{\text{laneselect}})[i_3, \text{extend}_{1,N}^s(\text{ishr_u}_N(i_3, N - 1))])$$

Note: Relaxed lane selection is non-deterministic when the mask mixes set and cleared bits, since the value of the high bit may or may not be expanded to all bits. In the [deterministic profile](#), it behaves like `ibitselect`.

4.4 Types

Execution has to check and compare **types** in a few places, such as **executing** `call_indirect` or **instantiating** modules. It is an invariant of the semantics that all types occurring during execution are **closed**.

Note: Runtime type checks generally involve types from multiple modules or types not defined by a module at all, such that module-local **type indices** are not meaningful.

4.4.1 Instantiation

Any form of **type** can be *instantiated* into a **closed** type inside a **module instance** by **substituting** each **type index** x occurring in it with the corresponding **defined type** `moduleinst.types[x]`.

$$\text{clos}_{\text{moduleinst}}(t) = t[:= \text{moduleinst.types}]$$

Note: This is the runtime equivalent to **type closure**.

4.5 Values

4.5.1 Value Typing

For the purpose of checking argument **values** against the parameter types of exported **functions**, values are classified by **value types**. The following auxiliary typing rules specify this typing relation relative to a **store** S in which possibly referenced addresses live.

Numeric Values `t.const c`

- The value is valid with **number type** t .

$$\overline{S \vdash t.\text{const } c : t}$$

Vector Values `t.const c`

- The value is valid with **vector type** t .

$$\overline{S \vdash t.\text{const } c : t}$$

Null References `ref.null t`

- The **heap type** must be **valid** under the empty context.
- Then the value is valid with **reference type** (`ref null t'`), where the **heap type** t' is the least type that **matches** t .

$$\frac{\vdash t \text{ ok} \quad t' \in \{\text{none}, \text{nofunc}, \text{noextern}\} \quad \vdash t' \leq t}{S \vdash \text{ref.null } t : (\text{ref null } t')}$$

Note: A null reference is typed with the least type in its respective hierarchy. That ensures that it is compatible with any nullable type in that hierarchy.

Scalar References $\text{ref.i31 } i$

- The value is valid with reference type (ref i31).

$$\frac{}{S \vdash \text{ref.i31 } i : \text{ref i31}}$$

Structure References $\text{ref.struct } a$

- The structure address a must exist in the store.
- Let structinst be the structure instance $S.\text{structs}[a]$.
- Let deftype be the defined type structinst.type .
- The expansion of deftype must be a struct type.
- Then the value is valid with reference type (ref deftype).

$$\frac{\text{deftype} = S.\text{structs}[a].\text{type} \quad \text{expand}(\text{deftype}) = \text{struct structtype}}{S \vdash \text{ref.struct } a : \text{ref } \text{deftype}}$$

Array References $\text{ref.array } a$

- The array address a must exist in the store.
- Let arrayinst be the array instance $S.\text{arrays}[a]$.
- Let deftype be the defined type arrayinst.type .
- The expansion of deftype must be an array type.
- Then the value is valid with reference type (ref arraytype).

$$\frac{\text{deftype} = S.\text{arrays}[a].\text{type} \quad \text{expand}(\text{deftype}) = \text{array arraytype}}{S \vdash \text{ref.array } a : \text{ref } \text{deftype}}$$

Exception References $\text{ref.exn } a$

- The store entry $S.\text{exns}[a]$ must exist.
- Then the value is valid with reference type exnref .

$$\frac{S.\text{exns}[a] = \text{exninst}}{S \vdash \text{ref.exn } a : \text{exnref}}$$

Function References $\text{ref.func } a$

- The function address a must exist in the store.
- Let funcinst be the function instance $S.\text{funcs}[a]$.
- Let deftype be the defined type funcinst.type .
- The expansion of deftype must be a function type.
- Then the value is valid with reference type (ref func).

$$\frac{\text{deftype} = S.\text{funcs}[a].\text{type} \quad \text{expand}(\text{deftype}) = \text{func func}}{S \vdash \text{ref.func } a : \text{ref } \text{deftype}}$$

Host References $\text{ref.host } a$

- The value is valid with **reference type** (**ref any**).

$$\frac{}{S \vdash \text{ref.host } a : \text{ref any}}$$

Note: A host reference is considered internalized by this rule.

External References $\text{ref.extern } ref$

- The reference value ref must be valid with some **reference type** (**ref null[?] t**).
- The **heap type** t must **match** the heap type **any**.
- Then the value is valid with **reference type** (**ref null[?] extern**).

$$\frac{S \vdash ref : \text{ref null}^? t \quad \vdash t \leq \text{any}}{S \vdash \text{ref.extern } ref : \text{ref null}^? \text{extern}}$$

Subsumption

- The value must be valid with some value type t .
- The value type t **matches** another **valid** type t' .
- Then the value is valid with type t' .

$$\frac{S \vdash val : t \quad \vdash t' \text{ ok} \quad \vdash t \leq t'}{S \vdash val : t'}$$

4.5.2 External Typing

For the purpose of checking **external values** against **imports**, such values are classified by **external types**. The following auxiliary typing rules specify this typing relation relative to a **store** S in which the referenced instances live.

func a

- The store entry $S.\text{funcs}[a]$ must exist.
- Then **func** a is valid with **external type** **func** $S.\text{funcs}[a].\text{type}$.

$$\frac{}{S \vdash \text{func } a : \text{func } S.\text{funcs}[a].\text{type}}$$

table a

- The store entry $S.tables[a]$ must exist.
- Then table a is valid with external type $table\ S.tables[a].type$.

$$\frac{}{S \vdash \text{table } a : \text{table } S.tables[a].type}$$

mem a

- The store entry $S.mems[a]$ must exist.
- Then mem a is valid with external type $\text{mem } S.mems[a].type$.

$$\frac{}{S \vdash \text{mem } a : \text{mem } S.mems[a].type}$$

global a

- The store entry $S.globals[a]$ must exist.
- Then global a is valid with external type $\text{global } S.globals[a].type$.

$$\frac{}{S \vdash \text{global } a : \text{global } S.globals[a].type}$$

tag a

- The store entry $S.tags[a]$ must exist.
- Let $tagtype$ be the function type $S.tags[a].type$.
- Then tag a is valid with external type $\text{tag } tagtype$.

$$\frac{}{S \vdash \text{tag } a : \text{tag } S.tags[a].type}$$

Subsumption

- The external value must be valid with some external type et .
- The external type et matches another valid type et' .
- Then the external value is valid with type et' .

$$\frac{S \vdash \text{externval} : et \quad \vdash et' \text{ ok} \quad \vdash et \leq et'}{S \vdash \text{externval} : et'}$$

4.6 Instructions

WebAssembly computation is performed by executing individual [instructions](#).

4.6.1 Numeric Instructions

Numeric instructions are defined in terms of the generic [numeric operators](#). The mapping of numeric instructions to their underlying operators is expressed by the following definition:

$$\begin{aligned} op_{iN}(i_1, \dots, i_k) &= iop_N(i_1, \dots, i_k) \\ op_{fN}(z_1, \dots, z_k) &= fop_N(z_1, \dots, z_k) \end{aligned}$$

And for [conversion operators](#):

$$cvtop_{t_1, t_2}^{sx?}(c) = cvtop_{|t_1|, |t_2|}^{sx?}(c)$$

Where the underlying operators are partial, the corresponding instruction will [trap](#) when the result is not defined. Where the underlying operators are non-deterministic, because they may return one of multiple possible NaN values, so are the corresponding instructions.

Note: For example, the result of instruction `i32.add` applied to operands i_1, i_2 invokes `addi32(i_1, i_2)`, which maps to the generic `iadd32(i_1, i_2)` via the above definition. Similarly, `i64.trunc_f32_s` applied to z invokes `truncf32, i64s(z)`, which maps to the generic `trunc32, 64s(z)`.

t.const c

1. Push the value *t.const c* to the stack.

Note: No formal reduction rule is required for this instruction, since [const](#) instructions already are [values](#).

t.unop

1. Assert: due to [validation](#), a value of [value type](#) *t* is on the top of the stack.
2. Pop the value *t.const c₁* from the stack.
3. If *unop_t(c₁)* is defined, then:
 - a. Let *c* be a possible result of computing *unop_t(c₁)*.
 - b. Push the value *t.const c* to the stack.
4. Else:
 - a. Trap.

$$\begin{aligned} (t.\text{const } c_1) \ t.\text{unop} &\hookrightarrow (t.\text{const } c) && (\text{if } c \in \text{unop}_t(c_1)) \\ (t.\text{const } c_1) \ t.\text{unop} &\hookrightarrow \text{trap} && (\text{if } \text{unop}_t(c_1) = \{\}) \end{aligned}$$

t.binop

1. Assert: due to **validation**, two values of **value type** *t* are on the top of the stack.
2. Pop the value *t.const* *c*₂ from the stack.
3. Pop the value *t.const* *c*₁ from the stack.
4. If *binop*_{*t*}(*c*₁, *c*₂) is defined, then:
 - a. Let *c* be a possible result of computing *binop*_{*t*}(*c*₁, *c*₂).
 - b. Push the value *t.const* *c* to the stack.
5. Else:
 - a. Trap.

$$\begin{aligned} (t.\text{const } c_1) (t.\text{const } c_2) t.\text{binop} &\hookrightarrow (t.\text{const } c) && (\text{if } c \in \text{binop}_t(c_1, c_2)) \\ (t.\text{const } c_1) (t.\text{const } c_2) t.\text{binop} &\hookrightarrow \text{trap} && (\text{if } \text{binop}_t(c_1, c_2) = \{\}) \end{aligned}$$

t.testop

1. Assert: due to **validation**, a value of **value type** *t* is on the top of the stack.
2. Pop the value *t.const* *c*₁ from the stack.
3. Let *c* be the result of computing *testop*_{*t*}(*c*₁).
4. Push the value *i32.const* *c* to the stack.

$$(t.\text{const } c_1) t.\text{testop} \hookrightarrow (i32.\text{const } c) \quad (\text{if } c = \text{testop}_t(c_1))$$

t.relop

1. Assert: due to **validation**, two values of **value type** *t* are on the top of the stack.
2. Pop the value *t.const* *c*₂ from the stack.
3. Pop the value *t.const* *c*₁ from the stack.
4. Let *c* be the result of computing *relop*_{*t*}(*c*₁, *c*₂).
5. Push the value *i32.const* *c* to the stack.

$$(t.\text{const } c_1) (t.\text{const } c_2) t.\text{relop} \hookrightarrow (i32.\text{const } c) \quad (\text{if } c = \text{relop}_t(c_1, c_2))$$

t₂.cvtop_t1_sx?

1. Assert: due to **validation**, a value of **value type** *t*₁ is on the top of the stack.
2. Pop the value *t₁.const* *c*₁ from the stack.
3. If *cvtop*_{*t*₁,*t*₂}^{*sx?*}(*c*₁) is defined:
 - a. Let *c*₂ be a possible result of computing *cvtop*_{*t*₁,*t*₂}^{*sx?*}(*c*₁).
 - b. Push the value *t₂.const* *c*₂ to the stack.
4. Else:
 - a. Trap.

$$\begin{aligned} (t_1.\text{const } c_1) t_2.\text{cvtop}_{t_1, t_2}^{sx?} &\hookrightarrow (t_2.\text{const } c_2) && (\text{if } c_2 \in \text{cvtop}_{t_1, t_2}^{sx?}(c_1)) \\ (t_1.\text{const } c_1) t_2.\text{cvtop}_{t_1, t_2}^{sx?} &\hookrightarrow \text{trap} && (\text{if } \text{cvtop}_{t_1, t_2}^{sx?}(c_1) = \{\}) \end{aligned}$$

4.6.2 Reference Instructions

`ref.null x`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let deftype be the defined type $F.\text{module.types}[x]$.
4. Push the value `ref.null deftype` to the stack.

$$F; (\text{ref.null } x) \hookrightarrow F; (\text{ref.null } \text{deftype}) \quad (\text{if } \text{deftype} = F.\text{module.types}[x])$$

Note: No formal reduction rule is required for the case `ref.null absheapttype` , since the instruction form is already a value.

`ref.func x`

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module.funcaddrs}[x]$ exists.
3. Let a be the function address $F.\text{module.funcaddrs}[x]$.
4. Push the value `ref.func a` to the stack.

$$F; (\text{ref.func } x) \hookrightarrow F; (\text{ref.func } a) \quad (\text{if } a = F.\text{module.funcaddrs}[x])$$

`ref.is_null`

1. Assert: due to validation, a reference value is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is `ref.null ht` , then:
 - a. Push the value `i32.const 1` to the stack.
4. Else:
 - a. Push the value `i32.const 0` to the stack.

$$\begin{aligned} \text{ref } \text{ref.is_null} &\hookrightarrow (\text{i32.const } 1) && (\text{if } \text{ref} = \text{ref.null } ht) \\ \text{ref } \text{ref.is_null} &\hookrightarrow (\text{i32.const } 0) && (\text{otherwise}) \end{aligned}$$

`ref.as_non_null`

1. Assert: due to validation, a reference value is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is `ref.null ht` , then:
 - a. Trap.
4. Push the value ref back to the stack.

$$\begin{aligned} \text{ref } \text{ref.as_non_null} &\hookrightarrow \text{trap} && (\text{if } \text{ref} = \text{ref.null } ht) \\ \text{ref } \text{ref.as_non_null} &\hookrightarrow \text{ref} && (\text{otherwise}) \end{aligned}$$

`ref.eq`

1. Assert: due to **validation**, two **reference values** are on the top of the stack.
 2. Pop the value ref_2 from the stack.
 3. Pop the value ref_1 from the stack.
 4. If ref_1 is the same as ref_2 , then:
 - a. Push the value `i32.const 1` to the stack.
 5. Else:
 - a. Push the value `i32.const 0` to the stack.
- $$\begin{array}{lll}
 ref_1 \ ref_2 \ \text{ref.eq} & \hookrightarrow & (\text{i32.const } 1) \quad (\text{if } ref_1 = (\text{ref.null } ht_1) \wedge ref_2 = (\text{ref.null } ht_2)) \\
 ref_1 \ ref_2 \ \text{ref.eq} & \hookrightarrow & (\text{i32.const } 1) \quad (\text{if } ref_1 = ref_2) \\
 ref_1 \ ref_2 \ \text{ref.eq} & \hookrightarrow & (\text{i32.const } 0) \quad (\text{otherwise})
 \end{array}$$

`ref.test rt`

1. Let F be the **current frame**.
 2. Let rt_1 be the **reference type** $\text{clos}_{F.\text{module}}(rt)$.
 3. Assert: due to **validation**, rt_1 is **closed**.
 4. Assert: due to **validation**, a **reference value** is on the top of the stack.
 5. Pop the value ref from the stack.
 6. Assert: due to **validation**, the **reference value** is **valid** with some **reference type**.
 7. Let rt_2 be the **reference type** of ref .
 8. If the **reference type** rt_2 **matches** rt_1 , then:
 - a. Push the value `i32.const 1` to the stack.
 9. Else:
 - a. Push the value `i32.const 0` to the stack.
- $$\begin{array}{lll}
 S; F; ref \ (\text{ref.test } rt) & \hookrightarrow & (\text{i32.const } 1) \quad (\text{if } S \vdash ref : rt' \wedge \vdash rt' \leq \text{clos}_{F.\text{module}}(rt)) \\
 S; F; ref \ (\text{ref.test } rt) & \hookrightarrow & (\text{i32.const } 0) \quad (\text{otherwise})
 \end{array}$$

`ref.cast rt`

1. Let F be the **current frame**.
2. Let rt_1 be the **reference type** $\text{clos}_{F.\text{module}}(rt)$.
3. Assert: due to **validation**, rt_1 is **closed**.
4. Assert: due to **validation**, a **reference value** is on the top of the stack.
5. Pop the value ref from the stack.
6. Assert: due to **validation**, the **reference value** is **valid** with some **reference type**.
7. Let rt_2 be the **reference type** of ref .
8. If the **reference type** rt_2 **matches** rt_1 , then:
 - a. Push the value ref back to the stack.
9. Else:
 - a. Trap.

$$\begin{aligned}
S; F; \text{ref}(\text{ref.cast } rt) &\hookrightarrow \text{ref} && (\text{if } S \vdash \text{ref} : rt' \wedge \vdash rt' \leq \text{clos}_{F.\text{module}}(rt)) \\
S; F; \text{ref}(\text{ref.cast } rt) &\hookrightarrow \text{trap} && (\text{otherwise})
\end{aligned}$$

ref.i31

1. Assert: due to **validation**, a value of type **i32** is on the top of the stack.
2. Pop the value **i32.const** i from the stack.
3. Let j be the result of computing **wrap**_{32,31}(i).
4. Push the reference value (**ref.i31** j) to the stack.

$$(\text{i32.const } i) \text{ ref.i31} \hookrightarrow (\text{ref.i31 wrap}_{32,31}(i))$$

i31.get_sx

1. Assert: due to **validation**, a value of type (**ref null i31**) is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is **ref.null** t , then:
 - a. Trap.
4. Assert: due to **validation**, a ref is a scalar reference.
5. Let **ref.i31** i be the reference value ref .
6. Let j be the result of computing **extend**_{31,32}^{sx}(i).
7. Push the value **i32.const** j to the stack.

$$\begin{aligned}
(\text{ref.i31 } i) \text{ i31.get_sx} &\hookrightarrow (\text{i32.const extend}_{31,32}^{\text{sx}}(i)) \\
(\text{ref.null } t) \text{ i31.get_sx} &\hookrightarrow \text{trap}
\end{aligned}$$

struct.new x

1. Let F be the **current frame**.
2. Assert: due to **validation**, the defined type $F.\text{module.types}[x]$ exists.
3. Let deftype be the defined type $F.\text{module.types}[x]$.
4. Assert: due to **validation**, the expansion of deftype is a structure type.
5. Let **struct** ft^* be the expanded structure type of deftype .
6. Let n be the length of the field type sequence ft^* .
7. Assert: due to **validation**, n values are on the top of the stack.
8. Pop the n values val^* from the stack.
9. For every value val_i in val^* and corresponding field type ft_i in ft^* :
 - a. Let fieldval_i be the result of computing **pack** _{ft_i} (val_i).
10. Let fieldval^* the concatenation of all field values fieldval_i .
11. Let si be the **structure instance** {type deftype , fields fieldval^* }.
12. Let a be the length of $S.\text{structs}$.
13. Append si to $S.\text{structs}$.
14. Push the **structure reference** **ref.struct** a to the stack.

$$\begin{aligned}
S; F; val^n (\text{struct.new } x) &\hookrightarrow S'; F; (\text{ref.struct } |S.\text{structs}|) \\
&\quad (\text{if expand}(F.\text{module.types}[x]) = \text{struct } ft^n \\
&\quad \wedge si = \{\text{type } F.\text{module.types}[x], \text{fields } (\text{pack}_{ft}(val))^n\} \\
&\quad \wedge S' = S \text{ with structs} = S.\text{structs } si)
\end{aligned}$$

`struct.new_default` x

1. Let F be the **current frame**.
2. Assert: due to **validation**, the **defined type** $F.\text{module.types}[x]$ exists.
3. Let $deftype$ be the **defined type** $F.\text{module.types}[x]$.
4. Assert: due to **validation**, the **expansion** of $deftype$ is a **structure type**.
5. Let $\text{struct } ft^*$ be the **expanded structure type** of $deftype$.
6. Let n be the length of the **field type** sequence ft^* .
7. For every **field type** ft_i in ft^* :
 - a. Let t_i be the **value type** $\text{unpack}(ft_i)$.
 - b. Assert: due to **validation**, default_{t_i} is defined.
 - c. Push the **value** default_{t_i} to the stack.
8. Execute the instruction $(\text{struct.new } x)$.

$$\begin{aligned}
F; (\text{struct.new_default } x) &\hookrightarrow (\text{default}_{\text{unpack}(ft)})^n (\text{struct.new } x) \\
&\quad (\text{if expand}(F.\text{module.types}[x]) = \text{struct } ft^n)
\end{aligned}$$

`struct.get_sx?` $x \ y$

1. Let F be the **current frame**.
2. Assert: due to **validation**, the **defined type** $F.\text{module.types}[x]$ exists.
3. Let $deftype$ be the **defined type** $F.\text{module.types}[x]$.
4. Assert: due to **validation**, the **expansion** of $deftype$ is a **structure type** with at least $y + 1$ fields.
5. Let $\text{struct } ft^*$ be the **expanded structure type** of $deftype$.
6. Let ft_y be the y -th **field type** of ft^* .
7. Assert: due to **validation**, a **value** of type $(\text{ref null } x)$ is on the top of the stack.
8. Pop the **value** ref from the stack.
9. If ref is **ref.null** t , then:
 - a. Trap.
10. Assert: due to **validation**, a ref is a **structure reference**.
11. Let **ref.struct** a be the reference value ref .
12. Assert: due to **validation**, the **structure instance** $S.\text{structs}[a]$ exists and has at least $y + 1$ fields.
13. Let $fieldval$ be the **field value** $S.\text{structs}[a].\text{fields}[y]$.
14. Let val be the result of computing $\text{unpack}_{ft_y}^{sx?}(fieldval)$.
15. Push the **value** val to the stack.

$$\begin{aligned}
S; F; (\text{ref.struct } a) (\text{struct.get_sx? } x \ y) &\hookrightarrow val && (\text{if expand}(F.\text{module.types}[x]) = \text{struct } ft^n \\
&&& \wedge val = \text{unpack}_{ft^n[y]}^{sx?}(S.\text{structs}[a].\text{fields}[y])) \\
S; F; (\text{ref.null } t) (\text{struct.get_sx? } x \ y) &\hookrightarrow \text{trap}
\end{aligned}$$

`struct.set x y`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let deftype be the defined type $F.\text{module.types}[x]$.
4. Assert: due to validation, the expansion of deftype is a structure type with at least $y + 1$ fields.
5. Let $\text{struct } ft^*$ be the expanded structure type of deftype .
6. Let ft_y be the y -th field type of ft^* .
7. Assert: due to validation, a value is on the top of the stack.
8. Pop the value val from the stack.
9. Assert: due to validation, a value of type (ref null x) is on the top of the stack.
10. Pop the value ref from the stack.
11. If ref is `ref.null t`, then:
 - a. Trap.
12. Assert: due to validation, a ref is a structure reference.
13. Let $\text{ref.struct } a$ be the reference value ref .
14. Assert: due to validation, the structure instance $S.\text{structs}[a]$ exists and has at least $y + 1$ fields.
15. Let fieldval be the result of computing $\text{pack}_{ft_y}(val)$.
16. Replace the field value $S.\text{structs}[a].\text{fields}[y]$ with fieldval .

$$S; F; (\text{ref.struct } a) \text{ val } (\text{struct.set } x y) \hookrightarrow S'; \epsilon \quad \left(\begin{array}{l} \text{if } \text{expand}(F.\text{module.types}[x]) = \text{struct } ft^n \\ \wedge S' = S \text{ with } \text{structs}[a].\text{fields}[y] = \text{pack}_{ft^n[y]}(val) \end{array} \right)$$

$$S; F; (\text{ref.null } t) \text{ val } (\text{struct.set } x y) \hookrightarrow \text{trap}$$

`array.new x`

1. Assert: due to validation, a value of type `i32` is on the top of the stack.
2. Pop the value `(i32.const n)` from the stack.
3. Assert: due to validation, a value is on the top of the stack.
4. Pop the value val from the stack.
5. Push the value val to the stack n times.
6. Execute the instruction `(array.new_fixed x n)`.

$$val \text{ (i32.const } n) \text{ (array.new } x) \hookrightarrow val^n \text{ (array.new_fixed } x n)$$

`array.new_default x`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let deftype be the defined type $F.\text{module.types}[x]$.
4. Assert: due to validation, the expansion of deftype is an array type.
5. Let $\text{array } ft$ be the expanded array type of deftype .
6. Assert: due to validation, a value of type `i32` is on the top of the stack.
7. Pop the value `i32.const n` from the stack.

8. Let t be the value type `unpack(ft)`.
9. Assert: due to validation, `default t` is defined.
10. Push the value `default t` to the stack n times.
11. Execute the instruction `(array.new_fixed x n)`.

$$F; (i32.const\ n)\ (array.new_default\ x) \hookrightarrow (\text{default}_{\text{unpack}(ft)})^n\ (array.new_fixed\ x\ n)$$

$$(\text{if expand}(F.\text{module.types}[x]) = \text{array } ft)$$

`array.new_fixed x n`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let $deftype$ be the defined type $F.\text{module.types}[x]$.
4. Assert: due to validation, the expansion of $deftype$ is a array type.
5. Let array ft be the expanded array type of $deftype$.
6. Assert: due to validation, n values are on the top of the stack.
7. Pop the n values val^* from the stack.
8. For every value val_i in val^* :
 - a. Let $fieldval_i$ be the result of computing $\text{pack}_{ft}(val_i)$.
9. Let $fieldval^*$ be the concatenation of all field values $fieldval_i$.
10. Let ai be the array instance $\{\text{type } deftype, \text{fields } fieldval^*\}$.
11. Let a be the length of $S.\text{arrays}$.
12. Append ai to $S.\text{arrays}$.
13. Push the array reference `ref.array a` to the stack.

$$S; F; val^n\ (array.new_fixed\ x\ n) \hookrightarrow S'; F; (\text{ref.array } |S.\text{arrays}|)$$

$$(\text{if expand}(F.\text{module.types}[x]) = \text{array } ft)$$

$$\wedge ai = \{\text{type } F.\text{module.types}[x], \text{fields } (\text{pack}_{ft}(val))^n\}$$

$$\wedge S' = S \text{ with arrays} = S.\text{arrays } ai$$

`array.new_data x y`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let $deftype$ be the defined type $F.\text{module.types}[x]$.
4. Assert: due to validation, the expansion of $deftype$ is an array type.
5. Let array ft be the expanded array type of $deftype$.
6. Assert: due to validation, the data address $F.\text{module.dataaddrs}[y]$ exists.
7. Let da be the data address $F.\text{module.dataaddrs}[y]$.
8. Assert: due to validation, the data instance $S.\text{datas}[da]$ exists.
9. Let $datainst$ be the data instance $S.\text{datas}[da]$.
10. Assert: due to validation, two values of type `i32` are on the top of the stack.
11. Pop the value `i32.const n` from the stack.
12. Pop the value `i32.const s` from the stack.

13. Assert: due to **validation**, the **field type** ft has a defined **bit width**.
14. Let z be the **bit width** of **field type** ft divided by eight.
15. If the sum of s and n times z is larger than the length of $datainst.data$, then:
 - a. Trap.
16. Let b^* be the **byte sequence** $datainst.data[s : n \cdot z]$.
17. Let t be the **value type unpack**(ft).
18. For each of the n consecutive subsequences b'^z of b^* :
 - a. Assert: due to **validation**, $bytes_{ft}$ is defined.
 - b. Let c_i be the constant for which $bytes_{ft}(c_i)$ is b'^z .
 - c. Push the value $t.const\ c_i$ to the stack.
19. Execute the instruction (**array.new_fixed** $x\ n$).

$$S; F; (i32.const\ s)\ (i32.const\ n)\ (array.new_data\ x\ y) \hookrightarrow \text{trap}$$

$$(\text{if expand}(F.module.types[x]) = \text{array } ft$$

$$\wedge s + n \cdot |ft|/8 > |S.datas[F.module.dataaddrs[y]].data|)$$

$$S; F; (i32.const\ s)\ (i32.const\ n)\ (array.new_data\ x\ y) \hookrightarrow (t.const\ c)^n\ (array.new_fixed\ x\ n)$$

$$(\text{if expand}(F.module.types[x]) = \text{array } ft$$

$$\wedge t = \text{unpack}(ft)$$

$$\wedge \text{concat}((bytes_{ft}(c))^n) = S.datas[F.module.dataaddrs[y]].data)$$

array.new_elem $x\ y$

1. Let F be the **current frame**.
2. Assert: due to **validation**, the **element address** $F.module.elemaddrs[y]$ exists.
3. Let ea be the **element address** $F.module.elemaddrs[y]$.
4. Assert: due to **validation**, the **element instance** $S.elems[ea]$ exists.
5. Let $eleminst$ be the **element instance** $S.elems[ea]$.
6. Assert: due to **validation**, two **values** of type **i32** are on the top of the stack.
7. Pop the value (**i32.const** n) from the stack.
8. Pop the value (**i32.const** s) from the stack.
9. If the sum of s and n is larger than the length of $eleminst.elem$, then:
 - a. Trap.
10. Let ref^* be the **reference sequence** $eleminst.elem[s : n]$.
11. Push the references ref^* to the stack.
12. Execute the instruction (**array.new_fixed** $x\ n$).

$$S; F; (i32.const\ s)\ (i32.const\ n)\ (array.new_elem\ x\ y) \hookrightarrow \text{trap}$$

$$(\text{if } s + n > |S.elems[F.module.elemaddrs[y]].elem|)$$

$$S; F; (i32.const\ s)\ (i32.const\ n)\ (array.new_elem\ x\ y) \hookrightarrow ref^n\ (array.new_fixed\ x\ n)$$

$$(\text{if } ref^n = S.elems[F.module.elemaddrs[y]].elem[s : n])$$

`array.getsx? x`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.module.types[x]$ exists.
3. Let $deftype$ be the defined type $F.module.types[x]$.
4. Assert: due to validation, the expansion of $deftype$ is an array type.
5. Let $array\ ft$ be the expanded array type of $deftype$.
6. Assert: due to validation, a value of type `i32` is on the top of the stack.
7. Pop the value `i32.const i` from the stack.
8. Assert: due to validation, a value of type `(ref null x)` is on the top of the stack.
9. Pop the value ref from the stack.
10. If ref is `ref.null t`, then:
 - a. Trap.
11. Assert: due to validation, ref is an array reference.
12. Let $ref.array\ a$ be the reference value ref .
13. Assert: due to validation, the array instance $S.arrays[a]$ exists.
14. If n is larger than or equal to the length of $S.arrays[a].fields$, then:
 - a. Trap.
15. Let $fieldval$ be the field value $S.arrays[a].fields[i]$.
16. Let val be the result of computing $unpack_{ft}^{sx?}(fieldval)$.
17. Push the value val to the stack.

$$\begin{aligned}
 &S; F; (ref.array\ a)\ (i32.const\ i)\ (array.get_{sx?}\ x) \hookrightarrow \text{trap} \\
 &\quad (\text{if } i \geq |arrays[a].fields|) \\
 &S; F; (ref.array\ a)\ (i32.const\ i)\ (array.get_{sx?}\ x) \hookrightarrow val \\
 &\quad (\text{if } \text{expand}(F.module.types[x]) = array\ ft \\
 &\quad \quad \wedge val = \text{unpack}_{ft}^{sx?}(S.arrays[a].fields[i])) \\
 &S; F; (ref.null\ t)\ (i32.const\ i)\ (array.get_{sx?}\ x) \hookrightarrow \text{trap}
 \end{aligned}$$

`array.set x`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.module.types[x]$ exists.
3. Let $deftype$ be the defined type $F.module.types[x]$.
4. Assert: due to validation, the expansion of $deftype$ is an array type.
5. Let $array\ ft$ be the expanded array type of $deftype$.
6. Assert: due to validation, a value is on the top of the stack.
7. Pop the value val from the stack.
8. Assert: due to validation, a value of type `i32` is on the top of the stack.
9. Pop the value `i32.const i` from the stack.
10. Assert: due to validation, a value of type `(ref null x)` is on the top of the stack.
11. Pop the value ref from the stack.

12. If ref is $ref.null\ t$, then:
 - a. Trap.
13. Assert: due to **validation**, ref is an array reference.
14. Let $ref.array\ a$ be the reference value ref .
15. Assert: due to **validation**, the array instance $S.arrays[a]$ exists.
16. If n is larger than or equal to the length of $S.arrays[a].fields$, then:
 - a. Trap.
17. Let $fieldval$ be the result of computing $pack_{ft}(val)$.
18. Replace the field value $S.arrays[a].fields[i]$ with $fieldval$.

$$\begin{aligned}
 &S; F; (ref.array\ a)\ (i32.const\ i)\ val\ (array.set\ x) \hookrightarrow \text{trap} \\
 &\quad (\text{if } i \geq |arrays[a].fields|) \\
 &S; F; (ref.array\ a)\ (i32.const\ i)\ val\ (array.set\ x) \hookrightarrow S'; \epsilon \\
 &\quad (\text{if } \text{expand}(F.module.types[x]) = \text{array } ft \\
 &\quad \quad \wedge S' = S \text{ with } arrays[a].fields[i] = pack_{ft}(val)) \\
 &S; F; (ref.null\ t)\ (i32.const\ i)\ val\ (array.set\ x) \hookrightarrow \text{trap}
 \end{aligned}$$

array.len

1. Assert: due to **validation**, a value of type **(ref null array)** is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is $ref.null\ t$, then:
 - a. Trap.
4. Assert: due to **validation**, ref is an array reference.
5. Let $ref.array\ a$ be the reference value ref .
6. Assert: due to **validation**, the array instance $S.arrays[a]$ exists.
7. Let n be the length of $S.arrays[a].fields$.
8. Push the value $(i32.const\ n)$ to the stack.

$$\begin{aligned}
 &S; (ref.array\ a)\ array.len \hookrightarrow (i32.const\ |S.arrays[a].fields|) \\
 &S; (ref.null\ t)\ array.len \hookrightarrow \text{trap}
 \end{aligned}$$

array.fill x

1. Assert: due to **validation**, a value of type **i32** is on the top of the stack.
2. Pop the value n from the stack.
3. Assert: due to **validation**, a value is on the top of the stack.
4. Pop the value val from the stack.
5. Assert: due to **validation**, a value of type **i32** is on the top of the stack.
6. Pop the value d from the stack.
7. Assert: due to **validation**, a value of type **(ref null x)** is on the top of the stack.
8. Pop the value ref from the stack.
9. If ref is $ref.null\ t$, then:
 - a. Trap.

10. Assert: due to *validation*, *ref* is an array reference.
11. Let *ref.array a* be the reference value *ref*.
12. Assert: due to *validation*, the array instance *S.arrays[a]* exists.
13. If $d + n$ is larger than the length of *S.arrays[a].fields*, then:
 - a. Trap.
14. If $n = 0$, then:
 - a. Return.
15. Push the value *ref.array a* to the stack.
16. Push the value *i32.const d* to the stack.
17. Push the value *val* to the stack.
18. Execute the instruction *array.set x*.
19. Push the value *ref.array a* to the stack.
20. Assert: due to the earlier check against the array size, $d + 1 < 2^{32}$.
21. Push the value *i32.const (d + 1)* to the stack.
22. Push the value *val* to the stack.
23. Push the value *i32.const (n - 1)* to the stack.
24. Execute the instruction *array.fill x*.

$$\begin{aligned}
 S; (\text{ref.array } a) (\text{i32.const } d) \text{ val } (\text{i32.const } n) (\text{array.fill } x) &\hookrightarrow \text{trap} \\
 &\quad (\text{if } d + n > |S.\text{arrays}[a].\text{fields}|) \\
 S; (\text{ref.array } a) (\text{i32.const } d) \text{ val } (\text{i32.const } 0) (\text{array.fill } x) &\hookrightarrow \epsilon \\
 &\quad (\text{otherwise}) \\
 S; (\text{ref.array } a) (\text{i32.const } d) \text{ val } (\text{i32.const } n + 1) (\text{array.fill } x) &\hookrightarrow \\
 &\quad (\text{ref.array } a) (\text{i32.const } d) \text{ val } (\text{array.set } x) \\
 &\quad (\text{ref.array } a) (\text{i32.const } d + 1) \text{ val } (\text{i32.const } n) (\text{array.fill } x) \\
 &\quad (\text{otherwise}) \\
 S; (\text{ref.null } t) (\text{i32.const } d) \text{ val } (\text{i32.const } n) (\text{array.fill } x) &\hookrightarrow \text{trap}
 \end{aligned}$$

array.copy x y

1. Let *F* be the current frame.
2. Assert: due to *validation*, the defined type *F.module.types[y]* exists.
3. Let *deftype* be the defined type *F.module.types[y]*.
4. Assert: due to *validation*, the expansion of *deftype* is an array type.
5. Let *array mut st* be the expanded array type *deftype*.
6. Assert: due to *validation*, a value of type *i32* is on the top of the stack.
7. Pop the value *i32.const n* from the stack.
8. Assert: due to *validation*, a value of type *i32* is on the top of the stack.
9. Pop the value *i32.const s* from the stack.
10. Assert: due to *validation*, a value of type *(ref null y)* is on the top of the stack.
11. Pop the value *ref₂* from the stack.
12. Assert: due to *validation*, a value of type *i32* is on the top of the stack.

13. Pop the value `i32.const d` from the stack.
14. Assert: due to [validation](#), a value of type `(ref null x)` is on the top of the stack.
15. Pop the value `ref1` from the stack.
16. If `ref1` is `ref.null t`, then:
 - a. Trap.
17. Assert: due to [validation](#), `ref1` is an array reference.
18. Let `ref.array a1` be the reference value `ref1`.
19. If `ref2` is `ref.null t`, then:
 - a. Trap.
20. Assert: due to [validation](#), `ref2` is an array reference.
21. Let `ref.array a2` be the reference value `ref2`.
22. Assert: due to [validation](#), the array instance `S.arrays[a1]` exists.
23. Assert: due to [validation](#), the array instance `S.arrays[a2]` exists.
24. If $d + n$ is larger than the length of `S.arrays[a1].fields`, then:
 - a. Trap.
25. If $s + n$ is larger than the length of `S.arrays[a2].fields`, then:
 - a. Trap.
26. If $n = 0$, then:
 - a. Return.
27. If $d \leq s$, then:
 - a. Push the value `ref.array a1` to the stack.
 - b. Push the value `i32.const d` to the stack.
 - c. Push the value `ref.array a2` to the stack.
 - d. Push the value `i32.const s` to the stack.
 - e. Execute `getfield(st)`.
 - f. Execute the instruction `array.set x`.
 - g. Push the value `ref.array a1` to the stack.
 - h. Assert: due to the earlier check against the array size, $d + 1 < 2^{32}$.
 - i. Push the value `i32.const (d + 1)` to the stack.
 - j. Push the value `ref.array a2` to the stack.
 - k. Assert: due to the earlier check against the array size, $s + 1 < 2^{32}$.
 - l. Push the value `i32.const (s + 1)` to the stack.
28. Else:
 - a. Push the value `ref.array a1` to the stack.
 - b. Assert: due to the earlier check against the array size, $d + n - 1 < 2^{32}$.
 - c. Push the value `i32.const (d + n - 1)` to the stack.
 - d. Push the value `ref.array a2` to the stack.
 - e. Assert: due to the earlier check against the array size, $s + n - 1 < 2^{32}$.
 - f. Push the value `i32.const (s + n - 1)` to the stack.

- g. Execute `getfield(st)`.
 - h. Execute the instruction `array.set x`.
 - i. Push the value `ref.array a1` to the stack.
 - j. Push the value `i32.const d` to the stack.
 - k. Push the value `ref.array a2` to the stack.
 - l. Push the value `i32.const s` to the stack.
29. Push the value `i32.const (n - 1)` to the stack.
30. Execute the instruction `array.copy x y`.

$S; F; (\text{ref.array } a_1) (\text{i32.const } d) (\text{ref.array } a_2) (\text{i32.const } s) (\text{i32.const } n) (\text{array.copy } x \ y) \hookrightarrow \text{trap}$
 (if $d + n > |S.\text{arrays}[a_1].\text{fields}| \vee s + n > |S.\text{arrays}[a_2].\text{fields}|$)

$S; F; (\text{ref.array } a_1) (\text{i32.const } d) (\text{ref.array } a_2) (\text{i32.const } s) (\text{i32.const } 0) (\text{array.copy } x \ y) \hookrightarrow \epsilon$
 (otherwise)

$S; F; (\text{ref.array } a_1) (\text{i32.const } d) (\text{ref.array } a_2) (\text{i32.const } s) (\text{i32.const } n + 1) (\text{array.copy } x \ y) \hookrightarrow$
 $(\text{ref.array } a_1) (\text{i32.const } d)$
 $(\text{ref.array } a_2) (\text{i32.const } s) \text{getfield}(st)$
 $(\text{array.set } x)$
 $(\text{ref.array } a_1) (\text{i32.const } d + 1) (\text{ref.array } a_2) (\text{i32.const } s + 1) (\text{i32.const } n) (\text{array.copy } x \ y)$
 (otherwise, if $d \leq s \wedge F.\text{module.types}[y] = \text{array mut } st$)

$S; F; (\text{ref.array } a_1) (\text{i32.const } d) (\text{ref.array } a_2) (\text{i32.const } s) (\text{i32.const } n + 1) (\text{array.copy } x \ y) \hookrightarrow$
 $(\text{ref.array } a_1) (\text{i32.const } d + n)$
 $(\text{ref.array } a_2) (\text{i32.const } s + n) \text{getfield}(st)$
 $(\text{array.set } x)$
 $(\text{ref.array } a_1) (\text{i32.const } d) (\text{ref.array } a_2) (\text{i32.const } s) (\text{i32.const } n) (\text{array.copy } x \ y)$
 (otherwise, if $d > s \wedge F.\text{module.types}[y] = \text{array mut } st$)

$S; F; (\text{ref.null } t) (\text{i32.const } d) \text{val} (\text{i32.const } s) (\text{i32.const } n) (\text{array.copy } x \ y) \hookrightarrow \text{trap}$

$S; F; \text{val} (\text{i32.const } d) (\text{ref.null } t) (\text{i32.const } s) (\text{i32.const } n) (\text{array.copy } x \ y) \hookrightarrow \text{trap}$

Where:

$$\begin{aligned} \text{getfield}(\text{valtype}) &= \text{array.get } y \\ \text{getfield}(\text{packedtype}) &= \text{array.get_u } y \end{aligned}$$

`array.init_data x y`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.\text{module.types}[x]$ exists.
3. Let deftype be the defined type $F.\text{module.types}[x]$.
4. Assert: due to validation, the expansion of deftype is an array type.
5. Let array ft be the expanded array type deftype .
6. Assert: due to validation, the data address $F.\text{module.dataaddrs}[y]$ exists.
7. Let da be the data address $F.\text{module.dataaddrs}[y]$.
8. Assert: due to validation, the data instance $S.\text{datas}[da]$ exists.
9. Let datainst be the data instance $S.\text{datas}[da]$.
10. Assert: due to validation, three values of type `i32` are on the top of the stack.

11. Pop the value `i32.const n` from the stack.
12. Pop the value `i32.const s` from the stack.
13. Pop the value `i32.const d` from the stack.
14. Assert: due to [validation](#), a value of type `(ref null x)` is on the top of the stack.
15. Pop the value `ref` from the stack.
16. If `ref` is `ref.null t`, then:
 - a. Trap.
17. Assert: due to [validation](#), `ref` is an array reference.
18. Let `ref.array a` be the reference value `ref`.
19. Assert: due to [validation](#), the array instance `S.arrays[a]` exists.
20. Assert: due to [validation](#), the field type `ft` has a defined bit width.
21. Let `z` be the bit width of field type `ft` divided by eight.
22. If `d + n` is larger than the length of `S.arrays[a].fields`, or the sum of `s` and `n` times `z` is larger than the length of `datainst.data`, then:
 - a. Trap.
23. If `n = 0`, then:
 - a. Return.
24. Let `b*` be the byte sequence `datainst.data[s : z]`.
25. Let `t` be the value type `unpack(ft)`.
26. Assert: due to [validation](#), `bytesft` is defined.
27. Let `c` be the constant for which `bytesft(c)` is `b*`.
28. Push the value `ref.array a` to the stack.
29. Push the value `i32.const d` to the stack.
30. Push the value `t.const c` to the stack.
31. Execute the instruction `array.set x`.
32. Push the value `ref.array a` to the stack.
33. Push the value `i32.const (d + 1)` to the stack.
34. Push the value `i32.const (s + z)` to the stack.
35. Push the value `i32.const (n - 1)` to the stack.
36. Execute the instruction `array.init_data x y`.

$$\begin{aligned}
 &S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n) (\text{array.init_data } x \ y) \hookrightarrow \text{trap} \\
 &\quad (\text{if } d + n > |S.\text{arrays}[a].\text{fields}| \\
 &\quad \vee (F.\text{module.types}[x] = \text{array } ft \wedge s + n \cdot |ft|/8 > |S.\text{datas}[F.\text{module.dataaddrs}[y]].\text{data}|)) \\
 &S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } 0) (\text{array.init_data } x \ y) \hookrightarrow \epsilon \\
 &\quad (\text{otherwise}) \\
 &S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n + 1) (\text{array.init_data } x \ y) \hookrightarrow \\
 &\quad (\text{ref.array } a) (\text{i32.const } d) (t.\text{const } c) (\text{array.set } x) \\
 &\quad (\text{ref.array } a) (\text{i32.const } d + 1) (\text{i32.const } s + |ft|/8) (\text{i32.const } n) (\text{array.init_data } x \ y) \\
 &\quad (\text{otherwise, if } F.\text{module.types}[x] = \text{array } ft \\
 &\quad \quad \wedge t = \text{unpack}(ft) \\
 &\quad \quad \wedge \text{bytes}_{ft}(c) = S.\text{datas}[F.\text{module.dataaddrs}[y]].\text{data}[s : |ft|/8]) \\
 &S; F; (\text{ref.null } t) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n) (\text{array.init_data } x \ y) \hookrightarrow \text{trap}
 \end{aligned}$$

`array.init_elem x y`

1. Let F be the current frame.
2. Assert: due to validation, the defined type $F.module.types[x]$ exists.
3. Let $deftype$ be the defined type $F.module.types[x]$.
4. Assert: due to validation, the expansion of $deftype$ is an array type.
5. Let $array\ ft$ be the expanded array type $deftype$.
6. Assert: due to validation, the element address $F.module.elemaddrs[y]$ exists.
7. Let ea be the element address $F.module.elemaddrs[y]$.
8. Assert: due to validation, the element instance $S.elems[ea]$ exists.
9. Let $eleminst$ be the element instance $S.elems[ea]$.
10. Assert: due to validation, three values of type `i32` are on the top of the stack.
11. Pop the value `i32.const n` from the stack.
12. Pop the value `i32.const s` from the stack.
13. Pop the value `i32.const d` from the stack.
14. Assert: due to validation, a value of type `(ref null x)` is on the top of the stack.
15. Pop the value ref from the stack.
16. If ref is `ref.null t`, then:
 - a. Trap.
17. Assert: due to validation, ref is an array reference.
18. Let $ref.array\ a$ be the reference value ref .
19. Assert: due to validation, the array instance $S.arrays[a]$ exists.
20. If $d + n$ is larger than the length of $S.arrays[a].fields$, or $s + n$ is larger than the length of $eleminst.elem$, then:
 - a. Trap.
21. If $n = 0$, then:
 - a. Return.
22. Let ref' be the reference value $eleminst.elem[s]$.
23. Push the value $ref.array\ a$ to the stack.
24. Push the value `i32.const d` to the stack.
25. Push the value ref' to the stack.
26. Execute the instruction `array.set x`.
27. Push the value $ref.array\ a$ to the stack.
28. Push the value `i32.const (d + 1)` to the stack.
29. Push the value `i32.const (s + 1)` to the stack.
30. Push the value `i32.const (n - 1)` to the stack.
31. Execute the instruction `array.init_elem x y`.

$$\begin{aligned}
& S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n) (\text{array.init_elem } x \ y) \hookrightarrow \text{trap} \\
& \quad (\text{if } d + n > |S.\text{arrays}[a].\text{fields}| \\
& \quad \vee s + n > |S.\text{elems}[F.\text{module}.\text{elemaddrs}[y]].\text{elem}|) \\
& S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } 0) (\text{array.init_elem } x \ y) \hookrightarrow \epsilon \\
& \quad (\text{otherwise}) \\
& S; F; (\text{ref.array } a) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n + 1) (\text{array.init_elem } x \ y) \hookrightarrow \\
& \quad (\text{ref.array } a) (\text{i32.const } d) \text{ ref } (\text{array.set } x) \\
& \quad (\text{ref.array } a) (\text{i32.const } d + 1) (\text{i32.const } s + 1) (\text{i32.const } n) (\text{array.init_elem } x \ y) \\
& \quad (\text{otherwise, if ref} = S.\text{elems}[F.\text{module}.\text{elemaddrs}[y]].\text{elem}[s]) \\
& S; F; (\text{ref.null } t) (\text{i32.const } d) (\text{i32.const } s) (\text{i32.const } n) (\text{array.init_elem } x \ y) \hookrightarrow \text{trap}
\end{aligned}$$

any.convert_extern

1. Assert: due to **validation**, a **reference value** is on the top of the stack.
2. Pop the value *ref* from the stack.
3. If *ref* is **ref.null ht**, then:
 - a. Push the reference value (**ref.null any**) to the stack.
4. Else:
 - a. Assert: due to **validation**, a *ref* is an **external reference**.
 - b. Let **ref.extern ref'** be the reference value *ref*.
 - c. Push the reference value *ref'* to the stack.

$$\begin{aligned}
(\text{ref.null } ht) \text{ any.convert_extern} & \hookrightarrow (\text{ref.null any}) \\
(\text{ref.extern } ref) \text{ any.convert_extern} & \hookrightarrow ref
\end{aligned}$$

extern.convert_any

1. Assert: due to **validation**, a **reference value** is on the top of the stack.
2. Pop the value *ref* from the stack.
3. If *ref* is **ref.null ht**, then:
 - a. Push the reference value (**ref.null extern**) to the stack.
4. Else:
 - a. Let *ref'* be the reference value (**ref.extern ref**).
 - b. Push the reference value *ref'* to the stack.

$$\begin{aligned}
(\text{ref.null } ht) \text{ extern.convert_any} & \hookrightarrow (\text{ref.null extern}) \\
ref \text{ extern.convert_any} & \hookrightarrow (\text{ref.extern } ref) \quad (\text{if } ref \neq (\text{ref.null } ht))
\end{aligned}$$

4.6.3 Vector Instructions

Vector instructions that operate bitwise are handled as integer operations of respective width.

$$op_{vN}(i_1, \dots, i_k) = iop_N(i_1, \dots, i_k)$$

Most other vector instructions are defined in terms of numeric operators that are applied lane-wise according to the given [shape](#).

$$op_{txN}(n_1, \dots, n_k) = \text{lanes}_{txN}^{-1}(op_t(i_1, \dots, i_k)^*) \quad (\text{if } i_1^* = \text{lanes}_{txN}(n_1) \wedge \dots \wedge i_k^* = \text{lanes}_{txN}(n_k))$$

Note: For example, the result of instruction `i32x4.add` applied to operands v_1, v_2 invokes `add_i32x4(v_1, v_2)`, which maps to $\text{lanes}_{i32x4}^{-1}(\text{add}_{i32}(i_1, i_2)^*)$, where i_1^* and i_2^* are sequences resulting from invoking $\text{lanes}_{i32x4}(v_1)$ and $\text{lanes}_{i32x4}(v_2)$ respectively.

For non-deterministic operators this definition is generalized to sets:

$$op_{txN}(n_1, \dots, n_k) = \{\text{lanes}_{txN}^{-1}(i^*) \mid i^* \in \times op_t(i_1, \dots, i_k)^* \wedge i_1^* = \text{lanes}_{txN}(n_1) \wedge \dots \wedge i_k^* = \text{lanes}_{txN}(n_k)\}$$

where $\times \{x^*\}^N$ transforms a sequence of N sets of values into a set of sequences of N values by computing the set product:

$$\times(S_1 \dots S_N) = \{x_1 \dots x_N \mid x_1 \in S_1 \wedge \dots \wedge x_N \in S_N\}$$

`v128.const c`

1. Push the value `v128.const c` to the stack.

Note: No formal reduction rule is required for this instruction, since `const` instructions coincide with [values](#).

`v128.vvunop`

1. Assert: due to [validation](#), a value of [value type v128](#) is on the top of the stack.
2. Pop the value `v128.const c1` from the stack.
3. Let c be the result of computing $\text{vvunop}_{v128}(c_1)$.
4. Push the value `v128.const c` to the stack.

$$(\text{v128.const } c_1) \text{ v128.vvunop} \hookrightarrow (\text{v128.const } c) \quad (\text{if } c = \text{vvunop}_{v128}(c_1))$$

`v128.vvbinop`

1. Assert: due to [validation](#), two values of [value type v128](#) are on the top of the stack.
2. Pop the value `v128.const c2` from the stack.
3. Pop the value `v128.const c1` from the stack.
4. Let c be the result of computing $\text{vvbinop}_{v128}(c_1, c_2)$.
5. Push the value `v128.const c` to the stack.

$$(\text{v128.const } c_1) (\text{v128.const } c_2) \text{ v128.vvbinop} \hookrightarrow (\text{v128.const } c) \quad (\text{if } c = \text{vvbinop}_{v128}(c_1, c_2))$$

v128.vternop

1. Assert: due to [validation](#), three values of [value type v128](#) are on the top of the stack.
2. Pop the value `v128.const` c_3 from the stack.
3. Pop the value `v128.const` c_2 from the stack.
4. Pop the value `v128.const` c_1 from the stack.
5. Let c be the result of computing $vternop_{v128}(c_1, c_2, c_3)$.
6. Push the value `v128.const` c to the stack.

$$(v128.const\ c_1)\ (v128.const\ c_2)\ (v128.const\ c_3)\ v128.vternop \hookrightarrow (v128.const\ c) \quad (\text{if } c = vternop_{v128}(c_1, c_2, c_3))$$
v128.any_true

1. Assert: due to [validation](#), a value of [value type v128](#) is on the top of the stack.
2. Pop the value `v128.const` c_1 from the stack.
3. Let i be the result of computing $ine_{128}(c_1, 0)$.
4. Push the value `i32.const` i onto the stack.

$$(v128.const\ c_1)\ v128.any_true \hookrightarrow (i32.const\ i) \quad (\text{if } i = ine_{128}(c_1, 0))$$
i8x16.swizzle

1. Assert: due to [validation](#), two values of [value type v128](#) are on the top of the stack.
2. Pop the value `v128.const` c_2 from the stack.
3. Let i^* be the result of computing $lanes_{i8x16}(c_2)$.
4. Pop the value `v128.const` c_1 from the stack.
5. Let j^* be the result of computing $lanes_{i8x16}(c_1)$.
6. Let c^* be the concatenation of the two sequences j^* and 0^{240} .
7. Let c' be the result of computing $lanes_{i8x16}^{-1}(c^*[i^*[0]] \dots c^*[i^*[15]])$.
8. Push the value `v128.const` c' onto the stack.

$$(v128.const\ c_1)\ (v128.const\ c_2)\ i8x16.swizzle \hookrightarrow (v128.const\ c') \\ (\text{if } i^* = lanes_{i8x16}(c_2) \\ \wedge c^* = lanes_{i8x16}(c_1)\ 0^{240} \\ \wedge c' = lanes_{i8x16}^{-1}(c^*[i^*[0]] \dots c^*[i^*[15]]))$$
i8x16.relaxed_swizzle

1. Assert: due to [validation](#), two values of [value type v128](#) are on the top of the stack.
2. Pop the value `v128.const` c_2 from the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let c' be the result of computing $lanes_{i8x16}^{-1}(frelaxed_swizzle(lanes_{i8x16}(c_1), lanes_{i8x16}(c_2)))$.
5. Push the value `v128.const` c' onto the stack.

$$(v128.const\ c_1)\ (v128.const\ c_2)\ i8x16.frelaxed_swizzle \hookrightarrow (v128.const\ c') \\ (\text{if } c' = lanes_{i8x16}^{-1}(frelaxed_swizzle(lanes_{i8x16}(c_1), lanes_{i8x16}(c_2))))$$

`i8x16.shuffle` x^*

1. Assert: due to **validation**, two values of **value type** `v128` are on the top of the stack.
2. Assert: due to **validation**, for all x_i in x^* it holds that $x_i < 32$.
3. Pop the value `v128.const` c_2 from the stack.
4. Let i_2^* be the result of computing `lanesi8x16(c_2)`.
5. Pop the value `v128.const` c_1 from the stack.
6. Let i_1^* be the result of computing `lanesi8x16(c_1)`.
7. Let i^* be the concatenation of the two sequences i_1^* and i_2^* .
8. Let c be the result of computing `lanesi8x16-1($i^*[x^*[0]] \dots i^*[x^*[15]]$)`.
9. Push the value `v128.const` c onto the stack.

$$\begin{aligned} &(\text{v128.const } c_1) (\text{v128.const } c_2) (\text{i8x16.shuffle } x^*) \hookrightarrow (\text{v128.const } c) \\ &(\text{if } i^* = \text{lanes}_{\text{i8x16}}(c_1) \text{ lanes}_{\text{i8x16}}(c_2) \\ &\quad \wedge c = \text{lanes}_{\text{i8x16}}^{-1}(i^*[x^*[0]] \dots i^*[x^*[15]])) \end{aligned}$$

`shape.splat`

1. Let t be the type `unpack(shape)`.
2. Assert: due to **validation**, a value of **value type** t is on the top of the stack.
3. Pop the value `t.const` c_1 from the stack.
4. Let N be the integer `dim(shape)`.
5. Let c be the result of computing `lanesshape-1(c_1^N)`.
6. Push the value `v128.const` c to the stack.

$$(t.\text{const } c_1) \text{ shape.splat} \hookrightarrow (\text{v128.const } c) \quad (\text{if } t = \text{unpack}(\text{shape}) \wedge c = \text{lanes}_{\text{shape}}^{-1}(c_1^{\dim(\text{shape})}))$$

`t1×N.extract_lanesx?` x

1. Assert: due to **validation**, $x < N$.
2. Assert: due to **validation**, a value of **value type** `v128` is on the top of the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let i^* be the result of computing `lanest1×N(c_1)`.
5. Let t_2 be the type `unpack($t_1 \times N$)`.
6. Let c_2 be the result of computing `extendt1,t2sx?($i^*[x]$)`.
7. Push the value `t2.const` c_2 to the stack.

$$\begin{aligned} &(\text{v128.const } c_1) (t_1 \times N.\text{extract_lane } x) \hookrightarrow (t_2.\text{const } c_2) \\ &(\text{if } t_2 = \text{unpack}(t_1 \times N) \\ &\quad \wedge c_2 = \text{extend}_{t_1, t_2}^{\text{sx?}}(\text{lanes}_{t_1 \times N}(c_1)[x])) \end{aligned}$$

shape.replace_lane x

1. Assert: due to **validation**, $x < \dim(shape)$.
2. Let t_2 be the type `unpack(shape)`.
3. Assert: due to **validation**, a value of **value type** t_1 is on the top of the stack.
4. Pop the value $t_2.\text{const } c_2$ from the stack.
5. Assert: due to **validation**, a value of **value type** `v128` is on the top of the stack.
6. Pop the value $v128.\text{const } c_1$ from the stack.
7. Let i^* be the result of computing $\text{lanes}_{shape}(c_1)$.
8. Let c be the result of computing $\text{lanes}_{shape}^{-1}(i^* \text{ with } [x] = c_2)$.
9. Push $v128.\text{const } c$ on the stack.

$$\begin{aligned}
 & (v128.\text{const } c_1) (t_2.\text{const } c_2) (shape.\text{replace_lane } x) \hookrightarrow (v128.\text{const } c) \\
 & \quad (\text{if } i^* = \text{lanes}_{shape}(c_1) \\
 & \quad \wedge c = \text{lanes}_{shape}^{-1}(i^* \text{ with } [x] = c_2))
 \end{aligned}$$

shape.vunop

1. Assert: due to **validation**, a value of **value type** `v128` is on the top of the stack.
2. Pop the value $v128.\text{const } c_1$ from the stack.
3. Let c be the result of computing $vunop_{shape}(c_1)$.
4. Push the value $v128.\text{const } c$ to the stack.

$$(v128.\text{const } c_1) shape.vunop \hookrightarrow (v128.\text{const } c) \quad (\text{if } c = vunop_{shape}(c_1))$$

shape.vbinop

1. Assert: due to **validation**, two values of **value type** `v128` are on the top of the stack.
2. Pop the value $v128.\text{const } c_2$ from the stack.
3. Pop the value $v128.\text{const } c_1$ from the stack.
4. If $vbinop_{shape}(c_1, c_2)$ is defined:
 - a. Let c be a possible result of computing $vbinop_{shape}(c_1, c_2)$.
 - b. Push the value $v128.\text{const } c$ to the stack.
5. Else:
 - a. Trap.

$$\begin{aligned}
 & (v128.\text{const } c_1) (v128.\text{const } c_2) shape.vbinop \hookrightarrow (v128.\text{const } c) \quad (\text{if } c \in vbinop_{shape}(c_1, c_2)) \\
 & (v128.\text{const } c_1) (v128.\text{const } c_2) shape.vbinop \hookrightarrow \text{trap} \quad (\text{if } vbinop_{shape}(c_1, c_2) = \{\})
 \end{aligned}$$

shape.vternop

1. Assert: due to [validation](#), three values of [value type v128](#) are on the top of the stack.
2. Pop the value [v128.const](#) c_3 from the stack.
3. Pop the value [v128.const](#) c_2 from the stack.
4. Pop the value [v128.const](#) c_1 from the stack.
5. Let c be the result of computing $vternop_{shape}(c_1, c_2, c_3)$.
6. Push the value [v128.const](#) c to the stack.

$$(\text{v128.const } c_1) (\text{v128.const } c_2) (\text{v128.const } c_3) \text{v128.vternop} \hookrightarrow (\text{v128.const } c) \\ (\text{if } c = vternop_{shape}(c_1, c_2, c_3))$$

txN.relaxed_laneselect

1. Assert: due to [validation](#), three values of [value type v128](#) are on the top of the stack.
2. Pop the value [v128.const](#) c_3 from the stack.
3. Pop the value [v128.const](#) c_2 from the stack.
4. Pop the value [v128.const](#) c_1 from the stack.
5. Let N be the [bit width](#) $|t|$ of [value type](#) t .
6. Let c be the result of computing $irelaxed_laneselect_{txN}(c_1, c_2, c_3)$.
7. Push the value [v128.const](#) c to the stack.

$$(\text{v128.const } c_1) (\text{v128.const } c_2) (\text{v128.const } c_3) \text{v128.relaxed_laneselect} \hookrightarrow (\text{v128.const } c) \\ (\text{if } c = irelaxed_laneselect_{txN}(c_1, c_2, c_3)^*)$$

txN.vrelop

1. Assert: due to [validation](#), two values of [value type v128](#) are on the top of the stack.
2. Pop the value [v128.const](#) c_2 from the stack.
3. Pop the value [v128.const](#) c_1 from the stack.
4. Let i_1^* be the result of computing $lanes_{txN}(c_1)$.
5. Let i_2^* be the result of computing $lanes_{txN}(c_2)$.
6. Let i^* be the result of computing $vrelop_t(i_1^*, i_2^*)$.
7. Let j^* be the result of computing $extend^s_{1,|t|}(i^*)$.
8. Let c be the result of computing $lanes_{txN}^{-1}(j^*)$.
9. Push the value [v128.const](#) c to the stack.

$$(\text{v128.const } c_1) (\text{v128.const } c_2) \text{txN.vrelop} \hookrightarrow (\text{v128.const } c) \\ (\text{if } c = lanes_{txN}^{-1}(extend^s_{1,|t|}(vrelop_t(lanes_{txN}(c_1), lanes_{txN}(c_2)))))$$

txN.vishiftop

1. Assert: due to **validation**, a value of **value type** i32 is on the top of the stack.
2. Pop the value **i32.const** s from the stack.
3. Assert: due to **validation**, a value of **value type** v128 is on the top of the stack.
4. Pop the value **v128.const** c_1 from the stack.
5. Let i^* be the result of computing $\text{lanes}_{txN}(c_1)$.
6. Let j^* be the result of computing $\text{vishiftop}_t(i^*, s^N)$.
7. Let c be the result of computing $\text{lanes}_{txN}^{-1}(j^*)$.
8. Push the value **v128.const** c to the stack.

$$\begin{aligned}
 (\text{v128.const } c_1) (\text{i32.const } s) \text{ txN.vishiftop} &\hookrightarrow (\text{v128.const } c) \\
 &\quad (\text{if } i^* = \text{lanes}_{txN}(c_1) \\
 &\quad \wedge c = \text{lanes}_{txN}^{-1}(\text{vishiftop}_t(i^*, s^N)))
 \end{aligned}$$

shape.all_true

1. Assert: due to **validation**, a value of **value type** v128 is on the top of the stack.
2. Pop the value **v128.const** c from the stack.
3. Let i_1^* be the result of computing $\text{lanes}_{shape}(c)$.
4. Let i be the result of computing $\text{bool}(\bigwedge (i_1 \neq 0)^*)$.
5. Push the value **i32.const** i onto the stack.

$$\begin{aligned}
 (\text{v128.const } c) \text{ shape.all_true} &\hookrightarrow (\text{i32.const } i) \\
 &\quad (\text{if } i_1^* = \text{lanes}_{shape}(c) \\
 &\quad \wedge i = \text{bool}(\bigwedge (i_1 \neq 0)^*))
 \end{aligned}$$

txN.bitmask

1. Assert: due to **validation**, a value of **value type** v128 is on the top of the stack.
2. Pop the value **v128.const** c from the stack.
3. Let i_1^N be the result of computing $\text{lanes}_{txN}(c)$.
4. Let B be the **bit width** $|t|$ of **value type** t .
5. Let i_2^N be the result of computing $\text{ilt}_{s_B}(i_1^N, 0^N)$.
6. Let j^* be the concatenation of the two sequences i_2^N and 0^{32-N} .
7. Let i be the result of computing $\text{ibits}_{32}^{-1}(j^*)$.
8. Push the value **i32.const** i onto the stack.

$$(\text{v128.const } c) \text{ txN.bitmask} \hookrightarrow (\text{i32.const } i) \quad (\text{if } i = \text{ibits}_{32}^{-1}(\text{ilt}_{s_{|t|}}(\text{lanes}_{txN}(c), (0)^N)(0)^{32-N}))$$

$t_2 \times N$.*narrow* _{$t_1 \times M$} _{$_{sx}$}

1. Assert: due to **syntax**, $N = 2 \cdot M$.
2. Assert: due to **validation**, two values of **value type v128** are on the top of the stack.
3. Pop the value **v128.const** c_2 from the stack.
4. Let i_2^M be the result of computing $\text{lanes}_{t_1 \times M}(c_2)$.
5. Let d_2^M be the result of computing $\text{narrow}_{|t_1|, |t_2|}^{sx}(i_2^M)$.
6. Pop the value **v128.const** c_1 from the stack.
7. Let i_1^M be the result of computing $\text{lanes}_{t_1 \times M}(c_1)$.
8. Let d_1^M be the result of computing $\text{narrow}_{|t_1|, |t_2|}^{sx}(i_1^M)$.
9. Let j^N be the concatenation of the two sequences d_1^M and d_2^M .
10. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(j^N)$.
11. Push the value **v128.const** c onto the stack.

$$\begin{aligned} & (\text{v128.const } c_1) (\text{v128.const } c_2) t_2 \times N.\text{narrow}_{t_1 \times M}_{sx} \hookrightarrow (\text{v128.const } c) \\ & \quad (\text{if } d_1^M = \text{narrow}_{|t_1|, |t_2|}^{sx}(\text{lanes}_{t_1 \times M}(c_1)) \\ & \quad \wedge d_2^M = \text{narrow}_{|t_1|, |t_2|}^{sx}(\text{lanes}_{t_1 \times M}(c_2)) \\ & \quad \wedge c = \text{lanes}_{t_2 \times N}^{-1}(d_1^M d_2^M)) \end{aligned}$$

$t_2 \times N$.*vcvtop* _{$t_1 \times M$} _{$_{sx}$}

1. Assert: due to **syntax**, $N = M$.
2. Assert: due to **validation**, a value of **value type v128** is on the top of the stack.
3. Pop the value **v128.const** c_1 from the stack.
4. Let i^* be the result of computing $\text{lanes}_{t_1 \times M}(c_1)$.
5. Let j^* be the result of computing $\text{vcvtop}_{|t_1|, |t_2|}^{sx}(i^*)$.
6. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(j^*)$.
7. Push the value **v128.const** c onto the stack.

$$\begin{aligned} & (\text{v128.const } c_1) t_2 \times N.\text{vcvtop}_{t_1 \times M}_{sx} \hookrightarrow (\text{v128.const } c) \\ & \quad (\text{if } c = \text{lanes}_{t_2 \times N}^{-1}(\text{vcvtop}_{|t_1|, |t_2|}^{sx}(\text{lanes}_{t_1 \times M}(c_1)))) \end{aligned}$$

$t_2 \times N$.*vcvtop_half* _{$t_1 \times M$} _{$_{sx}?$}

1. Assert: due to **syntax**, $N = M/2$.
2. Assert: due to **validation**, a value of **value type v128** is on the top of the stack.
3. Pop the value **v128.const** c_1 from the stack.
4. Let i^* be the result of computing $\text{lanes}_{t_1 \times M}(c_1)$.
5. If *half* is low, then:
 - a. Let j^* be the sequence $i^*[0 : N]$.
6. Else:
 - a. Let j^* be the sequence $i^*[N : N]$.
7. Let k^* be the result of computing $\text{vcvtop}_{|t_1|, |t_2|}^{sx?}(j^*)$.
8. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(k^*)$.

9. Push the value `v128.const` c onto the stack.

$$(\text{v128.const } c_1) \ t_2 \times N . \text{vcvtop_half_} t_1 \times M _sx? \hookrightarrow (\text{v128.const } c) \\ (\text{if } c = \text{lanes}_{t_2 \times N}^{-1}(\text{vcvtop}_{|t_1|, |t_2|}^{sx?}(\text{lanes}_{t_1 \times M}(c_1)[\text{half}(0, N) : N])))$$

where:

$$\begin{aligned} \text{low}(x, y) &= x \\ \text{high}(x, y) &= y \end{aligned}$$

$t_2 \times N . \text{vcvtop_} t_1 \times M _sx? _zero$

1. Assert: due to `syntax`, $N = 2 \cdot M$.
2. Assert: due to `validation`, a value of `value type v128` is on the top of the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let i^* be the result of computing $\text{lanes}_{t_1 \times M}(c_1)$.
5. Let j^* be the result of computing $\text{vcvtop}_{|t_1|, |t_2|}^{sx?}(i^*)$.
6. Let k^* be the concatenation of the two sequences j^* and 0^M .
7. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(k^*)$.
8. Push the value `v128.const` c onto the stack.

$$(\text{v128.const } c_1) \ t_2 \times N . \text{vcvtop_} t_1 \times M _sx? _zero \hookrightarrow (\text{v128.const } c) \\ (\text{if } c = \text{lanes}_{t_2 \times N}^{-1}(\text{vcvtop}_{|t_1|, |t_2|}^{sx?}(\text{lanes}_{t_1 \times M}(c_1)) \ 0^M))$$

`i32x4.dot_i16x8_s`

1. Assert: due to `validation`, two values of `value type v128` are on the top of the stack.
2. Pop the value `v128.const` c_2 from the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let i_1^* be the result of computing $\text{lanes}_{i16 \times 8}(c_1)$.
5. Let j_1^* be the result of computing $\text{extend}_{16, 32}^s(i_1^*)$.
6. Let i_2^* be the result of computing $\text{lanes}_{i16 \times 8}(c_2)$.
7. Let j_2^* be the result of computing $\text{extend}_{16, 32}^s(i_2^*)$.
8. Let $(k_1 \ k_2)^*$ be the result of computing $\text{imul}_{32}(j_1^*, j_2^*)$.
9. Let k^* be the result of computing $\text{iadd}_{32}(k_1, k_2)^*$.
10. Let c be the result of computing $\text{lanes}_{i32 \times 4}^{-1}(k^*)$.
11. Push the value `v128.const` c onto the stack.

$$(\text{v128.const } c_1) \ (\text{v128.const } c_2) \ \text{i32x4.dot_i16x8_s} \hookrightarrow (\text{v128.const } c) \\ (\text{if } (i_1 \ i_2)^* = \text{imul}_{32}(\text{extend}_{16, 32}^s(\text{lanes}_{i16 \times 8}(c_1)), \text{extend}_{16, 32}^s(\text{lanes}_{i16 \times 8}(c_2))) \\ \wedge j^* = \text{iadd}_{32}(i_1, i_2)^* \\ \wedge c = \text{lanes}_{i32 \times 4}^{-1}(j^*))$$

`i16x8.relaxed_dot_i8x16_i7x16_s`

1. Assert: due to **validation**, two values of **value type v128** are on the top of the stack.
2. Pop the value `v128.const` c_2 from the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let $(i_1\ i_2)^8$ be the result of computing `irelaxed_dot_mul8,16(lanesi8x16(c_1), lanesi8x16(c_2))`
5. Let j^8 be the result of computing `iadd_sat_s16(i_1, i_2)8`.
6. Let c be the result of computing `lanesi16x8-1(j^8)`.
7. Push the value `v128.const` c onto the stack.

$$\begin{aligned}
 &(\text{v128.const } c_1) (\text{v128.const } c_2) \text{ i16x8.relaxed_dot_i8x16_i7x16_s} \hookrightarrow (\text{v128.const } c) \\
 &(\text{if } (i_1\ i_2)^8 = \text{irelaxed_dot_mul}_{8,16}(\text{lanes}_{i8x16}(c_1), \text{lanes}_{i8x16}(c_2)) \\
 &\quad \wedge j^8 = \text{iadd_sat_s16}(i_1, i_2)^8 \\
 &\quad \wedge c = \text{lanes}_{i16x8}^{-1}(j^8))
 \end{aligned}$$

`i32x4.relaxed_dot_i8x16_i7x16_add_s`

1. Assert: due to **validation**, three values of **value type v128** are on the top of the stack.
2. Pop the value `v128.const` c_3 from the stack.
3. Pop the value `v128.const` c_2 from the stack.
4. Pop the value `v128.const` c_1 from the stack.
5. Let $(i_1\ i_2)^8$ be the result of computing `irelaxed_dot_mul8,16(lanesi8x16(c_1), lanesi8x16(c_2))`
6. Let $(j_1\ j_2)^4$ be the result of computing `iadd_sat_s16(i_1, i_2)8`.
7. Let j^4 be the result of computing `iadd32(extends16,32(j_1), extends16,32(j_2))4`.
8. Let k^4 be the result of computing `lanesi32x4(c_3)`.
9. Let l^4 be the result of computing `iadd32(j, k)4`.
10. Let c be the result of computing `lanesi32x4-1(l^4)`.
11. Push the value `v128.const` c onto the stack.

$$\begin{aligned}
 &(\text{v128.const } c_1) (\text{v128.const } c_2) (\text{v128.const } c_3) \text{ i32x4.relaxed_dot_i8x16_i7x16_add_s} \hookrightarrow (\text{v128.const } c) \\
 &(\text{if } (i_1\ i_2)^8 = \text{irelaxed_dot_mul}_{8,16}(\text{lanes}_{i8x16}(c_1), \text{lanes}_{i8x16}(c_2)) \\
 &\quad \wedge (j_1\ j_2)^4 = \text{iadd_sat_s16}(i_1, i_2)^8 \\
 &\quad \wedge j^4 = \text{iadd}_{32}(\text{extend}^s_{16,32}(j_1), \text{extend}^s_{16,32}(j_2))^4 \\
 &\quad \wedge k^4 = \text{lanes}_{i32x4}(c_3) \\
 &\quad \wedge l^4 = \text{iadd}_{32}(j, k)^4 \\
 &\quad \wedge c = \text{lanes}_{i32x4}^{-1}(l^4))
 \end{aligned}$$

`t2xN.extmul_half_t1xM_sx`

1. Assert: due to **syntax**, $N = M/2$.
2. Assert: due to **validation**, two values of **value type v128** are on the top of the stack.
3. Pop the value `v128.const` c_2 from the stack.
4. Pop the value `v128.const` c_1 from the stack.
5. Let i_1^* be the result of computing `lanest1xM(c_1)`.
6. Let i_2^* be the result of computing `lanest1xM(c_2)`.
7. If *half* is low, then:

- a. Let j_1^* be the sequence $i_1^*[0 : N]$.
- b. Let j_2^* be the sequence $i_2^*[0 : N]$.
8. Else:
 - a. Let j_1^* be the sequence $i_1^*[N : N]$.
 - b. Let j_2^* be the sequence $i_2^*[N : N]$.
9. Let k_1^* be the result of computing $\text{extend}_{|t_1|, |t_2|}^{sx}(j_1^*)$.
10. Let k_2^* be the result of computing $\text{extend}_{|t_1|, |t_2|}^{sx}(j_2^*)$.
11. Let k^* be the result of computing $\text{imul}_{|t_2|}(k_1^*, k_2^*)$.
12. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(k^*)$.
13. Push the value `v128.const` c onto the stack.

$$(\text{v128.const } c_1) (\text{v128.const } c_2) t_2 \times N.\text{extmul_half_}t_1 \times M_sx \hookrightarrow (\text{v128.const } c)$$

$$(\text{if } i^* = \text{lanes}_{t_1 \times M}(c_1)[\text{half}(0, N) : N] \\ \wedge j^* = \text{lanes}_{t_1 \times M}(c_2)[\text{half}(0, N) : N] \\ \wedge c = \text{lanes}_{t_2 \times N}^{-1}(\text{imul}_{|t_2|}(\text{extend}_{|t_1|, |t_2|}^{sx}(i^*), \text{extend}_{|t_1|, |t_2|}^{sx}(j^*))))$$

where:

$$\begin{aligned} \text{low}(x, y) &= x \\ \text{high}(x, y) &= y \end{aligned}$$

$t_2 \times N.\text{extadd_pairwise_}t_1 \times M_sx$

1. Assert: due to `syntax`, $N = M/2$.
2. Assert: due to `validation`, a value of `value type v128` is on the top of the stack.
3. Pop the value `v128.const` c_1 from the stack.
4. Let i^* be the result of computing $\text{lanes}_{t_1 \times M}(c_1)$.
5. Let $(j_1 j_2)^*$ be the result of computing $\text{extend}_{|t_1|, |t_2|}^{sx}(i^*)$.
6. Let k^* be the result of computing $\text{iadd}_{|t_2|}(j_1, j_2)^*$.
7. Let c be the result of computing $\text{lanes}_{t_2 \times N}^{-1}(k^*)$.
8. Push the value `v128.const` c to the stack.

$$(\text{v128.const } c_1) t_2 \times N.\text{extadd_pairwise_}t_1 \times M_sx \hookrightarrow (\text{v128.const } c)$$

$$(\text{if } (i_1 i_2)^* = \text{extend}_{|t_1|, |t_2|}^{sx}(\text{lanes}_{t_1 \times M}(c_1)) \\ \wedge j^* = \text{iadd}_{|t_2|}(i_1, i_2)^* \\ \wedge c = \text{lanes}_{t_2 \times N}^{-1}(j^*))$$

4.6.4 Parametric Instructions

`drop`

1. Assert: due to `validation`, a value is on the top of the stack.
2. Pop the value `val` from the stack.

$$\text{val drop} \hookrightarrow \epsilon$$

`select (t^*)?`

1. Assert: due to **validation**, a value of **value type** `i32` is on the top of the stack.
2. Pop the value `i32.const c` from the stack.
3. Assert: due to **validation**, two more values (of the same **value type**) are on the top of the stack.
4. Pop the value `val_2` from the stack.
5. Pop the value `val_1` from the stack.
6. If c is not 0, then:
 - a. Push the value `val_1` back to the stack.
7. Else:
 - a. Push the value `val_2` back to the stack.

$$\begin{array}{ll} \text{val}_1 \text{ val}_2 \text{ (i32.const } c \text{) (select } t^? \text{)} & \hookrightarrow \text{val}_1 \quad (\text{if } c \neq 0) \\ \text{val}_1 \text{ val}_2 \text{ (i32.const } c \text{) (select } t^? \text{)} & \hookrightarrow \text{val}_2 \quad (\text{if } c = 0) \end{array}$$

Note: In future versions of WebAssembly, `select` may allow more than one value per choice.

4.6.5 Variable Instructions

`local.get x`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{locals}[x]$ exists and is non-empty.
3. Let `val` be the value $F.\text{locals}[x]$.
4. Push the value `val` to the stack.

$$F; (\text{local.get } x) \hookrightarrow F; \text{val} \quad (\text{if } F.\text{locals}[x] = \text{val})$$

`local.set x`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{locals}[x]$ exists.
3. Assert: due to **validation**, a value is on the top of the stack.
4. Pop the value `val` from the stack.
5. Replace $F.\text{locals}[x]$ with the value `val` .

$$F; \text{val} (\text{local.set } x) \hookrightarrow F'; \epsilon \quad (\text{if } F' = F \text{ with locals}[x] = \text{val})$$

`local.tee x`

1. Assert: due to [validation](#), a value is on the top of the stack.
2. Pop the value *val* from the stack.
3. Push the value *val* to the stack.
4. Push the value *val* to the stack.
5. [Execute](#) the instruction `local.set x`.

$$val\ (local.tee\ x) \hookrightarrow val\ val\ (local.set\ x)$$

`global.get x`

1. Let *F* be the [current frame](#).
2. Assert: due to [validation](#), *F.module.globaladdrs*[*x*] exists.
3. Let *a* be the [global address](#) *F.module.globaladdrs*[*x*].
4. Assert: due to [validation](#), *S.globals*[*a*] exists.
5. Let *glob* be the [global instance](#) *S.globals*[*a*].
6. Let *val* be the value *glob.value*.
7. Push the value *val* to the stack.

$$S; F; (global.get\ x) \hookrightarrow S; F; val \\ (if\ S.globals[F.module.globaladdrs[x]].value = val)$$

`global.set x`

1. Let *F* be the [current frame](#).
2. Assert: due to [validation](#), *F.module.globaladdrs*[*x*] exists.
3. Let *a* be the [global address](#) *F.module.globaladdrs*[*x*].
4. Assert: due to [validation](#), *S.globals*[*a*] exists.
5. Let *glob* be the [global instance](#) *S.globals*[*a*].
6. Assert: due to [validation](#), a value is on the top of the stack.
7. Pop the value *val* from the stack.
8. Replace *glob.value* with the value *val*.

$$S; F; val\ (global.set\ x) \hookrightarrow S'; F; \epsilon \\ (if\ S' = S\ with\ globals[F.module.globaladdrs[x]].value = val)$$

Note: [Validation](#) ensures that the global is, in fact, marked as mutable.

4.6.6 Table Instructions

`table.get x`

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module.tableaddrs}[x]$ exists.
3. Let a be the table address $F.\text{module.tableaddrs}[x]$.
4. Assert: due to validation, $S.\text{tables}[a]$ exists.
5. Let tab be the table instance $S.\text{tables}[a]$.
6. Assert: due to validation, a value of some address type at is on the top of the stack.
7. Pop the value $at.\text{const } i$ from the stack.
8. If i is not smaller than the length of $tab.\text{elem}$, then:
 - a. Trap.
9. Let val be the value $tab.\text{elem}[i]$.
10. Push the value val to the stack.

$$\begin{aligned}
 S; F; (at.\text{const } i) \text{ (table.get } x) &\hookrightarrow S; F; val \\
 &\quad (\text{if } S.\text{tables}[F.\text{module.tableaddrs}[x]].\text{elem}[i] = val) \\
 S; F; (at.\text{const } i) \text{ (table.get } x) &\hookrightarrow S; F; \text{trap} \\
 &\quad (\text{otherwise})
 \end{aligned}$$

`table.set x`

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module.tableaddrs}[x]$ exists.
3. Let a be the table address $F.\text{module.tableaddrs}[x]$.
4. Assert: due to validation, $S.\text{tables}[a]$ exists.
5. Let tab be the table instance $S.\text{tables}[a]$.
6. Assert: due to validation, a reference value is on the top of the stack.
7. Pop the value val from the stack.
8. Assert: due to validation, a value of some address type at is on the top of the stack.
9. Pop the value $at.\text{const } i$ from the stack.
10. If i is not smaller than the length of $tab.\text{elem}$, then:
 - a. Trap.
11. Replace the element $tab.\text{elem}[i]$ with val .

$$\begin{aligned}
 S; F; (at.\text{const } i) \text{ val (table.set } x) &\hookrightarrow S'; F; \epsilon \\
 &\quad (\text{if } S' = S \text{ with tables}[F.\text{module.tableaddrs}[x]].\text{elem}[i] = val) \\
 S; F; (at.\text{const } i) \text{ val (table.set } x) &\hookrightarrow S; F; \text{trap} \\
 &\quad (\text{otherwise})
 \end{aligned}$$

`table.size x`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.tableaddrs}[x]$ exists.
3. Let a be the **table address** $F.\text{module.tableaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{tables}[a]$ exists.
5. Let tab be the **table instance** $S.\text{tables}[a]$.
6. Let at *limits* be the **table type** $tab.type$.
7. Let sz be the length of $tab.elem$.
8. Push the value $at.\text{const } sz$ to the stack.

$$S; F; (\text{table.size } x) \hookrightarrow S; F; (at.\text{const } sz) \\ \text{(if } |S.\text{tables}[F.\text{module.tableaddrs}[x]].elem| = sz \\ \wedge S.\text{tables}[F.\text{module.tableaddrs}[x]].type = at \text{ lim } t)$$

`table.grow x`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.tableaddrs}[x]$ exists.
3. Let a be the **table address** $F.\text{module.tableaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{tables}[a]$ exists.
5. Let tab be the **table instance** $S.\text{tables}[a]$.
6. Let sz be the length of $S.\text{tables}[a]$.
7. Assert: due to **validation**, a value of some **address type** at is on the top of the stack.
8. Pop the value $t.\text{const } n$ from the stack.
9. Assert: due to **validation**, a **reference value** is on the top of the stack.
10. Pop the value val from the stack.
11. Let err be the value $2^{|at|} - 1$, for which $\text{signed}_{|at|}(err)$ is -1 .
12. Either:
 - a. If **growing** tab by n entries with initialization value val succeeds, then:
 - i. Push the value $at.\text{const } sz$ to the stack.
 - b. Else:
 - i. Push the value $at.\text{const } err$ to the stack.
13. Or:
 - a. push the value $at.\text{const } err$ to the stack.

$$S; F; val (at.\text{const } n) (\text{table.grow } x) \hookrightarrow S'; F; (at.\text{const } sz) \\ \text{(if } F.\text{module.tableaddrs}[x] = a \\ \wedge sz = |S.\text{tables}[a].elem| \\ \wedge S' = S \text{ with } \text{tables}[a] = \text{growtable}(S.\text{tables}[a], n, val)) \\ S; F; val (at.\text{const } n) (\text{table.grow } x) \hookrightarrow S; F; (at.\text{const } \text{signed}_{|at|}^{-1}(-1))$$

Note: The `table.grow` instruction is non-deterministic. It may either succeed, returning the old table size sz , or fail, returning -1 . Failure *must* occur if the referenced table instance has a maximum size defined that would

be exceeded. However, failure *can* occur in other cases as well. In practice, the choice depends on the [resources](#) available to the [embedder](#).

`table.fill x`

1. Let F be the current frame.
2. Assert: due to [validation](#), $F.\text{module.tableaddrs}[x]$ exists.
3. Let ta be the table address $F.\text{module.tableaddrs}[x]$.
4. Assert: due to [validation](#), $S.\text{tables}[ta]$ exists.
5. Let tab be the table instance $S.\text{tables}[ta]$.
6. Assert: due to [validation](#), a value of some [address type](#) at is on the top of the stack.
7. Pop the value $at.\text{const } n$ from the stack.
8. Assert: due to [validation](#), a [reference value](#) is on the top of the stack.
9. Pop the value val from the stack.
10. Assert: due to [validation](#), a value of [address type](#) at is on the top of the stack.
11. Pop the value $at.\text{const } i$ from the stack.
12. If $i + n$ is larger than the length of $tab.\text{elem}$, then:
 - a. Trap.
13. If n is 0, then:
 - a. Return.
14. Push the value $at.\text{const } i$ to the stack.
15. Push the value val to the stack.
16. Execute the instruction `table.set x`.
17. Push the value $at.\text{const } (i + 1)$ to the stack.
18. Push the value val to the stack.
19. Push the value $at.\text{const } (n - 1)$ to the stack.
20. Execute the instruction `table.fill x`.

$$S; F; (at.\text{const } i) \text{ val } (at.\text{const } n) (\text{table.fill } x) \hookrightarrow S; F; \text{trap} \quad (\text{if } i + n > |S.\text{tables}[F.\text{module.tableaddrs}[x]].\text{elem}|)$$

$$S; F; (at.\text{const } i) \text{ val } (at.\text{const } 0) (\text{table.fill } x) \hookrightarrow S; F; \epsilon \quad (\text{otherwise})$$

$$S; F; (at.\text{const } i) \text{ val } (at.\text{const } n + 1) (\text{table.fill } x) \hookrightarrow S; F; (at.\text{const } i) \text{ val } (\text{table.set } x) \quad (at.\text{const } i + 1) \text{ val } (at.\text{const } n) (\text{table.fill } x) \quad (\text{otherwise})$$

`table.copy x y`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.tableaddrs}[x]$ exists.
3. Let ta_x be the **table address** $F.\text{module.tableaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{tables}[ta_x]$ exists.
5. Let tab_x be the **table instance** $S.\text{tables}[ta_x]$.
6. Assert: due to **validation**, $F.\text{module.tableaddrs}[y]$ exists.
7. Let ta_y be the **table address** $F.\text{module.tableaddrs}[y]$.
8. Assert: due to **validation**, $S.\text{tables}[ta_y]$ exists.
9. Let tab_y be the **table instance** $S.\text{tables}[ta_y]$.
10. Assert: due to **validation**, a value of some **address type** at_n is on the top of the stack.
11. Pop the value $at_n.\text{const } n$ from the stack.
12. Assert: due to **validation**, a value of some **address type** at_s is on the top of the stack.
13. Pop the value $at_s.\text{const } s$ from the stack.
14. Assert: due to **validation**, a value of some **address type** at_d is on the top of the stack.
15. Pop the value $at_d.\text{const } d$ from the stack.
16. If $s + n$ is larger than the length of $tab_y.\text{elem}$ or $d + n$ is larger than the length of $tab_x.\text{elem}$, then:
 - a. Trap.
20. If $n = 0$, then:
 - a. Return.
21. If $d \leq s$, then:
 - a. Push the value $at_d.\text{const } d$ to the stack.
 - b. Push the value $at_s.\text{const } s$ to the stack.
 - c. Execute the instruction `table.get y`.
 - d. Execute the instruction `table.set x`.
 - e. Assert: due to the earlier check against the table size, $d + 1 < 2^{32}$.
 - f. Push the value $at_d.\text{const } (d + 1)$ to the stack.
 - g. Assert: due to the earlier check against the table size, $s + 1 < 2^{32}$.
 - h. Push the value $at_s.\text{const } (s + 1)$ to the stack.
22. Else:
 - a. Assert: due to the earlier check against the table size, $d + n - 1 < 2^{32}$.
 - b. Push the value $at_d.\text{const } (d + n - 1)$ to the stack.
 - c. Assert: due to the earlier check against the table size, $s + n - 1 < 2^{32}$.
 - d. Push the value $at_s.\text{const } (s + n - 1)$ to the stack.
 - e. Execute the instruction `table.get y`.
 - f. Execute the instruction `table.set x`.
 - g. Push the value $at_d.\text{const } d$ to the stack.
 - h. Push the value $at_s.\text{const } s$ to the stack.
23. Push the value $at_n.\text{const } (n - 1)$ to the stack.

24. Execute the instruction `table.copy x y`.

$$\begin{aligned}
& S; F; (at_d.\text{const } d) (at_s.\text{const } s) (at_n.\text{const } n) (\text{table.copy } x y) \hookrightarrow S; F; \text{trap} \\
& \quad (\text{if } s + n > |S.\text{tables}[F.\text{module}.\text{tableaddrs}[y]].\text{elem}| \\
& \quad \vee d + n > |S.\text{tables}[F.\text{module}.\text{tableaddrs}[x]].\text{elem}|) \\
& S; F; (at_d.\text{const } d) (at_s.\text{const } s) (at_n.\text{const } 0) (\text{table.copy } x y) \hookrightarrow S; F; \epsilon \\
& \quad (\text{otherwise}) \\
& S; F; (at_d.\text{const } d) (at_s.\text{const } s) (at_n.\text{const } n + 1) (\text{table.copy } x y) \hookrightarrow \\
& \quad S; F; (at_d.\text{const } d) (at_s.\text{const } s) (\text{table.get } y) (\text{table.set } x) \\
& \quad (at_d.\text{const } d + 1) (at_s.\text{const } s + 1) (at_n.\text{const } n) (\text{table.copy } x y) \\
& \quad (\text{otherwise, if } d \leq s) \\
& S; F; (at_d.\text{const } d) (at_s.\text{const } s) (at_n.\text{const } n + 1) (\text{table.copy } x y) \hookrightarrow \\
& \quad S; F; (at_d.\text{const } d + n) (at_s.\text{const } s + n) (\text{table.get } y) (\text{table.set } x) \\
& \quad (at_d.\text{const } d) (at_s.\text{const } s) (at_n.\text{const } n) (\text{table.copy } x y) \\
& \quad (\text{otherwise, if } d > s)
\end{aligned}$$

`table.init x y`

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module}.\text{tableaddrs}[x]$ exists.
3. Let ta be the table address $F.\text{module}.\text{tableaddrs}[x]$.
4. Assert: due to validation, $S.\text{tables}[ta]$ exists.
5. Let tab be the table instance $S.\text{tables}[ta]$.
6. Assert: due to validation, $F.\text{module}.\text{elemaddrs}[y]$ exists.
7. Let ea be the element address $F.\text{module}.\text{elemaddrs}[y]$.
8. Assert: due to validation, $S.\text{elems}[ea]$ exists.
9. Let $elem$ be the element instance $S.\text{elems}[ea]$.
10. Assert: due to validation, a value of value type `i32` is on the top of the stack.
11. Pop the value `i32.const n` from the stack.
12. Assert: due to validation, a value of value type `i32` is on the top of the stack.
13. Pop the value `i32.const s` from the stack.
14. Assert: due to validation, a value of some address type at is on the top of the stack.
15. Pop the value $at.\text{const } d$ from the stack.
16. If $s + n$ is larger than the length of $elem.\text{elem}$ or $d + n$ is larger than the length of $tab.\text{elem}$, then:
 - a. Trap.
17. If $n = 0$, then:
 - a. Return.
18. Let val be the reference value $elem.\text{elem}[s]$.
19. Push the value $at.\text{const } d$ to the stack.
20. Push the value val to the stack.
21. Execute the instruction `table.set x`.
22. Assert: due to the earlier check against the table size, $d + 1 < 2^{32}$.
23. Push the value $at.\text{const } (d + 1)$ to the stack.

24. Assert: due to the earlier check against the segment size, $s + 1 < 2^{32}$.
25. Push the value `i32.const` $(s + 1)$ to the stack.
26. Push the value `i32.const` $(n - 1)$ to the stack.
27. Execute the instruction `table.init` x y .

$$\begin{aligned}
& S; F; (at.const\ d) (i32.const\ s) (i32.const\ n) (table.init\ x\ y) \quad \hookrightarrow \quad S; F; trap \\
& \quad (\text{if } s + n > |S.elems[F.module.elemaddrs[y]].elem| \\
& \quad \vee d + n > |S.tables[F.module.tableaddrs[x]].elem|) \\
& S; F; (at.const\ d) (i32.const\ s) (i32.const\ 0) (table.init\ x\ y) \quad \hookrightarrow \quad S; F; \epsilon \\
& \quad (\text{otherwise}) \\
& S; F; (at.const\ d) (i32.const\ s) (i32.const\ n + 1) (table.init\ x\ y) \quad \hookrightarrow \\
& \quad S; F; (at.const\ d)\ val\ (table.set\ x) \\
& \quad (at.const\ d + 1) (i32.const\ s + 1) (i32.const\ n) (table.init\ x\ y) \\
& \quad (\text{otherwise, if } val = S.elems[F.module.elemaddrs[y]].elem[s])
\end{aligned}$$

`elem.drop` x

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.module.elemaddrs[x]$ exists.
3. Let a be the **element address** $F.module.elemaddrs[x]$.
4. Assert: due to **validation**, $S.elems[a]$ exists.
5. Replace $S.elems[a].elem$ with ϵ .

$$\begin{aligned}
& S; F; (elem.drop\ x) \quad \hookrightarrow \quad S'; F; \epsilon \\
& \quad (\text{if } S' = S \text{ with } elems[F.module.elemaddrs[x]].elem = \epsilon)
\end{aligned}$$

4.6.7 Memory Instructions

Note: The alignment `memarg.align` in load and store instructions does not affect the semantics. It is an indication that the offset ea at which the memory is accessed is intended to satisfy the property $ea \bmod 2^{\text{memarg.align}} = 0$. A WebAssembly implementation can use this hint to optimize for the intended use. Unaligned access violating that property is still allowed and must succeed regardless of the annotation. However, it may be substantially slower on some hardware.

`t.load` x `memarg` **and** `t.loadNsx` x `memarg`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.module.memaddrs[x]$ exists.
3. Let a be the **memory address** $F.module.memaddrs[x]$.
4. Assert: due to **validation**, $S.mems[a]$ exists.
5. Let mem be the **memory instance** $S.mems[a]$.
6. Assert: due to **validation**, a value of some **address type** at is on the top of the stack.
7. Pop the value `at.const` i from the stack.
8. Let ea be the integer $i + memarg.offset$.

9. If N is not part of the instruction, then:
 - a. Let N be the bit width $|t|$ of number type t .
10. If $ea + N/8$ is larger than the length of $mem.data$, then:
 - a. Trap.
11. Let b^* be the byte sequence $mem.data[ea : N/8]$.
12. If N and sx are part of the instruction, then:
 - a. Let n be the integer for which $bytes_{iN}(n) = b^*$.
 - b. Let c be the result of computing $extend_{N,|t|}^{sx}(n)$.
13. Else:
 - a. Let c be the constant for which $bytes_t(c) = b^*$.
14. Push the value $t.const\ c$ to the stack.

$$\begin{aligned}
 S; F; (at.const\ i)\ (t.load\ x\ memarg) &\hookrightarrow S; F; (t.const\ c) \\
 &\quad (\text{if } ea = i + memarg.offset \\
 &\quad \wedge ea + |t|/8 \leq |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \wedge bytes_t(c) = S.mems[F.module.memaddrs[x]].data[ea : |t|/8]) \\
 S; F; (at.const\ i)\ (t.loadN_{sx}\ x\ memarg) &\hookrightarrow S; F; (t.const\ extend_{N,|t|}^{sx}(n)) \\
 &\quad (\text{if } ea = i + memarg.offset \\
 &\quad \wedge ea + N/8 \leq |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \wedge bytes_{iN}(n) = S.mems[F.module.memaddrs[x]].data[ea : N/8]) \\
 S; F; (at.const\ i)\ (t.load(N_{sx})^2\ x\ memarg) &\hookrightarrow S; F; trap \\
 &\quad (\text{otherwise})
 \end{aligned}$$

v128.loadMxN_{sx} x memarg

1. Let F be the current frame.
2. Assert: due to validation, $F.module.memaddrs[x]$ exists.
3. Let a be the memory address $F.module.memaddrs[x]$.
4. Assert: due to validation, $S.mems[a]$ exists.
5. Let mem be the memory instance $S.mems[a]$.
6. Assert: due to validation, a value of some address type at is on the top of the stack.
7. Pop the value $at.const\ i$ from the stack.
8. Let ea be the integer $i + memarg.offset$.
9. If $ea + M \cdot N/8$ is larger than the length of $mem.data$, then:
 - a. Trap.
10. Let b^* be the byte sequence $mem.data[ea : M \cdot N/8]$.
11. Let m_k be the integer for which $bytes_{iM}(m_k) = b^*[k \cdot M/8 : M/8]$.
12. Let W be the integer $M \cdot 2$.
13. Let n_k be the result of computing $extend_{M,W}^{sx}(m_k)$.
14. Let c be the result of computing $lanes_{iW \times N}^{-1}(n_0 \dots n_{N-1})$.
15. Push the value **v128.const** c to the stack.

$$\begin{aligned}
& S; F; (at.\text{const } i) (\text{v128.loadMxN_sx } x \text{ memarg}) \hookrightarrow S; F; (\text{v128.const } c) \\
& \quad (\text{if } ea = i + \text{memarg.offset} \\
& \quad \wedge ea + M \cdot N/8 \leq |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| \\
& \quad \wedge \text{bytes}_{iM}(m_k) = S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}[ea + k \cdot M/8 : M/8]) \\
& \quad \wedge W = M \cdot 2 \\
& \quad \wedge c = \text{lanes}_{iW \times N}^{-1}(\text{extend}_{M,W}^{sx}(m_0) \dots \text{extend}_{M,W}^{sx}(m_{N-1}))) \\
& S; F; (at.\text{const } i) (\text{v128.loadMxN_sx } x \text{ memarg}) \hookrightarrow S; F; \text{trap} \\
& \quad (\text{otherwise})
\end{aligned}$$

v128.loadN_splat *x memarg*

1. Let *F* be the **current frame**.
2. Assert: due to **validation**, *F.module.memaddrs*[*x*] exists.
3. Let *a* be the **memory address** *F.module.memaddrs*[*x*].
4. Assert: due to **validation**, *S.mems*[*a*] exists.
5. Let *mem* be the **memory instance** *S.mems*[*a*].
6. Assert: due to **validation**, a value of some **address type** *at* is on the top of the stack.
7. Pop the value *at.const i* from the stack.
8. Let *ea* be the integer *i* + *memarg.offset*.
9. If *ea* + *N*/8 is larger than the length of *mem.data*, then:
 - a. Trap.
10. Let *b** be the byte sequence *mem.data*[*ea* : *N*/8].
11. Let *n* be the integer for which *bytes*_{*iN*}(*n*) = *b**.
12. Let *L* be the integer 128/*N*.
13. Let *c* be the result of computing *lanes*_{*iN* × *L*}⁻¹(*n*^{*L*}).
14. Push the value **v128.const** *c* to the stack.

$$\begin{aligned}
& S; F; (at.\text{const } i) (\text{v128.loadN_splat } x \text{ memarg}) \hookrightarrow S; F; (\text{v128.const } c) \\
& \quad (\text{if } ea = i + \text{memarg.offset} \\
& \quad \wedge ea + N/8 \leq |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| \\
& \quad \wedge \text{bytes}_{iN}(n) = S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}[ea : N/8] \\
& \quad \wedge c = \text{lanes}_{iN \times L}^{-1}(n^L)) \\
& S; F; (at.\text{const } i) (\text{v128.loadN_splat } x \text{ memarg}) \hookrightarrow S; F; \text{trap} \\
& \quad (\text{otherwise})
\end{aligned}$$

v128.loadN_zero *x memarg*

1. Let *F* be the **current frame**.
2. Assert: due to **validation**, *F.module.memaddrs*[*x*] exists.
3. Let *a* be the **memory address** *F.module.memaddrs*[*x*].
4. Assert: due to **validation**, *S.mems*[*a*] exists.
5. Let *mem* be the **memory instance** *S.mems*[*a*].
6. Assert: due to **validation**, a value of some **address type** *at* is on the top of the stack.
7. Pop the value *at.const i* from the stack.
8. Let *ea* be the integer *i* + *memarg.offset*.

9. If $ea + N/8$ is larger than the length of $mem.data$, then:
 - a. Trap.
10. Let b^* be the byte sequence $mem.data[ea : N/8]$.
11. Let n be the integer for which $bytes_{iN}(n) = b^*$.
12. Let c be the result of computing $extend^u_{N,128}(n)$.
13. Push the value `v128.const` c to the stack.

$$\begin{aligned}
 &S; F; (at.const\ i) (v128.loadN_zero\ x\ memarg) \hookrightarrow S; F; (v128.const\ c) \\
 &\quad (\text{if } ea = i + memarg.offset \\
 &\quad \wedge ea + N/8 \leq |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \wedge bytes_{iN}(n) = S.mems[F.module.memaddrs[x]].data[ea : N/8] \\
 &\quad \wedge c = extend^u_{N,128}(n) \\
 &S; F; (at.const\ i) (v128.loadN_zero\ x\ memarg) \hookrightarrow S; F; trap \\
 &\quad (\text{otherwise})
 \end{aligned}$$

`v128.loadN_lane` $x\ memarg\ y$

1. Let F be the `current frame`.
2. Assert: due to `validation`, $F.module.memaddrs[x]$ exists.
3. Let a be the `memory address` $F.module.memaddrs[x]$.
4. Assert: due to `validation`, $S.mems[a]$ exists.
5. Let mem be the `memory instance` $S.mems[a]$.
6. Assert: due to `validation`, a value of `value type` `v128` is on the top of the stack.
7. Pop the value `v128.const` v from the stack.
8. Assert: due to `validation`, a value of some `address type` at is on the top of the stack.
9. Pop the value `at.const` i from the stack.
10. Let ea be the integer $i + memarg.offset$.
11. If $ea + N/8$ is larger than the length of $mem.data$, then:
 - a. Trap.
12. Let b^* be the byte sequence $mem.data[ea : N/8]$.
13. Let r be the constant for which $bytes_{iN}(r) = b^*$.
14. Let L be $128/N$.
15. Let j^* be the result of computing $lanes_{iN \times L}(v)$.
16. Let c be the result of computing $lanes_{iN \times L}^{-1}(j^* \text{ with } [y] = r)$.
17. Push the value `v128.const` c to the stack.

$$\begin{aligned}
 &S; F; (at.const\ i) (v128.const\ v) (v128.loadN_lane\ x\ memarg\ y) \hookrightarrow S; F; (v128.const\ c) \\
 &\quad (\text{if } ea = i + memarg.offset \\
 &\quad \wedge ea + N/8 \leq |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \wedge bytes_{iN}(r) = S.mems[F.module.memaddrs[x]].data[ea : N/8] \\
 &\quad \wedge L = 128/N \\
 &\quad \wedge c = lanes_{iN \times L}^{-1}(lanes_{iN \times L}(v) \text{ with } [y] = r) \\
 &S; F; (at.const\ i) (v128.const\ v) (v128.loadN_lane\ x\ memarg\ y) \hookrightarrow S; F; trap \\
 &\quad (\text{otherwise})
 \end{aligned}$$

t.store x memarg **and** *t.storeN x memarg*

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module.memaddrs}[x]$ exists.
3. Let a be the memory address $F.\text{module.memaddrs}[x]$.
4. Assert: due to validation, $S.\text{mems}[a]$ exists.
5. Let mem be the memory instance $S.\text{mems}[a]$.
6. Assert: due to validation, a value of value type t is on the top of the stack.
7. Pop the value $t.\text{const } c$ from the stack.
8. Assert: due to validation, a value of some address type at is on the top of the stack.
9. Pop the value $at.\text{const } i$ from the stack.
10. Let ea be the integer $i + \text{memarg.offset}$.
11. If N is not part of the instruction, then:
 - a. Let N be the bit width $|t|$ of number type t .
12. If $ea + N/8$ is larger than the length of $mem.\text{data}$, then:
 - a. Trap.
13. If N is part of the instruction, then:
 - a. Let n be the result of computing $\text{wrap}_{|t|,N}(c)$.
 - b. Let b^* be the byte sequence $\text{bytes}_{iN}(n)$.
14. Else:
 - a. Let b^* be the byte sequence $\text{bytes}_t(c)$.
15. Replace the bytes $mem.\text{data}[ea : N/8]$ with b^* .

$$\begin{aligned}
 &S; F; (at.\text{const } i) (t.\text{const } c) (t.\text{store } x \text{ memarg}) \hookrightarrow S'; F; \epsilon \\
 &\quad (\text{if } ea = i + \text{memarg.offset} \\
 &\quad \wedge ea + |t|/8 \leq |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| \\
 &\quad \wedge S' = S \text{ with } \text{mems}[F.\text{module.memaddrs}[x]].\text{data}[ea : |t|/8] = \text{bytes}_t(c)) \\
 &S; F; (at.\text{const } i) (t.\text{const } c) (t.\text{storeN } x \text{ memarg}) \hookrightarrow S'; F; \epsilon \\
 &\quad (\text{if } ea = i + \text{memarg.offset} \\
 &\quad \wedge ea + N/8 \leq |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| \\
 &\quad \wedge S' = S \text{ with } \text{mems}[F.\text{module.memaddrs}[x]].\text{data}[ea : N/8] = \text{bytes}_{iN}(\text{wrap}_{|t|,N}(c)) \\
 &S; F; (at.\text{const } i) (t.\text{const } c) (t.\text{storeN}^? x \text{ memarg}) \hookrightarrow S; F; \text{trap} \\
 &\quad (\text{otherwise})
 \end{aligned}$$

v128.storeN_lane x memarg y

1. Let F be the current frame.
2. Assert: due to validation, $F.\text{module.memaddrs}[x]$ exists.
3. Let a be the memory address $F.\text{module.memaddrs}[x]$.
4. Assert: due to validation, $S.\text{mems}[a]$ exists.
5. Let mem be the memory instance $S.\text{mems}[a]$.
6. Assert: due to validation, a value of value type $v128$ is on the top of the stack.
7. Pop the value $v128.\text{const } c$ from the stack.

8. Assert: due to **validation**, a value of some **address type** at is on the top of the stack.
9. Pop the value $at.\text{const } i$ from the stack.
10. Let ea be the integer $i + \text{memarg.offset}$.
11. If $ea + N/8$ is larger than the length of mem.data , then:
 - a. Trap.
12. Let L be $128/N$.
13. Let j^* be the result of computing $\text{lanes}_{iN \times L}(c)$.
14. Let b^* be the result of computing $\text{bytes}_{iN}(j^*[y])$.
15. Replace the bytes $\text{mem.data}[ea : N/8]$ with b^* .

$$\begin{aligned}
 &S; F; (at.\text{const } i) (\text{v128.const } c) (\text{v128.store}_{N_lane \ x \ memarg \ y}) \hookrightarrow S'; F; \epsilon \\
 &\quad (\text{if } ea = i + \text{memarg.offset} \\
 &\quad \wedge ea + N \leq |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| \\
 &\quad \wedge L = 128/N \\
 &\quad \wedge S' = S \text{ with } \text{mems}[F.\text{module.memaddrs}[x]].\text{data}[ea : N/8] = \text{bytes}_{iN}(\text{lanes}_{iN \times L}(c)[y])) \\
 &S; F; (at.\text{const } i) (\text{v128.const } c) (\text{v128.store}_{N_lane \ x \ memarg \ y}) \hookrightarrow S; F; \text{trap} \\
 &\quad (\text{otherwise})
 \end{aligned}$$

memory.size x

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.memaddrs}[x]$ exists.
3. Let a be the **memory address** $F.\text{module.memaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{mems}[a]$ exists.
5. Let mem be the **memory instance** $S.\text{mems}[a]$.
6. Let $at \text{ limits}$ be the **memory type** mem.type .
7. Let sz be the length of mem.data divided by the **page size**.
8. Push the value $at.\text{const } sz$ to the stack.

$$\begin{aligned}
 &S; F; (\text{memory.size } x) \hookrightarrow S; F; (at.\text{const } sz) \\
 &\quad (\text{if } |S.\text{mems}[F.\text{module.memaddrs}[x]].\text{data}| = sz \cdot 64 \text{ Ki} \\
 &\quad \wedge S.\text{mems}[F.\text{module.memaddrs}[x]].\text{type} = at \text{ lim})
 \end{aligned}$$

memory.grow x

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.memaddrs}[x]$ exists.
3. Let a be the **memory address** $F.\text{module.memaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{mems}[a]$ exists.
5. Let mem be the **memory instance** $S.\text{mems}[a]$.
6. Let sz be the length of $S.\text{mems}[a]$ divided by the **page size**.
7. Assert: due to **validation**, a value of some **address type** at is on the top of the stack.
8. Pop the value $at.\text{const } n$ from the stack.
9. Let err be the at value $2^{|at|} - 1$, for which $\text{signed}_{|at|}(err)$ is -1 .

10. Either:
 - a. If `growing mem` by `n` pages succeeds, then:
 - i. Push the value `at.const sz` to the stack.
 - b. Else:
 - i. Push the value `at.const err` to the stack.
11. Or:
 - a. Push the value `at.const err` to the stack.

$$\begin{aligned}
 S; F; (at.const\ n) \text{ (memory.grow } x) &\hookrightarrow S'; F; (at.const\ sz) \\
 &\quad (\text{if } F.module.memaddrs[x] = a \\
 &\quad \wedge sz = |S.mems[a].data|/64\text{ Ki} \\
 &\quad \wedge S' = S \text{ with } mems[a] = growmem(S.mems[a], n)) \\
 S; F; (at.const\ n) \text{ (memory.grow } x) &\hookrightarrow S; F; (at.const\ signed_{|at|}^{-1}(-1))
 \end{aligned}$$

Note: The `memory.grow` instruction is non-deterministic. It may either succeed, returning the old memory size `sz`, or fail, returning `-1`. Failure *must* occur if the referenced memory instance has a maximum size defined that would be exceeded. However, failure *can* occur in other cases as well. In practice, the choice depends on the `resources` available to the `embedder`.

`memory.fill x`

1. Let `F` be the `current frame`.
2. Assert: due to `validation`, `F.module.memaddrs[x]` exists.
3. Let `ma` be the `memory address` `F.module.memaddrs[x]`.
4. Assert: due to `validation`, `S.mems[ma]` exists.
5. Let `mem` be the `memory instance` `S.mems[ma]`.
6. Assert: due to `validation`, a value of some `address type` `at` is on the top of the stack.
7. Pop the value `at.const n` from the stack.
8. Assert: due to `validation`, a value is on the top of the stack.
9. Pop the value `val` from the stack.
10. Assert: due to `validation`, a value of `address type` `at` is on the top of the stack.
11. Pop the value `at.const d` from the stack.
12. If `d + n` is larger than the length of `mem.data`, then:
 - a. Trap.
13. If `n = 0`, then:
 - a. Return.
14. Push the value `i32.const d` to the stack.
15. Push the value `val` to the stack.
16. Execute the instruction `at.store8 {offset 0, align 0}`.
17. Assert: due to the earlier check against the memory size, $d + 1 < 2^{32}$.
18. Push the value `at.const (d + 1)` to the stack.
19. Push the value `val` to the stack.

20. Push the value $at_const (n - 1)$ to the stack.
21. Execute the instruction `memory.fill` x .

$$\begin{aligned}
S; F; (at_const d) \text{ val } (at_const n) \text{ memory.fill } x &\hookrightarrow S; F; \text{trap} \\
&\quad (\text{if } d + n > |S.mems[F.module.memaddrs[x]].data|) \\
S; F; (at_const d) \text{ val } (at_const 0) \text{ memory.fill } x &\hookrightarrow S; F; \epsilon \\
&\quad (\text{otherwise}) \\
S; F; (at_const d) \text{ val } (at_const n + 1) \text{ memory.fill } x &\hookrightarrow \\
S; F; (at_const d) \text{ val } (at_store8 x \{offset 0, align 0\}) & \\
(at_const d + 1) \text{ val } (at_const n) \text{ memory.fill } x & \\
&\quad (\text{otherwise})
\end{aligned}$$

`memory.copy` $x y$

1. Let F be the `current frame`.
2. Assert: due to `validation`, $F.module.memaddrs[x]$ exists.
3. Assert: due to `validation`, $F.module.memaddrs[y]$ exists.
4. Let da be the `memory address` $F.module.memaddrs[x]$.
5. Let sa be the `memory address` $F.module.memaddrs[y]$.
6. Assert: due to `validation`, $S.mems[da]$ exists.
7. Assert: due to `validation`, $S.mems[sa]$ exists.
8. Let mem_d be the `memory instance` $S.mems[da]$.
9. Let mem_s be the `memory instance` $S.mems[sa]$.
10. Assert: due to `validation`, a value of some `address type` at_n is on the top of the stack.
11. Pop the value $at_n.const n$ from the stack.
12. Assert: due to `validation`, a value of some `address type` at_s is on the top of the stack.
13. Pop the value $at_s.const s$ from the stack.
14. Assert: due to `validation`, a value of some `address type` at_d is on the top of the stack.
15. Pop the value $at_d.const d$ from the stack.
16. If $s + n$ is larger than the length of $mem_s.data$ or $d + n$ is larger than the length of $mem_d.data$, then:
 - a. Trap.
17. If $n = 0$, then:
 - a. Return.
18. If $d \leq s$, then:
 - a. Push the value $at_d.const d$ to the stack.
 - b. Push the value $at_s.const s$ to the stack.
 - c. Execute the instruction `i32.load8_u` $y \{offset 0, align 0\}$.
 - d. Execute the instruction `i32.store8` $x \{offset 0, align 0\}$.
 - e. Assert: due to the earlier check against the memory size, $d + 1 < 2^{32}$.
 - f. Push the value $at_d.const (d + 1)$ to the stack.
 - g. Assert: due to the earlier check against the memory size, $s + 1 < 2^{32}$.
 - h. Push the value $at_s.const (s + 1)$ to the stack.

19. Else:
 - a. Assert: due to the earlier check against the memory size, $d + n - 1 < 2^{32}$.
 - b. Push the value $at_d.const (d + n - 1)$ to the stack.
 - c. Assert: due to the earlier check against the memory size, $s + n - 1 < 2^{32}$.
 - d. Push the value $at_s.const (s + n - 1)$ to the stack.
 - e. Execute the instruction `i32.load8_u y {offset 0, align 0}`.
 - f. Execute the instruction `i32.store8 x {offset 0, align 0}`.
 - g. Push the value $at_d.const d$ to the stack.
 - h. Push the value $at_s.const s$ to the stack.
20. Push the value $at_n.const (n - 1)$ to the stack.
21. Execute the instruction `memory.copy x y`.

$$\begin{aligned}
 &S; F; (at_x.const d) (at_y.const s) (at_n.const n) \text{ memory.copy } x y \quad \hookrightarrow \quad S; F; \text{trap} \\
 &\quad (\text{if } (d + n > |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \vee s + n > |S.mems[F.module.memaddrs[y]].data|)) \\
 &S; F; (at_x.const d) (at_y.const s) (at_n.const 0) \text{ memory.copy } x y \quad \hookrightarrow \quad S; F; \epsilon \\
 &\quad (\text{otherwise}) \\
 &S; F; (at_x.const d) (at_y.const s) (at_n.const n + 1) \text{ memory.copy } x y \quad \hookrightarrow \\
 &\quad S; F; (at_x.const d) \\
 &\quad \quad (at_y.const s) (i32.load8_u y \{offset 0, align 0\}) \\
 &\quad \quad (i32.store8 x \{offset 0, align 0\}) \\
 &\quad \quad (at_x.const d + 1) (at_y.const s + 1) (at_n.const n) \text{ memory.copy } x y \\
 &\quad (\text{otherwise, if } d \leq s) \\
 &S; F; (at_x.const d) (at_y.const s) (at_n.const n + 1) \text{ memory.copy } x y \quad \hookrightarrow \\
 &\quad S; F; (at_x.const d + n) \\
 &\quad \quad (at_y.const s + n) (i32.load8_u y \{offset 0, align 0\}) \\
 &\quad \quad (i32.store8 x \{offset 0, align 0\}) \\
 &\quad \quad (at_x.const d) (at_y.const s) (at_n.const n) \text{ memory.copy } x y \\
 &\quad (\text{otherwise, if } d > s)
 \end{aligned}$$

`memory.init x y`

1. Let F be the current frame.
2. Assert: due to validation, $F.module.memaddrs[x]$ exists.
3. Let ma be the memory address $F.module.memaddrs[x]$.
4. Assert: due to validation, $S.mems[ma]$ exists.
5. Let mem be the memory instance $S.mems[ma]$.
6. Assert: due to validation, $F.module.dataaddrs[y]$ exists.
7. Let da be the data address $F.module.dataaddrs[y]$.
8. Assert: due to validation, $S.datas[da]$ exists.
9. Let $data$ be the data instance $S.datas[da]$.
10. Assert: due to validation, a value of value type `i32` is on the top of the stack.
11. Pop the value `i32.const n` from the stack.
12. Assert: due to validation, a value of value type `i32` is on the top of the stack.
13. Pop the value `i32.const s` from the stack.

14. Assert: due to [validation](#), a value of some [address type](#) at is on the top of the stack.
15. Pop the value $at.const\ d$ from the stack.
16. If $s + n$ is larger than the length of $data.data$ or $d + n$ is larger than the length of $mem.data$, then:
 - a. Trap.
17. If $n = 0$, then:
 - a. Return.
18. Let b be the byte $data.data[s]$.
19. Push the value $at.const\ d$ to the stack.
20. Push the value $i32.const\ b$ to the stack.
21. Execute the instruction $i32.store8\ x\ \{\text{offset } 0, \text{align } 0\}$.
22. Assert: due to the earlier check against the memory size, $d + 1 < 2^{32}$.
23. Push the value $at.const\ (d + 1)$ to the stack.
24. Assert: due to the earlier check against the memory size, $s + 1 < 2^{32}$.
25. Push the value $i32.const\ (s + 1)$ to the stack.
26. Push the value $i32.const\ (n - 1)$ to the stack.
27. Execute the instruction $memory.init\ x\ y$.

$$\begin{aligned}
 S; F; (at.const\ d) (i32.const\ s) (i32.const\ n) (memory.init\ x\ y) &\hookrightarrow S; F; \text{trap} \\
 &\quad (\text{if } d + n > |S.mems[F.module.memaddrs[x]].data| \\
 &\quad \vee s + n > |S.datas[F.module.dataaddrs[y]].data|) \\
 S; F; (at.const\ d) (i32.const\ s) (i32.const\ 0) (memory.init\ x\ y) &\hookrightarrow S; F; \epsilon \\
 &\quad (\text{otherwise}) \\
 S; F; (at.const\ d) (i32.const\ s) (i32.const\ n + 1) (memory.init\ x\ y) &\hookrightarrow \\
 S; F; (at.const\ d) (i32.const\ b) (i32.store8\ x\ \{\text{offset } 0, \text{align } 0\}) & \\
 (at.const\ d + 1) (i32.const\ s + 1) (i32.const\ n) (memory.init\ x\ y) & \\
 (\text{otherwise, if } b = S.datas[F.module.dataaddrs[y]].data[s]) &
 \end{aligned}$$

[data.drop](#) x

1. Let F be the [current frame](#).
2. Assert: due to [validation](#), $F.module.dataaddrs[x]$ exists.
3. Let a be the [data address](#) $F.module.dataaddrs[x]$.
4. Assert: due to [validation](#), $S.datas[a]$ exists.
5. Replace $S.datas[a]$ with the [data instance](#) $\{\text{data } \epsilon\}$.

$$\begin{aligned}
 S; F; (\text{data.drop } x) &\hookrightarrow S'; F; \epsilon \\
 &\quad (\text{if } S' = S \text{ with } datas[F.module.dataaddrs[x]] = \{\text{data } \epsilon\})
 \end{aligned}$$

4.6.8 Control Instructions

`nop`

1. Do nothing.

$$\text{nop} \hookrightarrow \epsilon$$

`unreachable`

1. Trap.

$$\text{unreachable} \hookrightarrow \text{trap}$$

`block blocktype instr* end`

1. Let F be the current frame.
2. Assert: due to [validation](#), $\text{instrtype}_{S;F}(\text{blocktype})$ is defined.
3. Let $[t_1^m] \rightarrow [t_2^n]$ be the instruction type $\text{instrtype}_{S;F}(\text{blocktype})$.
4. Let L be the label whose arity is n and whose continuation is the end of the block.
5. Assert: due to [validation](#), there are at least m values on the top of the stack.
6. Pop the values val^m from the stack.
7. Enter the block $\text{val}^m \text{ instr}^*$ with label L .

$$S; F; \text{val}^m \text{ block } bt \text{ instr}^* \text{ end} \hookrightarrow S; F; \text{label}_n\{\epsilon\} \text{ val}^m \text{ instr}^* \text{ end} \\ (\text{if } \text{instrtype}_{S;F}(bt) = [t_1^m] \rightarrow [t_2^n])$$

`loop blocktype instr* end`

1. Let F be the current frame.
2. Assert: due to [validation](#), $\text{instrtype}_{S;F}(\text{blocktype})$ is defined.
3. Let $[t_1^m] \rightarrow [t_2^n]$ be the instruction type $\text{instrtype}_{S;F}(\text{blocktype})$.
4. Let L be the label whose arity is m and whose continuation is the start of the loop.
5. Assert: due to [validation](#), there are at least m values on the top of the stack.
6. Pop the values val^m from the stack.
7. Enter the block $\text{val}^m \text{ instr}^*$ with label L .

$$S; F; \text{val}^m \text{ loop } bt \text{ instr}^* \text{ end} \hookrightarrow S; F; \text{label}_m\{\text{loop } bt \text{ instr}^* \text{ end}\} \text{ val}^m \text{ instr}^* \text{ end} \\ (\text{if } \text{instrtype}_{S;F}(bt) = [t_1^m] \rightarrow [t_2^n])$$

if *blocktype* *instr*₁^{*} else *instr*₂^{*} end

1. Assert: due to **validation**, a value of **value type** *i32* is on the top of the stack.
2. Pop the value *i32.const* *c* from the stack.
3. If *c* is non-zero, then:
 - a. Execute the block instruction **block** *blocktype* *instr*₁^{*} end.
4. Else:
 - a. Execute the block instruction **block** *blocktype* *instr*₂^{*} end.

$$\begin{aligned}
 (i32.const\ c)\ \text{if}\ bt\ instr_1^* \text{ else } instr_2^* \text{ end} &\hookrightarrow \text{block } bt\ instr_1^* \text{ end} \\
 &\quad (\text{if } c \neq 0) \\
 (i32.const\ c)\ \text{if}\ bt\ instr_1^* \text{ else } instr_2^* \text{ end} &\hookrightarrow \text{block } bt\ instr_2^* \text{ end} \\
 &\quad (\text{if } c = 0)
 \end{aligned}$$

throw *x*

1. Let *F* be the current frame.
2. Assert: due to **validation**, *F.module.tagaddrs*[*x*] exists.
3. Let *ta* be the tag address *F.module.tagaddrs*[*x*].
4. Assert: due to **validation**, *S.tags*[*ta*] exists.
5. Let *ti* be the tag instance *S.tags*[*ta*].
6. Let $[t^n] \rightarrow [t'^*]$ be the tag type *ti.type*.
7. Assert: due to **validation**, there are at least *n* values on the top of the stack.
8. Pop the *n* values *val*^{*n*} from the stack.
9. Let *ea* be the **exception** address resulting from **allocating** an exception instance with tag address *ta* and initializer values *val*^{*n*}.
10. Let *exn* be *S.exns*[*ea*]
11. Push the value **ref.exn** *ea* to the stack.
12. Execute the instruction **throw_ref**.

$$\begin{aligned}
 S; F; val^n\ (\text{throw } x) &\hookrightarrow S'; F; (\text{ref.exn } |S.exns|)\ \text{throw_ref} \quad (\text{if } F.module.tagaddrs[x] = a \\
 &\quad \wedge S.tags[a].type = [t^n] \rightarrow [] \\
 &\quad \wedge exn = \{\text{tag } a, \text{fields } val^n\} \\
 &\quad \wedge S' = S \text{ with } exns = S.exns\ exn)
 \end{aligned}$$

throw_ref

1. Assert: due to **validation**, a **reference** is on the top of the stack.
2. Pop the reference *ref* from the stack.
3. If *ref* is **ref.null** *ht*, then:
 - a. Trap.
4. Assert: due to **validation**, *ref* is an **exception** reference.
5. Let **ref.exn** *ea* be *ref*.
6. Assert: due to **validation**, *S.exns*[*ea*] exists.

7. Let *exn* be the **exception instance** *S.exns*[*ea*].
8. Let *a* be the **tag address** *exn.tag*.
9. While the stack is not empty and the top of the stack is not an **exception handler**, do:
 - a. Pop the top element from the stack.
10. Assert: the stack is now either empty, or there is an exception handler on the top of the stack.
11. If the stack is empty, then:
 - a. Return the exception (*ref.exn a*) as a **result**.
12. Assert: there is an **exception handler** on the top of the stack.
13. Pop the exception handler *handler_n{catch*}* from the stack.
14. If *catch** is empty, then:
 - a. Push the exception reference *ref.exn ea* back to the stack.
 - b. Execute the instruction *throw_ref* again.
15. Else:
 - a. Let *F* be the **current frame**.
 - b. Let *catch₁* be the first **catch clause** in *catch** and *catch'** the remaining clauses.
 - c. If *catch₁* is of the form *catch x l* and the **tag address** *a* equals *F.module.tagaddrs*[*x*], then:
 - i. Push the values *exn.fields* to the stack.
 - ii. Execute the instruction *br l*.
 - d. Else if *catch₁* is of the form *catch_ref x l* and the **tag address** *a* equals *F.module.tagaddrs*[*x*], then:
 - i. Push the values *exn.fields* to the stack.
 - ii. Push the exception reference *ref.exn ea* to the stack.
 - iii. Execute the instruction *br l*.
 - e. Else if *catch₁* is of the form *catch_all l*, then:
 - i. Execute the instruction *br l*.
 - f. Else if *catch₁* is of the form *catch_all_ref l*, then:
 - i. Push the exception reference *ref.exn ea* to the stack.
 - ii. Execute the instruction *br l*.
 - g. Else:
 1. Push the modified handler *handler_n{catch'**} back to the stack.
 2. Push the exception reference *ref.exn ea* back to the stack.
 3. Execute the instruction *throw_ref* again.

$$\begin{aligned}
 & (\text{ref.null } ht) \text{ throw_ref} \hookrightarrow \text{trap} \\
 & \text{handler}_n\{\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} \hookrightarrow (\text{ref.exn } a) \text{ throw_ref} \\
 S; F; \text{handler}_n\{(\text{catch } x \ l) \text{ catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} & \hookrightarrow \text{exn.fields } (\text{br } l) \\
 & \quad (\text{if } \text{exn} = S.\text{exns}[a] \\
 & \quad \quad \wedge \text{exn.tag} = F.\text{module.tagaddrs}[x]) \\
 S; F; \text{handler}_n\{(\text{catch_ref } x \ l) \text{ catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} & \hookrightarrow \text{exn.fields } (\text{ref.exn } a) (\text{br } l) \\
 & \quad (\text{if } \text{exn} = S.\text{exns}[a] \\
 & \quad \quad \wedge \text{exn.tag} = F.\text{module.tagaddrs}[x]) \\
 & \quad \text{handler}_n\{(\text{catch_all } l) \text{ catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} \hookrightarrow (\text{br } l) \\
 & \text{handler}_n\{(\text{catch_all_ref } l) \text{ catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} \hookrightarrow (\text{ref.exn } a) (\text{br } l) \\
 & \text{handler}_n\{\text{catch}_1 \text{ catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} \hookrightarrow \text{handler}_n\{\text{catch}^*\} T[(\text{ref.exn } a) \text{ throw_ref}] \text{ end} \\
 & \quad (\text{otherwise})
 \end{aligned}$$

try_table blocktype catch instr* end*

1. Assert: due to **validation**, $\text{instrtype}_{S;F}(\text{blocktype})$ is defined.
2. Let $[t_1^m] \rightarrow [t_2^n]$ be the instruction type $\text{instrtype}_{S;F}(\text{blocktype})$.
3. Assert: due to **validation**, there are at least m values on the top of the stack.
4. Pop the values val^m from the stack.
5. Let L be the label whose arity is n and whose continuation is the end of the *try_table* instruction.
6. Enter the block $\text{val}^m \text{ instr}_1^*$ with label L and exception handler $\text{handler}_n\{\text{catch}^*\}$.

$$\begin{aligned}
 F; \text{val}^m (\text{try_table } bt \text{ catch}^* \text{ instr}^* \text{ end}) & \hookrightarrow F; \text{handler}_n\{\text{catch}^*\} (\text{label}_n\{\epsilon\} \text{val}^m \text{ instr}^* \text{ end}) \text{ end} \\
 & \quad (\text{if } \text{instrtype}_{S;F}(bt) = [t_1^m] \rightarrow [t_2^n] \wedge (F.\text{module.tagaddrs}[x] = a_x)^*)
 \end{aligned}$$

br l

1. Assert: due to **validation**, the stack contains at least $l + 1$ labels.
2. Let L be the l -th label appearing on the stack, starting from the top and counting from zero.
3. Let n be the arity of L .
4. Assert: due to **validation**, there are at least n values on the top of the stack.
5. Pop the values val^n from the stack.
6. Repeat $l + 1$ times:
 - a. While the top of the stack is a value or a **handler**, do:
 - i. Pop the value or handler from the stack.
 - b. Assert: due to **validation**, the top of the stack now is a label.
 - c. Pop the label from the stack.
7. Push the values val^n to the stack.
8. Jump to the continuation of L .

$$\text{label}_n\{\text{instr}^*\} B^l[\text{val}^n (\text{br } l)] \text{ end} \hookrightarrow \text{val}^n \text{ instr}^*$$

br_if l

1. Assert: due to **validation**, a value of **value type** `i32` is on the top of the stack.
2. Pop the value `i32.const` c from the stack.
3. If c is non-zero, then:
 - a. **Execute** the instruction `br` l .
4. Else:
 - a. Do nothing.

$$\begin{aligned} (\text{i32.const } c) (\text{br_if } l) &\hookrightarrow (\text{br } l) && (\text{if } c \neq 0) \\ (\text{i32.const } c) (\text{br_if } l) &\hookrightarrow \epsilon && (\text{if } c = 0) \end{aligned}$$

br_table $l^* l_N$

1. Assert: due to **validation**, a value of **value type** `i32` is on the top of the stack.
2. Pop the value `i32.const` i from the stack.
3. If i is smaller than the length of l^* , then:
 - a. Let l_i be the label $l^*[i]$.
 - b. **Execute** the instruction `br` l_i .
4. Else:
 - a. **Execute** the instruction `br` l_N .

$$\begin{aligned} (\text{i32.const } i) (\text{br_table } l^* l_N) &\hookrightarrow (\text{br } l_i) && (\text{if } l^*[i] = l_i) \\ (\text{i32.const } i) (\text{br_table } l^* l_N) &\hookrightarrow (\text{br } l_N) && (\text{if } |l^*| \leq i) \end{aligned}$$

br_on_null l

1. Assert: due to **validation**, a **reference value** is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is `ref.null` ht , then:
 - a. **Execute** the instruction `(br` $l).$
4. Else:
 - a. Push the value ref back to the stack.

$$\begin{aligned} ref (\text{br_on_null } l) &\hookrightarrow (\text{br } l) && (\text{if } ref = \text{ref.null } ht) \\ ref (\text{br_on_null } l) &\hookrightarrow ref && (\text{otherwise}) \end{aligned}$$

`br_on_non_null l`

1. Assert: due to **validation**, a **reference value** is on the top of the stack.
2. Pop the value ref from the stack.
3. If ref is `ref.null ht`, then:
 - a. Do nothing.
4. Else:
 - a. Push the value ref back to the stack.
 - b. Execute the instruction `(br l)`.

$$\begin{aligned} ref \text{ (br_on_non_null } l) &\hookrightarrow \epsilon && \text{(if } ref = \text{ref.null ht)} \\ ref \text{ (br_on_non_null } l) &\hookrightarrow ref \text{ (br } l) && \text{(otherwise)} \end{aligned}$$

`br_on_cast l rt1 rt2`

1. Let F be the **current frame**.
2. Let rt'_2 be the **reference type** $\text{clos}_{F.\text{module}}(rt_2)$.
3. Assert: due to **validation**, rt'_2 is **closed**.
4. Assert: due to **validation**, a **reference value** is on the top of the stack.
5. Pop the value ref from the stack.
6. Assert: due to **validation**, the **reference value** is **valid** with some **reference type**.
7. Let rt be the **reference type** of ref .
8. Push the value ref back to the stack.
9. If the **reference type** rt **matches** rt'_2 , then:
 - a. Execute the instruction `(br l)`.

$$\begin{aligned} S; F; ref \text{ (br_on_cast } l \text{ } rt_1 \text{ } rt_2) &\hookrightarrow ref \text{ (br } l) && \text{(if } S \vdash ref : rt \wedge \vdash rt \leq \text{clos}_{F.\text{module}}(rt_2)) \\ S; F; ref \text{ (br_on_cast } l \text{ } rt_1 \text{ } rt_2) &\hookrightarrow ref && \text{(otherwise)} \end{aligned}$$

`br_on_cast_fail l rt1 rt2`

1. Let F be the **current frame**.
2. Let rt'_2 be the **reference type** $\text{clos}_{F.\text{module}}(rt_2)$.
3. Assert: due to **validation**, rt'_2 is **closed**.
4. Assert: due to **validation**, a **reference value** is on the top of the stack.
5. Pop the value ref from the stack.
6. Assert: due to **validation**, the **reference value** is **valid** with some **reference type**.
7. Let rt be the **reference type** of ref .
8. Push the value ref back to the stack.
9. If the **reference type** rt **does not match** rt'_2 , then:
 - a. Execute the instruction `(br l)`.

$$\begin{aligned} S; F; ref \text{ (br_on_cast_fail } l \text{ } rt_1 \text{ } rt_2) &\hookrightarrow ref && \text{(if } S \vdash ref : rt \wedge \vdash rt \leq \text{clos}_{F.\text{module}}(rt_2)) \\ S; F; ref \text{ (br_on_cast_fail } l \text{ } rt_1 \text{ } rt_2) &\hookrightarrow ref \text{ (br } l) && \text{(otherwise)} \end{aligned}$$

return

1. Let F be the **current frame**.
2. Let n be the arity of F .
3. Assert: due to **validation**, there are at least n values on the top of the stack.
4. Pop the results val^n from the stack.
5. Assert: due to **validation**, the stack contains at least one **frame**.
6. While the top of the stack is not a frame, do:
 - a. Pop the top element from the stack.
7. Assert: the top of the stack is the frame F .
8. Pop the frame from the stack.
9. Push val^n to the stack.
10. Jump to the instruction after the original call that pushed the frame.

$$\text{frame}_n\{F\} B^*[val^n \text{ return}] \text{ end} \hookrightarrow val^n$$

call x

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.funcaddrs}[x]$ exists.
3. Let a be the **function address** $F.\text{module.funcaddrs}[x]$.
4. **Invoke** the function instance at address a .

$$F; (\text{call } x) \hookrightarrow F; (\text{invoke } a) \quad (\text{if } F.\text{module.funcaddrs}[x] = a)$$

call_ref x

1. Assert: due to **validation**, a null or **function reference** is on the top of the stack.
2. Pop the reference value r from the stack.
3. If r is **ref.null ht**, then:
 - a. Trap.
4. Assert: due to **validation**, r is a **function reference**.
5. Let **ref.func a** be the reference r .
6. **Invoke** the function instance at address a .

$$\begin{aligned} F; (\text{ref.func } a) (\text{call_ref } x) &\hookrightarrow F; (\text{invoke } a) \\ F; (\text{ref.null ht}) (\text{call_ref } x) &\hookrightarrow F; \text{trap} \end{aligned}$$

`call_indirect x y`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.tableaddrs}[x]$ exists.
3. Let ta be the **table address** $F.\text{module.tableaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{tables}[ta]$ exists.
5. Let tab be the **table instance** $S.\text{tables}[ta]$.
6. Assert: due to **validation**, $F.\text{module.types}[y]$ is defined.
7. Let dt_{expect} be the **defined type** $F.\text{module.types}[y]$.
8. Assert: due to **validation**, a value with **value type** `i32` is on the top of the stack.
9. Pop the value `i32.const i` from the stack.
10. If i is not smaller than the length of $tab.\text{elem}$, then:
 - a. Trap.
11. Let r be the **reference** $tab.\text{elem}[i]$.
12. If r is `ref.null ht`, then:
 - a. Trap.
13. Assert: due to **validation of table mutation**, r is a **function reference**.
14. Let `ref.func a` be the **function reference** r .
15. Assert: due to **validation of table mutation**, $S.\text{funcs}[a]$ exists.
16. Let f be the **function instance** $S.\text{funcs}[a]$.
17. Let dt_{actual} be the **defined type** $f.\text{type}$.
18. If dt_{actual} does not **match** dt_{expect} , then:
 - a. Trap.
19. **Invoke** the function instance at address a .

$$\begin{aligned}
 S; F; (i32.\text{const } i) (\text{call_indirect } x y) &\hookrightarrow S; F; (\text{invoke } a) \\
 &\quad (\text{if } S.\text{tables}[F.\text{module.tableaddrs}[x]].\text{elem}[i] = \text{ref.func } a \\
 &\quad \wedge S.\text{funcs}[a] = f \\
 &\quad \wedge S \vdash F.\text{module.types}[y] \leq f.\text{type}) \\
 S; F; (i32.\text{const } i) (\text{call_indirect } x y) &\hookrightarrow S; F; \text{trap} \\
 &\quad (\text{otherwise})
 \end{aligned}$$

`return_call x`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.funcaddrs}[x]$ exists.
3. Let a be the **function address** $F.\text{module.funcaddrs}[x]$.
4. **Tail-invoke** the function instance at address a .

$$(\text{return_call } x) \hookrightarrow (\text{return_invoke } a) \quad (\text{if } (\text{call } x) \hookrightarrow (\text{invoke } a))$$

`return_call_ref x`

1. Assert: due to **validation**, a **function reference** is on the top of the stack.
2. Pop the reference value r from the stack.
3. If r is `ref.null ht`, then:
 - a. Trap.
4. Assert: due to **validation**, r is a **function reference**.
5. Let `ref.func a` be the reference r .
6. **Tail-invoke** the function instance at address a .

$$\begin{array}{lll} \text{val } (\text{return_call_ref } x) & \hookrightarrow & (\text{return_invoke } a) & (\text{if } \text{val } (\text{call_ref } x) \hookrightarrow (\text{invoke } a)) \\ \text{val } (\text{return_call_ref } x) & \hookrightarrow & \text{trap} & (\text{if } \text{val } (\text{call_ref } x) \hookrightarrow \text{trap}) \end{array}$$

`return_call_indirect x y`

1. Let F be the **current frame**.
2. Assert: due to **validation**, $F.\text{module.tableaddrs}[x]$ exists.
3. Let ta be the **table address** $F.\text{module.tableaddrs}[x]$.
4. Assert: due to **validation**, $S.\text{tables}[ta]$ exists.
5. Let tab be the **table instance** $S.\text{tables}[ta]$.
6. Assert: due to **validation**, $F.\text{module.types}[y]$ exists.
7. Let dt_{expect} be the **defined type** $F.\text{module.types}[y]$.
8. Assert: due to **validation**, a value with **value type** `i32` is on the top of the stack.
9. Pop the value `i32.const i` from the stack.
10. If i is not smaller than the length of $tab.\text{elem}$, then:
 - a. Trap.
11. If $tab.\text{elem}[i]$ is uninitialized, then:
 - a. Trap.
12. Let a be the **function address** $tab.\text{elem}[i]$.
13. Assert: due to **validation**, $S.\text{funcs}[a]$ exists.
14. Let f be the **function instance** $S.\text{funcs}[a]$.
15. Let dt_{actual} be the **defined type** $f.\text{type}$.
16. If dt_{actual} does not **match** dt_{expect} , then:
 - a. Trap.
17. **Tail-invoke** the function instance at address a .

$$\begin{array}{lll} \text{val } (\text{return_call_indirect } x y) & \hookrightarrow & (\text{return_invoke } a) & (\text{if } \text{val } (\text{call_indirect } x y) \hookrightarrow (\text{invoke } a)) \\ \text{val } (\text{return_call_indirect } x y) & \hookrightarrow & \text{trap} & (\text{if } \text{val } (\text{call_indirect } x y) \hookrightarrow \text{trap}) \end{array}$$

4.6.9 Blocks

The following auxiliary rules define the semantics of executing an [instruction sequence](#) that forms a [block](#).

Entering $instr^*$ with label L

1. Push L to the stack.
2. Jump to the start of the instruction sequence $instr^*$.

Note: No formal reduction rule is needed for entering an instruction sequence, because the label L is embedded in the [administrative instruction](#) that structured control instructions reduce to directly.

Exiting $instr^*$ with label L

When the end of a block is reached without a jump, [exception](#), or [trap](#) aborting it, then the following steps are performed.

1. Pop all values val^* from the top of the stack.
2. Assert: due to [validation](#), the label L is now on the top of the stack.
3. Pop the label from the stack.
4. Push val^* back to the stack.
5. Jump to the position after the [end](#) of the [structured control instruction](#) associated with the label L .

$$\text{label}_n\{instr^*\} val^* \text{ end} \quad \hookrightarrow \quad val^*$$

Note: This semantics also applies to the instruction sequence contained in a [loop](#) instruction. Therefore, execution of a loop falls off the end, unless a backwards branch is performed explicitly.

4.6.10 Exception Handling

The following auxiliary rules define the semantics of entering and exiting [try_table](#) blocks.

Entering $instr^*$ with label L and exception handler H

1. Push H to the stack.
2. Push L onto the stack.
3. Jump to the start of the instruction sequence $instr^*$.

Note: No formal reduction rule is needed for entering an exception [handler](#) because it is an [administrative instruction](#) that the [try_table](#) instruction reduces to directly.

Exiting an exception handler

When the end of a `try_table` block is reached without a jump, `exception`, or `trap`, then the following steps are performed.

1. Let m be the number of values on the top of the stack.
2. Pop the values val^m from the stack.
3. Assert: due to `validation`, a handler and a label are now on the top of the stack.
4. Pop the label from the stack.
5. Pop the handler H from the stack.
6. Push val^m back to the stack.
7. Jump to the position after the `end` of the administrative instruction associated with the handler H .

$$\text{handler}_m\{\text{catch}^*\} \text{ } val^m \text{ end} \hookrightarrow val^m$$

4.6.11 Function Calls

The following auxiliary rules define the semantics of invoking a `function instance` through one of the `call instructions` and returning from it.

Invocation of function address a

1. Assert: due to `validation`, $S.\text{funcs}[a]$ exists.
2. Let f be the `function instance`, $S.\text{funcs}[a]$.
3. Let $\text{func } [t_1^n] \rightarrow [t_2^m]$ be the `composite type` $\text{expand}(f.\text{type})$.
4. Let local^* be the list of `locals` $f.\text{code}.\text{locals}$.
5. Let $\text{instr}^* \text{ end}$ be the `expression` $f.\text{code}.\text{body}$.
6. Assert: due to `validation`, n values are on the top of the stack.
7. Pop the values val^n from the stack.
8. Let F be the `frame` $\{\text{module } f.\text{module}, \text{locals } val^n (\text{default}_t)^*\}$.
9. Push the activation of F with arity m to the stack.
10. Let L be the `label` whose arity is m and whose continuation is the end of the function.
11. `Enter` the instruction sequence instr^* with label L .

$$\begin{aligned} S; val^n (\text{invoke } a) &\hookrightarrow S; \text{frame}_m\{F\} \text{label}_m\{\} \text{instr}^* \text{end end} \\ &\quad (\text{if } S.\text{funcs}[a] = f \\ &\quad \wedge \text{expand}(f.\text{type}) = \text{func } [t_1^n] \rightarrow [t_2^m] \\ &\quad \wedge f.\text{code} = \{\text{type } x, \text{locals } \{\text{type } t\}^k, \text{body } \text{instr}^* \text{end}\} \\ &\quad \wedge F = \{\text{module } f.\text{module}, \text{locals } val^n (\text{default}_t)^k\}) \end{aligned}$$

Note: For non-defaultable types, the respective local is left uninitialized by these rules.

Tail-invocation of function address a

1. Assert: due to **validation**, $S.\text{funcs}[a]$ exists.
2. Let $\text{func } [t_1^n] \rightarrow [t_2^m]$ be the **composite type** $\text{expand}(S.\text{funcs}[a].\text{type})$.
3. Assert: due to **validation**, there are at least n values on the top of the stack.
4. Pop the results val^n from the stack.
5. Assert: due to **validation**, the stack contains at least one **frame**.
6. While the top of the stack is not a frame, do:
 - a. Pop the top element from the stack.
7. Assert: the top of the stack is a frame.
8. Pop the frame from the stack.
9. Push val^n to the stack.
10. **Invoke** the function instance at address a .

$$S; \text{frame}_m\{F\} B^*[\text{val}^n (\text{return_invoke } a)] \text{ end} \quad \hookrightarrow \quad \text{val}^n (\text{invoke } a) \quad (\text{if } \text{expand}(S.\text{funcs}[a].\text{type}) = \text{func } [t_1^n] \rightarrow [t_2^m])$$
Returning from a function

When the end of a function is reached without a jump (including through **return**), or an **exception** or **trap** aborting it, then the following steps are performed.

1. Let F be the **current frame**.
2. Let n be the arity of the activation of F .
3. Assert: due to **validation**, there are n values on the top of the stack.
4. Pop the results val^n from the stack.
5. Assert: due to **validation**, the frame F is now on the top of the stack.
6. Pop the frame from the stack.
7. Push val^n back to the stack.
8. Jump to the instruction after the original call.

$$\text{frame}_n\{F\} \text{ val}^n \text{ end} \quad \hookrightarrow \quad \text{val}^n$$
Host Functions

Invoking a **host function** has non-deterministic behavior. It may either terminate with a **trap**, an **exception**, or return regularly. However, in the latter case, it must consume and produce the right number and types of WebAssembly **values** on the stack, according to its **function type**.

A host function may also modify the **store**. However, all store modifications must result in an **extension** of the original store, i.e., they must only modify mutable contents and must not have instances removed. Furthermore,

the resulting store must be **valid**, i.e., all data and code in it is well-typed.

$$\begin{aligned}
 S; val^n (\text{invoke } a) &\hookrightarrow S'; \text{result} \\
 &\text{(if } S.\text{funcs}[a] = \{\text{type } \text{deftype}, \text{hostcode } hf\} \\
 &\quad \wedge \text{expand}(\text{deftype}) = \text{func } [t_1^n] \rightarrow [t_2^m] \\
 &\quad \wedge (S'; \text{result}) \in hf(S; val^n)) \\
 S; val^n (\text{invoke } a) &\hookrightarrow S; val^n (\text{invoke } a) \\
 &\text{(if } S.\text{funcs}[a] = \{\text{type } \text{deftype}, \text{hostcode } hf\} \\
 &\quad \wedge \text{expand}(\text{deftype}) = \text{func } [t_1^n] \rightarrow [t_2^m] \\
 &\quad \wedge \perp \in hf(S; val^n))
 \end{aligned}$$

Here, $hf(S; val^n)$ denotes the implementation-defined execution of host function hf in current store S with arguments val^n . It yields a set of possible outcomes, where each element is either a pair of a modified store S' and a **result** or the special value \perp indicating divergence. A host function is non-deterministic if there is at least one argument for which the set of outcomes is not singular.

For a WebAssembly implementation to be **sound** in the presence of host functions, every **host function instance** must be **valid**, which means that it adheres to suitable pre- and post-conditions: under a **valid store** S , and given arguments val^n matching the ascribed parameter types t_1^n , executing the host function must yield a non-empty set of possible outcomes each of which is either divergence or consists of a valid store S' that is an **extension** of S and a result matching the ascribed return types t_2^m . All these notions are made precise in the **Appendix**.

Note: A host function can call back into WebAssembly by **invoking** a function **exported** from a **module**. However, the effects of any such call are subsumed by the non-deterministic behavior allowed for the host function.

4.6.12 Expressions

An **expression** is *evaluated* relative to a **current frame** pointing to its containing **module instance**.

1. Jump to the start of the instruction sequence $instr^*$ of the expression.
2. Execute the instruction sequence.
3. Assert: due to **validation**, the top of the stack contains a **value**.
4. Pop the **value** val from the stack.

The value val is the result of the evaluation.

$$S; F; instr^* \hookrightarrow S'; F'; instr'^* \quad (\text{if } S; F; instr^* \text{ end} \hookrightarrow S'; F'; instr'^* \text{ end})$$

Note: Evaluation iterates this reduction rule until reaching a value. Expressions constituting **function** bodies are executed during function **invocation**.

4.7 Modules

For modules, the execution semantics primarily defines **instantiation**, which **allocates** instances for a module and its contained definitions, initializes **tables** and **memories** from contained **element** and **data** segments, and invokes the **start function** if present. It also includes **invocation** of exported functions.

4.7.1 Allocation

New instances of functions, tables, memories, globals, tags, element segments, and data segments, as well as dynamic data types like structures, arrays, or exceptions, are *allocated* in a store S , as defined by the following auxiliary functions.

Functions

1. Let $func$ be the function to allocate and $moduleinst$ its module instance.
2. Let $deftype$ be the defined type $moduleinst.types[func.type]$.
3. Let a be the first free function address in S .
4. Let $funcinst$ be the function instance $\{type\ deptype, module\ moduleinst, code\ func\}$.
6. Append $funcinst$ to the $funcs$ of S .
7. Return a .

$$\begin{aligned} allocfunc(S, func, moduleinst) &= S', funcaddr \\ \text{where:} \\ deptype &= moduleinst.types[func.type] \\ funcaddr &= |S.funcs| \\ funcinst &= \{type\ deptype, module\ moduleinst, code\ func\} \\ S' &= S \oplus \{funcs\ funcinst\} \end{aligned}$$

Host Functions

1. Let $hostfunc$ be the host function to allocate and $deftype$ its defined type.
2. Let a be the first free function address in S .
3. Let $funcinst$ be the function instance $\{type\ deptype, hostcode\ hostfunc\}$.
4. Append $funcinst$ to the $funcs$ of S .
5. Return a .

$$\begin{aligned} allochostfunc(S, deptype, hostfunc) &= S', funcaddr \\ \text{where:} \\ funcaddr &= |S.funcs| \\ funcinst &= \{type\ deptype, hostcode\ hostfunc\} \\ S' &= S \oplus \{funcs\ funcinst\} \end{aligned}$$

Note: Host functions are never allocated by the WebAssembly semantics itself, but may be allocated by the embedder.

Tables

1. Let *tabletype* be the table type of the table to allocate and *ref* the initialization value.
2. Let (*addrtype* {min *n*, max *m*[?]} *reftype*) be the structure of table type *tabletype*.
3. Let *a* be the first free table address in *S*.
4. Let *tableinst* be the table instance {type *tabletype*, elem *ref*^{*n*}} with *n* elements set to *ref*.
5. Append *tableinst* to the tables of *S*.
6. Return *a*.

$$\text{alloctable}(S, \text{tabletype}, \text{ref}) = S', \text{tableaddr}$$

where:

$$\begin{aligned} \text{tabletype} &= \text{addrtype } \{\min n, \max m^?\} \text{ reftype} \\ \text{tableaddr} &= |S.\text{tables}| \\ \text{tableinst} &= \{\text{type } \text{tabletype}, \text{elem } \text{ref}^n\} \\ S' &= S \oplus \{\text{tables } \text{tableinst}\} \end{aligned}$$

Memories

1. Let *memtype* be the memory type of the memory to allocate.
2. Let (*addrtype* {min *n*, max *m*[?]}) be the structure of memory type *memtype*.
3. Let *a* be the first free memory address in *S*.
4. Let *meminst* be the memory instance {type *memtype*, data (0x00)^{*n*·64Ki}} that contains *n* pages of zeroed bytes.
5. Append *meminst* to the mems of *S*.
6. Return *a*.

$$\text{allocmem}(S, \text{memtype}) = S', \text{memaddr}$$

where:

$$\begin{aligned} \text{memtype} &= \text{addrtype } \{\min n, \max m^?\} \\ \text{memaddr} &= |S.\text{mems}| \\ \text{meminst} &= \{\text{type } \text{memtype}, \text{data } (0x00)^{n \cdot 64 \text{ Ki}}\} \\ S' &= S \oplus \{\text{mems } \text{meminst}\} \end{aligned}$$

Tags

1. Let *tagtype* be the tag type to allocate.
2. Let *a* be the first free tag address in *S*.
3. Let *taginst* be the tag instance {type *tagtype*}.
4. Append *taginst* to the tags of *S*.
5. Return *a*.

$$\text{alloctag}(S, \text{tagtype}) = S', \text{tagaddr}$$

where:

$$\begin{aligned} \text{tagaddr} &= |S.\text{tags}| \\ \text{taginst} &= \{\text{type } \text{tagtype}\} \\ S' &= S \oplus \{\text{tags } \text{taginst}\} \end{aligned}$$

Exceptions

1. Let *ta* be the **tag address** associated with the exception to allocate and *fields val** be the values to initialize the exception with.
2. Let *a* be the first free **exception address** in *S*.
3. Let *exninst* be the **exception instance** {tag *ta*, fields *val**}.
4. Append *exninst* to the **exns** of *S*.
5. Return *a*.

$$\text{allocexn}(S, \text{tagaddr}, \text{val}^*) = S', \text{exnaddr}$$

where:

$$\begin{aligned} \text{exnaddr} &= |S.\text{exns}| \\ \text{exninst} &= \{\text{tag } \text{tagaddr}, \text{fields } \text{val}^*\} \\ S' &= S \oplus \{\text{exns } \text{exninst}\} \end{aligned}$$

Globals

1. Let *globaltype* be the **global type** of the global to allocate and *val* its initialization **value**.
2. Let *a* be the first free **global address** in *S*.
3. Let *globalinst* be the **global instance** {type *globaltype*, value *val*}.
4. Append *globalinst* to the **globals** of *S*.
5. Return *a*.

$$\text{allocglobal}(S, \text{globaltype}, \text{val}) = S', \text{globaladdr}$$

where:

$$\begin{aligned} \text{globaladdr} &= |S.\text{globals}| \\ \text{globalinst} &= \{\text{type } \text{globaltype}, \text{value } \text{val}\} \\ S' &= S \oplus \{\text{globals } \text{globalinst}\} \end{aligned}$$

Element segments

1. Let *reftype* be the elements' type and *ref** the vector of **references** to allocate.
2. Let *a* be the first free **element address** in *S*.
3. Let *eleminst* be the **element instance** {type *reftype*, elem *ref**}.
4. Append *eleminst* to the **elems** of *S*.
5. Return *a*.

$$\text{allocelem}(S, \text{reftype}, \text{ref}^*) = S', \text{elemaddr}$$

where:

$$\begin{aligned} \text{elemaddr} &= |S.\text{elems}| \\ \text{eleminst} &= \{\text{type } \text{reftype}, \text{elem } \text{ref}^*\} \\ S' &= S \oplus \{\text{elems } \text{eleminst}\} \end{aligned}$$

Data segments

1. Let b^* be the vector of bytes to allocate.
2. Let a be the first free data address in S .
3. Let $datainst$ be the data instance $\{\text{data } b^*\}$.
4. Append $datainst$ to the datas of S .
5. Return a .

$$\begin{aligned} \text{allocdata}(S, b^*) &= S', dataaddr \\ \text{where:} \\ dataaddr &= |S.datas| \\ datainst &= \{\text{data } b^*\} \\ S' &= S \oplus \{\text{datas } datainst\} \end{aligned}$$

Growing tables

1. Let $tableinst$ be the table instance to grow, n the number of elements by which to grow it, and ref the initialization value.
2. Let len be n added to the length of $tableinst.elem$.
3. Let $(addrtype limits reftype)$ be the structure of table type $tableinst.type$.
4. Let $limits'$ be $limits$ with min updated to len .
5. If the table type $(addrtype limits' reftype)$ is not valid, then fail.
6. Append ref^n to $tableinst.elem$.
7. Set $tableinst.type$ to the table type $(addrtype limits' reftype)$.

$$\begin{aligned} \text{growtable}(tableinst, n, ref) &= tableinst \text{ with type} = addrtype \ limits' \ reftype \text{ with elem} = tableinst.elem \ ref^n \\ &\quad (\text{if } len = n + |tableinst.elem| \\ &\quad \wedge addrtype \ limits \ reftype = tableinst.type \\ &\quad \wedge limits' = limits \text{ with min} = len \\ &\quad \wedge \vdash addrtype \ limits' \ reftype \text{ ok}) \end{aligned}$$

Growing memories

1. Let $meminst$ be the memory instance to grow and n the number of pages by which to grow it.
2. Assert: The length of $meminst.data$ is divisible by the page size 64 Ki.
3. Let len be n added to the length of $meminst.data$ divided by the page size 64 Ki.
4. Let $(addrtype limits)$ be the structure of memory type $meminst.type$.
5. Let $limits'$ be $limits$ with min updated to len .
6. If the memory type $(addrtype limits')$ is not valid, then fail.
7. Append n times 64 Ki bytes with value 0x00 to $meminst.data$.
8. Set $meminst.type$ to the memory type $(addrtype limits')$.

$$\begin{aligned} \text{growmem}(meminst, n) &= meminst \text{ with type} = addrtype \ limits' \text{ with data} = meminst.data \ (0x00)^{n \cdot 64 \text{ Ki}} \\ &\quad (\text{if } len = n + |meminst.data|/64 \text{ Ki} \\ &\quad \wedge addrtype \ limits = meminst.type \\ &\quad \wedge limits' = limits \text{ with min} = len \\ &\quad \wedge \vdash addrtype \ limits' \text{ ok}) \end{aligned}$$

Modules

Todo: update prose for types

The allocation function for `modules` requires a suitable list of `external values` that are assumed to `match` the `import` vector of the module, a list of initialization `values` for the module's `globals`, and list of `reference` vectors for the module's `element segments`.

1. Let `module` be the `module` to allocate and $externval_{im}^*$ the vector of `external values` providing the module's imports, val_g^* the initialization `values` of the module's `globals`, ref_t^* the initializer `reference` of the module's `tables`, and $(ref_e^*)^*$ the `reference` vectors of the module's `element segments`.
2. For each defined type $deftype'_i$ in `module.types`, do:
 - a. Let $deftype_i$ be the `instantiation` $deftype'_i$ in `moduleinst` defined below.
3. For each function $func_i$ in `module.funcs`, do:
 - a. Let $funcaddr_i$ be the `function address` resulting from allocating $func_i$ for the `module instance` `moduleinst` defined below.
4. For each table $table_i$ in `module.tables`, do:
 - a. Let $limits_i$ t_i be the `table type` obtained by instantiating $table_i.type$ in `moduleinst` defined below.
 - b. Let $tableaddr_i$ be the `table address` resulting from allocating $table_i.type$ with initialization value $ref_t^*[i]$.
5. For each memory mem_i in `module.mems`, do:
 - a. Let $memtype_i$ be the `memory type` obtained by instantiating $mem_i.type$ in `moduleinst` defined below.
 - b. Let $memaddr_i$ be the `memory address` resulting from allocating $memtype_i$.
6. For each global $global_i$ in `module.globals`, do:
 - a. Let $globaltype_i$ be the `global type` obtained by instantiating $global_i.type$ in `moduleinst` defined below.
 - b. Let $globaladdr_i$ be the `global address` resulting from allocating $globaltype_i$ with initializer value $val_g^*[i]$.
7. For each tag tag_i in `module.tags`, do:
 - a. Let $tagtype$ be the `tag type` `module.types[tag_i.type]`.
 - b. Let $tagaddr_i$ be the `tag address` resulting from allocating $tagtype$.
8. For each element segment $elem_i$ in `module.ems`, do:
 - a. Let $reftype_i$ be the `element reference type` obtained by instantiating `<type-inst> elem_i.type` in `moduleinst` defined below.
 - b. Let $elemaddr_i$ be the `element address` resulting from allocating a `element instance` of `reference type` $reftype_i$ with contents $(ref_e^*)^*[i]$.
9. For each data segment $data_i$ in `module.datas`, do:
 - a. Let $dataaddr_i$ be the `data address` resulting from allocating a `data instance` with contents $data_i.init$.
10. Let $deftype^*$ be the concatenation of the defined types $deftype_i$ in index order.
11. Let $funcaddr^*$ be the concatenation of the function addresses $funcaddr_i$ in index order.
12. Let $tableaddr^*$ be the concatenation of the table addresses $tableaddr_i$ in index order.
13. Let $memaddr^*$ be the concatenation of the memory addresses $memaddr_i$ in index order.
14. Let $globaladdr^*$ be the concatenation of the global addresses $globaladdr_i$ in index order.
15. Let $tagaddr^*$ be the concatenation of the tag addresses $tagaddr_i$ in index order.
16. Let $elemaddr^*$ be the concatenation of the element addresses $elemaddr_i$ in index order.

17. Let $dataaddr^*$ be the concatenation of the data addresses $dataaddr_i$ in index order.
18. Let $funcaddr_{mod}^*$ be the list of function addresses extracted from $externval_{im}^*$, concatenated with $funcaddr^*$.
19. Let $tableaddr_{mod}^*$ be the list of table addresses extracted from $externval_{im}^*$, concatenated with $tableaddr^*$.
20. Let $memaddr_{mod}^*$ be the list of memory addresses extracted from $externval_{im}^*$, concatenated with $memaddr^*$.
21. Let $globaladdr_{mod}^*$ be the list of global addresses extracted from $externval_{im}^*$, concatenated with $globaladdr^*$.
22. Let $tagaddr_{mod}^*$ be the list of tag addresses extracted from $externval_{im}^*$, concatenated with $tagaddr^*$.
23. For each export $export_i$ in $module.exports$, do:
 - a. If $export_i$ is a function export for function index x , then let $externval_i$ be the external value $func(funcaddr_{mod}^*[x])$.
 - b. Else, if $export_i$ is a table export for table index x , then let $externval_i$ be the external value $table(tableaddr_{mod}^*[x])$.
 - c. Else, if $export_i$ is a memory export for memory index x , then let $externval_i$ be the external value $mem(memaddr_{mod}^*[x])$.
 - d. Else, if $export_i$ is a global export for global index x , then let $externval_i$ be the external value $global(globaladdr_{mod}^*[x])$.
 - e. Else, if $export_i$ is a tag export for tag index x , then let $externval_i$ be the external value $tag(tagaddr_{mod}^*[x])$.
 - f. Let $exportinst_i$ be the export instance $\{name(export_i.name), value externval_i\}$.
24. Let $exportinst^*$ be the concatenation of the export instances $exportinst_i$ in index order.
25. Let $moduleinst$ be the module instance $\{types deftype^*, funcaddrs funcaddr_{mod}^*, tableaddrs tableaddr_{mod}^*, memaddrs memaddr_{mod}^*, globaladdrs globaladdr_{mod}^*, tagaddrs tagaddr_{mod}^*, exports exportinst^*\}$.
26. Return $moduleinst$.

$$\text{allocmodule}(S, module, externval_{im}^*, val_g^*, ref_t^*, (ref_e^*)^*) = S', moduleinst$$

where:

$$\begin{aligned}
 table^* &= module.tables \\
 mem^* &= module.mems \\
 global^* &= module.globals \\
 elem^* &= module.elems \\
 data^* &= module.datas \\
 export^* &= module.exports \\
 moduleinst &= \{ \text{types } deftype^*, \\
 &\quad funcaddrs \text{ funcs}(externval_{im}^*) \text{ funcaddr}^*, \\
 &\quad tableaddrs \text{ tables}(externval_{im}^*) \text{ tableaddr}^*, \\
 &\quad memaddrs \text{ mems}(externval_{im}^*) \text{ memaddr}^*, \\
 &\quad globaladdrs \text{ globals}(externval_{im}^*) \text{ globaladdr}^*, \\
 &\quad tagaddrs \text{ tags}(externval_{im}^*) \text{ tagaddr}^*, \\
 &\quad elemaddrs \text{ elemaddr}^*, \\
 &\quad dataaddrs \text{ dataaddr}^*, \\
 &\quad exports \text{ exportinst}^* \} \\
 deftype^* &= alloctype^*(module.types) \\
 S_1, funcaddr^* &= allocfunc^*(S, module.funcs, moduleinst) \\
 S_2, tableaddr^* &= allocatable^*(S_1, \text{clos}_{moduleinst}(table.type)^*, ref_t^*) \quad (\text{where } (table.type)^* = (limits \ t)^*) \\
 S_3, memaddr^* &= allocmem^*(S_2, \text{clos}_{moduleinst}(mem.type)^*) \\
 S_4, globaladdr^* &= allocglobal^*(S_3, \text{clos}_{moduleinst}(global.type)^*, val_g^*) \\
 S_5, tagaddr^* &= alloc-tag^*(S_4, \text{clos}_{moduleinst}(tag)^*) \quad (\text{where } tag^* = module.tags) \\
 S_6, elemaddr^* &= alloc-elem^*(S_5, \text{clos}_{moduleinst}(elem.type)^*, (ref_e^*)^*) \\
 S', dataaddr^* &= allocdata^*(S_6, data.init^*) \\
 exportinst^* &= \{ \text{name } (export.name), \text{value } externval_{ex}^* \}^* \\
 funcs(externval_{ex}^*) &= (moduleinst.funcaddrs[x])^* \quad (\text{where } x^* = \text{funcs}(export^*)) \\
 tables(externval_{ex}^*) &= (moduleinst.tableaddrs[x])^* \quad (\text{where } x^* = \text{tables}(export^*)) \\
 mems(externval_{ex}^*) &= (moduleinst.memaddrs[x])^* \quad (\text{where } x^* = \text{mems}(export^*)) \\
 globals(externval_{ex}^*) &= (moduleinst.globaladdrs[x])^* \quad (\text{where } x^* = \text{globals}(export^*)) \\
 tags(externval_{ex}^*) &= (moduleinst.tagaddrs[x])^* \quad (\text{where } x^* = \text{tags}(export^*))
 \end{aligned}$$

Here, the notation allocx^* is shorthand for multiple allocations of object kind X , defined as follows:

$$\begin{aligned}
 \text{allocx}^*(S_0, X^n, \dots) &= S_n, a^n \\
 \text{where for all } i < n: \\
 S_{i+1}, a^n[i] &= \text{allocx}(S_i, X^n[i], \dots)
 \end{aligned}$$

Moreover, if the dots \dots are a sequence A^n (as for globals or tables), then the elements of this sequence are passed to the allocation function pointwise.

For types, however, allocation is defined in terms of [rolling](#) and [substitution](#) of all preceding types to produce a list of [closed defined types](#):

$$\begin{aligned}
 \text{alloctype}^*(rectype^n) &= deftype^* \\
 \text{where for all } i < n: \\
 rectype^n[i] &= \text{rec subtype}_i^{m_i} \\
 deftype^*[x_i : m_i] &= \text{roll}_{x_i}^*(\text{rec subtype}_i^{m_i})[:= deftype^*[0 : x_i]] \\
 x_{i+1} &= x_i + m_i \\
 x_n &= |deftype^*|
 \end{aligned}$$

Note: The definition of module allocation is mutually recursive with the allocation of its associated functions, because the resulting module instance $moduleinst$ is passed to the allocators as an argument, in order to form the necessary closures. In an implementation, this recursion is easily unraveled by mutating one or the other in a secondary step.

4.7.2 Instantiation

Given a store S , a module $module$ is instantiated with a list of external values $externval^n$ supplying the required imports as follows.

Instantiation checks that the module is **valid** and the provided imports **match** the declared types, and may **fail** with an error otherwise. Instantiation can also result in an **exception** or **trap** when initializing a **table** or **memory** from an **active segment** or when executing the **start** function. It is up to the **embedder** to define how such conditions are reported.

1. If $module$ is not **valid**, then:
 - a. Fail.
2. Assert: $module$ is **valid** with external types $externtype_{im}^m$ classifying its imports.
3. If the number m of imports is not equal to the number n of provided external values, then:
 - a. Fail.
4. For each external value $externval_i$ in $externval^n$ and external type $externtype'_i$ in $externtype_{im}^n$, do:
 - a. If $externval_i$ is not **valid** with an external type $externtype_i$ in store S , then:
 - i. Fail.
 - b. Let $externtype''_i$ be the external type obtained by instantiating $externtype'_i$ in $moduleinst$ defined below.
 - c. If $externtype_i$ does not **match** $externtype''_i$, then:
 - i. Fail.
6. Let F be the auxiliary frame $\{\text{module } moduleinst, \text{locals } \epsilon\}$, that consists of the final module instance $moduleinst$, defined below.
7. Push the frame F to the stack.
8. Let val_g^* be the vector of **global** initialization values determined by $module$ and $externval^n$. These may be calculated as follows.
 - a. For each **global** $global_i$ in $module.globals$, do:
 - i. Let val_{gi} be the result of **evaluating** the initializer expression $global_i.init$.
 - b. Assert: due to **validation**, the frame F is now on the top of the stack.
 - c. Let val_g^* be the concatenation of val_{gi} in index order.
9. Let ref_t^* be the vector of **table** initialization references determined by $module$ and $externval^n$. These may be calculated as follows.
 - a. For each **table** $table_i$ in $module.tables$, do:
 - i. Let val_{ti} be the result of **evaluating** the initializer expression $table_i.init$.
 - ii. Assert: due to **validation**, val_{ti} is a **reference**.
 - iii. Let ref_{ti} be the reference val_{ti} .
 - b. Assert: due to **validation**, the frame F is now on the top of the stack.
 - c. Let ref_t^* be the concatenation of ref_{ti} in index order.
10. Let $(ref_e^*)^*$ be the list of **reference** vectors determined by the **element segments** in $module$. These may be calculated as follows.
 - a. For each **element segment** $elem_i$ in $module.elems$, and for each element expression $expr_{ij}$ in $elem_i.init$, do:
 - i. Let ref_{ij} be the result of **evaluating** the initializer expression $expr_{ij}$.
 - b. Let ref_i^* be the concatenation of function elements ref_{ij} in order of index j .

- c. Let $(ref_e^*)^*$ be the concatenation of function element vectors ref_i^* in order of index i .
11. Let $moduleinst$ be a new module instance allocated from $module$ in store S with imports $externval^n$, global initializer values val_g^* , table initializer values ref_t^* , and element segment contents $(ref_e^*)^*$, and let S' be the extended store produced by module allocation.
12. For each element segment $elem_i$ in $module.elems$ whose `mode` is of the form `active {table $tableidx_i$, offset $instr_i^*$ end}`, do:
 - a. Let n be the length of the vector $elem_i.init$.
 - b. Execute the instruction sequence $instr_i^*$.
 - c. Execute the instruction `i32.const 0`.
 - d. Execute the instruction `i32.const n` .
 - e. Execute the instruction `table.init $tableidx_i$ i` .
 - f. Execute the instruction `elem.drop i` .
13. For each element segment $elem_i$ in $module.elems$ whose `mode` is of the form `declarative`, do:
 - a. Execute the instruction `elem.drop i` .
14. For each data segment $data_i$ in $module.datas$ whose `mode` is of the form `active {memory $memidx_i$, offset $dinstr_i^*$ end}`, do:
 - a. Let n be the length of the vector $data_i.init$.
 - b. Execute the instruction sequence $dinstr_i^*$.
 - c. Execute the instruction `i32.const 0`.
 - d. Execute the instruction `i32.const n` .
 - e. Execute the instruction `memory.init i` .
 - f. Execute the instruction `data.drop i` .
15. If the start function $module.start$ is not empty, then:
 - a. Let $start$ be the start function $module.start$.
 - b. Execute the instruction `call $start.func$` .
16. Assert: due to `validation`, the frame F is now on the top of the stack.
17. Pop the frame F from the stack.

$$\begin{aligned}
\text{instantiate}(S, \text{module}, \text{externval}^k) &= S'; F; \text{runelem}_0(\text{elem}^n[0]) \dots \text{runelem}_{n-1}(\text{elem}^n[n-1]) \\
&\quad \text{rundata}_0(\text{data}^m[0]) \dots \text{rundata}_{m-1}(\text{data}^m[m-1]) \\
&\quad (\text{call } \text{start.func})? \\
&\quad (\text{if } \vdash \text{module} : \text{externtype}_{\text{im}}^k \rightarrow \text{externtype}_{\text{ex}}^* \\
&\quad \wedge (S' \vdash \text{externval} : \text{externtype})^k \\
&\quad \wedge (S' \vdash \text{externtype} \leq \text{clos}_{\text{moduleinst}}(\text{externtype}_{\text{im}}))^k \\
&\quad \wedge \text{module.globals} = \text{global}^* \\
&\quad \wedge \text{module.elems} = \text{elem}^n \\
&\quad \wedge \text{module.datas} = \text{data}^m \\
&\quad \wedge \text{module.start} = \text{start}? \\
&\quad \wedge (\text{expr}_g = \text{global.init})^* \\
&\quad \wedge (\text{expr}_t = \text{table.init})^* \\
&\quad \wedge (\text{expr}_e = \text{elem.init})^n \\
&\quad \wedge S', \text{moduleinst} = \text{allocmodule}(S, \text{module}, \text{externval}^k, \text{val}^*, (\text{ref}^*)^n) \\
&\quad \wedge F = \{\text{module } \text{moduleinst}, \text{locals } \epsilon\} \\
&\quad \wedge (S'; F; \text{expr}_g \hookrightarrow^* S'; F; \text{val}_g \text{ end})^* \\
&\quad \wedge (S'; F; \text{expr}_t \hookrightarrow^* S'; F; \text{ref}_t \text{ end})^* \\
&\quad \wedge ((S'; F; \text{expr}_e \hookrightarrow^* S'; F; \text{ref}_e \text{ end})^*)^n
\end{aligned}$$

where:

$$\begin{aligned}
\text{runelem}_i(\{\text{type } et, \text{init } \text{expr}^n, \text{mode passive}\}) &= \epsilon \\
\text{runelem}_i(\{\text{type } et, \text{init } \text{expr}^n, \text{mode active}\{\text{table } x, \text{offset } \text{instr}^* \text{ end}\}\}) &= \\
&\quad \text{instr}^* (\text{i32.const } 0) (\text{i32.const } n) (\text{table.init } x \ i) (\text{elem.drop } i) \\
\text{runelem}_i(\{\text{type } et, \text{init } \text{expr}^n, \text{mode declarative}\}) &= \\
&\quad (\text{elem.drop } i) \\
\text{rundata}_i(\{\text{init } b^n, \text{mode passive}\}) &= \epsilon \\
\text{rundata}_i(\{\text{init } b^n, \text{mode active}\{\text{memory } x, \text{offset } \text{instr}^* \text{ end}\}\}) &= \\
&\quad \text{instr}^* (\text{i32.const } 0) (\text{i32.const } n) (\text{memory.init } x \ i) (\text{data.drop } i)
\end{aligned}$$

Note: Checking import types assumes that the `module` instance has already been `allocated` to compute the respective `closed defined types`. However, this forward reference merely is a way to simplify the specification. In practice, implementations will likely allocate or canonicalize types beforehand, when *compiling* a module, in a stage before instantiation and before imports are checked.

Similarly, module `allocation` and the `evaluation` of `global` and `table` initializers as well as `element segments` are mutually recursive because the global initialization values val_g^* , ref_t , and element segment contents $(\text{ref}^*)^*$ are passed to the module allocator while depending on the module instance `moduleinst` and store S' returned by allocation. Again, this recursion is just a specification device. In practice, the initialization values can be `determined` beforehand by staging module allocation such that first, the module's own `function instances` are pre-allocated in the store, then the initializer expressions are evaluated in order, allocating globals on the way, then the rest of the module instance is allocated, and finally the new function instances' `module` fields are set to that module instance. This is possible because `validation` ensures that initialization expressions cannot actually call a function, only take their reference.

All failure conditions are checked before any observable mutation of the store takes place. Store mutation is not atomic; it happens in individual steps that may be interleaved with other threads.

`Evaluation of constant expressions` does not affect the store.

4.7.3 Invocation

Once a **module** has been **instantiated**, any exported function can be *invoked* externally via its **function address** $funcaddr$ in the **store** S and an appropriate list val^* of argument **values**.

Invocation may *fail* with an error if the arguments do not fit the **function type**. Invocation can also result in an **exception** or **trap**. It is up to the **embedder** to define how such conditions are reported.

Note: If the **embedder** API performs type checks itself, either statically or dynamically, before performing an invocation, then no failure other than traps or exceptions can occur.

The following steps are performed:

1. Assert: $S.funcs[funcaddr]$ exists.
2. Let $funcinst$ be the **function instance** $S.funcs[funcaddr]$.
3. Let $func [t_1^n] \rightarrow [t_2^m]$ be the **composite type** $expand(funcinst.type)$.
4. If the length $|val^*|$ of the provided argument values is different from the number n of expected arguments, then:
 - a. Fail.
5. For each **value type** t_i in t_1^n and corresponding **value** val_i in val^* , do:
 - a. If val_i is not **valid** with value type t_i , then:
 - i. Fail.
6. Let F be the dummy **frame** $\{\text{module } \{\}, \text{locals } \epsilon\}$.
7. Push the frame F to the stack.
8. Push the values val^* to the stack.
9. **Invoke** the function instance at address $funcaddr$.

Once the function has returned, the following steps are executed:

1. Assert: due to **validation**, m **values** are on the top of the stack.
2. Pop val_{res}^m from the stack.
3. Assert: due to **validation**, the frame F is now on the top of the stack.
4. Pop the frame F from the stack.

The values val_{res}^m are returned as the results of the invocation.

$$\begin{aligned}
 \text{invoke}(S, funcaddr, val^n) &= S; F; val^n \text{ (invoke } funcaddr) \\
 &\text{(if } expand(S.funcs[funcaddr].type) = func [t_1^n] \rightarrow [t_2^m] \\
 &\quad \wedge (S \vdash val : t_1)^n \\
 &\quad \wedge F = \{\text{module } \{\}, \text{locals } \epsilon\})
 \end{aligned}$$

5.1 Conventions

The binary format for WebAssembly **modules** is a dense linear *encoding* of their **abstract syntax**.²⁹

The format is defined by an *attribute grammar* whose only terminal symbols are **bytes**. A byte sequence is a well-formed encoding of a module if and only if it is generated by the grammar.

Each production of this grammar has exactly one synthesized attribute: the abstract syntax that the respective byte sequence encodes. Thus, the attribute grammar implicitly defines a *decoding* function (i.e., a parsing function for the binary format).

Except for a few exceptions, the binary grammar closely mirrors the grammar of the abstract syntax.

Note: Some phrases of abstract syntax have multiple possible encodings in the binary format. For example, numbers may be encoded as if they had optional leading zeros. Implementations of decoders must support all possible alternatives; implementations of encoders can pick any allowed encoding.

The recommended extension for files containing WebAssembly modules in binary format is “.wasm” and the recommended **Media Type**²⁸ is “application/wasm”.

5.1.1 Grammar

The following conventions are adopted in defining grammar rules for the binary format. They mirror the conventions used for **abstract syntax**. In order to distinguish symbols of the binary syntax from symbols of the abstract syntax, **typewriter** font is adopted for the former.

- Terminal symbols are **bytes** expressed in hexadecimal notation: 0x0F.
- Nonterminal symbols are written in typewriter font: `valtype`, `instr`.
- B^n is a sequence of $n \geq 0$ iterations of B .
- B^* is a possibly empty sequence of iterations of B . (This is a shorthand for B^n used where n is not relevant.)

²⁹ Additional encoding layers – for example, introducing compression – may be defined on top of the basic representation defined here. However, such layers are outside the scope of the current specification.

²⁸ <https://www.iana.org/assignments/media-types/media-types.xhtml>

- $B^?$ is an optional occurrence of B . (This is a shorthand for B^n where $n \leq 1$.)
- $x:B$ denotes the same language as the nonterminal B , but also binds the variable x to the attribute synthesized for B . A pattern may also be used instead of a variable, e.g., $7:B$.
- Productions are written $\text{sym} ::= B_1 \Rightarrow A_1 \mid \dots \mid B_n \Rightarrow A_n$, where each A_i is the attribute that is synthesized for sym in the given case, usually from attribute variables bound in B_i .
- Some productions are augmented by side conditions in parentheses, which restrict the applicability of the production. They provide a shorthand for a combinatorial expansion of the production into many separate cases.
- If the same meta variable or non-terminal symbol appears multiple times in a production (in the syntax or in an attribute), then all those occurrences must have the same instantiation. (This is a shorthand for a side condition requiring multiple different variables to be equal.)

Note: For example, the [binary grammar](#) for [number types](#) is given as follows:

$$\begin{array}{llll} \text{numtype} & ::= & 0x7F & \Rightarrow & i32 \\ & & | & & 0x7E & \Rightarrow & i64 \\ & & | & & 0x7D & \Rightarrow & f32 \\ & & | & & 0x7C & \Rightarrow & f64 \end{array}$$

Consequently, the byte 0x7F encodes the type [i32](#), 0x7E encodes the type [i64](#), and so forth. No other byte value is allowed as the encoding of a number type.

The [binary grammar](#) for [limits](#) is defined as follows:

$$\begin{array}{llll} \text{limits} & ::= & 0x00 \ n:\text{u32} & \Rightarrow & \{\min n, \max \epsilon\} \\ & & | & & 0x01 \ n:\text{u32} \ m:\text{u32} & \Rightarrow & \{\min n, \max m\} \end{array}$$

That is, a limits pair is encoded as either the byte 0x00 followed by the encoding of a [u32](#) value, or the byte 0x01 followed by two such encodings. The variables n and m name the attributes of the respective [u32](#) nonterminals, which in this case are the actual [unsigned integers](#) those decode into. The attribute of the complete production then is the abstract syntax for the limit, expressed in terms of the former values.

5.1.2 Auxiliary Notation

When dealing with binary encodings the following notation is also used:

- ϵ denotes the empty byte sequence.
- $||B||$ is the length of the byte sequence generated from the production B in a derivation.

5.1.3 Vectors

[Vectors](#) are encoded with their [u32](#) length followed by the encoding of their element sequence.

$$\text{vec}(B) ::= n:\text{u32} \ (x:B)^n \Rightarrow x^n$$

5.2 Values

5.2.1 Bytes

Bytes encode themselves.

$$\begin{array}{lcl} \text{byte} & ::= & 0x00 \Rightarrow 0x00 \\ & | & \dots \\ & | & 0xFF \Rightarrow 0xFF \end{array}$$

5.2.2 Integers

All integers are encoded using the [LEB128](#)³⁰ variable-length integer encoding, in either unsigned or signed variant.

Unsigned integers are encoded in [unsigned LEB128](#)³¹ format. As an additional constraint, the total number of bytes encoding a value of type uN must not exceed $\text{ceil}(N/7)$ bytes.

$$\begin{array}{lcl} uN & ::= & n:\text{byte} \Rightarrow n \quad (\text{if } n < 2^7 \wedge n < 2^N) \\ & | & n:\text{byte } m:\text{u}(N-7) \Rightarrow 2^7 \cdot m + (n - 2^7) \quad (\text{if } n \geq 2^7 \wedge N > 7) \end{array}$$

Signed integers are encoded in [signed LEB128](#)³² format, which uses a two's complement representation. As an additional constraint, the total number of bytes encoding a value of type sN must not exceed $\text{ceil}(N/7)$ bytes.

$$\begin{array}{lcl} sN & ::= & n:\text{byte} \Rightarrow n \quad (\text{if } n < 2^6 \wedge n < 2^{N-1}) \\ & | & n:\text{byte} \Rightarrow n - 2^7 \quad (\text{if } 2^6 \leq n < 2^7 \wedge n \geq 2^7 - 2^{N-1}) \\ & | & n:\text{byte } m:\text{s}(N-7) \Rightarrow 2^7 \cdot m + (n - 2^7) \quad (\text{if } n \geq 2^7 \wedge N > 7) \end{array}$$

Uninterpreted integers are encoded as signed integers.

$$iN ::= n:sN \Rightarrow i \quad (\text{if } n = \text{signed}_N(i))$$

Note: The side conditions $N > 7$ in the productions for non-terminal bytes of the u and s encodings restrict the encoding's length. However, “trailing zeros” are still allowed within these bounds. For example, `0x03` and `0x83 0x00` are both well-formed encodings for the value 3 as a $u8$. Similarly, either of `0x7e` and `0xFE 0x7F` and `0xFE 0xFF 0x7F` are well-formed encodings of the value -2 as a $s16$.

The side conditions on the value n of terminal bytes further enforce that any unused bits in these bytes must be 0 for positive values and 1 for negative ones. For example, `0x83 0x10` is malformed as a $u8$ encoding. Similarly, both `0x83 0x3E` and `0xFF 0x7B` are malformed as $s8$ encodings.

5.2.3 Floating-Point

Floating-point values are encoded directly by their [IEEE 754](#)³³ (Section 3.4) bit pattern in [little endian](#)³⁴ byte order:

$$fN ::= b*:\text{byte}^{N/8} \Rightarrow \text{bytes}_{fN}^{-1}(b*)$$

³⁰ <https://en.wikipedia.org/wiki/LEB128>

³¹ https://en.wikipedia.org/wiki/LEB128#Unsigned_LEB128

³² https://en.wikipedia.org/wiki/LEB128#Signed_LEB128

³³ <https://ieeexplore.ieee.org/document/8766229>

³⁴ <https://en.wikipedia.org/wiki/Endianness#Little-endian>

5.2.4 Names

Names are encoded as a **vector** of bytes containing the **Unicode**³⁵ (Section 3.9) UTF-8 encoding of the name's character sequence.

$$\text{name} ::= b^*.\text{vec}(\text{byte}) \Rightarrow \text{name} \quad (\text{if } \text{utf8}(\text{name}) = b^*)$$

The auxiliary **utf8** function expressing this encoding is defined as follows:

$$\begin{aligned} \text{utf8}(c^*) &= (\text{utf8}(c))^* \\ \text{utf8}(c) &= b && (\text{if } c < \text{U}+80 \\ &&& \wedge c = b) \\ \text{utf8}(c) &= b_1 b_2 && (\text{if } \text{U}+80 \leq c < \text{U}+800 \\ &&& \wedge c = 2^6(b_1 - 0\text{x}C0) + (b_2 - 0\text{x}80)) \\ \text{utf8}(c) &= b_1 b_2 b_3 && (\text{if } \text{U}+800 \leq c < \text{U}+\text{D}800 \vee \text{U}+\text{E}000 \leq c < \text{U}+10000 \\ &&& \wedge c = 2^{12}(b_1 - 0\text{x}E0) + 2^6(b_2 - 0\text{x}80) + (b_3 - 0\text{x}80)) \\ \text{utf8}(c) &= b_1 b_2 b_3 b_4 && (\text{if } \text{U}+10000 \leq c < \text{U}+110000 \\ &&& \wedge c = 2^{18}(b_1 - 0\text{x}F0) + 2^{12}(b_2 - 0\text{x}80) + 2^6(b_3 - 0\text{x}80) + (b_4 - 0\text{x}80)) \\ &&& \text{where } b_2, b_3, b_4 < 0\text{x}C0 \end{aligned}$$

Note: Unlike in some other formats, name strings are not 0-terminated.

5.3 Types

Note: In some places, possible types include both type constructors or types denoted by **type indices**. Thus, the binary format for type constructors corresponds to the encodings of small negative *sN* values, such that they can unambiguously occur in the same place as (positive) type indices.

5.3.1 Number Types

Number types are encoded by a single byte.

$$\begin{array}{lll} \text{numtype} & ::= & 0\text{x}7\text{F} \Rightarrow \text{i}32 \\ & | & 0\text{x}7\text{E} \Rightarrow \text{i}64 \\ & | & 0\text{x}7\text{D} \Rightarrow \text{f}32 \\ & | & 0\text{x}7\text{C} \Rightarrow \text{f}64 \end{array}$$

5.3.2 Vector Types

Vector types are also encoded by a single byte.

$$\text{vectype} ::= 0\text{x}7\text{B} \Rightarrow \text{v}128$$

³⁵ <https://www.unicode.org/versions/latest/>

5.3.3 Heap Types

Heap types are encoded as either a single byte, or as a [type index](#) encoded as a positive [signed integer](#).

absheaptypes	::=	0x74	⇒	noexn
		0x73	⇒	nofunc
		0x72	⇒	noextern
		0x71	⇒	none
		0x70	⇒	func
		0x6F	⇒	extern
		0x6E	⇒	any
		0x6D	⇒	eq
		0x6C	⇒	i31
		0x6B	⇒	struct
		0x6A	⇒	array
		0x69	⇒	exn
heaptypes	::=	ht:absheaptypes	⇒	ht
		x:s33	⇒	x (if $x \geq 0$)

5.3.4 Reference Types

Reference types are either encoded by a single byte followed by a [heap type](#), or, as a short form, directly as an abstract heap type.

reftypes	::=	0x64 ht:heaptypes	⇒	ref ht
		0x63 ht:heaptypes	⇒	ref null ht
		ht:absheaptypes	⇒	ref null ht

5.3.5 Value Types

Value types are encoded with their respective encoding as a [number type](#), [vector type](#), or [reference type](#).

valtypes	::=	t:numtypes	⇒	t
		t:vectypes	⇒	t
		t:reftypes	⇒	t

Note: The type `bot` cannot occur in a module.

Value types can occur in contexts where [type indices](#) are also allowed, such as in the case of [block types](#). Thus, the binary format for types corresponds to the [signed LEB128](#)³⁶ encoding of small negative sN values, so that they can coexist with (positive) type indices in the future.

5.3.6 Result Types

Result types are encoded by the respective [vectors](#) of [value types](#).

resulttypes	::=	t*:vec(valtypes)	⇒	[t*]
-------------	-----	------------------	---	------

³⁶ https://en.wikipedia.org/wiki/LEB128#Signed_LEB128

5.3.7 Function Types

Function types are encoded by the respective [vectors](#) of parameter and result types.

$$\text{functype} ::= rt_1:\text{resulttype } rt_2:\text{resulttype} \Rightarrow rt_1 \rightarrow rt_2$$

5.3.8 Aggregate Types

Aggregate types are encoded with their respective field types.

<code>arraytype</code>	<code>::=</code>	<code>ft:fieldtype</code>	<code>=></code>	<code>ft</code>
<code>structtype</code>	<code>::=</code>	<code>ft*:vec(fieldtype)</code>	<code>=></code>	<code>ft*</code>
<code>fieldtype</code>	<code>::=</code>	<code>st:storagetype m:mut</code>	<code>=></code>	<code>m st</code>
<code>storagetype</code>	<code>::=</code>	<code>t:valtype</code>	<code>=></code>	<code>t</code>
		<code> t:packedtype</code>	<code>=></code>	<code>t</code>
<code>packedtype</code>	<code>::=</code>	<code>0x78</code>	<code>=></code>	<code>i8</code>
		<code> 0x77</code>	<code>=></code>	<code>i16</code>

5.3.9 Composite Types

Composite types are encoded by a distinct byte followed by a type encoding of the respective form.

<code>comptype</code>	<code>::=</code>	<code>0x5E at:arraytype</code>	<code>=></code>	<code>array at</code>
		<code> 0x5F st:structtype</code>	<code>=></code>	<code>struct st</code>
		<code> 0x60 ft:functype</code>	<code>=></code>	<code>func ft</code>

5.3.10 Recursive Types

Recursive types are encoded by the byte 0x4E followed by a [vector](#) of sub types. Additional shorthands are recognized for unary recursions and sub types without super types.

<code>rectype</code>	<code>::=</code>	<code>0x4E st*:vec(subtype)</code>	<code>=></code>	<code>rec st*</code>
		<code> st:subtype</code>	<code>=></code>	<code>rec st</code>
<code>subtype</code>	<code>::=</code>	<code>0x50 x*:vec(typeidx) ct:comptype</code>	<code>=></code>	<code>sub x* ct</code>
		<code> 0x4F x*:vec(typeidx) ct:comptype</code>	<code>=></code>	<code>sub final x* ct</code>
		<code> ct:comptype</code>	<code>=></code>	<code>sub final ϵ ct</code>

5.3.11 Limits

Limits are encoded with a preceding flag indicating whether a maximum is present, and a flag for the [address type](#).

<code>limits</code>	<code>::=</code>	<code>0x00 n:u64</code>	<code>=></code>	<code>(i32, {min n, max ϵ})</code>
		<code> 0x01 n:u64 m:u64</code>	<code>=></code>	<code>(i32, {min n, max m})</code>
		<code> 0x04 n:u64</code>	<code>=></code>	<code>(i64, {min n, max ϵ})</code>
		<code> 0x05 n:u64 m:u64</code>	<code>=></code>	<code>(i64, {min n, max m})</code>

5.3.12 Memory Types

Memory types are encoded with their [limits](#).

$$\text{memtype} ::= (at, lim):limits \Rightarrow at\ lim$$

5.3.13 Table Types

Table types are encoded with their [limits](#) and the encoding of their element [reference type](#).

$$\text{tabletype} ::= et:\text{reftype}\ (at, lim):limits \Rightarrow at\ lim\ et$$

5.3.14 Global Types

Global types are encoded by their [value type](#) and a flag for their [mutability](#).

<code>globaltype</code>	<code>::=</code>	<code>t:valtype m:mut</code>	<code>\Rightarrow</code>	<code>m t</code>
<code>mut</code>	<code>::=</code>	<code>0x00</code>	<code>\Rightarrow</code>	<code>const</code>
		<code> 0x01</code>	<code>\Rightarrow</code>	<code>var</code>

5.3.15 Tag Types

Tag types are encoded by a [type index](#) denoting a [function type](#).

$$\text{tagtype} ::= 0x00\ x:\text{typeid}\ x \Rightarrow x$$

Note: In future versions of WebAssembly, the preceding zero byte may encode additional flags.

5.4 Instructions

[Instructions](#) are encoded by *opcodes*. Each opcode is represented by a single byte, and is followed by the instruction's immediate arguments, where present. The only exception are [structured control instructions](#), which consist of several opcodes bracketing their nested instruction sequences.

Note: Gaps in the byte code ranges for encoding instructions are reserved for future extensions.

5.4.1 Control Instructions

[Control instructions](#) have varying encodings. For structured instructions, the instruction sequences forming nested blocks are delimited with explicit opcodes for `end` and `else`.

Block types are encoded in special compressed form, by either the byte 0x40 indicating the empty type, as a single value type, or as a [type index](#) encoded as a positive [signed integer](#).

	<code>blocktype</code>	<code>::=</code>	<code>0x40</code>	\Rightarrow	ϵ
			<code> </code>	\Rightarrow	t
			<code> </code>	\Rightarrow	x (if $x \geq 0$)
	<code>instr</code>	<code>::=</code>	<code>0x00</code>	\Rightarrow	unreachable
			<code> </code>	\Rightarrow	nop
			<code> </code>	\Rightarrow	block bt in^* end
			<code> </code>	\Rightarrow	loop bt in^* end
			<code> </code>	\Rightarrow	if bt in^* else ϵ
			<code> </code>	\Rightarrow	if bt in_1^* else in_2^*
			<code> </code>	\Rightarrow	throw x
			<code> </code>	\Rightarrow	throw_ref
			<code> </code>	\Rightarrow	br l
			<code> </code>	\Rightarrow	br_if l
			<code> </code>	\Rightarrow	br_table l^* l_N
			<code> </code>	\Rightarrow	return
			<code> </code>	\Rightarrow	call x
			<code> </code>	\Rightarrow	call_indirect x
			<code> </code>	\Rightarrow	return_call x
			<code> </code>	\Rightarrow	return_call_indirect x
			<code> </code>	\Rightarrow	call_ref x
			<code> </code>	\Rightarrow	return_call_ref x
			<code> </code>	\Rightarrow	try_table bt c^*
<code>0xD5</code>	<code>l:labelidx</code>	\Rightarrow	<code>br_on_null l</code>		
			<code> </code>	\Rightarrow	br_on_non_null l
			<code> </code>	\Rightarrow	br_on_cast l (r)
			<code> </code>	\Rightarrow	br_on_cast_fail l
	<code>catch</code>	<code>::=</code>	<code>0x00 x:tagidx l:labelidx</code>	\Rightarrow	catch x l
			<code> </code>	\Rightarrow	catch_ref x l
			<code> </code>	\Rightarrow	catch_all l
			<code> </code>	\Rightarrow	catch_all_ref l
	<code>castflags</code>	<code>::=</code>	<code>0:u8</code>	\Rightarrow	(ϵ, ϵ)
			<code> </code>	\Rightarrow	(null, ϵ)
			<code> </code>	\Rightarrow	(ϵ, null)
			<code> </code>	\Rightarrow	$(\text{null}, \text{null})$

Note: The `else` opcode `0x05` in the encoding of an `if` instruction can be omitted if the following instruction sequence is empty.

Unlike any other occurrence, the `type index` in a `block type` is encoded as a positive signed integer, so that its signed LEB128 bit pattern cannot collide with the encoding of `value types` or the special code `0x40`, which correspond to the LEB128 encoding of negative integers. To avoid any loss in the range of allowed indices, it is treated as a 33 bit signed integer.

5.4.2 Reference Instructions

Generic [reference instructions](#) are represented by single byte codes, others use prefixes and type operands.

<code>instr ::= ...</code>		
<code>0xD0 <i>t</i>:heaptype</code>	<code>⇒</code>	<code>ref.null <i>t</i></code>
<code>0xD1</code>	<code>⇒</code>	<code>ref.is_null</code>
<code>0xD2 <i>x</i>:funcidx</code>	<code>⇒</code>	<code>ref.func <i>x</i></code>
<code>0xD3</code>	<code>⇒</code>	<code>ref.eq</code>
<code>0xD4</code>	<code>⇒</code>	<code>ref.as_non_null</code>
<code>0xFB 0:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>struct.new <i>x</i></code>
<code>0xFB 1:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>struct.new_default <i>x</i></code>
<code>0xFB 2:u32 <i>x</i>:typeidx <i>y</i>:fieldidx</code>	<code>⇒</code>	<code>struct.get <i>x y</i></code>
<code>0xFB 3:u32 <i>x</i>:typeidx <i>y</i>:fieldidx</code>	<code>⇒</code>	<code>struct.get_s <i>x y</i></code>
<code>0xFB 4:u32 <i>x</i>:typeidx <i>y</i>:fieldidx</code>	<code>⇒</code>	<code>struct.get_u <i>x y</i></code>
<code>0xFB 5:u32 <i>x</i>:typeidx <i>y</i>:fieldidx</code>	<code>⇒</code>	<code>struct.set <i>x y</i></code>
<code>0xFB 6:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.new <i>x</i></code>
<code>0xFB 7:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.new_default <i>x</i></code>
<code>0xFB 8:u32 <i>x</i>:typeidx <i>n</i>:u32</code>	<code>⇒</code>	<code>array.new_fixed <i>x n</i></code>
<code>0xFB 9:u32 <i>x</i>:typeidx <i>y</i>:dataidx</code>	<code>⇒</code>	<code>array.new_data <i>x y</i></code>
<code>0xFB 10:u32 <i>x</i>:typeidx <i>y</i>:elemidx</code>	<code>⇒</code>	<code>array.new_elem <i>x y</i></code>
<code>0xFB 11:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.get <i>x</i></code>
<code>0xFB 12:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.get_s <i>x</i></code>
<code>0xFB 13:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.get_u <i>x</i></code>
<code>0xFB 14:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.set <i>x</i></code>
<code>0xFB 15:u32</code>	<code>⇒</code>	<code>array.len</code>
<code>0xFB 16:u32 <i>x</i>:typeidx</code>	<code>⇒</code>	<code>array.fill <i>x</i></code>
<code>0xFB 17:u32 <i>x</i>:typeidx <i>y</i>:typeidx</code>	<code>⇒</code>	<code>array.copy <i>x y</i></code>
<code>0xFB 18:u32 <i>x</i>:typeidx <i>y</i>:dataidx</code>	<code>⇒</code>	<code>array.init_data <i>x y</i></code>
<code>0xFB 19:u32 <i>x</i>:typeidx <i>y</i>:elemidx</code>	<code>⇒</code>	<code>array.init_elem <i>x y</i></code>
<code>0xFB 20:u32 <i>ht</i>:heaptype</code>	<code>⇒</code>	<code>ref.test (ref <i>ht</i>)</code>
<code>0xFB 21:u32 <i>ht</i>:heaptype</code>	<code>⇒</code>	<code>ref.test (ref null <i>ht</i>)</code>
<code>0xFB 22:u32 <i>ht</i>:heaptype</code>	<code>⇒</code>	<code>ref.cast (ref <i>ht</i>)</code>
<code>0xFB 23:u32 <i>ht</i>:heaptype</code>	<code>⇒</code>	<code>ref.cast (ref null <i>ht</i>)</code>
<code>0xFB 26:u32</code>	<code>⇒</code>	<code>any.convert_extern</code>
<code>0xFB 27:u32</code>	<code>⇒</code>	<code>extern.convert_any</code>
<code>0xFB 28:u32</code>	<code>⇒</code>	<code>ref.i31</code>
<code>0xFB 29:u32</code>	<code>⇒</code>	<code>i31.get_s</code>
<code>0xFB 30:u32</code>	<code>⇒</code>	<code>i31.get_u</code>

5.4.3 Parametric Instructions

[Parametric instructions](#) are represented by single byte codes, possibly followed by a type annotation.

<code>instr ::= ...</code>		
<code>0x1A</code>	<code>⇒</code>	<code>drop</code>
<code>0x1B</code>	<code>⇒</code>	<code>select</code>
<code>0x1C <i>t</i>*:vec(valtype)</code>	<code>⇒</code>	<code>select <i>t</i>*</code>

5.4.4 Variable Instructions

Variable instructions are represented by byte codes followed by the encoding of the respective [index](#).

```
instr ::= ...
      | 0x20 x:localidx    ⇒ local.get x
      | 0x21 x:localidx    ⇒ local.set x
      | 0x22 x:localidx    ⇒ local.tee x
      | 0x23 x:globalidx   ⇒ global.get x
      | 0x24 x:globalidx   ⇒ global.set x
```

5.4.5 Table Instructions

Table instructions are represented either by a single byte or a one byte prefix followed by a variable-length [unsigned integer](#).

```
instr ::= ...
      | 0x25 x:tableidx                ⇒ table.get x
      | 0x26 x:tableidx                ⇒ table.set x
      | 0xFC 12:u32 y:elemidx x:tableidx ⇒ table.init x y
      | 0xFC 13:u32 x:elemidx           ⇒ elem.drop x
      | 0xFC 14:u32 x:tableidx y:tableidx ⇒ table.copy x y
      | 0xFC 15:u32 x:tableidx          ⇒ table.grow x
      | 0xFC 16:u32 x:tableidx          ⇒ table.size x
      | 0xFC 17:u32 x:tableidx          ⇒ table.fill x
```

5.4.6 Memory Instructions

Each variant of [memory instruction](#) is encoded with a different byte code. Loads and stores are followed by the encoding of their [memory immediate](#), which includes the [memory index](#) if bit 6 of the flags field containing alignment is set; the memory index defaults to 0 otherwise.

<code>memarg</code>	<code>::= a:u32 o:u64</code>	$\Rightarrow 0 \{\text{align } a, \text{ offset } o\}$	(if $a < 2^6$)
	<code> a:u32 x:memidx o:u64</code>	$\Rightarrow x \{\text{align } (a - 2^6), \text{ offset } o\}$	(if $2^6 \leq a < 2^7$)
<code>instr</code>	<code>::= ...</code>		
	<code> 0x28 m:memarg</code>	\Rightarrow	<code>i32.load m</code>
	<code> 0x29 m:memarg</code>	\Rightarrow	<code>i64.load m</code>
	<code> 0x2A m:memarg</code>	\Rightarrow	<code>f32.load m</code>
	<code> 0x2B m:memarg</code>	\Rightarrow	<code>f64.load m</code>
	<code> 0x2C m:memarg</code>	\Rightarrow	<code>i32.load8_s m</code>
	<code> 0x2D m:memarg</code>	\Rightarrow	<code>i32.load8_u m</code>
	<code> 0x2E m:memarg</code>	\Rightarrow	<code>i32.load16_s m</code>
	<code> 0x2F m:memarg</code>	\Rightarrow	<code>i32.load16_u m</code>
	<code> 0x30 m:memarg</code>	\Rightarrow	<code>i64.load8_s m</code>
	<code> 0x31 m:memarg</code>	\Rightarrow	<code>i64.load8_u m</code>
	<code> 0x32 m:memarg</code>	\Rightarrow	<code>i64.load16_s m</code>
	<code> 0x33 m:memarg</code>	\Rightarrow	<code>i64.load16_u m</code>
	<code> 0x34 m:memarg</code>	\Rightarrow	<code>i64.load32_s m</code>
	<code> 0x35 m:memarg</code>	\Rightarrow	<code>i64.load32_u m</code>
	<code> 0x36 m:memarg</code>	\Rightarrow	<code>i32.store m</code>
	<code> 0x37 m:memarg</code>	\Rightarrow	<code>i64.store m</code>
	<code> 0x38 m:memarg</code>	\Rightarrow	<code>f32.store m</code>
	<code> 0x39 m:memarg</code>	\Rightarrow	<code>f64.store m</code>
	<code> 0x3A m:memarg</code>	\Rightarrow	<code>i32.store8 m</code>
	<code> 0x3B m:memarg</code>	\Rightarrow	<code>i32.store16 m</code>
	<code> 0x3C m:memarg</code>	\Rightarrow	<code>i64.store8 m</code>
	<code> 0x3D m:memarg</code>	\Rightarrow	<code>i64.store16 m</code>
	<code> 0x3E m:memarg</code>	\Rightarrow	<code>i64.store32 m</code>
	<code> 0x3F x:memidx</code>	\Rightarrow	<code>memory.size x</code>
	<code> 0x40 x:memidx</code>	\Rightarrow	<code>memory.grow x</code>
	<code> 0xFC 8:u32 y:dataidx x:memidx</code>	\Rightarrow	<code>memory.init x y</code>
	<code> 0xFC 9:u32 x:dataidx</code>	\Rightarrow	<code>data.drop x</code>
	<code> 0xFC 10:u32 x:memidx y:memidx</code>	\Rightarrow	<code>memory.copy x y</code>
	<code> 0xFC 11:u32 x:memidx</code>	\Rightarrow	<code>memory.fill x</code>

5.4.7 Numeric Instructions

All variants of [numeric instructions](#) are represented by separate byte codes.

The [const](#) instructions are followed by the respective literal.

<code>instr</code>	<code>::= ...</code>		
	<code> 0x41 n:i32</code>	\Rightarrow	<code>i32.const n</code>
	<code> 0x42 n:i64</code>	\Rightarrow	<code>i64.const n</code>
	<code> 0x43 z:f32</code>	\Rightarrow	<code>f32.const z</code>
	<code> 0x44 z:f64</code>	\Rightarrow	<code>f64.const z</code>

All other numeric instructions are plain opcodes without any immediates.

```
instr ::= ...  
      | 0x45 ⇒ i32.eqz  
      | 0x46 ⇒ i32.eq  
      | 0x47 ⇒ i32.ne  
      | 0x48 ⇒ i32.lt_s  
      | 0x49 ⇒ i32.lt_u  
      | 0x4A ⇒ i32.gt_s  
      | 0x4B ⇒ i32.gt_u  
      | 0x4C ⇒ i32.le_s  
      | 0x4D ⇒ i32.le_u  
      | 0x4E ⇒ i32.ge_s  
      | 0x4F ⇒ i32.ge_u  
  
      | 0x50 ⇒ i64.eqz  
      | 0x51 ⇒ i64.eq  
      | 0x52 ⇒ i64.ne  
      | 0x53 ⇒ i64.lt_s  
      | 0x54 ⇒ i64.lt_u  
      | 0x55 ⇒ i64.gt_s  
      | 0x56 ⇒ i64.gt_u  
      | 0x57 ⇒ i64.le_s  
      | 0x58 ⇒ i64.le_u  
      | 0x59 ⇒ i64.ge_s  
      | 0x5A ⇒ i64.ge_u  
  
      | 0x5B ⇒ f32.eq  
      | 0x5C ⇒ f32.ne  
      | 0x5D ⇒ f32.lt  
      | 0x5E ⇒ f32.gt  
      | 0x5F ⇒ f32.le  
      | 0x60 ⇒ f32.ge  
  
      | 0x61 ⇒ f64.eq  
      | 0x62 ⇒ f64.ne  
      | 0x63 ⇒ f64.lt  
      | 0x64 ⇒ f64.gt  
      | 0x65 ⇒ f64.le  
      | 0x66 ⇒ f64.ge  
  
      | 0x67 ⇒ i32.clz  
      | 0x68 ⇒ i32.ctz  
      | 0x69 ⇒ i32.popcnt  
      | 0x6A ⇒ i32.add  
      | 0x6B ⇒ i32.sub  
      | 0x6C ⇒ i32.mul  
      | 0x6D ⇒ i32.div_s  
      | 0x6E ⇒ i32.div_u  
      | 0x6F ⇒ i32.rem_s  
      | 0x70 ⇒ i32.rem_u  
      | 0x71 ⇒ i32.and  
      | 0x72 ⇒ i32.or  
      | 0x73 ⇒ i32.xor  
      | 0x74 ⇒ i32.shl  
      | 0x75 ⇒ i32.shr_s  
      | 0x76 ⇒ i32.shr_u  
      | 0x77 ⇒ i32.rotl  
      | 0x78 ⇒ i32.rotr
```

0x79	⇒	i64.clz
0x7A	⇒	i64.ctz
0x7B	⇒	i64.popcnt
0x7C	⇒	i64.add
0x7D	⇒	i64.sub
0x7E	⇒	i64.mul
0x7F	⇒	i64.div_s
0x80	⇒	i64.div_u
0x81	⇒	i64.rem_s
0x82	⇒	i64.rem_u
0x83	⇒	i64.and
0x84	⇒	i64.or
0x85	⇒	i64.xor
0x86	⇒	i64.shl
0x87	⇒	i64.shr_s
0x88	⇒	i64.shr_u
0x89	⇒	i64.rotl
0x8A	⇒	i64.rotr
0x8B	⇒	f32.abs
0x8C	⇒	f32.neg
0x8D	⇒	f32.ceil
0x8E	⇒	f32.floor
0x8F	⇒	f32.trunc
0x90	⇒	f32.nearest
0x91	⇒	f32.sqrt
0x92	⇒	f32.add
0x93	⇒	f32.sub
0x94	⇒	f32.mul
0x95	⇒	f32.div
0x96	⇒	f32.min
0x97	⇒	f32.max
0x98	⇒	f32.copysign
0x99	⇒	f64.abs
0x9A	⇒	f64.neg
0x9B	⇒	f64.ceil
0x9C	⇒	f64.floor
0x9D	⇒	f64.trunc
0x9E	⇒	f64.nearest
0x9F	⇒	f64.sqrt
0xA0	⇒	f64.add
0xA1	⇒	f64.sub
0xA2	⇒	f64.mul
0xA3	⇒	f64.div
0xA4	⇒	f64.min
0xA5	⇒	f64.max
0xA6	⇒	f64.copysign

	0xA7	⇒	i32.wrap_i64
	0xA8	⇒	i32.trunc_f32_s
	0xA9	⇒	i32.trunc_f32_u
	0xAA	⇒	i32.trunc_f64_s
	0xAB	⇒	i32.trunc_f64_u
	0xAC	⇒	i64.extend_i32_s
	0xAD	⇒	i64.extend_i32_u
	0xAE	⇒	i64.trunc_f32_s
	0xAF	⇒	i64.trunc_f32_u
	0xB0	⇒	i64.trunc_f64_s
	0xB1	⇒	i64.trunc_f64_u
	0xB2	⇒	f32.convert_i32_s
	0xB3	⇒	f32.convert_i32_u
	0xB4	⇒	f32.convert_i64_s
	0xB5	⇒	f32.convert_i64_u
	0xB6	⇒	f32.demote_f64
	0xB7	⇒	f64.convert_i32_s
	0xB8	⇒	f64.convert_i32_u
	0xB9	⇒	f64.convert_i64_s
	0xBA	⇒	f64.convert_i64_u
	0xBB	⇒	f64.promote_f32
	0xBC	⇒	i32.reinterpret_f32
	0xBD	⇒	i64.reinterpret_f64
	0xBE	⇒	f32.reinterpret_i32
	0xBF	⇒	f64.reinterpret_i64
	0xC0	⇒	i32.extend8_s
	0xC1	⇒	i32.extend16_s
	0xC2	⇒	i64.extend8_s
	0xC3	⇒	i64.extend16_s
	0xC4	⇒	i64.extend32_s

The saturating truncation instructions all have a one byte prefix, whereas the actual opcode is encoded by a variable-length [unsigned integer](#).

```
instr ::= ...
| 0xFC 0:u32 ⇒ i32.trunc_sat_f32_s
| 0xFC 1:u32 ⇒ i32.trunc_sat_f32_u
| 0xFC 2:u32 ⇒ i32.trunc_sat_f64_s
| 0xFC 3:u32 ⇒ i32.trunc_sat_f64_u
| 0xFC 4:u32 ⇒ i64.trunc_sat_f32_s
| 0xFC 5:u32 ⇒ i64.trunc_sat_f32_u
| 0xFC 6:u32 ⇒ i64.trunc_sat_f64_s
| 0xFC 7:u32 ⇒ i64.trunc_sat_f64_u
```

5.4.8 Vector Instructions

All variants of [vector instructions](#) are represented by separate byte codes. They all have a one byte prefix, whereas the actual opcode is encoded by a variable-length [unsigned integer](#).

Vector loads and stores are followed by the encoding of their *memory* immediate.


```

laneidx ::= l:byte ⇒ l
instr    ::= ...
          | 0xFD 0:u32 m:memarg ⇒ v128.load m
          | 0xFD 1:u32 m:memarg ⇒ v128.load8x8_s m
          | 0xFD 2:u32 m:memarg ⇒ v128.load8x8_u m
          | 0xFD 3:u32 m:memarg ⇒ v128.load16x4_s m
          | 0xFD 4:u32 m:memarg ⇒ v128.load16x4_u m
          | 0xFD 5:u32 m:memarg ⇒ v128.load32x2_s m
          | 0xFD 6:u32 m:memarg ⇒ v128.load32x2_u m
          | 0xFD 7:u32 m:memarg ⇒ v128.load8_splat m
          | 0xFD 8:u32 m:memarg ⇒ v128.load16_splat m
          | 0xFD 9:u32 m:memarg ⇒ v128.load32_splat m
          | 0xFD 10:u32 m:memarg ⇒ v128.load64_splat m
          | 0xFD 92:u32 m:memarg ⇒ v128.load32_zero m
          | 0xFD 93:u32 m:memarg ⇒ v128.load64_zero m
          | 0xFD 11:u32 m:memarg ⇒ v128.store m
          | 0xFD 84:u32 m:memarg l:laneidx ⇒ v128.load8_lane m l
          | 0xFD 85:u32 m:memarg l:laneidx ⇒ v128.load16_lane m l
          | 0xFD 86:u32 m:memarg l:laneidx ⇒ v128.load32_lane m l
          | 0xFD 87:u32 m:memarg l:laneidx ⇒ v128.load64_lane m l
          | 0xFD 88:u32 m:memarg l:laneidx ⇒ v128.store8_lane m l
          | 0xFD 89:u32 m:memarg l:laneidx ⇒ v128.store16_lane m l
          | 0xFD 90:u32 m:memarg l:laneidx ⇒ v128.store32_lane m l
          | 0xFD 91:u32 m:memarg l:laneidx ⇒ v128.store64_lane m l

```

The `const` instruction is followed by 16 immediate bytes, which are converted into a *i128* in *littleendian* byte order:

```

instr ::= ...
       | 0xFD 12:u32 (b:byte)16 ⇒ v128.const bytesi128-1(b0 ... b15)

```

The `shuffle` instruction is also followed by the encoding of 16 *laneidx* immediates.

```

instr ::= ...
       | 0xFD 13:u32 (l:laneidx)16 ⇒ i8x16.shuffle l16

```

`extract_lane` and `replace_lane` instructions are followed by the encoding of a *laneidx* immediate.

```

instr ::= ...
       | 0xFD 21:u32 l:laneidx ⇒ i8x16.extract_lane_s l
       | 0xFD 22:u32 l:laneidx ⇒ i8x16.extract_lane_u l
       | 0xFD 23:u32 l:laneidx ⇒ i8x16.replace_lane l
       | 0xFD 24:u32 l:laneidx ⇒ i16x8.extract_lane_s l
       | 0xFD 25:u32 l:laneidx ⇒ i16x8.extract_lane_u l
       | 0xFD 26:u32 l:laneidx ⇒ i16x8.replace_lane l
       | 0xFD 27:u32 l:laneidx ⇒ i32x4.extract_lane l
       | 0xFD 28:u32 l:laneidx ⇒ i32x4.replace_lane l
       | 0xFD 29:u32 l:laneidx ⇒ i64x2.extract_lane l
       | 0xFD 30:u32 l:laneidx ⇒ i64x2.replace_lane l
       | 0xFD 31:u32 l:laneidx ⇒ f32x4.extract_lane l
       | 0xFD 32:u32 l:laneidx ⇒ f32x4.replace_lane l
       | 0xFD 33:u32 l:laneidx ⇒ f64x2.extract_lane l
       | 0xFD 34:u32 l:laneidx ⇒ f64x2.replace_lane l

```

All other vector instructions are plain opcodes without any immediates.

```

instr ::= ...
| 0xFD 14:u32 ⇒ i8x16.swizzle
| 0xFD 15:u32 ⇒ i8x16.splat
| 0xFD 16:u32 ⇒ i16x8.splat
| 0xFD 17:u32 ⇒ i32x4.splat
| 0xFD 18:u32 ⇒ i64x2.splat
| 0xFD 19:u32 ⇒ f32x4.splat
| 0xFD 20:u32 ⇒ f64x2.splat

|
|
| 0xFD 35:u32 ⇒ i8x16.eq
| 0xFD 36:u32 ⇒ i8x16.ne
| 0xFD 37:u32 ⇒ i8x16.lt_s
| 0xFD 38:u32 ⇒ i8x16.lt_u
| 0xFD 39:u32 ⇒ i8x16.gt_s
| 0xFD 40:u32 ⇒ i8x16.gt_u
| 0xFD 41:u32 ⇒ i8x16.le_s
| 0xFD 42:u32 ⇒ i8x16.le_u
| 0xFD 43:u32 ⇒ i8x16.ge_s
| 0xFD 44:u32 ⇒ i8x16.ge_u

|
|
| 0xFD 45:u32 ⇒ i16x8.eq
| 0xFD 46:u32 ⇒ i16x8.ne
| 0xFD 47:u32 ⇒ i16x8.lt_s
| 0xFD 48:u32 ⇒ i16x8.lt_u
| 0xFD 49:u32 ⇒ i16x8.gt_s
| 0xFD 50:u32 ⇒ i16x8.gt_u
| 0xFD 51:u32 ⇒ i16x8.le_s
| 0xFD 52:u32 ⇒ i16x8.le_u
| 0xFD 53:u32 ⇒ i16x8.ge_s
| 0xFD 54:u32 ⇒ i16x8.ge_u

|
|
| 0xFD 55:u32 ⇒ i32x4.eq
| 0xFD 56:u32 ⇒ i32x4.ne
| 0xFD 57:u32 ⇒ i32x4.lt_s
| 0xFD 58:u32 ⇒ i32x4.lt_u
| 0xFD 59:u32 ⇒ i32x4.gt_s
| 0xFD 60:u32 ⇒ i32x4.gt_u
| 0xFD 61:u32 ⇒ i32x4.le_s
| 0xFD 62:u32 ⇒ i32x4.le_u
| 0xFD 63:u32 ⇒ i32x4.ge_s
| 0xFD 64:u32 ⇒ i32x4.ge_u

|
|
| 0xFD 214:u32 ⇒ i64x2.eq
| 0xFD 215:u32 ⇒ i64x2.ne
| 0xFD 216:u32 ⇒ i64x2.lt_s
| 0xFD 217:u32 ⇒ i64x2.gt_s
| 0xFD 218:u32 ⇒ i64x2.le_s
| 0xFD 219:u32 ⇒ i64x2.ge_s

|
|
| 0xFD 65:u32 ⇒ f32x4.eq
| 0xFD 66:u32 ⇒ f32x4.ne
| 0xFD 67:u32 ⇒ f32x4.lt
| 0xFD 68:u32 ⇒ f32x4.gt
| 0xFD 69:u32 ⇒ f32x4.le
| 0xFD 70:u32 ⇒ f32x4.ge

```

0xFD 71:u32	⇒	f64x2.eq
0xFD 72:u32	⇒	f64x2.ne
0xFD 73:u32	⇒	f64x2.lt
0xFD 74:u32	⇒	f64x2.gt
0xFD 75:u32	⇒	f64x2.le
0xFD 76:u32	⇒	f64x2.ge
0xFD 77:u32	⇒	v128.not
0xFD 78:u32	⇒	v128.and
0xFD 79:u32	⇒	v128.andnot
0xFD 80:u32	⇒	v128.or
0xFD 81:u32	⇒	v128.xor
0xFD 82:u32	⇒	v128.bitselect
0xFD 83:u32	⇒	v128.any_true
0xFD 96:u32	⇒	i8x16.abs
0xFD 97:u32	⇒	i8x16.neg
0xFD 98:u32	⇒	i8x16.popcnt
0xFD 99:u32	⇒	i8x16.all_true
0xFD 100:u32	⇒	i8x16.bitmask
0xFD 101:u32	⇒	i8x16.narrow_i16x8_s
0xFD 102:u32	⇒	i8x16.narrow_i16x8_u
0xFD 107:u32	⇒	i8x16.shl
0xFD 108:u32	⇒	i8x16.shr_s
0xFD 109:u32	⇒	i8x16.shr_u
0xFD 110:u32	⇒	i8x16.add
0xFD 111:u32	⇒	i8x16.add_sat_s
0xFD 112:u32	⇒	i8x16.add_sat_u
0xFD 113:u32	⇒	i8x16.sub
0xFD 114:u32	⇒	i8x16.sub_sat_s
0xFD 115:u32	⇒	i8x16.sub_sat_u
0xFD 118:u32	⇒	i8x16.min_s
0xFD 119:u32	⇒	i8x16.min_u
0xFD 120:u32	⇒	i8x16.max_s
0xFD 121:u32	⇒	i8x16.max_u
0xFD 123:u32	⇒	i8x16.avgr_u

0xFD 124:u32	⇒	i16x8.extadd_pairwise_i8x16_s
0xFD 125:u32	⇒	i16x8.extadd_pairwise_i8x16_u
0xFD 128:u32	⇒	i16x8.abs
0xFD 129:u32	⇒	i16x8.neg
0xFD 130:u32	⇒	i16x8.q15mulr_sat_s
0xFD 131:u32	⇒	i16x8.all_true
0xFD 132:u32	⇒	i16x8.bitmask
0xFD 133:u32	⇒	i16x8.narrow_i32x4_s
0xFD 134:u32	⇒	i16x8.narrow_i32x4_u
0xFD 135:u32	⇒	i16x8.extend_low_i8x16_s
0xFD 136:u32	⇒	i16x8.extend_high_i8x16_s
0xFD 137:u32	⇒	i16x8.extend_low_i8x16_u
0xFD 138:u32	⇒	i16x8.extend_high_i8x16_u
0xFD 139:u32	⇒	i16x8.shl
0xFD 140:u32	⇒	i16x8.shr_s
0xFD 141:u32	⇒	i16x8.shr_u
0xFD 142:u32	⇒	i16x8.add
0xFD 143:u32	⇒	i16x8.add_sat_s
0xFD 144:u32	⇒	i16x8.add_sat_u
0xFD 145:u32	⇒	i16x8.sub
0xFD 146:u32	⇒	i16x8.sub_sat_s
0xFD 147:u32	⇒	i16x8.sub_sat_u
0xFD 149:u32	⇒	i16x8.mul
0xFD 150:u32	⇒	i16x8.min_s
0xFD 151:u32	⇒	i16x8.min_u
0xFD 152:u32	⇒	i16x8.max_s
0xFD 153:u32	⇒	i16x8.max_u
0xFD 155:u32	⇒	i16x8.avgr_u
0xFD 156:u32	⇒	i16x8.extmul_low_i8x16_s
0xFD 157:u32	⇒	i16x8.extmul_high_i8x16_s
0xFD 158:u32	⇒	i16x8.extmul_low_i8x16_u
0xFD 159:u32	⇒	i16x8.extmul_high_i8x16_u
0xFD 126:u32	⇒	i32x4.extadd_pairwise_i16x8_s
0xFD 127:u32	⇒	i32x4.extadd_pairwise_i16x8_u
0xFD 160:u32	⇒	i32x4.abs
0xFD 161:u32	⇒	i32x4.neg
0xFD 163:u32	⇒	i32x4.all_true
0xFD 164:u32	⇒	i32x4.bitmask
0xFD 167:u32	⇒	i32x4.extend_low_i16x8_s
0xFD 168:u32	⇒	i32x4.extend_high_i16x8_s
0xFD 169:u32	⇒	i32x4.extend_low_i16x8_u
0xFD 170:u32	⇒	i32x4.extend_high_i16x8_u
0xFD 171:u32	⇒	i32x4.shl
0xFD 172:u32	⇒	i32x4.shr_s
0xFD 173:u32	⇒	i32x4.shr_u
0xFD 174:u32	⇒	i32x4.add
0xFD 177:u32	⇒	i32x4.sub
0xFD 181:u32	⇒	i32x4.mul
0xFD 182:u32	⇒	i32x4.min_s
0xFD 183:u32	⇒	i32x4.min_u
0xFD 184:u32	⇒	i32x4.max_s
0xFD 185:u32	⇒	i32x4.max_u
0xFD 186:u32	⇒	i32x4.dot_i16x8_s
0xFD 188:u32	⇒	i32x4.extmul_low_i16x8_s
0xFD 189:u32	⇒	i32x4.extmul_high_i16x8_s
0xFD 190:u32	⇒	i32x4.extmul_low_i16x8_u
0xFD 191:u32	⇒	i32x4.extmul_high_i16x8_u

0xFD 192:u32	⇒	i64x2.abs
0xFD 193:u32	⇒	i64x2.neg
0xFD 195:u32	⇒	i64x2.all_true
0xFD 196:u32	⇒	i64x2.bitmask
0xFD 199:u32	⇒	i64x2.extend_low_i32x4_s
0xFD 200:u32	⇒	i64x2.extend_high_i32x4_s
0xFD 201:u32	⇒	i64x2.extend_low_i32x4_u
0xFD 202:u32	⇒	i64x2.extend_high_i32x4_u
0xFD 203:u32	⇒	i64x2.shl
0xFD 204:u32	⇒	i64x2.shr_s
0xFD 205:u32	⇒	i64x2.shr_u
0xFD 206:u32	⇒	i64x2.add
0xFD 209:u32	⇒	i64x2.sub
0xFD 213:u32	⇒	i64x2.mul
0xFD 220:u32	⇒	i64x2.extmul_low_i32x4_s
0xFD 221:u32	⇒	i64x2.extmul_high_i32x4_s
0xFD 222:u32	⇒	i64x2.extmul_low_i32x4_u
0xFD 223:u32	⇒	i64x2.extmul_high_i32x4_u
0xFD 103:u32	⇒	f32x4.ceil
0xFD 104:u32	⇒	f32x4.floor
0xFD 105:u32	⇒	f32x4.trunc
0xFD 106:u32	⇒	f32x4.nearest
0xFD 224:u32	⇒	f32x4.abs
0xFD 225:u32	⇒	f32x4.neg
0xFD 227:u32	⇒	f32x4.sqrt
0xFD 228:u32	⇒	f32x4.add
0xFD 229:u32	⇒	f32x4.sub
0xFD 230:u32	⇒	f32x4.mul
0xFD 231:u32	⇒	f32x4.div
0xFD 232:u32	⇒	f32x4.min
0xFD 233:u32	⇒	f32x4.max
0xFD 234:u32	⇒	f32x4.pmin
0xFD 235:u32	⇒	f32x4.pmax
0xFD 116:u32	⇒	f64x2.ceil
0xFD 117:u32	⇒	f64x2.floor
0xFD 122:u32	⇒	f64x2.trunc
0xFD 148:u32	⇒	f64x2.nearest
0xFD 236:u32	⇒	f64x2.abs
0xFD 237:u32	⇒	f64x2.neg
0xFD 239:u32	⇒	f64x2.sqrt
0xFD 240:u32	⇒	f64x2.add
0xFD 241:u32	⇒	f64x2.sub
0xFD 242:u32	⇒	f64x2.mul
0xFD 243:u32	⇒	f64x2.div
0xFD 244:u32	⇒	f64x2.min
0xFD 245:u32	⇒	f64x2.max
0xFD 246:u32	⇒	f64x2.pmin
0xFD 247:u32	⇒	f64x2.pmax

0xFD 248:u32	⇒	i32x4.trunc_sat_f32x4_s
0xFD 249:u32	⇒	i32x4.trunc_sat_f32x4_u
0xFD 250:u32	⇒	f32x4.convert_i32x4_s
0xFD 251:u32	⇒	f32x4.convert_i32x4_u
0xFD 252:u32	⇒	i32x4.trunc_sat_f64x2_s_zero
0xFD 253:u32	⇒	i32x4.trunc_sat_f64x2_u_zero
0xFD 254:u32	⇒	f64x2.convert_low_i32x4_s
0xFD 255:u32	⇒	f64x2.convert_low_i32x4_u
0xFD 94:u32	⇒	f32x4.demote_f64x2_zero
0xFD 95:u32	⇒	f64x2.promote_low_f32x4
0xFD 256:u32	⇒	i16x8.relaxed_swizzle
0xFD 257:u32	⇒	i32x4.relaxed_trunc_f32x4_s
0xFD 258:u32	⇒	i32x4.relaxed_trunc_f32x4_u
0xFD 259:u32	⇒	i32x4.relaxed_trunc_f32x4_s_zero
0xFD 260:u32	⇒	i32x4.relaxed_trunc_f32x4_u_zero
0xFD 261:u32	⇒	f32x4.relaxed_madd
0xFD 262:u32	⇒	f32x4.relaxed_nmadd
0xFD 263:u32	⇒	f64x2.relaxed_madd
0xFD 264:u32	⇒	f64x2.relaxed_nmadd
0xFD 265:u32	⇒	i8x16.relaxed_laneselect
0xFD 266:u32	⇒	i16x8.relaxed_laneselect
0xFD 267:u32	⇒	i32x4.relaxed_laneselect
0xFD 268:u32	⇒	i64x2.relaxed_laneselect
0xFD 269:u32	⇒	f32x4.relaxed_min
0xFD 270:u32	⇒	f32x4.relaxed_max
0xFD 271:u32	⇒	f64x2.relaxed_min
0xFD 272:u32	⇒	f64x2.relaxed_max
0xFD 273:u32	⇒	i16x8.relaxed_q15mulr_s
0xFD 274:u32	⇒	i16x8.relaxed_dot_i8x16_i7x16_s
0xFD 275:u32	⇒	i16x8.relaxed_dot_i8x16_i7x16_add_s

5.4.9 Expressions

Expressions are encoded by their instruction sequence terminated with an explicit 0x0B opcode for *end*.

$$\text{expr} ::= (\text{in:instr})^* \text{0x0B} \Rightarrow \text{in}^* \text{end}$$

5.5 Modules

The binary encoding of modules is organized into *sections*. Most sections correspond to one component of a *module* record, except that *function definitions* are split into two sections, separating their type declarations in the *function section* from their bodies in the *code section*.

Note: This separation enables *parallel* and *streaming* compilation of the functions in a module.

Note: Section ids do not always correspond to the [order of sections](#) in the encoding of a module.

5.5.3 Custom Section

Custom sections have the id 0. They are intended to be used for debugging information or third-party extensions, and are ignored by the WebAssembly semantics. Their contents consist of a [name](#) further identifying the custom section, followed by an uninterpreted sequence of bytes for custom use.

$$\begin{aligned} \text{customsec} &::= \text{section}_0(\text{custom}) \\ \text{custom} &::= \text{name byte}^* \end{aligned}$$

Note: If an implementation interprets the data of a custom section, then errors in that data, or the placement of the section, must not invalidate the module.

5.5.4 Type Section

The *type section* has the id 1. It decodes into a vector of [recursive types](#) that represent the [types](#) component of a [module](#).

$$\text{typesec} ::= \text{rt}^*:\text{section}_1(\text{vec}(\text{rectype})) \Rightarrow \text{rt}^*$$

5.5.5 Import Section

The *import section* has the id 2. It decodes into a vector of [imports](#) that represent the [imports](#) component of a [module](#).

$$\begin{aligned} \text{importsec} &::= \text{im}^*:\text{section}_2(\text{vec}(\text{import})) &\Rightarrow \text{im}^* \\ \text{import} &::= \text{mod}:\text{name } \text{nm}:\text{name } \text{d}:\text{importdesc} &\Rightarrow \{\text{module } \text{mod}, \text{name } \text{nm}, \text{desc } \text{d}\} \\ \text{importdesc} &::= \begin{array}{l} 0x00 \text{ } x:\text{typeid}x \\ | \\ 0x01 \text{ } \text{tt}:\text{tabletype} \\ | \\ 0x02 \text{ } \text{mt}:\text{memtype} \\ | \\ 0x03 \text{ } \text{gt}:\text{globaltype} \\ | \\ 0x04 \text{ } \text{x}:\text{tagtype} \end{array} &\Rightarrow \begin{array}{l} \text{func } x \\ \\ \text{table } \text{tt} \\ \\ \text{mem } \text{mt} \\ \\ \text{global } \text{gt} \\ \\ \text{tag } x \end{array} \end{aligned}$$

5.5.6 Function Section

The *function section* has the id 3. It decodes into a vector of [type indices](#) that represent the [type](#) fields of the [functions](#) in the [funcs](#) component of a [module](#). The [locals](#) and [body](#) fields of the respective functions are encoded separately in the [code section](#).

$$\text{funcsec} ::= \text{x}^*:\text{section}_3(\text{vec}(\text{typeid}x)) \Rightarrow \text{x}^*$$

5.5.7 Table Section

The *table section* has the id 4. It decodes into a vector of *tables* that represent the *tables* component of a *module*.

<code>tablesec</code>	<code>::=</code>	<code>tab*:section₄(vec(table))</code>	\Rightarrow	<code>tab*</code>	
<code>table</code>	<code>::=</code>	<code>tt:tabletype</code>	\Rightarrow	<code>{type tt, init (ref.null ht)}</code>	if <code>tt = limits (ref null[?] ht)</code>
	<code> </code>	<code>0x40 0x00 tt:tabletype e:expr</code>	\Rightarrow	<code>{type tt, init e}</code>	

Note: The encoding of a table type cannot start with byte 0x40, hence decoding is unambiguous. The zero byte following it is reserved for future extensions.

5.5.8 Memory Section

The *memory section* has the id 5. It decodes into a vector of *memories* that represent the *mems* component of a *module*.

<code>memsec</code>	<code>::=</code>	<code>mem*:section₅(vec(mem))</code>	\Rightarrow	<code>mem*</code>
<code>mem</code>	<code>::=</code>	<code>mt:mentype</code>	\Rightarrow	<code>{type mt}</code>

5.5.9 Global Section

The *global section* has the id 6. It decodes into a vector of *globals* that represent the *globals* component of a *module*.

<code>globalsec</code>	<code>::=</code>	<code>glob*:section₆(vec(global))</code>	\Rightarrow	<code>glob*</code>
<code>global</code>	<code>::=</code>	<code>gt:globaltype e:expr</code>	\Rightarrow	<code>{type gt, init e}</code>

5.5.10 Export Section

The *export section* has the id 7. It decodes into a vector of *exports* that represent the *exports* component of a *module*.

<code>exportsec</code>	<code>::=</code>	<code>ex*:section₇(vec(export))</code>	\Rightarrow	<code>ex*</code>
<code>export</code>	<code>::=</code>	<code>nm:name d:exportdesc</code>	\Rightarrow	<code>{name nm, desc d}</code>
<code>exportdesc</code>	<code>::=</code>	<code>0x00 x:funcidx</code>	\Rightarrow	<code>func x</code>
	<code> </code>	<code>0x01 x:tableidx</code>	\Rightarrow	<code>table x</code>
	<code> </code>	<code>0x02 x:memidx</code>	\Rightarrow	<code>mem x</code>
	<code> </code>	<code>0x03 x:globalidx</code>	\Rightarrow	<code>global x</code>
	<code> </code>	<code>0x04 x>tagidx</code>	\Rightarrow	<code>tag x</code>

5.5.11 Start Section

The *start section* has the id 8. It decodes into an optional *start function* that represents the *start* component of a *module*.

<code>startsec</code>	<code>::=</code>	<code>st?:section₈(start)</code>	\Rightarrow	<code>st?</code>
<code>start</code>	<code>::=</code>	<code>x:funcidx</code>	\Rightarrow	<code>{func x}</code>

5.5.12 Element Section

The *element section* has the id 9. It decodes into a vector of *element segments* that represent the *elems* component of a *module*.

```

elemsec ::= seg*:section9(vec(elem)) ⇒ seg*
elem    ::= 0:u32 e:expr y*:vec(funcidx) ⇒
           {type (ref func), init ((ref.func y) end)*, mode active {table 0, offset e}}
           | 1:u32 et : elemkind y*:vec(funcidx) ⇒
           {type et, init ((ref.func y) end)*, mode passive}
           | 2:u32 x:tableidx e:expr et : elemkind y*:vec(funcidx) ⇒
           {type et, init ((ref.func y) end)*, mode active {table x, offset e}}
           | 3:u32 et : elemkind y*:vec(funcidx) ⇒
           {type et, init ((ref.func y) end)*, mode declarative}
           | 4:u32 e:expr el*:vec(expr) ⇒
           {type (ref null func), init el*, mode active {table 0, offset e}}
           | 5:u32 et : reftype el*:vec(expr) ⇒
           {type et, init el*, mode passive}
           | 6:u32 x:tableidx e:expr et : reftype el*:vec(expr) ⇒
           {type et, init el*, mode active {table x, offset e}}
           | 7:u32 et : reftype el*:vec(expr) ⇒
           {type et, init el*, mode declarative}
elemkind ::= 0x00 ⇒ (ref func)

```

Note: The initial integer can be interpreted as a bitfield. Bit 0 distinguishes a passive or declarative segment from an active segment, bit 1 indicates the presence of an explicit table index for an active segment and otherwise distinguishes passive from declarative segments, bit 2 indicates the use of element type and element *expressions* instead of element kind and element indices.

Additional element kinds may be added in future versions of WebAssembly.

5.5.13 Code Section

The *code section* has the id 10. It decodes into a vector of *code entries* that are pairs of *value type* vectors and *expressions*. They represent the *locals* and *body* field of the *functions* in the *funcs* component of a *module*. The *type* fields of the respective functions are encoded separately in the *function section*.

The encoding of each code entry consists of

- the *u32* size of the function code in bytes,
- the actual *function code*, which in turn consists of
 - the declaration of *locals*,
 - the function *body* as an *expression*.

Local declarations are compressed into a vector whose entries consist of

- a *u32* count,
- a *value type*,

denoting *count* locals of the same value type.

```

codesec ::= code*:section10(vec(code)) ⇒ code*
code    ::= size:u32 code:func          ⇒ code          (if size = ||func||)
func    ::= (local)*:vec(locals) e:expr ⇒ concat((local)*), e (if |concat((local)*)| < 232)
locals  ::= n:u32 t:valtype             ⇒ {type t}n

```

Here, *code* ranges over pairs $(valtype^*, expr)$. The meta function $concat((local^*)^*)$ concatenates all sequences $local_i^*$ in $(local^*)^*$. Any code for which the length of the resulting sequence is out of bounds of the maximum size of a **vector** is malformed.

Note: Like with **sections**, the code *size* is not needed for decoding, but can be used to skip functions when navigating through a binary. The module is malformed if a size does not match the length of the respective function code.

5.5.14 Data Section

The *data section* has the id 11. It decodes into a vector of **data segments** that represent the **datas** component of a **module**.

<code>datasec</code>	<code>::=</code>	<code>seg*:section₁₁(vec(data))</code>	\Rightarrow	<code>seg*</code>
<code>data</code>	<code>::=</code>	<code>0:u32 e:expr b*:vec(byte)</code>	\Rightarrow	<code>{init b*, mode active {memory 0, offset e}}</code>
		<code> 1:u32 b*:vec(byte)</code>	\Rightarrow	<code>{init b*, mode passive}</code>
		<code> 2:u32 x:memidx e:expr b*:vec(byte)</code>	\Rightarrow	<code>{init b*, mode active {memory x, offset e}}</code>

Note: The initial integer can be interpreted as a bitfield. Bit 0 indicates a passive segment, bit 1 indicates the presence of an explicit memory index for an active segment.

In the current version of WebAssembly, at most one memory may be defined or imported in a single module, so all valid **active** data segments have a **memory** value of 0.

5.5.15 Data Count Section

The *data count section* has the id 12. It decodes into an optional **u32** that represents the number of **data segments** in the **data section**. If this count does not match the length of the data segment vector, the module is malformed.

$$datacountsec ::= n?:section_{12}(u32) \Rightarrow n?$$

Note: The data count section is used to simplify single-pass validation. Since the data section occurs after the code section, the **memory.init** and **data.drop** instructions would not be able to check whether the data segment index is valid until the data section is read. The data count section occurs before the code section, so a single-pass validator can use this count instead of deferring validation.

5.5.16 Tag Section

The *tag section* has the id 13. It decodes into a vector of **tags** that represent the **tags** component of a **module**.

<code>tagsec</code>	<code>::=</code>	<code>tag*:section₁₃(vec(tag))</code>	\Rightarrow	<code>tag*</code>
<code>tag</code>	<code>::=</code>	<code>0x00 x:typeidx</code>	\Rightarrow	<code>{type x}</code>

5.5.17 Modules

The encoding of a [module](#) starts with a preamble containing a 4-byte magic number (the string ‘\0asm’) and a version field. The current version of the WebAssembly binary format is 1.

The preamble is followed by a sequence of [sections](#). [Custom sections](#) may be inserted at any place in this sequence, while other sections must occur at most once and in the prescribed order. All sections can be empty.

The lengths of vectors produced by the (possibly empty) [function](#) and [code](#) section must match up.

Similarly, the optional data count must match the length of the [data segment](#) vector. Furthermore, it must be present if any [data index](#) occurs in the code section.

```

magic      ::= 0x00 0x61 0x73 0x6D
version    ::= 0x01 0x00 0x00 0x00
module     ::= magic
              version
              customsec*
              rectype*:typesec
              customsec*
              import*:importsec
              customsec*
              typeidxn:funcsec
              customsec*
              table*:tablesec
              customsec*
              mem*:memsec
              customsec*
              tag*:tagsec
              customsec*
              global*:globalsec
              customsec*
              export*:exportsec
              customsec*
              start?:startsec
              customsec*
              elem*:elemsec
              customsec*
              m?:datacountsec
              customsec*
              coden:codesec
              customsec*
              datam:datasec
              customsec* ⇒ { types rectype*,
                             funcs funcn,
                             tables table*,
                             mems mem*,
                             globals global*,
                             tags tag*,
                             elems elem*,
                             datas datam,
                             start start?,
                             imports import*,
                             exports export* }
              (if m? ≠ ε ∨ dataidx(coden) = ∅)

```

where for each t_i^*, e_i in $code^n$,

$$func^n[i] = \{\text{type } typeidx^n[i], \text{locals } t_i^*, \text{body } e_i\}$$

Note: The version of the WebAssembly binary format may increase in the future if backward-incompatible changes have to be made to the format. However, such changes are expected to occur very infrequently, if ever. The binary format is intended to be forward-compatible, such that future extensions can be made without incrementing its version.

6.1 Conventions

The textual format for WebAssembly **modules** is a rendering of their **abstract syntax** into **S-expressions**³⁷.

Like the **binary format**, the text format is defined by an *attribute grammar*. A text string is a well-formed description of a module if and only if it is generated by the grammar. Each production of this grammar has at most one synthesized attribute: the abstract syntax that the respective character sequence expresses. Thus, the attribute grammar implicitly defines a *parsing* function. Some productions also take a **context** as an inherited attribute that records bound **identifiers**.

Except for a few exceptions, the core of the text grammar closely mirrors the grammar of the abstract syntax. However, it also defines a number of *abbreviations* that are “syntactic sugar” over the core syntax.

The recommended extension for files containing WebAssembly modules in text format is “.wat”. Files with this extension are assumed to be encoded in UTF-8, as per **Unicode**³⁸ (Section 2.5).

6.1.1 Grammar

The following conventions are adopted in defining grammar rules of the text format. They mirror the conventions used for **abstract syntax** and for the **binary format**. In order to distinguish symbols of the textual syntax from symbols of the abstract syntax, **typewriter** font is adopted for the former.

- Terminal symbols are either literal strings of characters enclosed in quotes or expressed as **Unicode**³⁹ scalar values: ‘module’, U+0A. (All characters written literally are unambiguously drawn from the 7-bit **ASCII**⁴⁰ subset of Unicode.)
- Nonterminal symbols are written in typewriter font: `valtype`, `instr`.
- T^n is a sequence of $n \geq 0$ iterations of T .
- T^* is a possibly empty sequence of iterations of T . (This is a shorthand for T^n used where n is not relevant.)
- T^+ is a sequence of one or more iterations of T . (This is a shorthand for T^n where $n \geq 1$.)
- $T^?$ is an optional occurrence of T . (This is a shorthand for T^n where $n \leq 1$.)

³⁷ <https://en.wikipedia.org/wiki/S-expression>

³⁸ <https://www.unicode.org/versions/latest/>

³⁹ <https://www.unicode.org/versions/latest/>

⁴⁰ <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

- $x:T$ denotes the same language as the nonterminal T , but also binds the variable x to the attribute synthesized for T . A pattern may also be used instead of a variable, e.g., $(x, y):T$.
- Productions are written $\text{sym} ::= T_1 \Rightarrow A_1 \mid \dots \mid T_n \Rightarrow A_n$, where each A_i is the attribute that is synthesized for sym in the given case, usually from attribute variables bound in T_i .
- Some productions are augmented by side conditions in parentheses, which restrict the applicability of the production. They provide a shorthand for a combinatorial expansion of the production into many separate cases.
- If the same meta variable or non-terminal symbol appears multiple times in a production (in the syntax or in an attribute), then all those occurrences must have the same instantiation.
- A distinction is made between *lexical* and *syntactic* productions. For the latter, arbitrary **white space** is allowed in any place where the grammar contains spaces. The productions defining **lexical syntax** and the syntax of **values** are considered lexical, all others are syntactic.

Note: For example, the **textual grammar** for **number types** is given as follows:

$$\begin{array}{llll} \text{numtype} & ::= & \text{'i32'} & \Rightarrow & \text{i32} \\ & & | & & \\ & & \text{'i64'} & \Rightarrow & \text{i64} \\ & & | & & \\ & & \text{'f32'} & \Rightarrow & \text{f32} \\ & & | & & \\ & & \text{'f64'} & \Rightarrow & \text{f64} \end{array}$$

The **textual grammar** for **limits** is defined as follows:

$$\begin{array}{llll} \text{limits} & ::= & n:\text{u32} & \Rightarrow & \{\min n, \max \epsilon\} \\ & & | & & \\ & & n:\text{u32} \ m:\text{u32} & \Rightarrow & \{\min n, \max m\} \end{array}$$

The variables n and m name the attributes of the respective **u32** nonterminals, which in this case are the actual **unsigned integers** those parse into. The attribute of the complete production then is the abstract syntax for the limit, expressed in terms of the former values.

6.1.2 Abbreviations

In addition to the core grammar, which corresponds directly to the **abstract syntax**, the textual syntax also defines a number of *abbreviations* that can be used for convenience and readability.

Abbreviations are defined by *rewrite rules* specifying their expansion into the core syntax:

$$\text{abbreviation syntax} \quad \equiv \quad \text{expanded syntax}$$

These expansions are assumed to be applied, recursively and in order of appearance, before applying the core grammar rules to construct the abstract syntax.

6.1.3 Contexts

The text format allows the use of symbolic **identifiers** in place of **indices**. To resolve these identifiers into concrete indices, some grammar productions are indexed by an *identifier context* I as a synthesized attribute that records the declared identifiers in each **index space**. In addition, the context records the types defined in the module, so that **parameter** indices can be computed for **functions**.

It is convenient to define identifier contexts as *records* I with abstract syntax as follows:

$$I ::= \{ \begin{array}{ll} \text{types} & (name^?)^*, \\ \text{funcs} & (name^?)^*, \\ \text{tables} & (name^?)^*, \\ \text{mems} & (name^?)^*, \\ \text{globals} & (name^?)^*, \\ \text{tags} & (name^?)^*, \\ \text{elem} & (name^?)^*, \\ \text{data} & (name^?)^*, \\ \text{locals} & (name^?)^*, \\ \text{labels} & (name^?)^*, \\ \text{fields} & ((name^?)^*)^* \} \\ \text{typedefs} & subtype^* \end{array}$$

For each index space, such a context contains the list of *names* assigned to the defined indices, which were denoted by the corresponding *identifiers*. Unnamed indices are associated with empty (ϵ) entries in these lists. Fields have *dependent* name spaces, and hence a separate list of field identifiers per type.

An identifier context is *well-formed* if no index space contains duplicate identifiers. For fields, names need only be unique within a single type.

Conventions

To avoid unnecessary clutter, empty components are omitted when writing out identifier contexts. For example, the record $\{\}$ is shorthand for an *identifier context* whose components are all empty.

6.1.4 Vectors

Vectors are written as plain sequences, but with a restriction on the length of these sequence.

$$\text{vec}(A) ::= (x:A)^n \Rightarrow x^n \quad (\text{if } n < 2^{32})$$

6.2 Lexical Format

6.2.1 Characters

The text format assigns meaning to *source text*, which consists of a sequence of *characters*. Characters are assumed to be represented as valid *Unicode*⁴¹ (Section 2.4) *scalar values*.

$$\begin{array}{ll} \text{source} & ::= \text{char}^* \\ \text{char} & ::= \text{U+00} \mid \dots \mid \text{U+D7FF} \mid \text{U+E000} \mid \dots \mid \text{U+10FFFF} \end{array}$$

Note: While source text may contain any Unicode character in *comments* or *string* literals, the rest of the grammar is formed exclusively from the characters supported by the 7-bit *ASCII*⁴² subset of Unicode.

⁴¹ <https://www.unicode.org/versions/latest/>

⁴² <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

6.2.2 Tokens

The character stream in the source text is divided, from left to right, into a sequence of *tokens*, as defined by the following grammar.

```
token      ::= keyword | uN | sN | fN | string | id | '(' | ')' | reserved
keyword    ::= ('a' | ... | 'z') idchar*      (if occurring as a literal terminal in the grammar)
reserved   ::= (idchar | string | ',' | ';' | '[' | ']' | '{' | '}' )+
```

Tokens are formed from the input character stream according to the *longest match* rule. That is, the next token always consists of the longest possible sequence of characters that is recognized by the above lexical grammar. Tokens can be separated by *white space*, but except for strings, they cannot themselves contain whitespace.

Keyword tokens are defined either implicitly by an occurrence of a *terminal symbol* in literal form, such as ‘keyword’, in a *syntactic* production of this chapter, or explicitly where they arise in this chapter.

Any token that does not fall into any of the other categories is considered *reserved*, and cannot occur in source text.

Note: The effect of defining the set of reserved tokens is that all tokens must be separated by either parentheses, *white space*, or *comments*. For example, ‘0\$x’ is a single reserved token, as is “a”b”. Consequently, they are not recognized as two separate tokens ‘0’ and ‘\$x’, or “a” and ”b”, respectively, but instead disallowed. This property of tokenization is not affected by the fact that the definition of reserved tokens overlaps with other token classes.

6.2.3 White Space

White space is any sequence of literal space characters, formatting characters, *comments*, or *annotations*. The allowed formatting characters correspond to a subset of the [ASCII⁴³ format effectors](#), namely, *horizontal tabulation* (U+09), *line feed* (U+0A), and *carriage return* (U+0D).

```
space      ::= (' ' | format | comment)*
format     ::= newline | U+09
newline    ::= U+0A | U+0D | U+0D U+0A
```

The only relevance of white space is to separate *tokens*. It is otherwise ignored.

6.2.4 Comments

A *comment* can either be a *line comment*, started with a double semicolon ‘;;’ and extending to the end of the line, or a *block comment*, enclosed in delimiters ‘(;’ ... ‘;’)’. Block comments can be nested.

```
comment    ::= linecomment | blockcomment
linecomment ::= ‘;;’ linechar* (newline | eof)
linechar   ::= c:char                               (if c ≠ U+0A ∧ c ≠ U+0D)
blockcomment ::= ‘(;’ blockchar* ‘;’)
blockchar  ::= c:char                               (if c ≠ ‘;’ ∧ c ≠ ‘(’)
              | ‘;’                               (if the next character is not ‘)’)
              | ‘(’                               (if the next character is not ‘;’)
              | blockcomment
```

Here, the pseudo token *eof* indicates the end of the input. The *look-ahead* restrictions on the productions for *blockchar* disambiguate the grammar such that only well-bracketed uses of block comment delimiters are allowed.

Note: Any formatting and control characters are allowed inside comments.

⁴³ <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

6.2.5 Annotations

An *annotation* is a bracketed token sequence headed by an *annotation id* of the form ‘@id’ or ‘@’...’. No *space* is allowed between the opening parenthesis and this id. Annotations are intended to be used for third-party extensions; they can appear anywhere in a program but are ignored by the WebAssembly semantics itself, which treats them as *white space*.

Annotations can contain other parenthesized token sequences (including nested annotations), as long as they are well-nested. *String literals* and *comments* occurring in an annotation must also be properly nested and closed.

```
annot    ::= ‘(’ @annotid (space | token)* ‘)’
annotid  ::= idchar+ | name
```

Note: The annotation id is meant to be an identifier categorising the extension, and plays a role similar to the name of a *custom section*. By convention, annotations corresponding to a custom section should use the custom section’s name as an id.

Implementations are expected to ignore annotations with ids that they do not recognize. On the other hand, they may impose restrictions on annotations that they do recognize, e.g., requiring a specific structure by superimposing a more concrete grammar. It is up to an implementation how it deals with errors in such annotations.

6.3 Values

The grammar productions in this section define *lexical syntax*, hence no *white space* is allowed.

6.3.1 Integers

All *integers* can be written in either decimal or hexadecimal notation. In both cases, digits can optionally be separated by underscores.

```
sign      ::= ε ⇒ + | ‘+’ ⇒ + | ‘-’ ⇒ -
digit     ::= ‘0’ ⇒ 0 | ... | ‘9’ ⇒ 9
hexdigit  ::= d:digit ⇒ d
           | ‘A’ ⇒ 10 | ... | ‘F’ ⇒ 15
           | ‘a’ ⇒ 10 | ... | ‘f’ ⇒ 15

num       ::= d:digit                ⇒ d
           | n:num ‘_’? d:digit      ⇒ 10 · n + d
hexnum    ::= h:hexdigit             ⇒ h
           | n:hexnum ‘_’? h:hexdigit ⇒ 16 · n + h
```

The allowed syntax for integer literals depends on size and signedness. Moreover, their value must lie within the range of the respective type.

```
uN ::= n:num                ⇒ n      (if n < 2N)
    | ‘0x’ n:hexnum         ⇒ n      (if n < 2N)
sN ::= ±:sign n:num          ⇒ ±n     (if -2N-1 ≤ ±n < 2N-1)
    | ±:sign ‘0x’ n:hexnum  ⇒ ±n     (if -2N-1 ≤ ±n < 2N-1)
```

Uninterpreted integers can be written as either signed or unsigned, and are normalized to unsigned in the abstract syntax.

```
iN ::= n:uN                ⇒ n
    | i:sN                 ⇒ n      (if i = signed(n))
```

6.3.2 Floating-Point

Floating-point values can be represented in either decimal or hexadecimal notation.

<code>frac</code>	<code>::= d:digit</code>	$\Rightarrow d/10$
	<code> d:digit ‘_’? p:frac</code>	$\Rightarrow (d + p/10)/10$
<code>hexfrac</code>	<code>::= h:hexdigit</code>	$\Rightarrow h/16$
	<code> h:hexdigit ‘_’? p:hexfrac</code>	$\Rightarrow (h + p/16)/16$
<code>float</code>	<code>::= p:num ‘.’?</code>	$\Rightarrow p$
	<code> p:num ‘.’ q:frac</code>	$\Rightarrow p + q$
	<code> p:num ‘.’? (‘E’ ‘e’) ±:sign e:num</code>	$\Rightarrow p \cdot 10^{\pm e}$
	<code> p:num ‘.’ q:frac (‘E’ ‘e’) ±:sign e:num</code>	$\Rightarrow (p + q) \cdot 10^{\pm e}$
<code>hexfloat</code>	<code>::= ‘0x’ p:hexnum ‘.’?</code>	$\Rightarrow p$
	<code> ‘0x’ p:hexnum ‘.’ q:hexfrac</code>	$\Rightarrow p + q$
	<code> ‘0x’ p:hexnum ‘.’? (‘P’ ‘p’) ±:sign e:num</code>	$\Rightarrow p \cdot 2^{\pm e}$
	<code> ‘0x’ p:hexnum ‘.’ q:hexfrac (‘P’ ‘p’) ±:sign e:num</code>	$\Rightarrow (p + q) \cdot 2^{\pm e}$

The value of a literal must not lie outside the representable range of the corresponding IEEE 754⁴⁴ type (that is, a numeric value must not overflow to $\pm\infty$), but it may be rounded to the nearest representable value.

Note: Rounding can be prevented by using hexadecimal notation with no more significant bits than supported by the required type.

Floating-point values may also be written as constants for *infinity* or *canonical NaN* (*not a number*). Furthermore, arbitrary NaN values may be expressed by providing an explicit payload value.

<code>fN</code>	<code>::= ±:sign z:fNmag</code>	$\Rightarrow \pm z$	
<code>fNmag</code>	<code>::= z:float</code>	$\Rightarrow \text{float}_N(z)$	(if $\text{float}_N(z) \neq \pm\infty$)
	<code> z:hexfloat</code>	$\Rightarrow \text{float}_N(z)$	(if $\text{float}_N(z) \neq \pm\infty$)
	<code> ‘inf’</code>	$\Rightarrow \infty$	
	<code> ‘nan’</code>	$\Rightarrow \text{nan}(\text{canon}_N)$	
	<code> ‘nan:0x’ n:hexnum</code>	$\Rightarrow \text{nan}(n)$	(if $1 \leq n < 2^{\text{signif}(N)}$)

6.3.3 Strings

Strings denote sequences of bytes that can represent both textual and binary data. They are enclosed in quotation marks and may contain any character other than ASCII⁴⁵ control characters, quotation marks (“”), or backslash (“\”), except when expressed with an *escape sequence*.

<code>string</code>	<code>::= ‘’ (b*:stringelem)* ‘’</code>	$\Rightarrow \text{concat}((b^*)^*)$	(if $ \text{concat}((b^*)^*) < 2^{32}$)
<code>stringelem</code>	<code>::= c:stringchar</code>	$\Rightarrow \text{utf8}(c)$	
	<code> ‘\’ n:hexdigit m:hexdigit</code>	$\Rightarrow 16 \cdot n + m$	

Each character in a string literal represents the byte sequence corresponding to its UTF-8 Unicode⁴⁶ (Section 2.5) encoding, except for hexadecimal escape sequences ‘\hh’, which represent raw bytes of the respective value.

<code>stringchar</code>	<code>::= c:char</code>	$\Rightarrow c$	(if $c \geq \text{U}+20 \wedge c \neq \text{U}+7\text{F} \wedge c \neq \text{’’} \wedge c \neq \text{’\’}$)
	<code> ‘\t’</code>	$\Rightarrow \text{U}+09$	
	<code> ‘\n’</code>	$\Rightarrow \text{U}+0\text{A}$	
	<code> ‘\r’</code>	$\Rightarrow \text{U}+0\text{D}$	
	<code> ‘\’’</code>	$\Rightarrow \text{U}+22$	
	<code> ‘\’</code>	$\Rightarrow \text{U}+27$	
	<code> ‘\’</code>	$\Rightarrow \text{U}+5\text{C}$	
	<code> ‘\u{ n:hexnum }’</code>	$\Rightarrow \text{U}+(n)$	(if $n < 0\text{x}\text{D}800 \vee 0\text{x}\text{E}000 \leq n < 0\text{x}\text{110000}$)

⁴⁴ <https://ieeexplore.ieee.org/document/8766229>

⁴⁵ <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

⁴⁶ <https://www.unicode.org/versions/latest/>

6.3.4 Names

Names are strings denoting a literal character sequence. A name string must form a valid UTF-8 encoding as defined by [Unicode](#)⁴⁷ (Section 2.5) and is interpreted as a string of Unicode scalar values.

$$\text{name} ::= b^*:\text{string} \Rightarrow c^* \quad (\text{if } b^* = \text{utf8}(c^*))$$

Note: Presuming the source text is itself encoded correctly, strings that do not contain any uses of hexadecimal byte escapes are always valid names.

6.3.5 Identifiers

Indices can be given in both numeric and symbolic form. Symbolic *identifiers* that stand in lieu of indices start with ‘\$’, followed by either a sequence of printable [ASCII](#)⁴⁸ characters that does not contain a space, quotation mark, comma, semicolon, or bracket, or by a quoted **name**.

$$\begin{array}{llll} \text{id} & ::= & \$ c^*:\text{idchar}^+ & \Rightarrow c^* \\ & | & \$ c^*:\text{name} & \Rightarrow c^* \quad (\text{if } |c^*| > 0) \\ \text{idchar} & ::= & \begin{array}{l} \text{'0'} \mid \dots \mid \text{'9'} \\ \text{'A'} \mid \dots \mid \text{'Z'} \\ \text{'a'} \mid \dots \mid \text{'z'} \\ \text{'!'} \mid \text{'\#'} \mid \text{'\$'} \mid \text{'\%'} \mid \text{'\&'} \mid \text{'\''} \mid \text{'*'} \mid \text{'+'} \mid \text{'-'} \mid \text{'.'} \mid \text{'/'} \\ \text{'\:'} \mid \text{'<'} \mid \text{'='} \mid \text{'>'} \mid \text{'?'} \mid \text{'@'} \mid \text{'\'} \mid \text{'\^'} \mid \text{'_'} \mid \text{'\`'} \mid \text{'|'} \mid \text{'\~'} \end{array} \end{array}$$

Note: The value of an identifier character is the Unicode codepoint denoting it.

Conventions

The expansion rules of some abbreviations require insertion of a *fresh* identifier. That may be any syntactically valid identifier that does not already occur in the given source text.

6.4 Types

6.4.1 Number Types

$$\begin{array}{llll} \text{numtype}_I & ::= & \begin{array}{l} \text{'i32'} \\ \text{'i64'} \\ \text{'f32'} \\ \text{'f64'} \end{array} & \Rightarrow \begin{array}{l} \text{i32} \\ \text{i64} \\ \text{f32} \\ \text{f64} \end{array} \end{array}$$

⁴⁷ <https://www.unicode.org/versions/latest/>

⁴⁸ <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

6.4.2 Vector Types

$\text{vectype}_I ::= \text{'v128'} \Rightarrow \text{v128}$

6.4.3 Heap Types

$\text{absheaptypes} ::=$	'any'	\Rightarrow	any
	'eq'	\Rightarrow	eq
	'i31'	\Rightarrow	i31
	'struct'	\Rightarrow	struct
	'array'	\Rightarrow	array
	'none'	\Rightarrow	none
	'func'	\Rightarrow	func
	'nofunc'	\Rightarrow	nofunc
	'extern'	\Rightarrow	extern
	'noexn'	\Rightarrow	noexn
	'exn'	\Rightarrow	exn
	'noextern'	\Rightarrow	noextern
$\text{heaptypes}_I ::=$	$t:\text{absheaptypes}$	\Rightarrow	y
	$x:\text{typeid}_I$	\Rightarrow	x

6.4.4 Reference Types

$\text{reftype}_I ::=$	$\text{'(' 'ref' ht:heaptypes ')}$	\Rightarrow	ref ht
	$\text{'(' 'ref' 'null' ht:heaptypes ')}$	\Rightarrow	ref null ht

Abbreviations

There are shorthands for references to abstract heap types.

'anyref'	\equiv	$\text{'(' 'ref' 'null' 'any' ')}$
'eqref'	\equiv	$\text{'(' 'ref' 'null' 'eq' ')}$
'i31ref'	\equiv	$\text{'(' 'ref' 'null' 'i31' ')}$
'structref'	\equiv	$\text{'(' 'ref' 'null' 'struct' ')}$
'arrayref'	\equiv	$\text{'(' 'ref' 'null' 'array' ')}$
'nullref'	\equiv	$\text{'(' 'ref' 'null' 'none' ')}$
'funcref'	\equiv	$\text{'(' 'ref' 'null' 'func' ')}$
'nullfuncref'	\equiv	$\text{'(' 'ref' 'null' 'nofunc' ')}$
'exnref'	\equiv	$\text{'(' 'ref' 'null' 'exn' ')}$
'nullexnref'	\equiv	$\text{'(' 'ref' 'null' 'noexn' ')}$
'externref'	\equiv	$\text{'(' 'ref' 'null' 'extern' ')}$
'nullexternref'	\equiv	$\text{'(' 'ref' 'null' 'noextern' ')}$

6.4.5 Value Types

$$\begin{array}{lll} \text{valtype}_I & ::= & t:\text{numtype}_I \Rightarrow t \\ & | & t:\text{vectype}_I \Rightarrow t \\ & | & t:\text{reftype}_I \Rightarrow t \end{array}$$

6.4.6 Function Types

$$\begin{array}{lll} \text{functype}_I & ::= & \text{'(' 'func' } t_1^*:\text{vec}(\text{param}_I) \text{ } t_2^*:\text{vec}(\text{result}_I) \text{'')} \Rightarrow [t_1^*] \rightarrow [t_2^*] \\ \text{param}_I & ::= & \text{'(' 'param' id? } t:\text{valtype}_I \text{'')} \Rightarrow t \\ \text{result}_I & ::= & \text{'(' 'result' } t:\text{valtype}_I \text{'')} \Rightarrow t \end{array}$$

Note: The optional identifier names for parameters in a function type only have documentation purpose. They cannot be referenced from anywhere.

Abbreviations

Multiple anonymous parameters or results may be combined into a single declaration:

$$\begin{array}{ll} \text{'(' 'param' valtype* '')} & \equiv \text{'(' 'param' valtype '')'*} \\ \text{'(' 'result' valtype* '')} & \equiv \text{'(' 'result' valtype '')'*} \end{array}$$

6.4.7 Aggregate Types

$$\begin{array}{lll} \text{arraytype}_I & ::= & \text{'(' 'array' } ft:\text{fieldtype}_I \text{'')} \Rightarrow ft \\ \text{structtype}_I & ::= & \text{'(' 'struct' } ft^*:\text{vec}(\text{field}_I) \text{'')} \Rightarrow ft^* \\ \text{field}_I & ::= & \text{'(' 'field' id? } ft:\text{fieldtype}_I \text{'')} \Rightarrow ft \\ \text{fieldtype}_I & ::= & st:\text{storagetype} \Rightarrow \text{const } st \\ & | & \text{'(' 'mut' } st:\text{storagetype} \text{'')} \Rightarrow \text{var } st \\ \text{storagetype}_I & ::= & t:\text{valtype}_I \Rightarrow t \\ & | & t:\text{packedtype} \Rightarrow t \\ \text{packedtype} & ::= & \text{'i8'} \Rightarrow \text{i8} \\ & | & \text{'i16'} \Rightarrow \text{i16} \end{array}$$

Abbreviations

Multiple anonymous structure fields may be combined into a single declaration:

$$\text{'(' 'field' fieldtype* '')} \equiv \text{'(' 'field' fieldtype '')'*}$$

6.4.8 Composite Types

$$\begin{array}{lll} \text{comptype}_I & ::= & at:\text{arraytype}_I \Rightarrow \text{array } at \\ & | & st:\text{structtype}_I \Rightarrow \text{struct } at \\ & | & ft:\text{functype}_I \Rightarrow \text{func } ft \end{array}$$

6.4.9 Recursive Types

$$\begin{aligned} \text{rectype}_I &::= \text{'(' 'rec' } st^*: \text{vec}(\text{typedef}_I) \text{'})' &\Rightarrow \text{rec } st^* \\ \text{typedef}_I &::= \text{'(' 'type' id? } st: \text{subtype}_I \text{'})' &\Rightarrow st \\ \text{subtype}_I &::= \text{'(' 'sub' 'final'? } x^*: \text{vec}(\text{typeid}_I) \text{ } ct: \text{comptype}_I \text{'})' &\Rightarrow \text{sub final? } x^* ct \end{aligned}$$

Abbreviations

Singular recursive types can omit the ‘rec’ keyword:

$$\text{typedef} \equiv \text{'(' 'rec' typedef ')'}$$

Similarly, final sub types with no super-types can omit the `sub` keyword and arguments:

$$\text{comptype} \equiv \text{'(' 'sub' 'final' \epsilon \text{comptype ') '}$$

6.4.10 Address Types

$$\begin{aligned} \text{addrtype} &::= \text{'i32'} &\Rightarrow \text{i32} \\ &| \text{'i64'} &\Rightarrow \text{i64} \end{aligned}$$

Abbreviations

The address type can be omitted, in which case it defaults `i32`:

$$\text{' } \equiv \text{'i32'}$$

6.4.11 Limits

$$\begin{aligned} \text{limits} &::= n: \text{u32} &\Rightarrow \{\min n, \max \epsilon\} \\ &| n: \text{u32 } m: \text{u32} &\Rightarrow \{\min n, \max m\} \end{aligned}$$

6.4.12 Memory Types

$$\text{memtype}_I ::= at: \text{addrtype } lim: \text{limits} \Rightarrow at \text{ } lim$$

6.4.13 Table Types

$$\text{tabletype}_I ::= at: \text{addrtype } lim: \text{limits } et: \text{reftype}_I \Rightarrow at \text{ } lim \text{ } et$$

6.4.14 Global Types

$$\begin{aligned} \text{globaltype}_I &::= t:\text{valtype} && \Rightarrow \text{const } t \\ &| (' \text{'mut'} t:\text{valtype}_I ') && \Rightarrow \text{var } t \end{aligned}$$

6.5 Instructions

Instructions are syntactically distinguished into *plain* and *structured* instructions.

$$\begin{aligned} \text{instr}_I &::= \text{in}:\text{plaininstr}_I && \Rightarrow \text{in} \\ &| \text{in}:\text{blockinstr}_I && \Rightarrow \text{in} \end{aligned}$$

In addition, as a syntactic abbreviation, instructions can be written as S-expressions in *folded* form, to group them visually.

6.5.1 Labels

Structured control instructions can be annotated with a symbolic *label identifier*. They are the only *symbolic identifiers* that can be bound locally in an instruction sequence. The following grammar handles the corresponding update to the *identifier context* by *composing* the context with an additional label entry.

$$\begin{aligned} \text{label}_I &::= v:\text{id} && \Rightarrow v, \{\text{labels } v\} \oplus I && (\text{if } v \notin I.\text{labels}) \\ &| v:\text{id} && \Rightarrow v, \{\text{labels } v\} \oplus (I \text{ with labels}[i] = \epsilon) && (\text{if } I.\text{labels}[i] = v) \\ &| \epsilon && \Rightarrow \epsilon, \{\text{labels } (\epsilon)\} \oplus I \end{aligned}$$

Note: The new label entry is inserted at the *beginning* of the label list in the identifier context. This effectively shifts all existing labels up by one, mirroring the fact that control instructions are indexed relatively not absolutely.

If a label with the same name already exists, then it is shadowed and the earlier label becomes inaccessible.

6.5.2 Control Instructions

Structured control instructions can bind an optional symbolic *label identifier*. The same label identifier may optionally be repeated after the corresponding *end* or *else* keywords, to indicate the matching delimiters.

Their *block type* is given as a *type use*, analogous to the type of *functions*. However, the special case of a type use that is syntactically empty or consists of only a single *result* is not regarded as an *abbreviation* for an inline *function type*, but is parsed directly into an optional *value type*.

$$\begin{aligned} \text{blocktype}_I &::= (t:\text{result}_I)? && \Rightarrow t? \\ &| x, I':\text{typeuse}_I && \Rightarrow x \quad (\text{if } I' = \{\text{locals } (\epsilon)^*\}) \\ \text{blockinstr}_I &::= \text{'block'} (v^?, I'):\text{label}_I \text{ bt}:\text{blocktype}_I (\text{in}:\text{instr}_{I'})^* \text{'end'} v'^?:\text{id}^? \\ &\Rightarrow \text{block } bt \text{ in}^* \text{ end} && (\text{if } v'^? = \epsilon \vee v'^? = v^?) \\ &| \text{'loop'} (v^?, I'):\text{label}_I \text{ bt}:\text{blocktype}_I (\text{in}:\text{instr}_{I'})^* \text{'end'} v'^?:\text{id}^? \\ &\Rightarrow \text{loop } bt \text{ in}^* \text{ end} && (\text{if } v'^? = \epsilon \vee v'^? = v^?) \\ &| \text{'if'} (v^?, I'):\text{label}_I \text{ bt}:\text{blocktype}_I (\text{in}_1:\text{instr}_{I'})^* \text{'else'} v_1^?:\text{id}_1^? (\text{in}_2:\text{instr}_{I'})^* \text{'end'} v_2^?:\text{id}_2^? \\ &\Rightarrow \text{if } bt \text{ in}_1^* \text{ else } \text{in}_2^* \text{ end} && (\text{if } v_1^? = \epsilon \vee v_1^? = v^?, v_2^? = \epsilon \vee v_2^? = v^?) \\ &| \text{'try_table'} I':\text{label}_I \text{ bt}:\text{blocktype}_I (c:\text{catch}_I)^* (\text{in}:\text{instr}_{I'})^* \text{'end'} \text{id}^? \\ &\Rightarrow \text{try_table } bt \text{ c}^* \text{ in}^* \text{ end} && (\text{if } \text{id}^? = \epsilon \vee \text{id}^? = \text{label}) \\ \text{catch}_I &::= (' \text{'catch'} x:\text{tagidx}_I \text{ l}:\text{labelidx}_I ') && \Rightarrow \text{catch } x \text{ l} \\ &| (' \text{'catch_ref'} x:\text{tagidx}_I \text{ l}:\text{labelidx}_I ') && \Rightarrow \text{catch_ref } x \text{ l} \\ &| (' \text{'catch_all'} l:\text{labelidx}_I ') && \Rightarrow \text{catch_all } l \\ &| (' \text{'catch_all_ref'} l:\text{labelidx}_I ') && \Rightarrow \text{catch_all_ref } l \end{aligned}$$

Note: The side condition stating that the [identifier context](#) I' must only contain unnamed entries in the rule for [typeuse](#) block types enforces that no identifier can be bound in any [param](#) declaration for a block type.

All other control instruction are represented verbatim.

<code>plaininstr_I</code>	<code>::=</code>	<code>'unreachable'</code>	\Rightarrow	<code>unreachable</code>	
		<code>'nop'</code>	\Rightarrow	<code>nop</code>	
		<code>'br' l:labelidx_I</code>	\Rightarrow	<code>br l</code>	
		<code>'br_if' l:labelidx_I</code>	\Rightarrow	<code>br_if l</code>	
		<code>'br_table' l*:vec(labelidx_I) l_N:labelidx_I</code>	\Rightarrow	<code>br_table l* l_N</code>	
		<code>'br_on_null' l:labelidx_I</code>	\Rightarrow	<code>br_on_null l</code>	
		<code>'br_on_non_null' l:labelidx_I</code>	\Rightarrow	<code>br_on_non_null l</code>	
		<code>'br_on_cast' l:labelidx_I t₁:reftype t₂:reftype</code>	\Rightarrow	<code>br_on_cast l t₁ t₂</code>	
		<code>'br_on_cast_fail' l:labelidx_I t₁:reftype t₂:reftype</code>	\Rightarrow	<code>br_on_cast_fail l t₁ t₂</code>	
		<code>'return'</code>	\Rightarrow	<code>return</code>	
		<code>'call' x:funcidx_I</code>	\Rightarrow	<code>call x</code>	
		<code>'call_ref' x:typeidx</code>	\Rightarrow	<code>call_ref x</code>	
		<code>'call_indirect' x:tableidx y, I':typeuse_I</code>	\Rightarrow	<code>call_indirect x y</code>	(if $I' =$
		<code>'return_call' x:funcidx_I</code>	\Rightarrow	<code>return_call x</code>	
		<code>'return_call_ref' x:typeidx</code>	\Rightarrow	<code>return_call_ref x</code>	
		<code>'return_call_indirect' x:tableidx y, I':typeuse_I</code>	\Rightarrow	<code>return_call_indirect x y</code>	(if $I' =$
		<code>'throw' x:tagidx_I</code>	\Rightarrow	<code>throw x</code>	
		<code>'throw_ref'</code>	\Rightarrow	<code>throw_ref</code>	

Note: The side condition stating that the [identifier context](#) I' must only contain unnamed entries in the rule for [call_indirect](#) enforces that no identifier can be bound in any [param](#) declaration appearing in the type annotation.

Abbreviations

The 'else' keyword of an 'if' instruction can be omitted if the following instruction sequence is empty.

`'if' label blocktypeI instr* 'end'` \equiv `'if' label blocktypeI instr* 'else' 'end'`

Also, for backwards compatibility, the table index to 'call_indirect' and 'return_call_indirect' can be omitted, defaulting to 0.

`'return_call_indirect' typeuse` \equiv `'return_call_indirect' 0 typeuse` \equiv `'call_indirect' typeuse` \equiv `'call_indirect' 0 typeuse`

6.5.3 Reference Instructions

<code>plaininstr_I</code>	<code>::=</code>	<code>...</code>	
		<code>'ref.null' t:heaptype</code>	\Rightarrow <code>ref.null t</code>
		<code>'ref.func' x:funcidx</code>	\Rightarrow <code>ref.func x</code>
		<code>'ref.is_null'</code>	\Rightarrow <code>ref.is_null</code>
		<code>'ref.as_non_null'</code>	\Rightarrow <code>ref.as_non_null</code>
		<code>'ref.eq'</code>	\Rightarrow <code>ref.eq</code>
		<code>'ref.test' t:reftype</code>	\Rightarrow <code>ref.test t</code>
		<code>'ref.cast' t:reftype</code>	\Rightarrow <code>ref.cast t</code>
		<code>'struct.new' x:typeidx_I</code>	\Rightarrow <code>struct.new x</code>
		<code>'struct.new_default' x:typeidx_I</code>	\Rightarrow <code>struct.new_default x</code>
		<code>'struct.get' x:typeidx_I y:fieldidx_{I,x}</code>	\Rightarrow <code>struct.get x y</code>
		<code>'struct.get_u' x:typeidx_I y:fieldidx_{I,x}</code>	\Rightarrow <code>struct.get_u x y</code>
		<code>'struct.get_s' x:typeidx_I y:fieldidx_{I,x}</code>	\Rightarrow <code>struct.get_s x y</code>
		<code>'struct.set' x:typeidx_I y:fieldidx_{I,x}</code>	\Rightarrow <code>struct.set x y</code>
		<code>'array.new' x:typeidx_I</code>	\Rightarrow <code>array.new x</code>
		<code>'array.new_default' x:typeidx_I</code>	\Rightarrow <code>array.new_default x</code>
		<code>'array.new_fixed' x:typeidx_I n:u32</code>	\Rightarrow <code>array.new_fixed x n</code>
		<code>'array.new_data' x:typeidx_I y:dataidx_I</code>	\Rightarrow <code>array.new_data x y</code>
		<code>'array.new_elem' x:typeidx_I y:elemidx_I</code>	\Rightarrow <code>array.new_elem x y</code>
		<code>'array.get' x:typeidx_I</code>	\Rightarrow <code>array.get x</code>
		<code>'array.get_u' x:typeidx_I</code>	\Rightarrow <code>array.get_u x</code>
		<code>'array.get_s' x:typeidx_I</code>	\Rightarrow <code>array.get_s x</code>
		<code>'array.set' x:typeidx_I</code>	\Rightarrow <code>array.set x</code>
		<code>'array.len'</code>	\Rightarrow <code>array.len</code>
		<code>'array.fill' x:typeidx_I</code>	\Rightarrow <code>array.fill x</code>
		<code>'array.copy' x:typeidx_I y:typeidx_I</code>	\Rightarrow <code>array.copy x y</code>
		<code>'array.init_data' x:typeidx_I y:dataidx_I</code>	\Rightarrow <code>array.init_data x y</code>
		<code>'array.init_elem' x:typeidx_I y:elemidx_I</code>	\Rightarrow <code>array.init_elem x y</code>
		<code>'ref.i31'</code>	\Rightarrow <code>ref.i31</code>
		<code>'i31.get_u'</code>	\Rightarrow <code>i31.get_u</code>
		<code>'i31.get_s'</code>	\Rightarrow <code>i31.get_s</code>
		<code>'any.convert_extern'</code>	\Rightarrow <code>any.convert_extern</code>
		<code>'extern.convert_any'</code>	\Rightarrow <code>extern.convert_any</code>

6.5.4 Parametric Instructions

<code>plaininstr_I</code>	<code>::=</code>	<code>...</code>	
		<code>'drop'</code>	\Rightarrow <code>drop</code>
		<code>'select' ((t:result_I)*)?</code>	\Rightarrow <code>select (t*)?</code>

6.5.5 Variable Instructions

<code>plaininstr_I</code>	<code>::=</code>	<code>...</code>	
		<code>'local.get' x:localidx_I</code>	\Rightarrow <code>local.get x</code>
		<code>'local.set' x:localidx_I</code>	\Rightarrow <code>local.set x</code>
		<code>'local.tee' x:localidx_I</code>	\Rightarrow <code>local.tee x</code>
		<code>'global.get' x:globalidx_I</code>	\Rightarrow <code>global.get x</code>
		<code>'global.set' x:globalidx_I</code>	\Rightarrow <code>global.set x</code>

6.5.6 Table Instructions

<code>plaininstr_I</code>	::=	...	
		<code>'table.get' x:tableidx_I</code>	⇒ <code>table.get x</code>
		<code>'table.set' x:tableidx_I</code>	⇒ <code>table.set x</code>
		<code>'table.size' x:tableidx_I</code>	⇒ <code>table.size x</code>
		<code>'table.grow' x:tableidx_I</code>	⇒ <code>table.grow x</code>
		<code>'table.fill' x:tableidx_I</code>	⇒ <code>table.fill x</code>
		<code>'table.copy' x:tableidx_I y:tableidx_I</code>	⇒ <code>table.copy x y</code>
		<code>'table.init' x:tableidx_I y:elemidx_I</code>	⇒ <code>table.init x y</code>
		<code>'elem.drop' x:elemidx_I</code>	⇒ <code>elem.drop x</code>

Abbreviations

For backwards compatibility, all [table indices](#) may be omitted from table instructions, defaulting to 0.

<code>'table.get'</code>	≡	<code>'table.get' '0'</code>
<code>'table.set'</code>	≡	<code>'table.set' '0'</code>
<code>'table.size'</code>	≡	<code>'table.size' '0'</code>
<code>'table.grow'</code>	≡	<code>'table.grow' '0'</code>
<code>'table.fill'</code>	≡	<code>'table.fill' '0'</code>
<code>'table.copy'</code>	≡	<code>'table.copy' '0' '0'</code>
<code>'table.init' x:elemidx_I</code>	≡	<code>'table.init' '0' x:elemidx_I</code>

6.5.7 Memory Instructions

The offset and alignment immediates to memory instructions are optional. The offset defaults to 0, the alignment to the storage size of the respective memory access, which is its *natural alignment*. Lexically, an [offset](#) or [align](#)

phrase is considered a single **keyword token**, so no **white space** is allowed around the '='.

<code>memarg_N</code>	::=	<code>o:offset a:align_N</code>	⇒	{align
<code>offset</code>	::=	<code>'offset='o:u32</code>	⇒	<code>o</code>
		<code>ϵ</code>	⇒	<code>0</code>
<code>align_N</code>	::=	<code>'align='a:u32</code>	⇒	<code>a</code>
		<code>ϵ</code>	⇒	<code>N</code>
<code>plaininstr_I</code>	::=	...		
		<code>'i32.load' x:memidx m:memarg₄</code>	⇒	<code>i32.lo</code>
		<code>'i64.load' x:memidx m:memarg₈</code>	⇒	<code>i64.lo</code>
		<code>'f32.load' x:memidx m:memarg₄</code>	⇒	<code>f32.lo</code>
		<code>'f64.load' x:memidx m:memarg₈</code>	⇒	<code>f64.lo</code>
		<code>'v128.load' x:memidx m:memarg₁₆</code>	⇒	<code>v128.</code>
		<code>'i32.load8_s' x:memidx m:memarg₁</code>	⇒	<code>i32.lo</code>
		<code>'i32.load8_u' x:memidx m:memarg₁</code>	⇒	<code>i32.lo</code>
		<code>'i32.load16_s' x:memidx m:memarg₂</code>	⇒	<code>i32.lo</code>
		<code>'i32.load16_u' x:memidx m:memarg₂</code>	⇒	<code>i32.lo</code>
		<code>'i64.load8_s' x:memidx m:memarg₁</code>	⇒	<code>i64.lo</code>
		<code>'i64.load8_u' x:memidx m:memarg₁</code>	⇒	<code>i64.lo</code>
		<code>'i64.load16_s' x:memidx m:memarg₂</code>	⇒	<code>i64.lo</code>
		<code>'i64.load16_u' x:memidx m:memarg₂</code>	⇒	<code>i64.lo</code>
		<code>'i64.load32_s' x:memidx m:memarg₄</code>	⇒	<code>i64.lo</code>
		<code>'i64.load32_u' x:memidx m:memarg₄</code>	⇒	<code>i64.lo</code>
		<code>'v128.load8x8_s' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load8x8_u' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load16x4_s' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load16x4_u' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load32x2_s' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load32x2_u' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load8_splat' x:memidx m:memarg₁</code>	⇒	<code>v128.</code>
		<code>'v128.load16_splat' x:memidx m:memarg₂</code>	⇒	<code>v128.</code>
		<code>'v128.load32_splat' x:memidx m:memarg₄</code>	⇒	<code>v128.</code>
		<code>'v128.load64_splat' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load32_zero' x:memidx m:memarg₄</code>	⇒	<code>v128.</code>
		<code>'v128.load64_zero' x:memidx m:memarg₈</code>	⇒	<code>v128.</code>
		<code>'v128.load8_lane' x:memidx m:memarg₁ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.load16_lane' x:memidx m:memarg₂ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.load32_lane' x:memidx m:memarg₄ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.load64_lane' x:memidx m:memarg₈ y:u8</code>	⇒	<code>v128.</code>
		<code>'i32.store' x:memidx m:memarg₄</code>	⇒	<code>i32.st</code>
		<code>'i64.store' x:memidx m:memarg₈</code>	⇒	<code>i64.st</code>
		<code>'f32.store' x:memidx m:memarg₄</code>	⇒	<code>f32.st</code>
		<code>'f64.store' x:memidx m:memarg₈</code>	⇒	<code>f64.st</code>
		<code>'v128.store' x:memidx m:memarg₁₆</code>	⇒	<code>v128.</code>
		<code>'i32.store8' x:memidx m:memarg₁</code>	⇒	<code>i32.st</code>
		<code>'i32.store16' x:memidx m:memarg₂</code>	⇒	<code>i32.st</code>
		<code>'i64.store8' x:memidx m:memarg₁</code>	⇒	<code>i64.st</code>
		<code>'i64.store16' x:memidx m:memarg₂</code>	⇒	<code>i64.st</code>
		<code>'i64.store32' x:memidx m:memarg₄</code>	⇒	<code>i64.st</code>
		<code>'v128.store8_lane' x:memidx m:memarg₁ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.store16_lane' x:memidx m:memarg₂ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.store32_lane' x:memidx m:memarg₄ y:u8</code>	⇒	<code>v128.</code>
		<code>'v128.store64_lane' x:memidx m:memarg₈ y:u8</code>	⇒	<code>v128.</code>
<code>'memory.size' x:memidx</code>	⇒	<code>memory.size x</code>		
		<code>'memory.grow' x:memidx</code>	⇒	<code>memo</code>
		<code>'memory.fill' x:memidx</code>	⇒	<code>memo</code>
		<code>'memory.copy' x:memidx y:memidx</code>	⇒	<code>memo</code>
		<code>'memory.init' x:memidx y:dataidx_I</code>	⇒	<code>memo</code>
		<code>'data.drop' x:dataidx_I</code>	⇒	<code>data.o</code>

Abbreviations

As an abbreviation, the memory index can be omitted in all memory instructions, defaulting to 0.

<code>numtype.load memarg</code>	\equiv	<code>numtype.load '0' memarg</code>
<code>vectype.load memarg</code>	\equiv	<code>vectype.load '0' memarg</code>
<code>numtype.load N '_sx' memarg</code>	\equiv	<code>numtype.load N '_sx' '0' memarg</code>
<code>vectype.load NxM '_sx' memarg</code>	\equiv	<code>vectype.load NxM '_sx' '0' memarg</code>
<code>vectype.load N '_splat' memarg</code>	\equiv	<code>vectype.load N '_splat' '0' memarg</code>
<code>vectype.load N '_zero' memarg</code>	\equiv	<code>vectype.load N '_zero' '0' memarg</code>
<code>vectype.load N '_lane' memarg u8</code>	\equiv	<code>vectype.load N '_lane' '0' memarg u8</code>
<code>numtype.store memarg</code>	\equiv	<code>numtype.store '0' memarg</code>
<code>vectype.store memarg</code>	\equiv	<code>vectype.store '0' memarg</code>
<code>numtype.store N memarg</code>	\equiv	<code>numtype.store N '0' memarg</code>
<code>vectype.store N '_lane' memarg u8</code>	\equiv	<code>vectype.store N '_lane' '0' memarg u8</code>
<code>'memory.size'</code>	\equiv	<code>'memory.size' '0'</code>
<code>'memory.grow'</code>	\equiv	<code>'memory.grow' '0'</code>
<code>'memory.fill'</code>	\equiv	<code>'memory.fill' '0'</code>
<code>'memory.copy'</code>	\equiv	<code>'memory.copy' '0' '0'</code>
<code>'memory.init' x:elemidx_I</code>	\equiv	<code>'memory.init' '0' x:elemidx_I</code>

6.5.8 Numeric Instructions

```
plaininstrI ::= ...
| 'i32.const' n:i32  ⇒ i32.const n
| 'i64.const' n:i64  ⇒ i64.const n
| 'f32.const' z:f32  ⇒ f32.const z
| 'f64.const' z:f64  ⇒ f64.const z

| 'i32.clz'          ⇒ i32.clz
| 'i32.ctz'          ⇒ i32.ctz
| 'i32.popcnt'       ⇒ i32.popcnt
| 'i32.add'          ⇒ i32.add
| 'i32.sub'          ⇒ i32.sub
| 'i32.mul'          ⇒ i32.mul
| 'i32.div_s'        ⇒ i32.div_s
| 'i32.div_u'        ⇒ i32.div_u
| 'i32.rem_s'        ⇒ i32.rem_s
| 'i32.rem_u'        ⇒ i32.rem_u
| 'i32.and'          ⇒ i32.and
| 'i32.or'           ⇒ i32.or
| 'i32.xor'          ⇒ i32.xor
| 'i32.shl'          ⇒ i32.shl
| 'i32.shr_s'        ⇒ i32.shr_s
| 'i32.shr_u'        ⇒ i32.shr_u
| 'i32.rotl'         ⇒ i32.rotl
| 'i32.rotr'         ⇒ i32.rotr
```

'i64.clz'	⇒	i64.clz
'i64.ctz'	⇒	i64.ctz
'i64.popcnt'	⇒	i64.popcnt
'i64.add'	⇒	i64.add
'i64.sub'	⇒	i64.sub
'i64.mul'	⇒	i64.mul
'i64.div_s'	⇒	i64.div_s
'i64.div_u'	⇒	i64.div_u
'i64.rem_s'	⇒	i64.rem_s
'i64.rem_u'	⇒	i64.rem_u
'i64.and'	⇒	i64.and
'i64.or'	⇒	i64.or
'i64.xor'	⇒	i64.xor
'i64.shl'	⇒	i64.shl
'i64.shr_s'	⇒	i64.shr_s
'i64.shr_u'	⇒	i64.shr_u
'i64.rotl'	⇒	i64.rotl
'i64.rotr'	⇒	i64.rotr
'f32.abs'	⇒	f32.abs
'f32.neg'	⇒	f32.neg
'f32.ceil'	⇒	f32.ceil
'f32.floor'	⇒	f32.floor
'f32.trunc'	⇒	f32.trunc
'f32.nearest'	⇒	f32.nearest
'f32.sqrt'	⇒	f32.sqrt
'f32.add'	⇒	f32.add
'f32.sub'	⇒	f32.sub
'f32.mul'	⇒	f32.mul
'f32.div'	⇒	f32.div
'f32.min'	⇒	f32.min
'f32.max'	⇒	f32.max
'f32.copysign'	⇒	f32.copysign
'f64.abs'	⇒	f64.abs
'f64.neg'	⇒	f64.neg
'f64.ceil'	⇒	f64.ceil
'f64.floor'	⇒	f64.floor
'f64.trunc'	⇒	f64.trunc
'f64.nearest'	⇒	f64.nearest
'f64.sqrt'	⇒	f64.sqrt
'f64.add'	⇒	f64.add
'f64.sub'	⇒	f64.sub
'f64.mul'	⇒	f64.mul
'f64.div'	⇒	f64.div
'f64.min'	⇒	f64.min
'f64.max'	⇒	f64.max
'f64.copysign'	⇒	f64.copysign

	'i32.eqz'	⇒	i32.eqz
	'i32.eq'	⇒	i32.eq
	'i32.ne'	⇒	i32.ne
	'i32.lt_s'	⇒	i32.lt_s
	'i32.lt_u'	⇒	i32.lt_u
	'i32.gt_s'	⇒	i32.gt_s
	'i32.gt_u'	⇒	i32.gt_u
	'i32.le_s'	⇒	i32.le_s
	'i32.le_u'	⇒	i32.le_u
	'i32.ge_s'	⇒	i32.ge_s
	'i32.ge_u'	⇒	i32.ge_u
	'i64.eqz'	⇒	i64.eqz
	'i64.eq'	⇒	i64.eq
	'i64.ne'	⇒	i64.ne
	'i64.lt_s'	⇒	i64.lt_s
	'i64.lt_u'	⇒	i64.lt_u
	'i64.gt_s'	⇒	i64.gt_s
	'i64.gt_u'	⇒	i64.gt_u
	'i64.le_s'	⇒	i64.le_s
	'i64.le_u'	⇒	i64.le_u
	'i64.ge_s'	⇒	i64.ge_s
	'i64.ge_u'	⇒	i64.ge_u
	'f32.eq'	⇒	f32.eq
	'f32.ne'	⇒	f32.ne
	'f32.lt'	⇒	f32.lt
	'f32.gt'	⇒	f32.gt
	'f32.le'	⇒	f32.le
	'f32.ge'	⇒	f32.ge
	'f64.eq'	⇒	f64.eq
	'f64.ne'	⇒	f64.ne
	'f64.lt'	⇒	f64.lt
	'f64.gt'	⇒	f64.gt
	'f64.le'	⇒	f64.le
	'f64.ge'	⇒	f64.ge

'i32.wrap_i64'	⇒	i32.wrap_i64
'i32.trunc_f32_s'	⇒	i32.trunc_f32_s
'i32.trunc_f32_u'	⇒	i32.trunc_f32_u
'i32.trunc_f64_s'	⇒	i32.trunc_f64_s
'i32.trunc_f64_u'	⇒	i32.trunc_f64_u
'i32.trunc_sat_f32_s'	⇒	i32.trunc_sat_f32_s
'i32.trunc_sat_f32_u'	⇒	i32.trunc_sat_f32_u
'i32.trunc_sat_f64_s'	⇒	i32.trunc_sat_f64_s
'i32.trunc_sat_f64_u'	⇒	i32.trunc_sat_f64_u
'i64.extend_i32_s'	⇒	i64.extend_i32_s
'i64.extend_i32_u'	⇒	i64.extend_i32_u
'i64.trunc_f32_s'	⇒	i64.trunc_f32_s
'i64.trunc_f32_u'	⇒	i64.trunc_f32_u
'i64.trunc_f64_s'	⇒	i64.trunc_f64_s
'i64.trunc_f64_u'	⇒	i64.trunc_f64_u
'i64.trunc_sat_f32_s'	⇒	i64.trunc_sat_f32_s
'i64.trunc_sat_f32_u'	⇒	i64.trunc_sat_f32_u
'i64.trunc_sat_f64_s'	⇒	i64.trunc_sat_f64_s
'i64.trunc_sat_f64_u'	⇒	i64.trunc_sat_f64_u
'f32.convert_i32_s'	⇒	f32.convert_i32_s
'f32.convert_i32_u'	⇒	f32.convert_i32_u
'f32.convert_i64_s'	⇒	f32.convert_i64_s
'f32.convert_i64_u'	⇒	f32.convert_i64_u
'f32.demote_f64'	⇒	f32.demote_f64
'f64.convert_i32_s'	⇒	f64.convert_i32_s
'f64.convert_i32_u'	⇒	f64.convert_i32_u
'f64.convert_i64_s'	⇒	f64.convert_i64_s
'f64.convert_i64_u'	⇒	f64.convert_i64_u
'f64.promote_f32'	⇒	f64.promote_f32
'i32.reinterpret_f32'	⇒	i32.reinterpret_f32
'i64.reinterpret_f64'	⇒	i64.reinterpret_f64
'f32.reinterpret_i32'	⇒	f32.reinterpret_i32
'f64.reinterpret_i64'	⇒	f64.reinterpret_i64
'i32.extend8_s'	⇒	i32.extend8_s
'i32.extend16_s'	⇒	i32.extend16_s
'i64.extend8_s'	⇒	i64.extend8_s
'i64.extend16_s'	⇒	i64.extend16_s
'i64.extend32_s'	⇒	i64.extend32_s

6.5.9 Vector Instructions

Vector constant instructions have a mandatory [shape](#) descriptor, which determines how the following values are parsed.

'v128.const' 'i8x16' (n:i8) ¹⁶	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{i8} (n) ¹⁶)
'v128.const' 'i16x8' (n:i16) ⁸	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{i16} (n) ⁸)
'v128.const' 'i32x4' (n:i32) ⁴	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{i32} (n) ⁴)
'v128.const' 'i64x2' (n:i64) ²	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{i64} (n) ²)
'v128.const' 'f32x4' (z:f32) ⁴	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{f32} (z) ⁴)
'v128.const' 'f64x2' (z:f64) ²	⇒	v128.const bytes _{i128} ⁻¹ (bytes _{f64} (z) ²)
'i8x16.shuffle' (laneidx:u8) ¹⁶	⇒	i8x16.shuffle laneidx ¹⁶
'i8x16.swizzle'	⇒	i8x16.swizzle

'i8x16.splat'	⇒	i8x16.splat
'i16x8.splat'	⇒	i16x8.splat
'i32x4.splat'	⇒	i32x4.splat
'i64x2.splat'	⇒	i64x2.splat
'f32x4.splat'	⇒	f32x4.splat
'f64x2.splat'	⇒	f64x2.splat
'i8x16.extract_lane_s' laneidx:u8	⇒	i8x16.extract_lane_s laneidx
'i8x16.extract_lane_u' laneidx:u8	⇒	i8x16.extract_lane_u laneidx
'i8x16.replace_lane' laneidx:u8	⇒	i8x16.replace_lane laneidx
'i16x8.extract_lane_s' laneidx:u8	⇒	i16x8.extract_lane_s laneidx
'i16x8.extract_lane_u' laneidx:u8	⇒	i16x8.extract_lane_u laneidx
'i16x8.replace_lane' laneidx:u8	⇒	i16x8.replace_lane laneidx
'i32x4.extract_lane' laneidx:u8	⇒	i32x4.extract_lane laneidx
'i32x4.replace_lane' laneidx:u8	⇒	i32x4.replace_lane laneidx
'i64x2.extract_lane' laneidx:u8	⇒	i64x2.extract_lane laneidx
'i64x2.replace_lane' laneidx:u8	⇒	i64x2.replace_lane laneidx
'f32x4.extract_lane' laneidx:u8	⇒	f32x4.extract_lane laneidx
'f32x4.replace_lane' laneidx:u8	⇒	f32x4.replace_lane laneidx
'f64x2.extract_lane' laneidx:u8	⇒	f64x2.extract_lane laneidx
'f64x2.replace_lane' laneidx:u8	⇒	f64x2.replace_lane laneidx
'i8x16.eq'	⇒	i8x16.eq
'i8x16.ne'	⇒	i8x16.ne
'i8x16.lt_s'	⇒	i8x16.lt_s
'i8x16.lt_u'	⇒	i8x16.lt_u
'i8x16.gt_s'	⇒	i8x16.gt_s
'i8x16.gt_u'	⇒	i8x16.gt_u
'i8x16.le_s'	⇒	i8x16.le_s
'i8x16.le_u'	⇒	i8x16.le_u
'i8x16.ge_s'	⇒	i8x16.ge_s
'i8x16.ge_u'	⇒	i8x16.ge_u
'i16x8.eq'	⇒	i16x8.eq
'i16x8.ne'	⇒	i16x8.ne
'i16x8.lt_s'	⇒	i16x8.lt_s
'i16x8.lt_u'	⇒	i16x8.lt_u
'i16x8.gt_s'	⇒	i16x8.gt_s
'i16x8.gt_u'	⇒	i16x8.gt_u
'i16x8.le_s'	⇒	i16x8.le_s
'i16x8.le_u'	⇒	i16x8.le_u
'i16x8.ge_s'	⇒	i16x8.ge_s
'i16x8.ge_u'	⇒	i16x8.ge_u
'i32x4.eq'	⇒	i32x4.eq
'i32x4.ne'	⇒	i32x4.ne
'i32x4.lt_s'	⇒	i32x4.lt_s
'i32x4.lt_u'	⇒	i32x4.lt_u
'i32x4.gt_s'	⇒	i32x4.gt_s
'i32x4.gt_u'	⇒	i32x4.gt_u
'i32x4.le_s'	⇒	i32x4.le_s
'i32x4.le_u'	⇒	i32x4.le_u
'i32x4.ge_s'	⇒	i32x4.ge_s
'i32x4.ge_u'	⇒	i32x4.ge_u

'i64x2.eq'	⇒	i64x2.eq
'i64x2.ne'	⇒	i64x2.ne
'i64x2.lt_s'	⇒	i64x2.lt_s
'i64x2.gt_s'	⇒	i64x2.gt_s
'i64x2.le_s'	⇒	i64x2.le_s
'i64x2.ge_s'	⇒	i64x2.ge_s
'f32x4.eq'	⇒	f32x4.eq
'f32x4.ne'	⇒	f32x4.ne
'f32x4.lt'	⇒	f32x4.lt
'f32x4.gt'	⇒	f32x4.gt
'f32x4.le'	⇒	f32x4.le
'f32x4.ge'	⇒	f32x4.ge
'f64x2.eq'	⇒	f64x2.eq
'f64x2.ne'	⇒	f64x2.ne
'f64x2.lt'	⇒	f64x2.lt
'f64x2.gt'	⇒	f64x2.gt
'f64x2.le'	⇒	f64x2.le
'f64x2.ge'	⇒	f64x2.ge
'v128.not'	⇒	v128.not
'v128.and'	⇒	v128.and
'v128.andnot'	⇒	v128.andnot
'v128.or'	⇒	v128.or
'v128.xor'	⇒	v128.xor
'v128.bitselect'	⇒	v128.bitselect
'v128.any_true'	⇒	v128.any_true
'i8x16.abs'	⇒	i8x16.abs
'i8x16.neg'	⇒	i8x16.neg
'i8x16.all_true'	⇒	i8x16.all_true
'i8x16.bitmask'	⇒	i8x16.bitmask
'i8x16.narrow_i16x8_s'	⇒	i8x16.narrow_i16x8_s
'i8x16.narrow_i16x8_u'	⇒	i8x16.narrow_i16x8_u
'i8x16.shl'	⇒	i8x16.shl
'i8x16.shr_s'	⇒	i8x16.shr_s
'i8x16.shr_u'	⇒	i8x16.shr_u
'i8x16.add'	⇒	i8x16.add
'i8x16.add_sat_s'	⇒	i8x16.add_sat_s
'i8x16.add_sat_u'	⇒	i8x16.add_sat_u
'i8x16.sub'	⇒	i8x16.sub
'i8x16.sub_sat_s'	⇒	i8x16.sub_sat_s
'i8x16.sub_sat_u'	⇒	i8x16.sub_sat_u
'i8x16.min_s'	⇒	i8x16.min_s
'i8x16.min_u'	⇒	i8x16.min_u
'i8x16.max_s'	⇒	i8x16.max_s
'i8x16.max_u'	⇒	i8x16.max_u
'i8x16.avgr_u'	⇒	i8x16.avgr_u
'i8x16.popcnt'	⇒	i8x16.popcnt

'i16x8.abs'	⇒	i16x8.abs
'i16x8.neg'	⇒	i16x8.neg
'i16x8.all_true'	⇒	i16x8.all_true
'i16x8.bitmask'	⇒	i16x8.bitmask
'i16x8.narrow_i32x4_s'	⇒	i16x8.narrow_i32x4_s
'i16x8.narrow_i32x4_u'	⇒	i16x8.narrow_i32x4_u
'i16x8.extend_low_i8x16_s'	⇒	i16x8.extend_low_i8x16_s
'i16x8.extend_high_i8x16_s'	⇒	i16x8.extend_high_i8x16_s
'i16x8.extend_low_i8x16_u'	⇒	i16x8.extend_low_i8x16_u
'i16x8.extend_high_i8x16_u'	⇒	i16x8.extend_high_i8x16_u
'i16x8.shl'	⇒	i16x8.shl
'i16x8.shr_s'	⇒	i16x8.shr_s
'i16x8.shr_u'	⇒	i16x8.shr_u
'i16x8.add'	⇒	i16x8.add
'i16x8.add_sat_s'	⇒	i16x8.add_sat_s
'i16x8.add_sat_u'	⇒	i16x8.add_sat_u
'i16x8.sub'	⇒	i16x8.sub
'i16x8.sub_sat_s'	⇒	i16x8.sub_sat_s
'i16x8.sub_sat_u'	⇒	i16x8.sub_sat_u
'i16x8.mul'	⇒	i16x8.mul
'i16x8.min_s'	⇒	i16x8.min_s
'i16x8.min_u'	⇒	i16x8.min_u
'i16x8.max_s'	⇒	i16x8.max_s
'i16x8.max_u'	⇒	i16x8.max_u
'i16x8.avgr_u'	⇒	i16x8.avgr_u
'i16x8.q15mulr_sat_s'	⇒	i16x8.q15mulr_sat_s
'i16x8.extmul_low_i8x16_s'	⇒	i16x8.extmul_low_i8x16_s
'i16x8.extmul_high_i8x16_s'	⇒	i16x8.extmul_high_i8x16_s
'i16x8.extmul_low_i8x16_u'	⇒	i16x8.extmul_low_i8x16_u
'i16x8.extmul_high_i8x16_u'	⇒	i16x8.extmul_high_i8x16_u
'i16x8.extadd_pairwise_i8x16_s'	⇒	i16x8.extadd_pairwise_i8x16_s
'i16x8.extadd_pairwise_i8x16_u'	⇒	i16x8.extadd_pairwise_i8x16_u
'i32x4.abs'	⇒	i32x4.abs
'i32x4.neg'	⇒	i32x4.neg
'i32x4.all_true'	⇒	i32x4.all_true
'i32x4.bitmask'	⇒	i32x4.bitmask
'i32x4.extadd_pairwise_i16x8_s'	⇒	i32x4.extadd_pairwise_i16x8_s
'i32x4.extadd_pairwise_i16x8_u'	⇒	i32x4.extadd_pairwise_i16x8_u
'i32x4.extend_low_i16x8_s'	⇒	i32x4.extend_low_i16x8_s
'i32x4.extend_high_i16x8_s'	⇒	i32x4.extend_high_i16x8_s
'i32x4.extend_low_i16x8_u'	⇒	i32x4.extend_low_i16x8_u
'i32x4.extend_high_i16x8_u'	⇒	i32x4.extend_high_i16x8_u
'i32x4.shl'	⇒	i32x4.shl
'i32x4.shr_s'	⇒	i32x4.shr_s
'i32x4.shr_u'	⇒	i32x4.shr_u
'i32x4.add'	⇒	i32x4.add
'i32x4.sub'	⇒	i32x4.sub
'i32x4.mul'	⇒	i32x4.mul
'i32x4.min_s'	⇒	i32x4.min_s
'i32x4.min_u'	⇒	i32x4.min_u
'i32x4.max_s'	⇒	i32x4.max_s
'i32x4.max_u'	⇒	i32x4.max_u
'i32x4.dot_i16x8_s'	⇒	i32x4.dot_i16x8_s
'i32x4.extmul_low_i16x8_s'	⇒	i32x4.extmul_low_i16x8_s
'i32x4.extmul_high_i16x8_s'	⇒	i32x4.extmul_high_i16x8_s
'i32x4.extmul_low_i16x8_u'	⇒	i32x4.extmul_low_i16x8_u
'i32x4.extmul_high_i16x8_u'	⇒	i32x4.extmul_high_i16x8_u

'i64x2.abs'	⇒	i64x2.abs
'i64x2.neg'	⇒	i64x2.neg
'i64x2.all_true'	⇒	i64x2.all_true
'i64x2.bitmask'	⇒	i64x2.bitmask
'i64x2.extend_low_i32x4_s'	⇒	i64x2.extend_low_i32x4_s
'i64x2.extend_high_i32x4_s'	⇒	i64x2.extend_high_i32x4_s
'i64x2.extend_low_i32x4_u'	⇒	i64x2.extend_low_i32x4_u
'i64x2.extend_high_i32x4_u'	⇒	i64x2.extend_high_i32x4_u
'i64x2.shl'	⇒	i64x2.shl
'i64x2.shr_s'	⇒	i64x2.shr_s
'i64x2.shr_u'	⇒	i64x2.shr_u
'i64x2.add'	⇒	i64x2.add
'i64x2.sub'	⇒	i64x2.sub
'i64x2.mul'	⇒	i64x2.mul
'i64x2.extmul_low_i32x4_s'	⇒	i64x2.extmul_low_i32x4_s
'i64x2.extmul_high_i32x4_s'	⇒	i64x2.extmul_high_i32x4_s
'i64x2.extmul_low_i32x4_u'	⇒	i64x2.extmul_low_i32x4_u
'i64x2.extmul_high_i32x4_u'	⇒	i64x2.extmul_high_i32x4_u

'f32x4.abs'	⇒	f32x4.abs
'f32x4.neg'	⇒	f32x4.neg
'f32x4.sqrt'	⇒	f32x4.sqrt
'f32x4.ceil'	⇒	f32x4.ceil
'f32x4.floor'	⇒	f32x4.floor
'f32x4.trunc'	⇒	f32x4.trunc
'f32x4.nearest'	⇒	f32x4.nearest
'f32x4.add'	⇒	f32x4.add
'f32x4.sub'	⇒	f32x4.sub
'f32x4.mul'	⇒	f32x4.mul
'f32x4.div'	⇒	f32x4.div
'f32x4.min'	⇒	f32x4.min
'f32x4.max'	⇒	f32x4.max
'f32x4.pmin'	⇒	f32x4.pmin
'f32x4.pmax'	⇒	f32x4.pmax

'f64x2.abs'	⇒	f64x2.abs
'f64x2.neg'	⇒	f64x2.neg
'f64x2.sqrt'	⇒	f64x2.sqrt
'f64x2.ceil'	⇒	f64x2.ceil
'f64x2.floor'	⇒	f64x2.floor
'f64x2.trunc'	⇒	f64x2.trunc
'f64x2.nearest'	⇒	f64x2.nearest
'f64x2.add'	⇒	f64x2.add
'f64x2.sub'	⇒	f64x2.sub
'f64x2.mul'	⇒	f64x2.mul
'f64x2.div'	⇒	f64x2.div
'f64x2.min'	⇒	f64x2.min
'f64x2.max'	⇒	f64x2.max
'f64x2.pmin'	⇒	f64x2.pmin
'f64x2.pmax'	⇒	f64x2.pmax

'i32x4.trunc_sat_f32x4_s'	⇒	i32x4.trunc_sat_f32x4_s
'i32x4.trunc_sat_f32x4_u'	⇒	i32x4.trunc_sat_f32x4_u
'i32x4.trunc_sat_f64x2_s_zero'	⇒	i32x4.trunc_sat_f64x2_s_zero
'i32x4.trunc_sat_f64x2_u_zero'	⇒	i32x4.trunc_sat_f64x2_u_zero
'f32x4.convert_i32x4_s'	⇒	f32x4.convert_i32x4_s
'f32x4.convert_i32x4_u'	⇒	f32x4.convert_i32x4_u
'f64x2.convert_low_i32x4_s'	⇒	f64x2.convert_low_i32x4_s
'f64x2.convert_low_i32x4_u'	⇒	f64x2.convert_low_i32x4_u
'f32x4.demote_f64x2_zero'	⇒	f32x4.demote_f64x2_zero
'f64x2.promote_low_f32x4'	⇒	f64x2.promote_low_f32x4
'i16x8.relaxed_swizzle'	⇒	i16x8.relaxed_swizzle
'i32x4.relaxed_trunc_f32x4_s'	⇒	i32x4.relaxed_trunc_f32x4_s
'i32x4.relaxed_trunc_f32x4_u'	⇒	i32x4.relaxed_trunc_f32x4_u
'i32x4.relaxed_trunc_f32x4_s_zero'	⇒	i32x4.relaxed_trunc_f32x4_s_zero
'i32x4.relaxed_trunc_f32x4_u_zero'	⇒	i32x4.relaxed_trunc_f32x4_u_zero
'f32x4.relaxed_madd'	⇒	f32x4.relaxed_madd
'f32x4.relaxed_nmadd'	⇒	f32x4.relaxed_nmadd
'f64x2.relaxed_madd'	⇒	f64x2.relaxed_madd
'f64x2.relaxed_nmadd'	⇒	f64x2.relaxed_nmadd
'i8x16.relaxed_laneselect'	⇒	i8x16.relaxed_laneselect
'i16x8.relaxed_laneselect'	⇒	i16x8.relaxed_laneselect
'i32x4.relaxed_laneselect'	⇒	i32x4.relaxed_laneselect
'i64x2.relaxed_laneselect'	⇒	i64x2.relaxed_laneselect
'f32x4.relaxed_min'	⇒	f32x4.relaxed_min
'f32x4.relaxed_max'	⇒	f32x4.relaxed_max
'f64x2.relaxed_min'	⇒	f64x2.relaxed_min
'f64x2.relaxed_max'	⇒	f64x2.relaxed_max
'i16x8.relaxed_q15mulr_s'	⇒	i16x8.relaxed_q15mulr_s
'i16x8.relaxed_dot_i8x16_i7x16_s'	⇒	i16x8.relaxed_dot_i8x16_i7x16_s
'i16x8.relaxed_dot_i8x16_i7x16_add_s'	⇒	i16x8.relaxed_dot_i8x16_i7x16_add_s

6.5.10 Folded Instructions

Instructions can be written as S-expressions by grouping them into *folded* form. In that notation, an instruction is wrapped in parentheses and optionally includes nested folded instructions to indicate its operands.

In the case of [block instructions](#), the folded form omits the ‘end’ delimiter. For *if* instructions, both branches have to be wrapped into nested S-expressions, headed by the keywords ‘then’ and ‘else’.

The set of all phrases defined by the following abbreviations recursively forms the auxiliary syntactic class *foldedinstr*. Such a folded instruction can appear anywhere a regular instruction can.

(' plaininstr foldedinstr* ')	≡	foldedinstr* plaininstr
(' 'block' label blocktype instr* ')	≡	'block' label blocktype instr* 'end'
(' 'loop' label blocktype instr* ')	≡	'loop' label blocktype instr* 'end'
(' 'if' label blocktype foldedinstr* foldedinstr* 'if' label blocktype instr ₁ 'else' (instr ₂)? 'end'	≡	(' 'then' instr ₁ ')((' 'else' instr ₂ ')? ' ')
(' 'try_table' label blocktype catch* instr* ')	≡	'try_table' label blocktype catch* instr* 'end'

Note: For example, the instruction sequence

```
(local.get $x) (i32.const 2) i32.add (i32.const 3) i32.mul
```

can be folded into

```
(i32.mul (i32.add (local.get $x) (i32.const 2)) (i32.const 3))
```

Folded instructions are solely syntactic sugar, no additional syntactic or type-based checking is implied.

6.5.11 Expressions

Expressions are written as instruction sequences. No explicit ‘end’ keyword is included, since they only occur in bracketed positions.

$$\text{expr}_I ::= (\text{in:instr}_I)^* \Rightarrow \text{in}^* \text{end}$$

6.6 Modules

6.6.1 Indices

Indices can be given either in raw numeric form or as symbolic **identifiers** when bound by a respective construct. Such identifiers are looked up in the suitable space of the **identifier context** I .

$$\begin{array}{lll} \text{typeid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{types}[x] = v) \\ \text{funcid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{funcs}[x] = v) \\ \text{tableid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{tables}[x] = v) \\ \text{memid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{mems}[x] = v) \\ \text{globalid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{globals}[x] = v) \\ \text{tagid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{tags}[x] = v) \\ \text{elemid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{elem}[x] = v) \\ \text{dataid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{data}[x] = v) \\ \text{localid}_I & ::= & x::u32 \Rightarrow x \\ & | & v::id \Rightarrow x \quad (\text{if } I.\text{locals}[x] = v) \\ \text{labelid}_I & ::= & l::u32 \Rightarrow l \\ & | & v::id \Rightarrow l \quad (\text{if } I.\text{labels}[l] = v) \\ \text{fieldid}_{I,x} & ::= & i::u32 \Rightarrow i \\ & | & v::id \Rightarrow i \quad (\text{if } I.\text{fields}[x][i] = v) \end{array}$$

6.6.2 Type Uses

A *type use* is a reference to a function **type definition**. It may optionally be augmented by explicit inlined **parameter** and **result** declarations. That allows binding symbolic **identifiers** to name the **local indices** of parameters. If inline declarations are given, then their types must match the referenced **function type**.

$$\begin{array}{l} \text{typeuse}_I ::= \text{'(' 'type' } x::\text{typeid}_I \text{' ')} \Rightarrow x, I' \\ \quad (\text{if } I.\text{typedefs}[x] = \text{sub final (func } [t_1^*] \rightarrow [t_2^*]) \wedge I' = \{\text{locals } (\epsilon)^n\}) \\ \quad | \text{'(' 'type' } x::\text{typeid}_I \text{' ')} (t_1::\text{param})^* (t_2::\text{result})^* \Rightarrow x, I' \\ \quad (\text{if } I.\text{typedefs}[x] = \text{sub final (func } [t_1^*] \rightarrow [t_2^*]) \wedge I' = \{\text{locals id(param)}^*\} \text{ well-formed}) \end{array}$$

Note: If inline declarations are given, their types must be *syntactically* equal to the types from the indexed definition; possible type **substitutions** from other definitions that might make them equal are not taken into account. This is to simplify syntactic pre-processing.

The synthesized attribute of a **typeuse** is a pair consisting of both the used **type index** and the local **identifier context** containing possible parameter identifiers. The following auxiliary function extracts optional identifiers from parameters:

$$\text{id}(\text{'(' 'param' id}^? \text{ ... ')'}) = \text{id}^?$$

Note: Both productions overlap for the case that the function type is $\square \rightarrow \square$. However, in that case, they also produce the same results, so that the choice is immaterial.

The **well-formedness** condition on I' ensures that the parameters do not contain duplicate identifiers.

Abbreviations

A **typeuse** may also be replaced entirely by inline **parameter** and **result** declarations. In that case, a **type index** is automatically inserted:

$$(t_1:\text{param})^* (t_2:\text{result})^* \equiv \text{'(' 'type' } x \text{')' param}^* \text{ result}^*$$

where x is the smallest existing **type index** whose **recursive type** definition in the current module is of the form

$$\text{'(' 'rec' '(' 'type' '(' 'sub' 'final' '(' 'func' param}^* \text{ result}^* \text{')')')')' }$$

If no such index exists, then a new **recursive type** of the same form is inserted at the end of the module.

Abbreviations are expanded in the order they appear, such that previously inserted type definitions are reused by consecutive expansions.

6.6.3 Imports

The descriptors in imports can bind a symbolic function, table, memory, tag, or global **identifier**.

$$\begin{aligned} \text{import}_I &::= \text{'(' 'import' mod:name nm:name d:importdesc}_I \text{')'} \\ &\Rightarrow \{\text{module mod, name nm, desc d}\} \\ \text{importdesc}_I &::= \begin{array}{ll} \text{'(' 'func' id}^? x, I':\text{typeuse}_I \text{')'} & \Rightarrow \text{func } x \\ | \text{'(' 'table' id}^? tt:\text{tabletype}_I \text{')'} & \Rightarrow \text{table } tt \\ | \text{'(' 'memory' id}^? mt:\text{memtype}_I \text{')'} & \Rightarrow \text{mem } mt \\ | \text{'(' 'global' id}^? gt:\text{globaltype}_I \text{')'} & \Rightarrow \text{global } gt \\ | \text{'(' 'tag' id}^? tt:\text{tag} \text{')'} & \Rightarrow \text{tag } tt \end{array} \end{aligned}$$

Abbreviations

As an abbreviation, imports may also be specified inline with **function**, **table**, **memory**, **global**, or **tag** definitions; see the respective sections.

6.6.4 Functions

Function definitions can bind a symbolic **function identifier**, and **local identifiers** for its **parameters** and **locals**.

$$\begin{aligned} \text{func}_I &::= \text{'(' 'func' id}^? x, I':\text{typeuse}_I (\text{loc:local}_I)^* (\text{in:instr}_{I''})^* \text{')'} \\ &\Rightarrow \{\text{type } x, \text{locals loc}^*, \text{body in}^* \text{ end}\} \\ &\quad (\text{if } I'' = I \oplus I' \oplus \{\text{locals id(local)}^*\} \text{ well-formed}) \\ \text{local}_I &::= \text{'(' 'local' id}^? t:\text{valtype}_I \text{')'} \Rightarrow \{\text{type } t\} \end{aligned}$$

The definition of the local **identifier context** I'' uses the following auxiliary function to extract optional identifiers from locals:

$$\text{id}(\text{'(' 'local' id? ... ')'}) = \text{id?}$$

Note: The **well-formedness** condition on I'' ensures that parameters and locals do not contain duplicate identifiers.

Abbreviations

Multiple anonymous locals may be combined into a single declaration:

$$\text{'(' 'local' valtype* ')'} \equiv (\text{'(' 'local' valtype ')'})^*$$

Functions can be defined as **imports** or **exports** inline:

$$\begin{aligned} &\text{'(' 'func' id? (' 'import' name_1 name_2 ') typeuse ')'} \equiv \\ &\quad \text{'(' 'import' name_1 name_2 (' 'func' id? typeuse ') ')'} \\ &\text{'(' 'func' id? (' 'export' name ') ... ')'} \equiv \\ &\quad \text{'(' 'export' name (' 'func' id' ') ') (' 'func' id' ... ')'} \\ &\quad (\text{if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh}) \end{aligned}$$

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a function declaration can contain any number of exports, possibly followed by an import.

6.6.5 Tables

Table definitions can bind a symbolic **table identifier**.

$$\text{table}_I ::= \text{'(' 'table' id? tt:tabletype}_I \text{ e:expr}_I \text{ ')'} \Rightarrow \{\text{type tt, init e}\}$$

Abbreviations

A table's initialization **expression** can be omitted, in which case it defaults to **ref.null**:

$$\begin{aligned} \text{'(' 'table' id? tabletype ')'} &\equiv \text{'(' 'table' id? tabletype (' ref.null ht ') ')'} \\ &\quad (\text{if tabletype = limits (' 'ref' 'null'? ht ')}) \end{aligned}$$

An **element segment** can be given inline with a table definition, in which case its offset is 0 and the **limits** of the **table type** are inferred from the length of the given segment:

$$\begin{aligned} &\text{'(' 'table' id? addrtype? reftype (' 'elem' x^n:vec(funcidx) ') ')'} \equiv \\ &\quad \text{'(' 'table' id' addrtype? n n reftype ')'} \\ &\quad \text{'(' 'elem' (' 'table' id' ') (' addrtype'.const '0') reftype vec((' 'ref.func' funcidx ') ')')} \\ &\quad (\text{if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh,} \\ &\quad \text{if addrtype?} \neq \epsilon \wedge \text{addrtype' = addrtype?} \vee \text{addrtype?} = \epsilon \wedge \text{addrtype' = 'i32'}) \end{aligned}$$

Tables can be defined as **imports** or **exports** inline:

$$\begin{aligned} &\text{'(' 'table' id? (' 'import' name_1 name_2 ') tabletype ')'} \equiv \\ &\quad \text{'(' 'import' name_1 name_2 (' 'table' id? tabletype ') ')'} \\ &\text{'(' 'table' id? (' 'export' name ') ... ')'} \equiv \\ &\quad \text{'(' 'export' name (' 'table' id' ') ') (' 'table' id' ... ')'} \\ &\quad (\text{if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh}) \end{aligned}$$

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a table declaration can contain any number of exports, possibly followed by an import.

6.6.6 Memories

Memory definitions can bind a symbolic [memory identifier](#).

$$\text{mem}_I ::= \text{'(' 'memory' id? mt:memtype}_I \text{'')} \Rightarrow \{\text{type mt}\}$$

Abbreviations

A [data segment](#) can be given inline with a memory definition, in which case its offset is 0 and the [limits](#) of the [memory type](#) are inferred from the length of the data, rounded up to [page size](#):

$$\begin{aligned} \text{'(' 'memory' id? addrtype? (' 'data' b^n:datastring ')} &\equiv \\ \text{'(' 'memory' id? addrtype? m m ')} & \\ \text{'(' 'data' (' 'memory' id? ')} \text{'(' addrtype?.const '0' ')} \text{datastring ')} & \\ \text{(if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh,} & \\ \text{if addrtype?} \neq \epsilon \wedge \text{addrtype' = addrtype?} \vee \text{addrtype?} = \epsilon \wedge \text{addrtype' = 'i32',} & \\ \text{m = ceil(n/64 Ki))} & \end{aligned}$$

Memories can be defined as [imports](#) or [exports](#) inline:

$$\begin{aligned} \text{'(' 'memory' id? (' 'import' name}_1 \text{ name}_2 \text{'')} \text{ memtype ')} &\equiv \\ \text{'(' 'import' name}_1 \text{ name}_2 \text{'(' 'memory' id? memtype ')} \text{'')} & \\ \text{'(' 'memory' id? (' 'export' name ')} \text{'... ')} &\equiv \\ \text{'(' 'export' name (' 'memory' id? ')} \text{'')} \text{'(' 'memory' id' ... ')} & \\ \text{(if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh)} & \end{aligned}$$

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a memory declaration can contain any number of exports, possibly followed by an import.

6.6.7 Globals

Global definitions can bind a symbolic [global identifier](#).

$$\text{global}_I ::= \text{'(' 'global' id? gt:globaltype}_I \text{ e:expr}_I \text{'')} \Rightarrow \{\text{type gt, init e}\}$$

Abbreviations

Globals can be defined as [imports](#) or [exports](#) inline:

$$\begin{aligned} \text{'(' 'global' id? (' 'import' name}_1 \text{ name}_2 \text{'')} \text{ globaltype ')} &\equiv \\ \text{'(' 'import' name}_1 \text{ name}_2 \text{'(' 'global' id? globaltype ')} \text{'')} & \\ \text{'(' 'global' id? (' 'export' name ')} \text{'... ')} &\equiv \\ \text{'(' 'export' name (' 'global' id? ')} \text{'')} \text{'(' 'global' id' ... ')} & \\ \text{(if id?} \neq \epsilon \wedge \text{id' = id?} \vee \text{id?} = \epsilon \wedge \text{id' fresh)} & \end{aligned}$$

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a global declaration can contain any number of exports, possibly followed by an import.

6.6.8 Tags

An tag definition can bind a symbolic tag identifier.

$$\begin{aligned} \text{tag}_I &::= \text{'(' 'tag' id? } x, I' \text{:typeuse}_I \text{'(')} \\ &\Rightarrow \{\text{type } x\} \end{aligned}$$

Abbreviations

Tags can be defined as `imports` or `exports` inline:

$$\begin{aligned} \text{'(' 'tag' id? (' 'import' name}_1 \text{ name}_2 \text{'(' typeuse ')} &\equiv \\ \text{'(' 'import' name}_1 \text{ name}_2 \text{'(' 'tag' id? typeuse ')} &\text{'(')} \\ \text{'(' 'tag' id? (' 'export' name ')} \dots \text{'('} &\equiv \\ \text{'(' 'export' name (' 'tag' id? ')} &\text{'(' 'tag' id? ')} \dots \text{'('} \\ \text{(if id? } \neq \epsilon \wedge \text{id' = id? } \vee \text{id? = } \epsilon \wedge \text{id' fresh)} & \end{aligned}$$

Note: The latter abbreviation can be applied repeatedly, if “...” contains additional export clauses. Consequently, a memory declaration can contain any number of exports, possibly followed by an import.

6.6.9 Exports

The syntax for exports mirrors their `abstract syntax` directly.

$$\begin{aligned} \text{export}_I &::= \text{'(' 'export' nm:name d:exportdesc}_I \text{'(')} &\Rightarrow \{\text{name nm, desc d}\} \\ \text{exportdesc}_I &::= \text{'(' 'func' x:funcidx}_I \text{'(')} &\Rightarrow \text{func } x \\ &| \text{'(' 'table' x:tableidx}_I \text{'(')} &\Rightarrow \text{table } x \\ &| \text{'(' 'memory' x:memidx}_I \text{'(')} &\Rightarrow \text{mem } x \\ &| \text{'(' 'global' x:globalidx}_I \text{'(')} &\Rightarrow \text{global } x \\ &| \text{'(' 'tag' x:tagidx}_I \text{'(')} &\Rightarrow \text{tag } x \end{aligned}$$

Abbreviations

As an abbreviation, exports may also be specified inline with `function`, `table`, `memory`, `global`, or `tag` definitions; see the respective sections.

6.6.10 Start Function

A `start function` is defined in terms of its index.

$$\text{start}_I ::= \text{'(' 'start' x:funcidx}_I \text{'(')} \Rightarrow \{\text{func } x\}$$

Note: At most one start function may occur in a module, which is ensured by a suitable side condition on the `module` grammar.

6.6.11 Element Segments

Element segments allow for an optional [table index](#) to identify the table to initialize.

```

elemI      ::=  '(' 'elem' id? (et,y*):elemlistI ')'
                ⇒  {type et, init y*, mode passive}
                |  '(' 'elem' id? x:tableuseI '(' 'offset' e:exprI ')' (et,y*):elemlistI ')'
                ⇒  {type et, init y*, mode active {table x, offset e}}
                '(' 'elem' id? 'declare' (et,y*):elemlistI ')'
                ⇒  {type et, init y*, mode declarative}
elemlistI ::=  t:reftypeI y*:vec(elemexprI)    ⇒  (type t, init y*)
elemexprI ::=  '(' 'item' e:exprI ')'           ⇒  e
tableuseI ::=  '(' 'table' x:tableidxI ')'      ⇒  x

```

Abbreviations

As an abbreviation, a single instruction may occur in place of the offset of an active element segment or as an element expression:

```

(' instr ')  ≡  '(' 'offset' instr ')'
(' instr ')  ≡  '(' 'item' instr ')'

```

Also, the element list may be written as just a sequence of [function indices](#):

```

'func' vec(funcidxI)  ≡  '(' 'ref' 'func' vec('(' 'ref.func' funcidxI ')')

```

A table use can be omitted, defaulting to 0. Furthermore, for backwards compatibility with earlier versions of WebAssembly, if the table use is omitted, the 'func' keyword can be omitted as well.

```

ε  ≡  '(' 'table' '0' ')'
(' 'elem' id? '(' 'offset' exprI ')' vec(funcidxI) ')' ≡  '(' 'elem' id? '(' 'table' '0' ')' '(' 'offset' exprI ')'

```

As another abbreviation, element segments may also be specified inline with [table](#) definitions; see the respective section.

6.6.12 Data Segments

Data segments allow for an optional [memory index](#) to identify the memory to initialize. The data is written as a [string](#), which may be split up into a possibly empty sequence of individual string literals.

```

dataI      ::=  '(' 'data' id? b*:datastring ')'
                ⇒  {init b*, mode passive}
                |  '(' 'data' id? x:memuseI '(' 'offset' e:exprI ')' b*:datastring ')'
                ⇒  {init b*, mode active {memory x', offset e}}
datastring ::=  (b*:string)* ⇒  concat((b*)*)
memuseI    ::=  '(' 'memory' x:memidxI ')'      ⇒  x

```

Note: In the current version of WebAssembly, the only valid memory index is 0 or a symbolic [memory identifier](#) resolving to the same value.

Abbreviations

As an abbreviation, a single instruction may occur in place of the offset of an active data segment:

$$(' \text{instr} ') \equiv (' \text{'offset' instr} ')$$

Also, a memory use can be omitted, defaulting to 0.

$$\epsilon \equiv (' \text{'memory' '0'} ')$$

As another abbreviation, data segments may also be specified inline with `memory` definitions; see the respective section.

6.6.13 Modules

A module consists of a sequence of fields that can occur in any order. All definitions and their respective bound `identifiers` scope over the entire module, including the text preceding them.

A module may optionally bind an `identifier` that names the module. The name serves a documentary role only.

Note: Tools may include the module name in the `name` section of the `binary format`.

<code>module</code>	$::=$	<code>(' 'module' id? (m:modulefield_I)* ')</code>	\Rightarrow	$\oplus m^*$
		(if $I = \oplus \text{idc}(\text{modulefield})^*$ well-formed)		
<code>modulefield_I</code>	$::=$	<code>ty*:rectype_I</code>	\Rightarrow	<code>{types ty*}</code>
		<code>im:import_I</code>	\Rightarrow	<code>{imports im}</code>
		<code>fn:func_I</code>	\Rightarrow	<code>{funcs fn}</code>
		<code>ta:table_I</code>	\Rightarrow	<code>{tables ta}</code>
		<code>me:mem_I</code>	\Rightarrow	<code>{mems me}</code>
		<code>gl:global_I</code>	\Rightarrow	<code>{globals gl}</code>
		<code>tg>tag_I</code>	\Rightarrow	<code>{tags tg}</code>
		<code>el:elem_I</code>	\Rightarrow	<code>{elems el}</code>
		<code>da:data_I</code>	\Rightarrow	<code>{datas da}</code>
		<code>st:start_I</code>	\Rightarrow	<code>{start st}</code>
		<code>ex:export_I</code>	\Rightarrow	<code>{exports ex}</code>

The following restrictions are imposed on the composition of `modules`: $m_1 \oplus m_2$ is defined if and only if

- $m_1.\text{start} = \epsilon \vee m_2.\text{start} = \epsilon$
- $m_1.\text{funcs} = m_1.\text{tables} = m_1.\text{mems} = m_1.\text{globals} = m_1.\text{tags} = \epsilon \vee m_2.\text{imports} = \epsilon$

Note: The first condition ensures that there is at most one start function. The second condition enforces that all `imports` must occur before any regular definition of a `function`, `table`, `memory`, `global`, or `tag`, thereby maintaining the ordering of the respective `index spaces`.

The `well-formedness` condition on I in the grammar for `module` ensures that no namespace contains duplicate identifiers.

The definition of the initial `identifier context` I uses the following auxiliary definition which maps each relevant

definition to a singular context with one (possibly empty) identifier:

<code>idc('(' 'rec' typedef* ')')</code>	$= \bigoplus \text{idc}(\text{typedef})^*$
<code>idc('(' 'type' $v^?:\text{id}^?$ subtype ')')</code>	$= \{\text{types}(v^?), \text{fields idf}(\text{subtype}), \text{typedefs } st\}$
<code>idc('(' 'func' $v^?:\text{id}^?$... ')')</code>	$= \{\text{funcs}(v^?)\}$
<code>idc('(' 'table' $v^?:\text{id}^?$... ')')</code>	$= \{\text{tables}(v^?)\}$
<code>idc('(' 'memory' $v^?:\text{id}^?$... ')')</code>	$= \{\text{mems}(v^?)\}$
<code>idc('(' 'global' $v^?:\text{id}^?$... ')')</code>	$= \{\text{globals}(v^?)\}$
<code>idc('(' 'tag' $v^?:\text{id}^?$... ')')</code>	$= \{\text{tags}(v^?)\}$
<code>idc('(' 'elem' $v^?:\text{id}^?$... ')')</code>	$= \{\text{elem}(v^?)\}$
<code>idc('(' 'data' $v^?:\text{id}^?$... ')')</code>	$= \{\text{data}(v^?)\}$
<code>idc('(' 'import' ... (' 'func' $v^?:\text{id}^?$... ')')</code>	$= \{\text{funcs}(v^?)\}$
<code>idc('(' 'import' ... (' 'table' $v^?:\text{id}^?$... ')')</code>	$= \{\text{tables}(v^?)\}$
<code>idc('(' 'import' ... (' 'memory' $v^?:\text{id}^?$... ')')</code>	$= \{\text{mems}(v^?)\}$
<code>idc('(' 'import' ... (' 'global' $v^?:\text{id}^?$... ')')</code>	$= \{\text{globals}(v^?)\}$
<code>idc('(' ... ')')</code>	$= \{\}$
<code>idf('(' 'sub' ... comptype ')')</code>	$= \text{idf}(\text{comptype})$
<code>idf('(' 'struct' <i>Tfield</i>* ')')</code>	$= \bigoplus \text{idf}(\text{field})^*$
<code>idf('(' 'array' ... ')')</code>	$= \epsilon$
<code>idf('(' 'func' ... ')')</code>	$= \epsilon$
<code>idf('(' 'field' $v^?:\text{id}^?$... ')')</code>	$= v^?$

Abbreviations

In a source file, the `toplevel (module ...)` surrounding the module body may be omitted.

$$\text{modulefield}^* \equiv '(' \text{'module' modulefield}^* ')'$$

7.1 Embedding

A WebAssembly implementation will typically be *embedded* into a *host* environment. An *embedder* implements the connection between such a host environment and the WebAssembly semantics as defined in the main body of this specification. An embedder is expected to interact with the semantics in well-defined ways.

This section defines a suitable interface to the WebAssembly semantics in the form of entry points through which an embedder can access it. The interface is intended to be complete, in the sense that an embedder does not need to reference other functional parts of the WebAssembly specification directly.

Note: On the other hand, an embedder does not need to provide the host environment with access to all functionality defined in this interface. For example, an implementation may not support [parsing](#) of the [text format](#).

7.1.1 Types

In the description of the embedder interface, syntactic classes from the [abstract syntax](#) and the [runtime's abstract machine](#) are used as names for variables that range over the possible objects from that class. Hence, these syntactic classes can also be interpreted as types.

For numeric parameters, notation like $n : u32$ is used to specify a symbolic name in addition to the respective value range.

7.1.2 Booleans

Interface operation that are predicates return Boolean values:

$$bool ::= false \mid true$$

7.1.3 Exceptions and Errors

Invoking an exported function may throw or propagate exceptions, expressed by an auxiliary syntactic class:

$$\textit{exception} ::= \text{THROW } \textit{exnaddr}$$

The exception address *exnaddr* identifies the exception thrown.

Failure of an interface operation is also indicated by an auxiliary syntactic class:

$$\textit{error} ::= \text{error}$$

In addition to the error conditions specified explicitly in this section, such as invalid arguments or [exceptions](#) and [traps](#) resulting from [execution](#), implementations may also return errors when specific [implementation limitations](#) are reached.

Note: Errors are abstract and unspecific with this definition. Implementations can refine it to carry suitable classifications and diagnostic messages.

7.1.4 Pre- and Post-Conditions

Some operations state *pre-conditions* about their arguments or *post-conditions* about their results. It is the embedder's responsibility to meet the pre-conditions. If it does, the post conditions are guaranteed by the semantics.

In addition to pre- and post-conditions explicitly stated with each operation, the specification adopts the following conventions for [runtime objects](#) (*store*, *moduleinst*, *externval*, *addresses*):

- Every runtime object passed as a parameter must be [valid](#) per an implicit pre-condition.
- Every runtime object returned as a result is [valid](#) per an implicit post-condition.

Note: As long as an embedder treats runtime objects as abstract and only creates and manipulates them through the interface defined here, all implicit pre-conditions are automatically met.

7.1.5 Store

store_init() : *store*

1. Return the empty [store](#).

$$\textit{store_init}() = \{\}$$

7.1.6 Modules

module_decode(byte)* : *module* | *error*

1. If there exists a derivation for the [byte](#) sequence *byte** as a [module](#) according to the [binary grammar](#) for [modules](#), yielding a [module](#) *m*, then return *m*.
2. Else, return [error](#).

$$\begin{aligned} \textit{module_decode}(b^*) &= m && (\text{if } \textit{module} \xRightarrow{*} m:b^*) \\ \textit{module_decode}(b^*) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{module_parse}(\text{char}^*) : \text{module} \mid \text{error}$

1. If there exists a derivation for the source char^* as a **module** according to the text grammar for modules, yielding a **module** m , then return m .
2. Else, return **error**.

$$\begin{aligned} \text{module_parse}(c^*) &= m && (\text{if } \text{module} \xRightarrow{*} m:c^*) \\ \text{module_parse}(c^*) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{module_validate}(\text{module}) : \text{error}^?$

1. If **module** is **valid**, then return nothing.
2. Else, return **error**.

$$\begin{aligned} \text{module_validate}(m) &= \epsilon && (\text{if } \vdash m : \text{externtype}^* \rightarrow \text{externtype}'^*) \\ \text{module_validate}(m) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{module_instantiate}(\text{store}, \text{module}, \text{externval}^*) : (\text{store}, \text{moduleinst} \mid \text{exception} \mid \text{error})$

1. Try **instantiating** **module** in **store** with external values externval^* as imports:
 - a. If it succeeds with a **module** instance moduleinst , then let **result** be moduleinst .
 - b. Else, let **result** be **error**.
2. Return the new store paired with **result**.

$$\begin{aligned} \text{module_instantiate}(S, m, ev^*) &= (S', F.\text{module}) && (\text{if } \text{instantiate}(S, m, ev^*) \hookrightarrow^* S'; F; \epsilon) \\ \text{module_instantiate}(S, m, ev^*) &= (S', \text{error}) && (\text{otherwise, if } \text{instantiate}(S, m, ev^*) \hookrightarrow^* S'; F; \text{result}) \end{aligned}$$

Note: The store may be modified even in case of an error.

$\text{module_imports}(\text{module}) : (\text{name}, \text{name}, \text{externtype})^*$

1. Pre-condition: **module** is **valid** with the external import types externtype^* and external export types $\text{externtype}'^*$.
2. Let import^* be the **imports** $\text{module}.\text{imports}$.
3. Assert: the length of import^* equals the length of externtype^* .
4. For each import_i in import^* and corresponding externtype_i in externtype^* , do:
 - a. Let result_i be the triple $(\text{import}_i.\text{module}, \text{import}_i.\text{name}, \text{externtype}_i)$.
5. Return the concatenation of all result_i , in index order.
6. Post-condition: each externtype_i is **valid** under the empty context.

$$\begin{aligned} \text{module_imports}(m) &= (im.\text{module}, im.\text{name}, \text{externtype})^* \\ &\quad (\text{if } im^* = m.\text{imports} \wedge \vdash m : \text{externtype}^* \rightarrow \text{externtype}'^*) \end{aligned}$$

$\text{module_exports}(\text{module}) : (\text{name}, \text{externtype})^*$

1. Pre-condition: module is valid with the external import types externtype^* and external export types $\text{externtype}'^*$.
2. Let export^* be the exports $\text{module}.\text{exports}$.
3. Assert: the length of export^* equals the length of $\text{externtype}'^*$.
4. For each export_i in export^* and corresponding $\text{externtype}'_i$ in $\text{externtype}'^*$, do:
 - a. Let result_i be the pair $(\text{export}_i.\text{name}, \text{externtype}'_i)$.
5. Return the concatenation of all result_i , in index order.
6. Post-condition: each $\text{externtype}'_i$ is valid under the empty context.

$$\begin{aligned} \text{module_exports}(m) &= (\text{ex}.\text{name}, \text{externtype}')^* \\ &\quad (\text{if } \text{ex}^* = m.\text{exports} \wedge \vdash m : \text{externtype}^* \rightarrow \text{externtype}'^*) \end{aligned}$$

7.1.7 Module Instances

$\text{instance_export}(\text{moduleinst}, \text{name}) : \text{externval} \mid \text{error}$

1. Assert: due to validity of the module instance moduleinst , all its export names are different.
2. If there exists an exportinst_i in $\text{moduleinst}.\text{exports}$ such that $\text{name } \text{exportinst}_i.\text{name}$ equals name , then:
 - a. Return the external value $\text{exportinst}_i.\text{value}$.
3. Else, return `error`.

$$\begin{aligned} \text{instance_export}(m, \text{name}) &= m.\text{exports}[i].\text{value} && (\text{if } m.\text{exports}[i].\text{name} = \text{name}) \\ \text{instance_export}(m, \text{name}) &= \text{error} && (\text{otherwise}) \end{aligned}$$

7.1.8 Functions

$\text{func_alloc}(\text{store}, \text{functype}, \text{hostfunc}) : (\text{store}, \text{funcaddr})$

1. Pre-condition: the functype is valid under the empty context.
2. Let funcaddr be the result of allocating a host function in store with function type functype and host function code hostfunc .
3. Return the new store paired with funcaddr .

$$\text{func_alloc}(S, \text{ta}, \text{code}) = (S', a) \quad (\text{if } \text{allochostfunc}(S, \text{ta}, \text{code}) = S', a)$$

Note: This operation assumes that hostfunc satisfies the pre- and post-conditions required for a function instance with type functype .

Regular (non-host) function instances can only be created indirectly through module instantiation.

$\text{func_type}(\text{store}, \text{funcaddr}) : \text{functype}$

1. Let functype be the function type $S.\text{funcs}[a].\text{type}$.
2. Return functype .
3. Post-condition: the returned function type is valid.

$$\text{func_type}(S, a) = S.\text{funcs}[a].\text{type}$$

$\text{func_invoke}(\text{store}, \text{funcaddr}, \text{val}^*) : (\text{store}, \text{val}^* \mid \text{exception} \mid \text{error})$

1. Try **invoking** the function funcaddr in store with values val^* as arguments:
 - a. If it succeeds with values val'^* as results, then let result be val'^* .
 - b. Else if the outcome is an exception with a thrown **exception** ref.exn exnaddr as the result, then let result be **THROW** exnaddr
 - c. Else it has trapped, hence let result be **error**.
2. Return the new store paired with result .

$$\begin{aligned} \text{func_invoke}(S, a, v^*) &= (S', v'^*) && (\text{if } \text{invoke}(S, a, v^*) \hookrightarrow *S'; F; v'^*) \\ \text{func_invoke}(S, a, v^*) &= (S', \text{THROW } a') && (\text{if } \text{invoke}(S, a, v^*) \hookrightarrow *S'; F; T[(\text{ref.exn } a') \text{ throw_ref}]) \\ \text{func_invoke}(S, a, v^*) &= (S', \text{error}) && (\text{if } \text{invoke}(S, a, v^*) \hookrightarrow *S'; F; \text{trap}) \end{aligned}$$

Note: The store may be modified even in case of an error.

7.1.9 Tables

$\text{table_alloc}(\text{store}, \text{tabletype}, \text{ref}) : (\text{store}, \text{tableaddr})$

1. Pre-condition: the tabletype is valid under the empty context.
2. Let tableaddr be the result of allocating a table in store with table type tabletype and initialization value ref .
3. Return the new store paired with tableaddr .

$$\text{table_alloc}(S, tt, r) = (S', a) \quad (\text{if } \text{alloctable}(S, tt, r) = S', a)$$

$\text{table_type}(\text{store}, \text{tableaddr}) : \text{tabletype}$

1. Return $S.\text{tables}[a].\text{type}$.
2. Post-condition: the returned table type is valid under the empty context.

$$\text{table_type}(S, a) = S.\text{tables}[a].\text{type}$$

$\text{table_read}(\text{store}, \text{tableaddr}, i : u64) : \text{ref} \mid \text{error}$

1. Let ti be the table instance $\text{store.tables}[\text{tableaddr}]$.
2. If i is larger than or equal to the length of $ti.\text{elem}$, then return **error**.
3. Else, return the reference value $ti.\text{elem}[i]$.

$$\begin{aligned} \text{table_read}(S, a, i) &= r && (\text{if } S.\text{tables}[a].\text{elem}[i] = r) \\ \text{table_read}(S, a, i) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{table_write}(\text{store}, \text{tableaddr}, i : u64, \text{ref}) : \text{store} \mid \text{error}$

1. Let ti be the table instance $\text{store.tables}[\text{tableaddr}]$.
2. If i is larger than or equal to the length of $ti.\text{elem}$, then return **error**.
3. Replace $ti.\text{elem}[i]$ with the reference value ref .
4. Return the updated store.

$$\begin{aligned} \text{table_write}(S, a, i, r) &= S' && (\text{if } S' = S \text{ with } \text{tables}[a].\text{elem}[i] = r) \\ \text{table_write}(S, a, i, r) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{table_size}(\text{store}, \text{tableaddr}) : u64$

1. Return the length of $\text{store.tables}[\text{tableaddr}].\text{elem}$.

$$\text{table_size}(S, a) = n \quad (\text{if } |S.\text{tables}[a].\text{elem}| = n)$$

$\text{table_grow}(\text{store}, \text{tableaddr}, n : u64, \text{ref}) : \text{store} \mid \text{error}$

1. Try growing the table instance $\text{store.tables}[\text{tableaddr}]$ by n elements with initialization value ref :
 - a. If it succeeds, return the updated store.
 - b. Else, return **error**.

$$\begin{aligned} \text{table_grow}(S, a, n, r) &= S' && (\text{if } S' = S \text{ with } \text{tables}[a] = \text{growtable}(S.\text{tables}[a], n, r)) \\ \text{table_grow}(S, a, n, r) &= \text{error} && (\text{otherwise}) \end{aligned}$$

7.1.10 Memories

$\text{mem_alloc}(\text{store}, \text{memtype}) : (\text{store}, \text{memaddr})$

1. Pre-condition: the memtype is valid under the empty context.
2. Let memaddr be the result of allocating a memory in store with memory type memtype .
3. Return the new store paired with memaddr .

$$\text{mem_alloc}(S, mt) = (S', a) \quad (\text{if } \text{allocmem}(S, mt) = S', a)$$

$\text{mem_type}(\text{store}, \text{memaddr}) : \text{memtype}$

1. Return $S.\text{mems}[a].\text{type}$.
2. Post-condition: the returned **memory type** is **valid** under the empty context.

$$\text{mem_type}(S, a) = S.\text{mems}[a].\text{type}$$

$\text{mem_read}(\text{store}, \text{memaddr}, i : u64) : \text{byte} \mid \text{error}$

1. Let mi be the **memory instance** $\text{store.mems}[\text{memaddr}]$.
2. If i is larger than or equal to the length of $mi.\text{data}$, then return **error**.
3. Else, return the **byte** $mi.\text{data}[i]$.

$$\begin{aligned} \text{mem_read}(S, a, i) &= b && (\text{if } S.\text{mems}[a].\text{data}[i] = b) \\ \text{mem_read}(S, a, i) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{mem_write}(\text{store}, \text{memaddr}, i : u64, \text{byte}) : \text{store} \mid \text{error}$

1. Let mi be the **memory instance** $\text{store.mems}[\text{memaddr}]$.
2. If i is larger than or equal to the length of $mi.\text{data}$, then return **error**.
3. Replace $mi.\text{data}[i]$ with byte .
4. Return the updated store.

$$\begin{aligned} \text{mem_write}(S, a, i, b) &= S' && (\text{if } S' = S \text{ with } \text{mems}[a].\text{data}[i] = b) \\ \text{mem_write}(S, a, i, b) &= \text{error} && (\text{otherwise}) \end{aligned}$$

$\text{mem_size}(\text{store}, \text{memaddr}) : u64$

1. Return the length of $\text{store.mems}[\text{memaddr}].\text{data}$ divided by the **page size**.

$$\text{mem_size}(S, a) = n \quad (\text{if } |S.\text{mems}[a].\text{data}| = n \cdot 64 \text{ Ki})$$

$\text{mem_grow}(\text{store}, \text{memaddr}, n : u64) : \text{store} \mid \text{error}$

1. Try **growing** the **memory instance** $\text{store.mems}[\text{memaddr}]$ by n **pages**:
 - a. If it succeeds, return the updated store.
 - b. Else, return **error**.

$$\begin{aligned} \text{mem_grow}(S, a, n) &= S' && (\text{if } S' = S \text{ with } \text{mems}[a] = \text{growmem}(S.\text{mems}[a], n)) \\ \text{mem_grow}(S, a, n) &= \text{error} && (\text{otherwise}) \end{aligned}$$

7.1.11 Tags

$\text{tag_alloc}(\text{store}, \text{tagtype}) : (\text{store}, \text{tagaddr})$

1. Pre-condition: *tagtype* is valid.
2. Let *tagaddr* be the result of allocating a tag in *store* with tag type *tagtype*.
3. Return the new store paired with *tagaddr*.

$$\text{tag_alloc}(S, tt) = (S', a) \quad (\text{if } \text{alloctag}(S, tt) = S', a)$$

$\text{tag_type}(\text{store}, \text{tagaddr}) : \text{tagtype}$

1. Return $S.\text{tags}[a].\text{type}$.
2. Post-condition: the returned tag type is valid.

$$\text{tag_type}(S, a) = S.\text{tags}[a].\text{type}$$

7.1.12 Exceptions

$\text{exn_alloc}(\text{store}, \text{tagaddr}, \text{val}^*) : (\text{store}, \text{exnaddr})$

1. Pre-condition: *tagaddr* is an allocated tag address.
2. Let *exnaddr* be the result of allocating an exception instance in *store* with tag address *tagaddr* and initialization values *val**.
3. Return the new store paired with *exnaddr*.

$$\text{exn_alloc}(S, \text{tagaddr}, \text{val}^*) = (S \oplus \{\text{exns } \text{exninst}\}, |S.\text{exns}|) \quad (\text{if } \text{exninst} = \{\text{tag } \text{tagaddr}, \text{fields } \text{val}^*\})$$

$\text{exn_tag}(\text{store}, \text{exnaddr}) : \text{tagaddr}$

1. Let *exninst* be the exception instance $\text{store}.\text{exns}[\text{exnaddr}]$.
2. Return the tag address *exninst.tag*.

$$\text{exn_tag}(S, a) = \text{exninst.tag} \quad (\text{if } \text{exninst} = S.\text{exns}[a])$$

$\text{exn_read}(\text{store}, \text{exnaddr}) : \text{val}^*$

1. Let *exninst* be the exception instance $\text{store}.\text{exns}[\text{exnaddr}]$.
2. Return the values *exninst.fields*.

$$\text{exn_read}(S, a) = \text{exninst.fields} \quad (\text{if } \text{exninst} = S.\text{exns}[a])$$

7.1.13 Globals

$\text{global_alloc}(\text{store}, \text{globaltype}, \text{val}) : (\text{store}, \text{globaladdr})$

1. Pre-condition: the *globaltype* is valid under the empty context.
2. Let *globaladdr* be the result of allocating a global in *store* with global type *globaltype* and initialization value *val*.
3. Return the new store paired with *globaladdr*.

$$\text{global_alloc}(S, gt, v) = (S', a) \quad (\text{if } \text{allocglobal}(S, gt, v) = S', a)$$

$\text{global_type}(\text{store}, \text{globaladdr}) : \text{globaltype}$

1. Return $S.\text{globals}[a].\text{type}$.
2. Post-condition: the returned global type is valid under the empty context.

$$\text{global_type}(S, a) = S.\text{globals}[a].\text{type}$$

$\text{global_read}(\text{store}, \text{globaladdr}) : \text{val}$

1. Let *gi* be the global instance $\text{store.globals}[\text{globaladdr}]$.
2. Return the value *gi.value*.

$$\text{global_read}(S, a) = v \quad (\text{if } S.\text{globals}[a].\text{value} = v)$$

$\text{global_write}(\text{store}, \text{globaladdr}, \text{val}) : \text{store} \mid \text{error}$

1. Let *gi* be the global instance $\text{store.globals}[\text{globaladdr}]$.
2. Let *mut t* be the structure of the global type *gi.type*.
3. If *mut* is not *var*, then return *error*.
4. Replace *gi.value* with the value *val*.
5. Return the updated store.

$$\begin{aligned} \text{global_write}(S, a, v) &= S' && (\text{if } S.\text{globals}[a].\text{type} = \text{var } t \wedge S' = S \text{ with } \text{globals}[a].\text{value} = v) \\ \text{global_write}(S, a, v) &= \text{error} && (\text{otherwise}) \end{aligned}$$

7.1.14 Values

$\text{ref_type}(\text{store}, \text{ref}) : \text{reftype}$

1. Pre-condition: the reference *ref* is valid under store *S*.
2. Return the reference type *t* with which *ref* is valid.
3. Post-condition: the returned reference type is valid under the empty context.

$$\text{ref_type}(S, r) = t \quad (\text{if } S \vdash r : t)$$

Note: In future versions of WebAssembly, not all references may carry precise type information at run time. In such cases, this function may return a less precise supertype.

$\text{val_default}(\text{valtype}) : \text{val}$

1. If $\text{default}_{\text{valtype}}$ is not defined, then return *error*.
1. Else, return the value $\text{default}_{\text{valtype}}$.

$$\begin{aligned} \text{val_default}(t) &= v && (\text{if } \text{default}_t = v) \\ \text{val_default}(t) &= \text{error} && (\text{if } \text{default}_t = \epsilon) \end{aligned}$$

7.1.15 Matching

$\text{match_valtype}(\text{valtype}_1, \text{valtype}_2) : \text{bool}$

1. Pre-condition: the value types valtype_1 and valtype_2 are valid under the empty context.
2. If valtype_1 matches valtype_2 , then return *true*.
3. Else, return *false*.

$$\begin{aligned} \text{match_reftype}(t_1, t_2) &= \text{true} && (\text{if } \vdash t_1 \leq t_2) \\ \text{match_reftype}(t_1, t_2) &= \text{false} && (\text{otherwise}) \end{aligned}$$

$\text{match_externtype}(\text{externtype}_1, \text{externtype}_2) : \text{bool}$

1. Pre-condition: the extern types externtype_1 and externtype_2 are valid under the empty context.
2. If externtype_1 matches externtype_2 , then return *true*.
3. Else, return *false*.

$$\begin{aligned} \text{match_externtype}(et_1, et_2) &= \text{true} && (\text{if } \vdash et_1 \leq et_2) \\ \text{match_externtype}(et_1, et_2) &= \text{false} && (\text{otherwise}) \end{aligned}$$

7.2 Profiles

To enable the use of WebAssembly in as many environments as possible, *profiles* specify coherent language subsets that fit constraints imposed by common classes of host environments. A host platform can thereby decide to support the language only under a restricted profile, or even the intersection of multiple profiles.

7.2.1 Conventions

A profile modification is specified by decorating selected rules in the main body of this specification with a *profile annotation* that defines them as conditional on the choice of profile.

For that purpose, every profile defines a *profile marker*, an alphanumeric short-hand like ABC. A profile annotation of the form ^[!ABC XYZ] on a rule indicates that this rule is *excluded* for either of the profiles whose marker is ABC or XYZ.

There are two ways of subsetting the language in a profile:

- *Syntactic*, by *omitting* a feature, in which case certain constructs are removed from the syntax altogether.
- *Semantic*, by *restricting* a feature, in which case certain constructs are still present but some behaviours are ruled out.

Syntax Annotations

To omit a construct from a profile syntactically, respective productions in the grammar of the [abstract syntax](#) are annotated with an associated profile marker. This is defined to have the following implications:

1. Any production in the [binary](#) or [textual](#) syntax that produces abstract syntax with a marked construct is omitted by extension.
2. Any [validation](#) or [execution](#) rule that handles a marked construct is omitted by extension.

The overall effect is that the respective construct is no longer part of the language under a respective profile.

Note: For example, a “busy” profile marked `BUSY` could rule out the `nop` instruction by marking the production for it in the abstract syntax as follows:

$$\begin{array}{lcl} \text{instr} & ::= & \dots \\ \text{[!BUSY]} & | & \text{nop} \\ & | & \text{unreachable} \end{array}$$

A rule may be annotated by multiple markers, which could be the case if a construct is in the intersection of multiple features.

Semantics Annotations

To restrict certain behaviours in a profile, individual [validation](#) or [reduction](#) rules or auxiliary definitions are annotated with an associated marker.

This has the consequence that the respective rule is no longer applicable under the given profile.

Note: For example, an “infinite” profile marked `INF` could define that growing memory never fails:

$$\begin{array}{lcl} S; F; (\text{i32.const } n) \text{ memory.grow } x & \hookrightarrow & S'; F; (\text{i32.const } sz) \\ & & (\text{if } F.\text{module.memaddrs}[x] = a \\ & & \wedge sz = |S.\text{mems}[a].\text{data}|/64 \text{ Ki} \\ & & \wedge S' = S \text{ with } \text{mems}[a] = \text{growmem}(S.\text{mems}[a], n)) \\ \text{[!INF]} \quad S; F; (\text{i32.const } n) \text{ memory.grow } x & \hookrightarrow & S; F; (\text{i32.const signed}_{32}^{-1}(-1)) \end{array}$$

Properties

All profiles are defined such that the following properties are preserved:

- All profiles represent syntactic and semantic subsets of the [full profile](#), i.e., they do not *add* syntax or *alter* behaviour.
- All profiles are mutually compatible, i.e., no two profiles subset semantic behaviour in inconsistent or ambiguous ways, and any intersection of profiles preserves the properties described here.
- Profiles do not violate [soundness](#), i.e., all [configurations](#) valid under that profile still have well-defined execution behaviour.

Note: Tools are generally expected to handle and produce code for the full profile by default. In particular, producers should not generate code that *depends* on specific profiles. Instead, all code should preserve correctness when executed under the full profile.

Moreover, profiles should be considered static and fixed for a given platform or ecosystem. Runtime conditioning on the “current” profile is not intended and should be avoided.

7.2.2 Defined Profiles

Note: The number of defined profiles is expected to remain small in the future. Profiles are intended for broad and permanent use cases only. In particular, profiles are not intended for language versioning.

Full Profile (FUL)

The *full* profile contains the complete language and all possible behaviours. It imposes no restrictions, i.e., all rules and definitions are active. All other profiles define sub-languages of this profile.

Deterministic Profile (DET)

The *deterministic* profile excludes all rules marked ^[DET]. It defines a sub-language that does not exhibit any incidental non-deterministic behaviour:

- All NaN values generated by floating-point instructions are canonical and positive.
- All relaxed vector instructions have a fixed behaviour that does not depend on the implementation.

Even under this profile, the `memory.grow` and `table.grow` instructions technically remain non-deterministic, in order to be able to indicate resource exhaustion.

Note: In future versions of WebAssembly, new non-deterministic behaviour may be added to the language, such that the deterministic profile will induce additional restrictions.

7.3 Implementation Limitations

Implementations typically impose additional restrictions on a number of aspects of a WebAssembly module or execution. These may stem from:

- physical resource limits,
- constraints imposed by the embedder or its environment,
- limitations of selected implementation strategies.

This section lists allowed limitations. Where restrictions take the form of numeric limits, no minimum requirements are given, nor are the limits assumed to be concrete, fixed numbers. However, it is expected that all implementations have “reasonably” large limits to enable common applications.

Note: A conforming implementation is not allowed to leave out individual *features*. However, designated subsets of WebAssembly may be specified in the future.

7.3.1 Syntactic Limits

Structure

An implementation may impose restrictions on the following dimensions of a module:

- the number of [types](#) in a [module](#)
- the number of [functions](#) in a [module](#), including imports
- the number of [tables](#) in a [module](#), including imports
- the number of [memories](#) in a [module](#), including imports
- the number of [globals](#) in a [module](#), including imports
- the number of [tags](#) in a [module](#), including imports
- the number of [element segments](#) in a [module](#)
- the number of [data segments](#) in a [module](#)
- the number of [imports](#) to a [module](#)
- the number of [exports](#) from a [module](#)
- the number of [sub types](#) in a [recursive type](#)
- the subtyping depth of a [sub type](#)
- the number of fields in a [structure type](#)
- the number of parameters in a [function type](#)
- the number of results in a [function type](#)
- the number of parameters in a [block type](#)
- the number of results in a [block type](#)
- the number of [locals](#) in a [function](#)
- the number of [instructions](#) in a [function body](#)
- the number of [instructions](#) in a [structured control instruction](#)
- the number of [structured control instructions](#) in a [function](#)
- the nesting depth of [structured control instructions](#)
- the number of [label indices](#) in a [br_table](#) instruction
- the number of instructions in a [constant expression](#)
- the length of the array in a [array.new_fixed](#) instruction
- the length of an [element segment](#)
- the length of a [data segment](#)
- the length of a [name](#)
- the range of [characters](#) in a [name](#)

If the limits of an implementation are exceeded for a given module, then the implementation may reject the [validation](#), compilation, or [instantiation](#) of that module with an embedder-specific error.

Note: The last item allows [embedders](#) that operate in limited environments without support for [Unicode](#)⁴⁹ to limit the names of [imports](#) and [exports](#) to common subsets like [ASCII](#)⁵⁰.

⁴⁹ <https://www.unicode.org/versions/latest/>

⁵⁰ <https://webstore.ansi.org/RecordDetail.aspx?sku=INCITS+4-1986%5bR2012%5d>

Binary Format

For a module given in [binary format](#), additional limitations may be imposed on the following dimensions:

- the size of a [module](#)
- the size of any [section](#)
- the size of an individual [function's code](#)
- the size of a [structured control instruction](#)
- the size of an individual [constant expression's](#) instruction sequence
- the number of [sections](#)

Text Format

For a module given in [text format](#), additional limitations may be imposed on the following dimensions:

- the size of the [source text](#)
- the size of any syntactic element
- the size of an individual [token](#)
- the nesting depth of [folded instructions](#)
- the length of symbolic [identifiers](#)
- the range of literal [characters](#) allowed in the [source text](#)

7.3.2 Validation

An implementation may defer [validation](#) of individual [functions](#) until they are first [invoked](#).

If a function turns out to be invalid, then the invocation, and every consecutive call to the same function, results in a [trap](#).

Note: This is to allow implementations to use interpretation or just-in-time compilation for functions. The function must still be fully validated before execution of its body begins.

7.3.3 Execution

Restrictions on the following dimensions may be imposed during [execution](#) of a WebAssembly program:

- the number of allocated [module instances](#)
- the number of allocated [function instances](#)
- the number of allocated [table instances](#)
- the number of allocated [memory instances](#)
- the number of allocated [global instances](#)
- the number of allocated [tag instances](#)
- the number of allocated [structure instances](#)
- the number of allocated [array instances](#)
- the number of allocated [exception instances](#)
- the size of a [table instance](#)

- the size of a [memory instance](#)
- the size of an [array instance](#)
- the number of [frames](#) on the [stack](#)
- the number of [labels](#) on the [stack](#)
- the number of [values](#) on the [stack](#)

If the runtime limits of an implementation are exceeded during execution of a computation, then it may terminate that computation and report an embedder-specific error to the invoking code.

Some of the above limits may already be verified during instantiation, in which case an implementation may report exceedance in the same manner as for [syntactic limits](#).

Note: Concrete limits are usually not fixed but may be dependent on specifics, interdependent, vary over time, or depend on other implementation- or embedder-specific situations or events.

7.4 Type Soundness

The [type system](#) of WebAssembly is *sound*, implying both *type safety* and *memory safety* with respect to the WebAssembly semantics. For example:

- All types declared and derived during validation are respected at run time; e.g., every [local](#) or [global](#) variable will only contain type-correct values, every [instruction](#) will only be applied to operands of the expected type, and every [function invocation](#) always evaluates to a result of the right type (if it does not diverge, throw an exception, or [trap](#)).
- No memory location will be read or written except those explicitly defined by the program, i.e., as a [local](#), a [global](#), an element in a [table](#), or a location within a linear [memory](#).
- There is no undefined behavior, i.e., the [execution rules](#) cover all possible cases that can occur in a [valid](#) program, and the rules are mutually consistent.

Soundness also is instrumental in ensuring additional properties, most notably, *encapsulation* of function and module scopes: no [locals](#) can be accessed outside their own function and no [module](#) components can be accessed outside their own module unless they are explicitly [exported](#) or [imported](#).

The typing rules defining WebAssembly [validation](#) only cover the *static* components of a WebAssembly program. In order to state and prove soundness precisely, the typing rules must be extended to the *dynamic* components of the abstract [runtime](#), that is, the [store](#), [configurations](#), and [administrative instructions](#).⁵¹

7.4.1 Contexts

In order to check [rolled up](#) recursive types, the [context](#) is locally extended with an additional component that records the [sub type](#) corresponding to each [recursive type index](#) within the current [recursive type](#):

$$C ::= \{ \dots, \text{recs } subtype^* \}$$

⁵¹ The formalization and theorems are derived from the following article: Andreas Haas, Andreas Rossberg, Derek Schuff, Ben Titzer, Dan Gohman, Luke Wagner, Alon Zakai, JF Bastien, Michael Holman. [Bringing the Web up to Speed with WebAssembly](#)^{Page 273},⁵². Proceedings of the 38th ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI 2017). ACM 2017.

⁵² <https://dl.acm.org/citation.cfm?doid=3062341.3062363>

7.4.2 Types

Well-formedness for [extended type forms](#) is defined as follows.

Heap Type *bot*

- The heap type is valid.

$$\overline{C \vdash \text{bot ok}}$$

Heap Type *rec i*

- The recursive type index *i* must exist in *C.recs*.
- Then the heap type is valid.

$$\frac{C.\text{recs}[i] = \text{subtype}}{C \vdash \text{rec } i \text{ ok}}$$

Value Type *bot*

- The value type is valid.

$$\overline{C \vdash \text{bot ok}}$$

Recursive Types *rec subtype**

- Let *C'* be the current context *C*, but where *recs* is *subtype**.
- There must be a [type index](#) *x*, such that for each sub type *subtype_i* in *subtype**:
 - Under the context *C'*, the sub type *subtype_i* must be valid for type index *x + i* and recursive type index *i*.
- Then the recursive type is valid for the [type index](#) *x*.

$$\frac{\overline{C \vdash \text{rec } \epsilon \text{ ok}(x, i)} \quad \frac{C, \text{recs } \text{subtype}^* \vdash \text{rec } \text{subtype}^* \text{ ok}(x, 0)}{C \vdash \text{rec } \text{subtype}^* \text{ ok}(x)} \quad \frac{C \vdash \text{subtype} \text{ ok}(x, i) \quad C \vdash \text{rec } \text{subtype}'^* \text{ ok}(x + 1, i + 1)}{C \vdash \text{rec } \text{subtype } \text{subtype}'^* \text{ ok}(x, i)}$$

Note: These rules are a generalisation of the ones [previously given](#).

Sub types *sub final? ht* comptype*

- The [composite type](#) *comptype* must be valid.
- The sequence *ht** may be no longer than 1.
- For every [heap type](#) *ht_k* in *ht**:
 - The [heap type](#) *ht_k* must be ordered before a [type index](#) *x* and [recursive type index](#) *i*, meaning:
 - * Either *ht_k* is a [defined type](#).
 - * Or *ht_k* is a [type index](#) *y_k* that is smaller than *x*.
 - * Or *ht_k* is a [recursive type index](#) *rec j_k* where *j_k* is smaller than *i*.

- Let sub type $subtype_k$ be the unrolling of the heap type ht_k , meaning:
 - * Either ht_k is a defined type $deftype_k$, then $subtype_k$ must be the unrolling of $deftype_k$.
 - * Or ht_k is a type index y_k , then $subtype_k$ must be the unrolling of the defined type $C.types[y_k]$.
 - * Or ht_k is a recursive type index $rec\ j_k$, then $subtype_k$ must be $C.recs[j_k]$.
- The sub type $subtype_k$ must not contain **final**.
- Let $comptype'_k$ be the composite type in $subtype_k$.
- The composite type $comptype$ must match $comptype'_k$.
- Then the sub type is valid for the type index x and recursive type index i .

$$\frac{\begin{array}{c} |ht^*| \leq 1 \quad (ht \prec x, i)^* \quad (\text{unroll}_C(ht) = \text{sub } ht'^* \text{ } comptype')^* \\ C \vdash comptype \text{ ok} \quad (C \vdash comptype \leq comptype')^* \end{array}}{C \vdash \text{sub final? } ht^* \text{ } comptype \text{ ok}(x, i)}$$

where:

$$\begin{aligned} (deftype \prec x, i) &= \text{true} \\ (y \prec x, i) &= y < x \\ (rec\ j \prec x, i) &= j < i \\ \text{unroll}_C(deftype) &= \text{unroll}(deftype) \\ \text{unroll}_C(y) &= \text{unroll}(C.types[y]) \\ \text{unroll}_C(rec\ j) &= C.recs[j] \end{aligned}$$

Note: This rule is a generalisation of the ones previously given, which only allowed type indices as supertypes.

7.4.3 Subtyping

In a rolled-up recursive type, a recursive type indices $rec\ i$ matches another heap type ht if:

- Let $\text{sub final? } ht'^* \text{ } comptype$ be the sub type $C.recs[i]$.
- The heap type ht is contained in ht'^* .

$$\frac{C.recs[i] = \text{sub final? } (ht_1^* \text{ } ht \text{ } ht_2^*) \text{ } comptype}{C \vdash \text{rec } i \leq ht}$$

Note: This rule is only invoked when checking validity of rolled-up recursive types.

7.4.4 Results

Results can be classified by result types as follows.

Results val^*

- For each value val_i in val^* :
 - The value val_i is valid with some value type t_i .
- Let t^* be the concatenation of all t_i .
- Then the result is valid with result type $[t^*]$.

$$\frac{(S \vdash val : t)^*}{S \vdash val^* : [t^*]}$$

Results $T[(\text{ref.exn } a) \text{ throw_ref}]$

- The value `ref.exn a` must be valid.
- Then the result is valid with result type $[t^*]$, for any sequence t'^* of value types.

Results `trap`

- The result is valid with result type $[t^*]$, for any valid closed result types.

$$\frac{\frac{\vdash [t^*] \text{ ok}}{S \vdash \text{trap} : [t^*]}}{S \vdash \text{tag } \text{tagaddr} : \text{tag } [t^*] \rightarrow [] \quad (S \vdash \text{val} : t)^*} \quad S \vdash T[(\text{ref.exn } a) \text{ throw_ref}] : [t'^*]$$

7.4.5 Store Validity

The following typing rules specify when a runtime store S is *valid*. A valid store must consist of `function`, `table`, `memory`, `global`, `tag`, `element`, `data`, `structure`, `array`, `exception`, and `module` instances that are themselves valid, relative to S .

To that end, each kind of instance is classified by a respective `function`, `table`, `memory`, `global`, `tag`, `element`, or `data` type, or just `ok` in the case of `structures`, `arrays`, or `exceptions`. Module instances are classified by *module contexts*, which are regular `contexts` repurposed as module types describing the `index spaces` defined by a module.

Store S

- Each function instance funcinst_i in $S.\text{funcs}$ must be valid with some function type functype_i .
- Each table instance tableinst_i in $S.\text{tables}$ must be valid with some table type tabletype_i .
- Each memory instance meminst_i in $S.\text{mems}$ must be valid with some memory type memtype_i .
- Each global instance globalinst_i in $S.\text{globals}$ must be valid with some global type globaltype_i .
- Each tag instance taginst_i in $S.\text{tags}$ must be valid with some tag type tagtype_i .
- Each element instance eleminst_i in $S.\text{elems}$ must be valid with some reference type reftype_i .
- Each data instance datainst_i in $S.\text{datas}$ must be valid.
- Each structure instance structinst_i in $S.\text{structs}$ must be valid.
- Each array instance arrayinst_i in $S.\text{arrays}$ must be valid.
- Each exception instance exninst_i in $S.\text{exns}$ must be valid.
- No `reference` to a bound `structure address` must be reachable from itself through a path consisting only of indirections through immutable structure, or array `fields` or fields of `exception` instances.
- No `reference` to a bound `array address` must be reachable from itself through a path consisting only of indirections through immutable structure or array `fields` or fields of `exception` instances.
- No `reference` to a bound `exception address` must be reachable from itself through a path consisting only of indirections through immutable structure or array `fields` or fields of `exception` instances.
- Then the store is valid.

$$\begin{array}{c}
 (S \vdash \text{funcinst} : \text{deftype})^* \quad (S \vdash \text{tableinst} : \text{tabletype})^* \\
 (S \vdash \text{meminst} : \text{memtype})^* \quad (S \vdash \text{globalinst} : \text{globaltype})^* \\
 (S \vdash \text{taginst} : \text{tagtype})^* \\
 (S \vdash \text{eleminst} : \text{reftype})^* \quad (S \vdash \text{datainst} : \text{ok})^* \\
 (S \vdash \text{structinst} : \text{ok})^* \quad (S \vdash \text{arrayinst} : \text{ok})^* \quad (S \vdash \text{exninst} : \text{ok})^* \\
 S = \{\text{funcs } \text{funcinst}^*, \text{globals } \text{globalinst}^*, \text{tables } \text{tableinst}^*, \text{mems } \text{meminst}^*, \text{tags } \text{taginst}^*, \\
 \text{elems } \text{eleminst}^*, \text{datas } \text{datainst}^*, \text{structs } \text{structinst}^*, \text{arrays } \text{arrayinst}^*, \text{exns } \text{exninst}^*\} \\
 (S.\text{structs}[a_s] = \text{structinst})^* \quad ((\text{ref.struct } a_s) \gg_S^+ (\text{ref.struct } a_s))^* \\
 (S.\text{arrays}[a_a] = \text{arrayinst})^* \quad ((\text{ref.array } a_a) \gg_S^+ (\text{ref.array } a_a))^* \\
 (S.\text{exns}[a_e] = \text{exninst})^* \quad ((\text{ref.exn } a_e) \gg_S^+ (\text{ref.exn } a_e))^* \\
 \hline
 \vdash S \text{ ok}
 \end{array}$$

where $\text{val}_1 \gg_S^+ \text{val}_2$ denotes the transitive closure of the following *immutable reachability* relation on *values*:

$$\begin{array}{ll}
 (\text{ref.struct } a) & \gg_S \quad S.\text{structs}[a].\text{fields}[i] \quad \text{if } \text{expand}(S.\text{structs}[a].\text{type}) = \text{struct } ft_1^i (\text{const } st) ft_2^* \\
 (\text{ref.array } a) & \gg_S \quad S.\text{arrays}[a].\text{fields}[i] \quad \text{if } \text{expand}(S.\text{arrays}[a].\text{type}) = \text{array } (\text{const } st) \\
 (\text{ref.exn } a) & \gg_S \quad S.\text{exns}[a].\text{fields}[i] \\
 (\text{ref.extern } ref) & \gg_S \quad ref
 \end{array}$$

Note: The constraint on reachability through immutable fields prevents the presence of cyclic data structures that can not be constructed in the language. Cycles can only be formed using mutation.

Function Instances {type *functype*, module *moduleinst*, code *func*}

- The function type *functype* must be *valid* under an empty context.
- The module instance *moduleinst* must be *valid* with some context *C*.
- Under context *C*:
 - The function *func* must be *valid* with some function type *functype'*.
 - The function type *functype'* must match *functype*.
- Then the function instance is *valid* with function type *functype*.

$$\frac{\begin{array}{c} \vdash \text{functype} \text{ ok} \quad S \vdash \text{moduleinst} : C \\ C \vdash \text{func} : \text{functype}' \quad C \vdash \text{functype}' \leq \text{functype} \end{array}}{S \vdash \{\text{type } \text{functype}, \text{module } \text{moduleinst}, \text{code } \text{func}\} : \text{functype}}$$

Host Function Instances {type *functype*, hostcode *hf*}

- The function type *functype* must be *valid* under an empty context.
- Let $[t_1^*] \rightarrow [t_2^*]$ be the function type *functype*.
- For every *valid* store S_1 extending *S* and every sequence val^* of *values* whose *types* coincide with t_1^* :
 - Executing *hf* in store S_1 with arguments val^* has a non-empty set of possible outcomes.
 - For every element *R* of this set:
 - * Either *R* must be \perp (i.e., divergence).
 - * Or *R* consists of a *valid* store S_2 extending S_1 and a *result* *result* whose *type* coincides with t_2^* .
- Then the function instance is *valid* with function type *functype*.

$$\begin{array}{c}
\forall S_1, val^*, \vdash S_1 \text{ ok} \wedge \vdash S \preceq S_1 \wedge S_1 \vdash val^* : [t_1^*] \implies \\
hf(S_1; val^*) \supset \emptyset \wedge \\
\forall R \in hf(S_1; val^*), R = \perp \vee \\
\vdash [t_1^*] \rightarrow [t_2^*] \text{ ok} \quad \exists S_2, result, \vdash S_2 \text{ ok} \wedge \vdash S_1 \preceq S_2 \wedge S_2 \vdash result : [t_2^*] \wedge R = (S_2; result) \\
\hline
S \vdash \{\text{type } [t_1^*] \rightarrow [t_2^*], \text{hostcode } hf\} : [t_1^*] \rightarrow [t_2^*]
\end{array}$$

Note: This rule states that, if appropriate pre-conditions about store and arguments are satisfied, then executing the host function must satisfy appropriate post-conditions about store and results. The post-conditions match the ones in the [execution rule](#) for invoking host functions.

Any store under which the function is invoked is assumed to be an extension of the current store. That way, the function itself is able to make sufficient assumptions about future stores.

Table Instances $\{\text{type } addrtype \text{ limits } t, \text{elem } ref^*\}$

- The table type *addrtype limits t* must be valid under the empty context.
- The length of *ref** must equal *limits.min*.
- For each reference *ref_i* in the table's elements *refⁿ*:
 - The reference *ref_i* must be valid with some reference type *t'_i*.
 - The reference type *t'_i* must match the reference type *t*.
- Then the table instance is valid with table type *addrtype limits t*.

$$\frac{\vdash addrtype \text{ limits } t \text{ ok} \quad n = \text{limits.min} \quad (S \vdash ref : t')^n \quad (\vdash t' \leq t)^n}{S \vdash \{\text{type } addrtype \text{ limits } t, \text{elem } ref^n\} : addrtype \text{ limits } t}$$

Memory Instances $\{\text{type } addrtype \text{ limits}, \text{data } b^*\}$

- The memory type *addrtype limits* must be valid under the empty context.
- The length of *b** must equal *limits.min* multiplied by the page size 64 Ki.
- Then the memory instance is valid with memory type *addrtype limits*.

$$\frac{\vdash addrtype \text{ limits ok} \quad n = \text{limits.min} \cdot 64 \text{ Ki}}{S \vdash \{\text{type } addrtype \text{ limits}, \text{data } b^n\} : addrtype \text{ limits}}$$

Global Instances $\{\text{type } mut \text{ } t, \text{value } val\}$

- The global type *mut t* must be valid under the empty context.
- The value *val* must be valid with some value type *t'*.
- The value type *t'* must match the value type *t*.
- Then the global instance is valid with global type *mut t*.

$$\frac{\vdash mut \text{ } t \text{ ok} \quad S \vdash val : t' \quad \vdash t' \leq t}{S \vdash \{\text{type } mut \text{ } t, \text{value } val\} : mut \text{ } t}$$

Tag Instances $\{\text{type } \text{tagtype}\}$

- The tag type tagtype must be valid under the empty context.
- Then the tag instance is valid with tag type tagtype .

$$\frac{\vdash \text{tagtype ok}}{S \vdash \{\text{type } \text{tagtype}\} : \text{tagtype}}$$

Element Instances $\{\text{type } t, \text{elem } \text{ref}^*\}$

- The reference type t must be valid under the empty context.
- For each reference ref_i in the elements ref^n :
 - The reference ref_i must be valid with some reference type t'_i .
 - The reference type t'_i must match the reference type t .
- Then the element instance is valid with reference type t .

$$\frac{\vdash t \text{ ok} \quad (S \vdash \text{ref} : t')^* \quad (\vdash t' \leq t)^*}{S \vdash \{\text{type } t, \text{elem } \text{ref}^*\} : t}$$

Data Instances $\{\text{data } b^*\}$

- The data instance is valid.

$$\overline{S \vdash \{\text{data } b^*\} \text{ ok}}$$

Structure Instances $\{\text{type } \text{deftype}, \text{fields } \text{fieldval}^*\}$

- The defined type deftype must be valid under the empty context.
- The expansion of deftype must be a structure type $\text{struct } \text{fieldtype}^*$.
- The length of the sequence of field values fieldval^* must be the same as the length of the sequence of field types fieldtype^* .
- For each field value fieldval_i in fieldval^* and corresponding field type fieldtype_i in fieldtype^* :
 - Let fieldtype_i be $\text{mut } \text{storage}_i$.
 - The field value fieldval_i must be valid with storage type storage_i .
- Then the structure instance is valid.

$$\frac{\vdash dt \text{ ok} \quad \text{expand}(dt) = \text{struct } (\text{mut } st)^* \quad (S \vdash fv : st)^*}{S \vdash \{\text{type } dt, \text{fields } fv^*\} \text{ ok}}$$

Array Instances $\{\text{type } \textit{deftype}, \text{fields } \textit{fieldval}^*\}$

- The defined type $\textit{deftype}$ must be valid under the empty context.
- The expansion of $\textit{deftype}$ must be an array type $\text{array } \textit{fieldtype}$.
- Let $\textit{fieldtype}$ be $\textit{mut storagetype}$.
- For each field value $\textit{fieldval}_i$ in $\textit{fieldval}^*$:
 - The field value $\textit{fieldval}_i$ must be valid with storage type $\textit{storagetype}$.
- Then the array instance is valid.

$$\frac{\vdash dt \text{ ok} \quad \text{expand}(dt) = \text{array } (\textit{mut } st) \quad (S \vdash fv : st)^*}{S \vdash \{\text{type } dt, \text{fields } fv^*\} \text{ ok}}$$

Field Values $\textit{fieldval}$

- If $\textit{fieldval}$ is a value val , then:
 - The value val must be valid with value type t .
 - Then the field value is valid with value type t .
- Else, $\textit{fieldval}$ is a packed value $\textit{packedval}$:
 - Let $\textit{packedtype.pack } i$ be the field value $\textit{fieldval}$.
 - Then the field value is valid with packed type $\textit{packedtype}$.

$$\overline{S \vdash pt.\text{pack } i : pt}$$

Exception Instances $\{\text{tag } a, \text{fields } \textit{val}^*\}$

- The store entry $S.\text{tags}[a]$ must exist.
- Let $[t^*] \rightarrow [t'^*]$ be the tag type $S.\text{tags}[a].\text{type}$.
- The result type $[t'^*]$ must be empty.
- The sequence $\textit{val}^a st$ of values must have the same length as the sequence t^* of value types.
- For each value \textit{val}_i in $\textit{val}^a st$ and corresponding value type t_i in t^* , the value \textit{val}_i must be valid with type t_i .
- Then the exception instance is valid.

$$\frac{S.\text{tags}[a] = \{\text{type} = [t^*] \rightarrow []\} \quad (S \vdash \textit{val} : t)^*}{S \vdash \{\text{tag } a, \text{fields } \textit{val}^*\} \text{ ok}}$$

Export Instances $\{\text{name } \textit{name}, \text{value } \textit{externval}\}$

- The external value $\textit{externval}$ must be valid with some external type $\textit{externtype}$.
- Then the export instance is valid.

$$\frac{S \vdash \textit{externval} : \textit{externtype}}{S \vdash \{\text{name } \textit{name}, \text{value } \textit{externval}\} \text{ ok}}$$

Module Instances *moduleinst*

- Each defined type *deftype_i* in *moduleinst.types* must be valid under the empty context.
- For each function address *funcaddr_i* in *moduleinst.funcaddrs*, the external value *func funcaddr_i* must be valid with some external type *func functype_i*.
- For each table address *tableaddr_i* in *moduleinst.tableaddrs*, the external value *table tableaddr_i* must be valid with some external type *table tabletype_i*.
- For each memory address *memaddr_i* in *moduleinst.memaddrs*, the external value *mem memaddr_i* must be valid with some external type *mem memtype_i*.
- For each global address *globaladdr_i* in *moduleinst.globaladdrs*, the external value *global globaladdr_i* must be valid with some external type *global globaltype_i*.
- For each tag address *tagaddr_i* in *moduleinst.tagaddrs*, the external value *tag tagaddr_i* must be valid with some external type *tag tagtype_i*.
- For each element address *elemaddr_i* in *moduleinst.elemaddrs*, the element instance *S.elems[elemaddr_i]* must be valid with some reference type *reftype_i*.
- For each data address *dataaddr_i* in *moduleinst.dataaddrs*, the data instance *S.datas[dataaddr_i]* must be valid.
- Each export instance *exportinst_i* in *moduleinst.exports* must be valid.
- For each export instance *exportinst_i* in *moduleinst.exports*, the name *exportinst_i.name* must be different from any other name occurring in *moduleinst.exports*.
- Let *deftype** be the concatenation of all *deftype_i* in order.
- Let *functype** be the concatenation of all *functype_i* in order.
- Let *tabletype** be the concatenation of all *tabletype_i* in order.
- Let *memtype** be the concatenation of all *memtype_i* in order.
- Let *globaltype** be the concatenation of all *globaltype_i* in order.
- Let *tagtype** be the concatenation of all *tagtype_i* in order.
- Let *reftype** be the concatenation of all *reftype_i* in order.
- Let *m* be the length of *moduleinst.funcaddrs*.
- Let *n* be the length of *moduleinst.dataaddrs*.
- Let *x** be the sequence of function indices from 0 to *m* − 1.
- Then the module instance is valid with context {types *deftype**, funcs *functype**, tables *tabletype**, mems *memtype**, globals *globaltype**, CTAGS~tagtype^ast, elems *reftype**, datas *okⁿ*, refs *x**}.

$$\frac{
 \begin{array}{l}
 (\vdash \text{deftype ok})^* \\
 (S \vdash \text{func } \text{funcaddr} : \text{func } \text{functype})^* \quad (S \vdash \text{table } \text{tableaddr} : \text{table } \text{tabletype})^* \\
 (S \vdash \text{mem } \text{memaddr} : \text{mem } \text{memtype})^* \quad (S \vdash \text{global } \text{globaladdr} : \text{global } \text{globaltype})^* \\
 (S \vdash \text{tag } \text{tagaddr} : \text{tag } \text{tagtype})^* \\
 (S \vdash S.\text{elems}[\text{elemaddr}] : \text{reftype})^* \quad (S \vdash S.\text{datas}[\text{dataaddr}] \text{ ok})^n \\
 (S \vdash \text{exportinst ok})^* \quad (\text{exportinst.name})^* \text{ disjoint}
 \end{array}
 }{
 \begin{array}{l}
 S \vdash \{ \text{types } \text{deftype}^*, \\
 \text{funcaddrs } \text{funcaddr}^*, \\
 \text{tableaddrs } \text{tableaddr}^*, \\
 \text{memaddrs } \text{memaddr}^*, \\
 \text{globaladdrs } \text{globaladdr}^*, \\
 \text{tagaddrs } \text{tagaddr}^*, \\
 \text{elemaddrs } \text{elemaddr}^*, \\
 \text{dataaddrs } \text{dataaddr}^n, \\
 \text{exports } \text{exportinst}^* \} : \{ \text{types } \text{deftype}^*, \\
 \text{funcs } \text{functype}^*, \\
 \text{tables } \text{tabletype}^*, \\
 \text{mems } \text{memtype}^*, \\
 \text{globals } \text{globaltype}^*, \\
 \text{tags } \text{tagtype}^*, \\
 \text{elems } \text{reftype}^*, \\
 \text{datas } \text{ok}^n, \\
 \text{refs } 0 \dots (|\text{funcaddr}^*| - 1) \}
 \end{array}$$

7.4.6 Configuration Validity

To relate the WebAssembly [type system](#) to its [execution semantics](#), the [typing rules for instructions](#) must be extended to [configurations](#) $S; T$, which relates the [store](#) to execution [threads](#).

Configurations and threads are classified by their [result type](#). In addition to the store S , threads are typed under a [return type](#) $\text{resulttype}^?$, which controls whether and with which type a [return](#) instruction is allowed. This type is absent (ϵ) except for instruction sequences inside an administrative [frame](#) instruction.

Finally, [frames](#) are classified with [frame contexts](#), which extend the [module contexts](#) of a frame's associated [module instance](#) with the [locals](#) that the frame contains.

Configurations $S; T$

- The [store](#) S must be [valid](#).
- Under no allowed return type, the [thread](#) T must be [valid](#) with some [result type](#) $[t^*]$.
- Then the configuration is valid with the [result type](#) $[t^*]$.

$$\frac{\vdash S \text{ ok} \quad S; \epsilon \vdash T : [t^*]}{\vdash S; T : [t^*]}$$

Threads $F; instr^*$

- Let $resulttype^?$ be the current allowed return type.
- The frame F must be **valid** with a context C .
- Let C' be the same context as C , but with **return** set to $resulttype^?$.
- Under context C' , the instruction sequence $instr^*$ must be **valid** with some type $[] \rightarrow [t^*]$.
- Then the thread is valid with the **result type** $[t^*]$.

$$\frac{S \vdash F : C \quad S; C, \text{return } resulttype^? \vdash instr^* : [] \rightarrow [t^*]}{S; resulttype^? \vdash F; instr^* : [t^*]}$$

Frames $\{locals\ val^*, module\ moduleinst\}$

- The module instance $moduleinst$ must be **valid** with some module context C .
- Each value val_i in val^* must be **valid** with some value type t_i .
- Let t^* be the concatenation of all t_i in order.
- Let C' be the same context as C , but with the value types t^* prepended to the **locals** vector.
- Then the frame is valid with **frame context** C' .

$$\frac{S \vdash moduleinst : C \quad (S \vdash val : t)^*}{S \vdash \{locals\ val^*, module\ moduleinst\} : (C, locals\ t^*)}$$

7.4.7 Administrative Instructions

Typing rules for **administrative instructions** are specified as follows. In addition to the context C , typing of these instructions is defined under a given store S .

To that end, all previous typing judgements $C \vdash prop$ are generalized to include the store, as in $S; C \vdash prop$, by implicitly adding S to all rules – S is never modified by the pre-existing rules, but it is accessed in the extra rules for **administrative instructions** given below.

trap

- The instruction is valid with any **valid instruction type** of the form $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{C \vdash [t_1^*] \rightarrow [t_2^*] \text{ ok}}{S; C \vdash \text{trap} : [t_1^*] \rightarrow [t_2^*]}$$

val

- The value val must be **valid** with value type t .
- Then it is valid as an instruction with type $[] \rightarrow [t]$.

$$\frac{S \vdash val : t}{S; C \vdash val : [] \rightarrow [t]}$$

invoke *funcaddr*

- The external function value *func funcaddr* must be valid with external function type *funcfunctype'*.
- Let $[t_1^*] \rightarrow [t_2^*]$ be the function type *functype*.
- Then the instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{S \vdash \text{func } \text{funcaddr} : \text{func } [t_1^*] \rightarrow [t_2^*]}{S; C \vdash \text{invoke } \text{funcaddr} : [t_1^*] \rightarrow [t_2^*]}$$

label_{*n*}{*instr*₀^{*}} *instr*^{*} end

- The instruction sequence *instr*₀^{*} must be valid with some type $[t_1^n] \rightarrow_{x^*} [t_2^*]$.
- Let *C'* be the same context as *C*, but with the result type $[t_1^n]$ prepended to the labels vector.
- Under context *C'*, the instruction sequence *instr*^{*} must be valid with type $[] \rightarrow_{x'^*} [t_2^*]$.
- Then the compound instruction is valid with type $[] \rightarrow [t_2^*]$.

$$\frac{S; C \vdash \text{instr}_0^* : [t_1^n] \rightarrow_{x^*} [t_2^*] \quad S; C, \text{labels } [t_1^n] \vdash \text{instr}^* : [] \rightarrow_{x'^*} [t_2^*]}{S; C \vdash \text{label}_n\{\text{instr}_0^*\} \text{ instr}^* \text{ end} : [] \rightarrow [t_2^*]}$$

handler_{*n*}{*catch*^{*}} *instr*^{*} end

- For every catch clause *catch*_{*i*} in *catch*^{*}, *catch*_{*i*} must be valid.
- The instruction sequence *instr*^{*} must be valid with some type $[t_1^*] \rightarrow [t_2^*]$.
- Then the compound instruction is valid with type $[t_1^*] \rightarrow [t_2^*]$.

$$\frac{(C \vdash \text{catch ok})^* \quad S; C \vdash \text{instr}^* : [t_1^*] \rightarrow [t_2^*]}{S; C \vdash \text{handler}_n\{\text{catch}^*\} \text{ instr}^* \text{ end} : [t_1^*] \rightarrow [t_2^*]}$$

frame_{*n*}{*F*} *instr*^{*} end

- Under the valid return type $[t^n]$, the thread *F*; *instr*^{*} must be valid with result type $[t^n]$.
- Then the compound instruction is valid with type $[] \rightarrow [t^n]$.

$$\frac{C \vdash [t^n] \text{ ok} \quad S; [t^n] \vdash F; \text{instr}^* : [t^n]}{S; C \vdash \text{frame}_n\{F\} \text{ instr}^* \text{ end} : [] \rightarrow [t^n]}$$

7.4.8 Store Extension

Programs can mutate the store and its contained instances. Any such modification must respect certain invariants, such as not removing allocated instances or changing immutable definitions. While these invariants are inherent to the execution semantics of WebAssembly instructions and modules, host functions do not automatically adhere to them. Consequently, the required invariants must be stated as explicit constraints on the invocation of host functions. Soundness only holds when the embedder ensures these constraints.

The necessary constraints are codified by the notion of store *extension*: a store state *S'* extends state *S*, written $S \preceq S'$, when the following rules hold.

Note: Extension does not imply that the new store is valid, which is defined separately above.

Store S

- The length of $S.funcs$ must not shrink.
- The length of $S.tables$ must not shrink.
- The length of $S.mems$ must not shrink.
- The length of $S.globals$ must not shrink.
- The length of $S.tags$ must not shrink.
- The length of $S.elems$ must not shrink.
- The length of $S.datas$ must not shrink.
- The length of $S.structs$ must not shrink.
- The length of $S.arrays$ must not shrink.
- The length of $S.exns$ must not shrink.
- For each function instance $funcinst_i$ in the original $S.funcs$, the new function instance must be an **extension** of the old.
- For each table instance $tableinst_i$ in the original $S.tables$, the new table instance must be an **extension** of the old.
- For each memory instance $meminst_i$ in the original $S.mems$, the new memory instance must be an **extension** of the old.
- For each global instance $globalinst_i$ in the original $S.globals$, the new global instance must be an **extension** of the old.
- For each tag instance $taginst_i$ in the original $S.tags$, the new tag instance must be an **extension** of the old.
- For each element instance $eleminst_i$ in the original $S.elems$, the new element instance must be an **extension** of the old.
- For each data instance $datainst_i$ in the original $S.datas$, the new data instance must be an **extension** of the old.
- For each structure instance $structinst_i$ in the original $S.structs$, the new structure instance must be an **extension** of the old.
- For each array instance $arrayinst_i$ in the original $S.arrays$, the new array instance must be an **extension** of the old.
- For each exception instance $exninst_i$ in the original $S.exns$, the new exception instance must be an **extension** of the old.

$$\begin{array}{lll}
S_1.funcs = funcinst_1^* & S_2.funcs = funcinst_1'^* funcinst_2^* & (\vdash funcinst_1 \preceq funcinst_1')^* \\
S_1.tables = tableinst_1^* & S_2.tables = tableinst_1'^* tableinst_2^* & (\vdash tableinst_1 \preceq tableinst_1')^* \\
S_1.mems = meminst_1^* & S_2.mems = meminst_1'^* meminst_2^* & (\vdash meminst_1 \preceq meminst_1')^* \\
S_1.globals = globalinst_1^* & S_2.globals = globalinst_1'^* globalinst_2^* & (\vdash globalinst_1 \preceq globalinst_1')^* \\
S_1.tags = taginst_1^* & S_2.tags = taginst_1'^* taginst_2^* & (\vdash taginst_1 \preceq taginst_1')^* \\
S_1.elems = eleminst_1^* & S_2.elems = eleminst_1'^* eleminst_2^* & (\vdash eleminst_1 \preceq eleminst_1')^* \\
S_1.datas = datainst_1^* & S_2.datas = datainst_1'^* datainst_2^* & (\vdash datainst_1 \preceq datainst_1')^* \\
S_1.structs = structinst_1^* & S_2.structs = structinst_1'^* structinst_2^* & (\vdash structinst_1 \preceq structinst_1')^* \\
S_1.arrays = arrayinst_1^* & S_2.arrays = arrayinst_1'^* arrayinst_2^* & (\vdash arrayinst_1 \preceq arrayinst_1')^* \\
S_1.exns = exninst_1^* & S_2.exns = exninst_1'^* exninst_2^* & (\vdash exninst_1 \preceq exninst_1')^* \\
\hline
& \vdash S_1 \preceq S_2
\end{array}$$

Function Instance *funcinst*

- A function instance must remain unchanged.

$$\overline{\vdash \text{funcinst} \preceq \text{funcinst}}$$

Table Instance *tableinst*

- The table type *tableinst.type* must remain unchanged.
- The length of *tableinst.elem* must not shrink.

$$\frac{n_1 \leq n_2}{\vdash \{\text{type } tt, \text{elem } (fa_1^?)^{n_1}\} \preceq \{\text{type } tt, \text{elem } (fa_2^?)^{n_2}\}}$$

Memory Instance *meminst*

- The memory type *meminst.type* must remain unchanged.
- The length of *meminst.data* must not shrink.

$$\frac{n_1 \leq n_2}{\vdash \{\text{type } mt, \text{data } b_1^{n_1}\} \preceq \{\text{type } mt, \text{data } b_2^{n_2}\}}$$

Global Instance *globalinst*

- The global type *globalinst.type* must remain unchanged.
- Let *mut t* be the structure of *globalinst.type*.
- If *mut* is *const*, then the value *globalinst.value* must remain unchanged.

$$\frac{\text{mut} = \text{var} \vee \text{val}_1 = \text{val}_2}{\vdash \{\text{type } (\text{mut } t), \text{value } \text{val}_1\} \preceq \{\text{type } (\text{mut } t), \text{value } \text{val}_2\}}$$

Tag Instance *taginst*

- A tag instance must remain unchanged.

$$\overline{\vdash \text{taginst} \preceq \text{taginst}}$$

Element Instance *eleminst*

- The reference type *eleminst.type* must remain unchanged.
- The vector *eleminst.elem* must:
 - either remain unchanged,
 - or shrink to length 0.

$$\overline{\vdash \{\text{type } t, \text{elem } a^*\} \preceq \{\text{type } t, \text{elem } a^*\}}$$

$$\overline{\vdash \{\text{type } t, \text{elem } a^*\} \preceq \{\text{type } t, \text{elem } \epsilon\}}$$

Data Instance *datainst*

- The vector *datainst.data* must:
 - either remain unchanged,
 - or shrink to length 0.

$$\frac{}{\vdash \{\text{data } b^*\} \preceq \{\text{data } b^*\}}$$

$$\frac{}{\vdash \{\text{data } b^*\} \preceq \{\text{data } \epsilon\}}$$

Structure Instance *structinst*

- The defined type *structinst.type* must remain unchanged.
- Assert: due to store well-formedness, the expansion of *structinst.type* is a structure type.
- Let struct *fieldtype** be the expansion of *structinst.type*.
- The length of the vector *structinst.fields* must remain unchanged.
- Assert: due to store well-formedness, the length of *structinst.fields* is the same as the length of *fieldtype**.
- For each field value *fieldval_i* in *structinst.fields* and corresponding field type *fieldtype_i* in *fieldtype**:
 - Let *mut_i st_i* be the structure of *fieldtype_i*.
 - If *mut_i* is *const*, then the field value *fieldval_i* must remain unchanged.

$$\frac{(mut = \text{var} \vee fieldval_1 = fieldval_2)^*}{\vdash \{\text{type } (mut \text{ st})^*, \text{fields } fieldval_1^*\} \preceq \{\text{type } (mut \text{ st})^*, \text{fields } fieldval_2^*\}}$$

Array Instance *arrayinst*

- The defined type *arrayinst.type* must remain unchanged.
- Assert: due to store well-formedness, the expansion of *arrayinst.type* is an array type.
- Let array *fieldtype* be the expansion of *arrayinst.type*.
- The length of the vector *arrayinst.fields* must remain unchanged.
- Let *mut st* be the structure of *fieldtype*.
- If *mut* is *const*, then the sequence of field values *arrayinst.fields* must remain unchanged.

$$\frac{mut = \text{var} \vee fieldval_1^* = fieldval_2^*}{\vdash \{\text{type } (mut \text{ st}), \text{fields } fieldval_1^*\} \preceq \{\text{type } (mut \text{ st}), \text{fields } fieldval_2^*\}}$$

Exception Instance *exninst*

- An exception instance must remain unchanged.

$$\frac{}{\vdash \text{exninst} \preceq \text{exninst}}$$

7.4.9 Theorems

Given the definition of **valid configurations**, the standard soundness theorems hold.⁵³⁵⁵

Theorem (Preservation). If a **configuration** $S;T$ is **valid** with **result type** $[t^*]$ (i.e., $\vdash S;T : [t^*]$), and steps to $S';T'$ (i.e., $S;T \hookrightarrow S';T'$), then $S';T'$ is a **valid configuration** with the same **result type** (i.e., $\vdash S';T' : [t^*]$). Furthermore, S' is an **extension** of S (i.e., $\vdash S \preceq S'$).

A **terminal thread** is one whose sequence of **instructions** is a **result**. A **terminal configuration** is a configuration whose thread is terminal.

Theorem (Progress). If a **configuration** $S;T$ is **valid** (i.e., $\vdash S;T : [t^*]$ for some **result type** $[t^*]$), then either it is terminal, or it can step to some configuration $S';T'$ (i.e., $S;T \hookrightarrow S';T'$).

From Preservation and Progress the soundness of the WebAssembly type system follows directly.

Corollary (Soundness). If a **configuration** $S;T$ is **valid** (i.e., $\vdash S;T : [t^*]$ for some **result type** $[t^*]$), then it either diverges or takes a finite number of steps to reach a **terminal configuration** $S';T'$ (i.e., $S;T \hookrightarrow^* S';T'$) that is **valid** with the same **result type** (i.e., $\vdash S';T' : [t^*]$) and where S' is an **extension** of S (i.e., $\vdash S \preceq S'$).

In other words, every thread in a **valid configuration** either runs forever, traps, throws an exception, or terminates with a result that has the expected type. Consequently, given a **valid store**, no computation defined by **instantiation** or **invocation** of a **valid module** can “crash” or otherwise (mis)behave in ways not covered by the **execution** semantics given in this specification.

7.5 Type System Properties

7.5.1 Principal Types

The **type system** of WebAssembly features both **subtyping** and simple forms of **polymorphism** for **instruction types**. That has the effect that every instruction or instruction sequence can be classified with multiple different instruction types.

However, the typing rules still allow deriving *principal types* for instruction sequences. That is, every valid instruction sequence has one particular type scheme, possibly containing some unconstrained place holder *type variables*, that is a subtype of all its valid instruction types, after substituting its type variables with suitable specific types.

Moreover, when deriving an instruction type in a “forward” manner, i.e., the *input* of the instruction sequence is already fixed to specific types, then it has a principal *output* type expressible without type variables, up to a possibly **polymorphic stack** bottom representable with one single variable. In other words, “forward” principal types are effectively *closed*.

Note: For example, in isolation, the instruction `ref.as_non_null` has the type $[(\text{ref } \text{null } ht)] \rightarrow [(\text{ref } ht)]$ for any choice of valid **heap type** ht . Moreover, if the input type $[(\text{ref } \text{null } ht)]$ is already determined, i.e., a specific ht is given, then the output type $[(\text{ref } ht)]$ is fully determined as well.

The implication of the latter property is that a validator for *complete* instruction sequences (as they occur in valid modules) can be implemented with a simple left-to-right **algorithm** that does not require the introduction of type variables.

A typing algorithm capable of handling *partial* instruction sequences (as might be considered for program analysis or program manipulation) needs to introduce type variables and perform substitutions, but it does not need to

⁵³ A machine-verified version of the formalization and soundness proof of the PLDI 2017 paper is described in the following article: Conrad Watt. *Mechanising and Verifying the WebAssembly Specification* Page 288. ⁵⁴. Proceedings of the 7th ACM SIGPLAN Conference on Certified Programs and Proofs (CPP 2018). ACM 2018.

⁵⁴ <https://dl.acm.org/citation.cfm?id=3167082>

⁵⁵ Machine-verified formalizations and soundness proofs of the semantics from the official specification are described in the following article: Conrad Watt, Xiaojia Rao, Jean Pichon-Pharabod, Martin Bodin, Philippa Gardner. *Two Mechanisations of WebAssembly 1.0*⁵⁶. Proceedings of the 24th International Symposium on Formal Methods (FM 2021). Springer 2021.

⁵⁶ https://link.springer.com/chapter/10.1007/978-3-030-90870-6_4

perform backtracking or record any non-syntactic constraints on these type variables.

Technically, the [syntax](#) of [heap](#), [value](#), and [result](#) types can be enriched with type variables as follows:

$$\begin{aligned}
 \text{null} &::= \text{null}^? \mid \alpha_{\text{null}} \\
 \text{heapttype} &::= \dots \mid \alpha_{\text{heapttype}} \\
 \text{ref\textit{type}} &::= \text{ref } \text{null } \text{heapttype} \\
 \text{val\textit{type}} &::= \dots \mid \alpha_{\text{val\textit{type}}} \mid \alpha_{\text{numvectype}} \\
 \text{result\textit{type}} &::= [\alpha_{\text{val\textit{type}}^*}^? \text{ val\textit{type}}^*]
 \end{aligned}$$

where each α_{xyz} ranges over a set of type variables for syntactic class xyz , respectively. The special class numvectype is defined as $\text{numtype} \mid \text{vectype} \mid \text{bot}$, and is only needed to handle unannotated [select](#) instructions.

A type is *closed* when it does not contain any type variables, and *open* otherwise. A *type substitution* σ is a finite mapping from type variables to closed types of the respective syntactic class. When applied to an open type, it replaces the type variables α from its domain with the respective $\sigma(\alpha)$.

Theorem (Principal Types). If an instruction sequence instr^* is [valid](#) with some closed [instruction type](#) instrtype (i.e., $C \vdash \text{instr}^* : \text{instrtype}$), then it is also valid with a possibly open instruction type instrtype_{\min} (i.e., $C \vdash \text{instr}^* : \text{instrtype}_{\min}$), such that for *every* closed type $\text{instrtype}'$ with which instr^* is valid (i.e., for all $C \vdash \text{instr}^* : \text{instrtype}'$), there exists a substitution σ , such that $\sigma(\text{instrtype}_{\min})$ is a subtype of $\text{instrtype}'$ (i.e., $C \vdash \sigma(\text{instrtype}_{\min}) \leq \text{instrtype}'$). Furthermore, instrtype_{\min} is unique up to the choice of type variables.

Theorem (Closed Principal Forward Types). If closed input type $[t_1^*]$ is given and the instruction sequence instr^* is [valid](#) with [instruction type](#) $[t_1^*] \rightarrow_{x^*} [t_2^*]$ (i.e., $C \vdash \text{instr}^* : [t_1^*] \rightarrow_{x^*} [t_2^*]$), then it is also valid with instruction type $[t_1^*] \rightarrow_{x^*} [\alpha_{\text{val\textit{type}}^*} t^*]$ (i.e., $C \vdash \text{instr}^* : [t_1^*] \rightarrow_{x^*} [\alpha_{\text{val\textit{type}}^*} t^*]$), where all t^* are closed, such that for *every* closed result type $[t_2'^*]$ with which instr^* is valid (i.e., for all $C \vdash \text{instr}^* : [t_1^*] \rightarrow_{x^*} [t_2'^*]$), there exists a substitution σ , such that $[t_2'^*] = [\sigma(\alpha_{\text{val\textit{type}}^*} t^*)]$.

7.5.2 Type Lattice

The [Principal Types](#) property depends on the existence of a *greatest lower bound* for any pair of types.

Theorem (Greatest Lower Bounds for Value Types). For any two value types t_1 and t_2 that are [valid](#) (i.e., $C \vdash t_1 \text{ ok}$ and $C \vdash t_2 \text{ ok}$), there exists a valid value type t that is a subtype of both t_1 and t_2 (i.e., $C \vdash t \text{ ok}$ and $C \vdash t \leq t_1$ and $C \vdash t \leq t_2$), such that *every* valid value type t' that also is a subtype of both t_1 and t_2 (i.e., for all $C \vdash t' \text{ ok}$ and $C \vdash t' \leq t_1$ and $C \vdash t' \leq t_2$), is a subtype of t (i.e., $C \vdash t' \leq t$).

Note: The greatest lower bound of two types may be [bot](#).

Theorem (Conditional Least Upper Bounds for Value Types). Any two value types t_1 and t_2 that are [valid](#) (i.e., $C \vdash t_1 \text{ ok}$ and $C \vdash t_2 \text{ ok}$) either have no common supertype, or there exists a valid value type t that is a supertype of both t_1 and t_2 (i.e., $C \vdash t \text{ ok}$ and $C \vdash t_1 \leq t$ and $C \vdash t_2 \leq t$), such that *every* valid value type t' that also is a supertype of both t_1 and t_2 (i.e., for all $C \vdash t' \text{ ok}$ and $C \vdash t_1 \leq t'$ and $C \vdash t_2 \leq t'$), is a supertype of t (i.e., $C \vdash t \leq t'$).

Note: If a top type was added to the type system, a least upper bound would exist for any two types.

Corollary (Type Lattice). Assuming the addition of a provisional top type, [value types](#) form a lattice with respect to their [subtype](#) relation.

Finally, value types can be partitioned into multiple disjoint hierarchies that are not related by subtyping, except through [bot](#).

Theorem (Disjoint Subtype Hierarchies). The greatest lower bound of two [value types](#) is [bot](#) or [ref bot](#) if and only if they do not have a least upper bound.

In other words, types that do not have common supertypes, do not have common subtypes either (other than [bot](#) or [ref bot](#)), and vice versa.

Note: Types from disjoint hierarchies can safely be represented in mutually incompatible ways in an implementation, because their values can never flow to the same place.

7.5.3 Compositionality

Valid instruction sequences can be freely *composed*, as long as their types match up.

Theorem (Composition). If two instruction sequences $instr_1^*$ and $instr_2^*$ are valid with types $[t_1^*] \rightarrow_{x_1^*} [t^*]$ and $[t^*] \rightarrow_{x_2^*} [t_2^*]$, respectively (i.e., $C \vdash instr_1^* : [t_1^*] \rightarrow_{x_1^*} [t^*]$ and $C \vdash instr_2^* : [t^*] \rightarrow_{x_2^*} [t_2^*]$), then the concatenated instruction sequence $(instr_1^* instr_2^*)$ is valid with type $[t_1^*] \rightarrow_{x_1^* x_2^*} [t_2^*]$ (i.e., $C \vdash instr_1^* instr_2^* : [t_1^*] \rightarrow_{x_1^* x_2^*} [t_2^*]$).

Note: More generally, instead of a shared type $[t^*]$, it suffices if the output type of $instr_1^*$ is a *subtype* of the input type of $instr_2^*$, since the subtype can always be weakened to its supertype by subsumption.

Inversely, valid instruction sequences can also freely be *decomposed*, that is, splitting them anywhere produces two instruction sequences that are both *valid*.

Theorem (Decomposition). If an instruction sequence $instr^*$ that is valid with type $[t_1^*] \rightarrow_{x^*} [t_2^*]$ (i.e., $C \vdash instr^* : [t_1^*] \rightarrow_{x^*} [t_2^*]$) is split into two instruction sequences $instr_1^*$ and $instr_2^*$ at any point (i.e., $instr^* = instr_1^* instr_2^*$), then these are separately valid with some types $[t_1^*] \rightarrow_{x_1^*} [t^*]$ and $[t^*] \rightarrow_{x_2^*} [t_2^*]$, respectively (i.e., $C \vdash instr_1^* : [t_1^*] \rightarrow_{x_1^*} [t^*]$ and $C \vdash instr_2^* : [t^*] \rightarrow_{x_2^*} [t_2^*]$), where $x^* = x_1^* x_2^*$.

Note: This property holds because validation is required even for unreachable code. Without that, $instr_2^*$ might not be valid in isolation.

7.6 Validation Algorithm

The specification of WebAssembly *validation* is purely *declarative*. It describes the constraints that must be met by a *module* or *instruction* sequence to be valid.

This section sketches the skeleton of a sound and complete *algorithm* for effectively validating code, i.e., sequences of *instructions*. (Other aspects of validation are straightforward to implement.)

In fact, the algorithm is expressed over the flat sequence of opcodes as occurring in the *binary format*, and performs only a single pass over it. Consequently, it can be integrated directly into a decoder.

The algorithm is expressed in typed pseudo code whose semantics is intended to be self-explanatory.

7.6.1 Data Structures

Types

Value types are representable as sets of enumerations:

```
type num_type = I32 | I64 | F32 | F64
type vec_type = V128
type heap_type =
  Any | Eq | I31 | Struct | Array | None |
  Func | Nofunc | Exn | Noexn | Extern | Noextern | Bot |
  Def(def : def_type)
type ref_type = Ref(heap : heap_type, null : bool)
type val_type = num_type | vec_type | ref_type | Bot
```

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```

func is_num(t : val_type) : bool =
  return t = I32 || t = I64 || t = F32 || t = F64 || t = Bot

func is_vec(t : val_type) : bool =
  return t = V128 || t = Bot

func is_ref(t : val_type) : bool =
  return not (is_num t || is_vec t) || t = Bot

```

Similarly, defined types `def_type` can be represented:

```

type packed_type = I8 | I16
type field_type = Field(val : val_type | packed_type, mut : bool)

type struct_type = Struct(fields : list(field_type))
type array_type = Array(fields : field_type)
type func_type = Func(params : list(val_type), results : list(val_type))
type comp_type = struct_type | array_type | func_type

type sub_type = Sub(super : list(def_type), body : comp_type, final : bool)
type rec_type = Rec(types : list(sub_type))

type def_type = Def(rec : rec_type, proj : int32)

func unpack_field(t : field_type) : val_type =
  if (it = I8 || t = I16) return I32
  return t

func expand_def(t : def_type) : comp_type =
  return t.rec.types[t.proj].body

```

These representations assume that all types have been *closed* by *substituting* all *type indices* (in *concrete heap types* and in *sub types*) with their respective *defined types*. This includes *recursive* references to enclosing *defined types*, such that type representations form graphs and may be *cyclic* for *recursive types*.

We assume that all types have been *canonicalized*, such that equality on two type representations holds if and only if their *closures* are syntactically equivalent, making it a constant-time check.

Note: For the purpose of type canonicalization, recursive references from a *heap type* to an enclosing *recursive type* (i.e., forward edges in the graph that form a cycle) need to be distinguished from references to previously defined types. However, this distinction does not otherwise affect validation, so is ignored here. In the graph representation, all recursive types are effectively infinitely *unrolled*.

We further assume that *validation* and *subtyping* checks are defined on value types, as well as a few auxiliary functions on composite types:

```

func validate_val_type(t : val_type)
func validate_ref_type(t : ref_type)

func matches_val(t1 : val_type, t2 : val_type) : bool
func matches_ref(t1 : val_type, t2 : val_type) : bool

func is_func(t : comp_type) : bool
func is_struct(t : comp_type) : bool
func is_array(t : comp_type) : bool

```

Finally, the following function computes the least precise supertype of a given [heap type](#) (its corresponding top type):

```
func top_heap_type(t : heap_type) : heap_type =
  switch (t)
  case (Any | Eq | I31 | Struct | Array | None)
    return Any
  case (Func | Nofunc)
    return Func
  case (Extern | Noextern)
    return Extern
  case (Def(dt))
    switch (dt.rec.types[dt.proj].body)
    case (Struct(_) | Array(_))
      return Any
    case (Func(_))
      return Func
  case (Bot)
    raise CannotOccurInSource
```

Context

Validation requires a [context](#) for checking uses of [indices](#). For the purpose of presenting the algorithm, it is maintained in a set of global variables:

```
var return_type : list(val_type)
var types : array(def_type)
var locals : array(val_type)
var locals_init : array(bool)
var globals : array(global_type)
var funcs : array(func_type)
var tables : array(table_type)
var mems : array(mem_type)
```

This assumes suitable representations for the various [types](#) besides [val_type](#), which are omitted here.

For locals, there is an additional array recording the initialization status of each local.

Stacks

The algorithm uses three separate stacks: the *value stack*, the *control stack*, and the *initialization stack*. The value stack tracks the [types](#) of operand values on the [stack](#). The control stack tracks surrounding [structured control instructions](#) and their associated [blocks](#). The initialization stack records all [locals](#) that have been initialized since the beginning of the function.

```
type val_stack = stack(val_type)
type init_stack = stack(u32)

type ctrl_stack = stack(ctrl_frame)
type ctrl_frame = {
  opcode : opcode
  start_types : list(val_type)
  end_types : list(val_type)
  val_height : nat
  init_height : nat
  unreachable : bool
}
```


For each entered block, the control stack records a *control frame* with the originating opcode, the types on the top of the operand stack at the start and end of the block (used to check its result as well as branches), the height of the operand stack at the start of the block (used to check that operands do not underflow the current block), the height of the initialization stack at the start of the block (used to reset initialization status at the end of the block), and a flag recording whether the remainder of the block is unreachable (used to handle [stack-polymorphic](#) typing after branches).

For the purpose of presenting the algorithm, these stacks are simply maintained as global variables:

```
var vals : val_stack
var inits : init_stack
var ctrls : ctrl_stack
```

However, these variables are not manipulated directly by the main checking function, but through a set of auxiliary functions:

```
func push_val(type : val_type) =
  vals.push(type)

func pop_val() : val_type =
  if (vals.size() = ctrls[0].height && ctrls[0].unreachable) return Bot
  error_if(vals.size() = ctrls[0].height)
  return vals.pop()

func pop_val(expect : val_type) : val_type =
  let actual = pop_val()
  error_if(not matches_val(actual, expect))
  return actual

func pop_num() : num_type | Bot =
  let actual = pop_val()
  error_if(not is_num(actual))
  return actual

func pop_ref() : ref_type =
  let actual = pop_val()
  error_if(not is_ref(actual))
  if (actual = Bot) return Ref(Bot, false)
  return actual

func push_vals(types : list(val_type)) = foreach (t in types) push_val(t)
func pop_vals(types : list(val_type)) : list(val_type) =
  var popped := []
  foreach (t in reverse(types)) popped.prepend(pop_val(t))
  return popped
```

Pushing an operand value simply pushes the respective type to the value stack.

Popping an operand value checks that the value stack does not underflow the current block and then removes one type. But first, a special case is handled where the block contains no known values, but has been marked as unreachable. That can occur after an unconditional branch, when the stack is typed [polymorphically](#). In that case, the Bot type is returned, because that is a *principal* choice trivially satisfying all use constraints.

A second function for popping an operand value takes an expected type, which the actual operand type is checked against. The types may differ by subtyping, including the case where the actual type is Bot, and thereby matches unconditionally. The function returns the actual type popped from the stack.

Finally, there are accumulative functions for pushing or popping multiple operand types.

Note: The notation `stack[i]` is meant to index the stack from the top, so that, e.g., `ctrls[0]` accesses the

element pushed last.

The initialization stack and the initialization status of locals is manipulated through the following functions:

```
func get_local(idx : u32) =
  error_if(not locals_init[idx])

func set_local(idx : u32) =
  if (not locals_init[idx])
    inits.push(idx)
    locals_init[idx] := true

func reset_locals(height : nat) =
  while (inits.size() > height)
    locals_init[inits.pop()] := false
```

Getting a local verifies that it is known to be initialized. When a local is set that was not set already, then its initialization status is updated and the change is recorded in the initialization stack. Thus, the initialization status of all locals can be reset to a previous state by denoting a specific height in the initialization stack.

The size of the initialization stack is bounded by the number of (non-defaultable) locals in a function, so can be preallocated by an algorithm.

The control stack is likewise manipulated through auxiliary functions:

```
func push_ctrl(opcode : opcode, in : list(val_type), out : list(val_type)) =
  let frame = ctrl_frame(opcode, in, out, vals.size(), inits.size(), false)
  ctrls.push(frame)
  push_vals(in)

func pop_ctrl() : ctrl_frame =
  error_if(ctrls.is_empty())
  let frame = ctrls[0]
  pop_vals(frame.end_types)
  error_if(vals.size() != frame.val_height)
  reset_locals(frame.init_height)
  ctrls.pop()
  return frame

func label_types(frame : ctrl_frame) : list(val_types) =
  return (if (frame.opcode = loop) frame.start_types else frame.end_types)

func unreachable() =
  vals.resize(ctrls[0].height)
  ctrls[0].unreachable := true
```

Pushing a control frame takes the types of the label and result values. It allocates a new frame record recording them along with the current height of the operand stack and marks the block as reachable.

Popping a frame first checks that the control stack is not empty. It then verifies that the operand stack contains the right types of values expected at the end of the exited block and pops them off the operand stack. Afterwards, it checks that the stack has shrunk back to its initial height. Finally, it undoes all changes to the initialization status of locals that happen inside the block.

The type of the [label](#) associated with a control frame is either that of the stack at the start or the end of the frame, determined by the opcode that it originates from.

Finally, the current frame can be marked as unreachable. In that case, all existing operand types are purged from the value stack, in order to allow for the [stack-polymorphism](#) logic in `pop_val` to take effect. Because every function has an implicit outermost label that corresponds to an implicit block frame, it is an invariant of the validation

algorithm that there always is at least one frame on the control stack when validating an instruction, and hence, *ctrls[0]* is always defined.

Note: Even with the unreachable flag set, consecutive operands are still pushed to and popped from the operand stack. That is necessary to detect invalid [examples](#) like ([unreachable](#) ([i32.const](#)) [i64.add](#)). However, a polymorphic stack cannot underflow, but instead generates Bot types as needed.

7.6.2 Validation of Opcode Sequences

The following function shows the validation of a number of representative instructions that manipulate the stack. Other instructions are checked in a similar manner.

```
func validate(opcode) =
  switch (opcode)
  case (i32.add)
    pop_val(I32)
    pop_val(I32)
    push_val(I32)

  case (drop)
    pop_val()

  case (select)
    pop_val(I32)
    let t1 = pop_val()
    let t2 = pop_val()
    error_if(not (is_num(t1) && is_num(t2) || is_vec(t1) && is_vec(t2)))
    error_if(t1 != t2 && t1 != Bot && t2 != Bot)
    push_val(if (t1 == Bot) t2 else t1)

  case (select t)
    pop_val(I32)
    pop_val(t)
    pop_val(t)
    push_val(t)

  case (ref.is_null)
    pop_ref()
    push_val(I32)

  case (ref.as_non_null)
    let rt = pop_ref()
    push_val(Ref(rt.heap, false))

  case (ref.test rt)
    validate_ref_type(rt)
    pop_val(Ref(top_heap_type(rt), true))
    push_val(I32)

  case (local.get x)
    get_local(x)
    push_val(locals[x])

  case (local.set x)
    pop_val(locals[x])
```

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```

    set_local(x)

    case (unreachable)
      unreachable()

    case (block t1*->t2*)
      pop_vals([t1*])
      push_ctrl(block, [t1*], [t2*])

    case (loop t1*->t2*)
      pop_vals([t1*])
      push_ctrl(loop, [t1*], [t2*])

    case (if t1*->t2*)
      pop_val(I32)
      pop_vals([t1*])
      push_ctrl(if, [t1*], [t2*])

    case (end)
      let frame = pop_ctrl()
      push_vals(frame.end_types)

    case (else)
      let frame = pop_ctrl()
      error_if(frame.opcode != if)
      push_ctrl(else, frame.start_types, frame.end_types)

    case (br n)
      error_if(ctrls.size() < n)
      pop_vals(label_types(ctrls[n]))
      unreachable()

    case (br_if n)
      error_if(ctrls.size() < n)
      pop_val(I32)
      pop_vals(label_types(ctrls[n]))
      push_vals(label_types(ctrls[n]))

    case (br_table n* m)
      pop_val(I32)
      error_if(ctrls.size() < m)
      let arity = label_types(ctrls[m]).size()
      foreach (n in n*)
        error_if(ctrls.size() < n)
        error_if(label_types(ctrls[n]).size() != arity)
        push_vals(pop_vals(label_types(ctrls[n])))
      pop_vals(label_types(ctrls[m]))
      unreachable()

    case (br_on_null n)
      error_if(ctrls.size() < n)
      let rt = pop_ref()
      pop_vals(label_types(ctrls[n]))
      push_vals(label_types(ctrls[n]))
      push_val(Ref(rt.heap, false))

```

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```

case (br_on_cast n rt1 rt2)
  validate_ref_type(rt1)
  validate_ref_type(rt2)
  pop_val(rt1)
  push_val(rt2)
  pop_vals(label_types(ctrls[n]))
  push_vals(label_types(ctrls[n]))
  pop_val(rt2)
  push_val(diff_ref_type(rt2, rt1))

case (return)
  pop_vals(return_types)
  unreachable()

case (call_ref x)
  let t = expand_def(types[x])
  error_if(not is_func(t))
  pop_vals(t.params)
  pop_val(Ref(Def(types[x])))
  push_vals(t.results)

case (return_call_ref x)
  let t = expand_def(types[x])
  error_if(not is_func(t))
  pop_vals(t.params)
  pop_val(Ref(Def(types[x])))
  error_if(t.results.len() != return_types.len())
  push_vals(t.results)
  pop_vals(return_types)
  unreachable()

case (struct.new x)
  let t = expand_def(types[x])
  error_if(not is_struct(t))
  for (ti in reverse(t.fields))
    pop_val(unpack_field(ti))
  push_val(Ref(Def(types[x])))

case (struct.set x n)
  let t = expand_def(types[x])
  error_if(not is_struct(t) || n >= t.fields.len())
  pop_val(Ref(Def(types[x])))
  pop_val(unpack_field(st.fields[n]))

case (throw x)
  pop_vals(tags[x].type.params)
  unreachable()

case (try_table t1*->t2* handler*)
  pop_vals([t1*])
  foreach (handler in handler*)
    error_if(ctrls.size() < handler.label)
    push_ctrl(catch, [], label_types(ctrls[handler.label]))
    switch (handler.clause)
      case (catch x)
        push_vals(tags[x].type.params)

```

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```

    case (catch_ref x)
      push_vals(tags[x].type.params)
      push_val(Exnref)
    case (catch_all)
      skip
    case (catch_all_ref)
      push_val(Exnref)
  pop_ctrl()
  push_ctrl(try_table, [t1*], [t2*])

```

Note: It is an invariant under the current WebAssembly instruction set that an operand of Bot type is never duplicated on the stack. This would change if the language were extended with stack instructions like `dup`. Under such an extension, the above algorithm would need to be refined by replacing the Bot type with proper *type variables* to ensure that all uses are consistent.

7.7 Custom Sections and Annotations

This appendix defines dedicated [custom sections](#) for WebAssembly’s [binary format](#) and [annotations](#) for the text format. Such sections or annotations do not contribute to, or otherwise affect, the WebAssembly semantics, and may be ignored by an implementation. However, they provide useful meta data that implementations can make use of to improve user experience or take compilation hints.

7.7.1 Name Section

The *name section* is a [custom section](#) whose name string is itself ‘name’. The name section should appear only once in a module, and only after the [data section](#).

The purpose of this section is to attach printable names to definitions in a module, which e.g. can be used by a debugger or when parts of the module are to be rendered in [text form](#).

Note: All [names](#) are represented in [Unicode](#)⁵⁷ encoded in UTF-8. Names need not be unique.

Subsections

The [data](#) of a name section consists of a sequence of *subsections*. Each subsection consists of a

- a one-byte subsection *id*,
- the *u32* size of the contents, in bytes,
- the actual *contents*, whose structure is dependent on the subsection id.

```

namesec          ::= section0(namedata)
namedata         ::= n:name (if n = ‘name’)
                  modulenamessubsec?
                  funcnamesubsec?
                  localnamesubsec?
                  typenamesubsec?
                  fieldnamesubsec?
                  tagnamesubsec?
namesubsectionN(B) ::= N:byte size:u32 B (if size = ||B||)

```

⁵⁷ <https://www.unicode.org/versions/latest/>

The following subsection ids are used:

Id	Subsection
0	module name
1	function names
2	local names
4	type names
10	field names
11	tag names

Each subsection may occur at most once, and in order of increasing id.

Name Maps

A *name map* assigns [names](#) to [indices](#) in a given [index space](#). It consists of a [vector](#) of index/name pairs in order of increasing index value. Each index must be unique, but the assigned names need not be.

```
namemap      ::= vec(nameassoc)
nameassoc    ::= idx name
```

An *indirect name map* assigns [names](#) to a two-dimensional [index space](#), where secondary indices are *grouped* by primary indices. It consists of a vector of primary index/name map pairs in order of increasing index value, where each name map in turn maps secondary indices to names. Each primary index must be unique, and likewise each secondary index per individual name map.

```
indirectnamemap ::= vec(indirectnameassoc)
indirectnameassoc ::= idx namemap
```

Module Names

The *module name subsection* has the id 0. It simply consists of a single [name](#) that is assigned to the module itself.

```
modulenamesubsec ::= namesubsection0(name)
```

Function Names

The *function name subsection* has the id 1. It consists of a [name map](#) assigning function names to [function indices](#).

```
funcnamesubsec ::= namesubsection1(namemap)
```

Local Names

The *local name subsection* has the id 2. It consists of an [indirect name map](#) assigning local names to [local indices](#) grouped by [function indices](#).

```
localnamesubsec ::= namesubsection2(indirectnamemap)
```

Type Names

The *type name subsection* has the id 4. It consists of a [name map](#) assigning type names to [type indices](#).

```
typenamesubsec ::= namesubsection4(namemap)
```

Field Names

The *field name subsection* has the id 10. It consists of an [indirect name map](#) assigning field names to [field indices](#) grouped by the [type indices](#) of their respective [structure types](#).

```
fieldnamesubsec ::= namesubsection10(indirectnamemap)
```

Tag Names

The *tag name subsection* has the id 11. It consists of a [name map](#) assigning tag names to [tag indices](#).

```
tagnamesubsec ::= namesubsection11(namemap)
```

7.7.2 Name Annotations

Name annotations are the textual analogue to the [name section](#) and provide a textual representation for it. Consequently, their id is @name.

Analogous to the name section, name annotations are allowed on [modules](#), [functions](#), and [locals](#) (including [parameters](#)). They can be placed where the text format allows binding occurrences of respective [identifiers](#). If both an identifier and a name annotation are given, the annotation is expected *after* the identifier. In that case, the annotation takes precedence over the identifier as a textual representation of the binding's name. At most one name annotation may be given per binding.

All name annotations have the following format:

```
nameannot ::= '(@name' string ')
```

Note: All name annotations can be arbitrary UTF-8 [strings](#). Names need not be unique.

Module Names

A *module name annotation* must be placed on a [module](#) definition, directly after the 'module' keyword, or if present, after the following module [identifier](#).

```
modulenameannot ::= nameannot
```


Function Names

A *function name annotation* must be placed on a [function](#) definition or function [import](#), directly after the ‘func’ keyword, or if present, after the following function [identifier](#) or.

```
funcnameannot ::= nameannot
```

Parameter Names

A *parameter name annotation* must be placed on a [parameter](#) declaration, directly after the ‘param’ keyword, or if present, after the following parameter [identifier](#). It may only be placed on a declaration that declares exactly one parameter.

```
paramnameannot ::= nameannot
```

Local Names

A *local name annotation* must be placed on a [local](#) declaration, directly after the ‘local’ keyword, or if present, after the following local [identifier](#). It may only be placed on a declaration that declares exactly one local.

```
localnameannot ::= nameannot
```

Type Names

A *type name annotation* must be placed on a [type](#) declaration, directly after the ‘type’ keyword, or if present, after the following type [identifier](#).

```
typenameannot ::= nameannot
```

Field Names

A *field name annotation* must be placed on the field of a [structure type](#), directly after the ‘field’ keyword, or if present, after the following field [identifier](#). It may only be placed on a declaration that declares exactly one field.

```
fieldnameannot ::= nameannot
```

Tag Names

A *tag name annotation* must be placed on a [tag declaration](#) or tag [import](#), directly after the ‘tag’ keyword, or if present, after the following tag [identifier](#).

```
tagnameannot ::= nameannot
```

7.7.3 Custom Annotations

Custom annotations are a generic textual representation for any [custom section](#). Their id is `@custom`. By generating custom annotations, tools converting between [binary format](#) and [text format](#) can maintain and round-trip the content of custom sections even when they do not recognize them.

Custom annotations must be placed inside a [module](#) definition. They must occur anywhere after the `'module'` keyword, or if present, after the following module [identifier](#). They must not be nested into other constructs.

```

customannot ::= '(@custom' string customplace? datastring ')'
customplace ::= '(' 'before' 'first' ')'
              | '(' 'before' sec ')'
              | '(' 'after' sec ')'
              | '(' 'after' 'last' ')'
sec          ::= 'type'
              | 'import'
              | 'func'
              | 'table'
              | 'memory'
              | 'global'
              | 'export'
              | 'start'
              | 'elem'
              | 'code'
              | 'data'
              | 'datacount'

```

The first [string](#) in a custom annotation denotes the name of the custom section it represents. The remaining strings collectively represent the section's payload data, written as a [data string](#), which can be split up into a possibly empty sequence of individual string literals (similar to [data segments](#)).

An arbitrary number of custom annotations (even of the same name) may occur in a module, each defining a separate custom section when converting to [binary format](#). Placement of the sections in the binary can be customized via explicit *placement* directives, that position them either directly before or directly after a known section. That section must exist and be non-empty in the binary encoding of the annotated module. The placements (*before first*) and (*after last*) denote virtual sections before the first and after the last known section, respectively. When the placement directive is omitted, it defaults to (*after last*).

If multiple placement directives appear for the same position, then the sections are all placed there, in order of their appearance in the text. For this purpose, the position after a section is considered different from the position before the consecutive section, and the former occurs before the latter.

Note: Future versions of WebAssembly may introduce additional sections between others or at the beginning or end of a module. Using *first* and *last* guarantees that placement will still go before or after any future section, respectively.

If a custom section with a specific section id is given as well as annotations representing the same custom section (e.g., `@name` [annotations](#) as well as a `@custom` annotation for a [name section](#)), then two sections are assumed to be created. Their relative placement will depend on the placement directive given for the `@custom` annotation as well as the implicit placement requirements of the custom section, which are applied to the other annotation.

Note: For example, the following module,

```

(module
  (@custom "A" "aaa")
  (type $t (func))
  (@custom "B" (after func) "bbb")
  (@custom "C" (before func) "ccc"))

```

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```
(@custom "D" (after last) "ddd")
(table 10 funcref)
(func (type $t))
(@custom "E" (after import) "eee")
(@custom "F" (before type) "fff")
(@custom "G" (after data) "ggg")
(@custom "H" (after code) "hhh")
(@custom "I" (after func) "iii")
(@custom "J" (before func) "jjj")
(@custom "K" (before first) "kkk")
)
```

will result in the following section ordering:

```
custom section "K"
custom section "F"
type section
custom section "E"
custom section "C"
custom section "J"
function section
custom section "B"
custom section "I"
table section
code section
custom section "H"
custom section "G"
custom section "A"
custom section "D"
```

7.8 Change History

Since the original release 1.0 of the WebAssembly specification, a number of proposals for extensions have been integrated. The following sections provide an overview of what has changed.

7.8.1 Release 2.0

Sign Extension Instructions

Added new numeric instructions for performing sign extension within integer representations.⁵⁸

- New [numeric instructions](#):
 - `inn.extendN_s`

⁵⁸ <https://github.com/WebAssembly/spec/tree/main/proposals/sign-extension-ops/>

Non-trapping Float-to-Int Conversions

Added new conversion instructions that avoid trapping when converting a floating-point number to an integer.⁵⁹

- New **numeric instructions**:
 - `inn.trunc_sat_fmm_sx`

Multiple Values

Generalized the result type of blocks and functions to allow for multiple values; in addition, introduced the ability to have block parameters.⁶⁰

- **Function types** allow more than one result
- **Block types** can be arbitrary function types

Reference Types

Added **funcref** and **externref** as new value types and respective instructions.⁶¹

- New **reference value types**:
 - `funcref`
 - `externref`
- New **reference instructions**:
 - `ref.null`
 - `ref.func`
 - `ref.is_null`
- Extended **parametric instruction**:
 - `select` with optional type immediate
- New **declarative form of element segment**

Table Instructions

Added instructions to directly access and modify tables.^{Page 304, 61}

- **Table types** allow any **reference type** as element type
- New **table instructions**:
 - `table.get`
 - `table.set`
 - `table.size`
 - `table.grow`

⁵⁹ <https://github.com/WebAssembly/spec/tree/main/proposals/nontrapping-float-to-int-conversion/>

⁶⁰ <https://github.com/WebAssembly/spec/tree/main/proposals/multi-value/>

⁶¹ <https://github.com/WebAssembly/spec/tree/main/proposals/reference-types/>

Multiple Tables

Added the ability to use multiple tables per module.⁶¹

- Modules may
 - `define` multiple tables
 - `import` multiple tables
 - `export` multiple tables
- Table instructions take a table index immediate:
 - `table.get`
 - `table.set`
 - `table.size`
 - `table.grow`
 - `call_indirect`
- Element segments take a table index

Bulk Memory and Table Instructions

Added instructions that modify ranges of memory or table entries.^{Page 304, 6162}

- New memory instructions:
 - `memory.fill`
 - `memory.init`
 - `memory.copy`
 - `data.drop`
- New table instructions:
 - `table.fill`
 - `table.init`
 - `table.copy`
 - `elem.drop`
- New `passive` form of data segment
- New `passive` form of element segment
- New `data count` section in binary format
- Active data and element segments boundaries are no longer checked at compile time but may trap instead

⁶² <https://github.com/WebAssembly/spec/tree/main/proposals/bulk-memory-operations/>

Vector Instructions

Added vector type and instructions that manipulate multiple numeric values in parallel (also known as *SIMD*, single instruction multiple data)⁶³

- New **value type**:
 - `v128`
- New **memory instructions**:
 - `v128.load`
 - `v128.loadNxM_sx`
 - `v128.loadN_zero`
 - `v128.loadN_splat`
 - `v128.loadN_lane`
 - `v128.store`
 - `v128.storeN_lane`
- New constant **vector instruction**:
 - `v128.const`
- New unary **vector instructions**:
 - `v128.not`
 - `iNxM.abs`
 - `iNxM.neg`
 - `i8x16.popcnt`
 - `fNxM.abs`
 - `fNxM.neg`
 - `fNxM.sqrt`
 - `fNxM.ceil`
 - `fNxM.floor`
 - `fNxM.trunc`
 - `fNxM.nearest`
- New binary **vector instructions**:
 - `v128.and`
 - `v128.andnot`
 - `v128.or`
 - `v128.xor`
 - `iNxM.add`
 - `iNxM.sub`
 - `iNxM.mul`
 - `iNxM.add_sat_sx`
 - `iNxM.sub_sat_sx`
 - `iNxM.min_sx`

⁶³ <https://github.com/WebAssembly/spec/tree/main/proposals/simd/>

- *iNxM.max_sx*
- *iNxM.shl*
- *iNxM.shr_sx*
- *fNxM.add*
- *fNxM.sub*
- *fNxM.mul*
- *fNxM.div*
- *i16x8.extadd_pairwise_i8x16_sx*
- *i32x4.extadd_pairwise_i16x8_sx*
- *iNxM.extmul_half_iN'xM'_sx*
- *i16x8.q15mulr_sat_s*
- *i32x4.dot_i16x8_s*
- *i8x16.avgr_u*
- *i16x8.avgr_u*
- *fNxM.min*
- *fNxM.max*
- *fNxM.pmin*
- *fNxM.pmax*
- New ternary [vector instruction](#):
 - *v128.bitselect*
- New test [vector instructions](#):
 - *v128.any_true*
 - *iNxM.all_true*
- New relational [vector instructions](#):
 - *iNxM.eq*
 - *iNxM.ne*
 - *iNxM.lt_sx*
 - *iNxM.gt_sx*
 - *iNxM.le_sx*
 - *iNxM.ge_sx*
 - *fNxM.eq*
 - *fNxM.ne*
 - *fNxM.lt*
 - *fNxM.gt*
 - *fNxM.le*
 - *fNxM.ge*
- New conversion [vector instructions](#):
 - *i32x4.trunc_sat_f32x4_sx*
 - *i32x4.trunc_sat_f64x2_sx_zero*

- `f32x4.convert_i32x4_sx`
- `f32x4.demote_f64x2_zero`
- `f64x2.convert_low_i32x4_sx`
- `f64x2.promote_low_f32x4`
- New lane access [vector instructions](#):
 - `iNxM.extract_lane_sx`[?]
 - `iNxM.replace_lane`
 - `fNxM.extract_lane`
 - `fNxM.replace_lane`
- New lane splitting/combining [vector instructions](#):
 - `iNxM.extend_half_iN'M'_sx`
 - `i8x16.narrow_i16x8_sx`
 - `i16x8.narrow_i32x4_sx`
- New byte reordering [vector instructions](#):
 - `i8x16.shuffle`
 - `i8x16.swizzle`
- New injection/projection [vector instructions](#):
 - `iNxM.splat`
 - `fNxM.splat`
 - `iNxM.bitmask`

7.8.2 Release 3.0

Extended Constant Expressions

Allowed basic numeric computations in constant expressions.⁶⁴

- Extended set of [constant instructions](#) with:
 - `inn.add`
 - `inn.sub`
 - `inn.mul`
 - `global.get` for any previously declared immutable [global](#)

Note: The [garbage collection](#) added further constant instructions.

⁶⁴ <https://github.com/WebAssembly/spec/blob/main/proposals/extended-const/>

Tail Calls

Added instructions to perform tail calls.⁶⁵

- New control instructions:
 - `return_call`
 - `return_call_indirect`

Exception Handling

Added tag definitions, imports, and exports, and instructions to throw and catch exceptions⁶⁶

- Modules may
 - `define tags`
 - `import tags`
 - `export tags`
- New heap types:
 - `exn`
 - `noexn`
- New reference type short-hands:
 - `exnref`
 - `nullexnref`
- New control instructions:
 - `throw`
 - `throw_ref`
 - `try_table`
- New tag section in binary format.

Multiple Memories

Added the ability to use multiple memories per module.⁶⁷

- Modules may
 - `define multiple memories`
 - `import multiple memories`
 - `export multiple memories`
- Memory instructions take a memory index immediate:
 - `memory.size`
 - `memory.grow`
 - `memory.fill`
 - `memory.copy`
 - `memory.init`

⁶⁵ <https://github.com/WebAssembly/spec/tree/main/proposals/tail-call/>

⁶⁶ <https://github.com/WebAssembly/spec/tree/main/proposals/exception-handling/>

⁶⁷ <https://github.com/WebAssembly/spec/blob/main/proposals/multi-memory/>

- `t.load`
 - `t.store`
 - `t.loadNsx`
 - `t.storeN`
 - `v128.loadNxM_sx`
 - `v128.loadN_zero`
 - `v128.loadN_splat`
 - `v128.loadN_lane`
 - `v128.storeN_lane`
- `Data segments` take a `memory index`

64-bit Address Space

Added the ability to declare an `i64` address type for tables and memories.⁶⁸

- `Address types` denote a subset of the integral number types
- `Table types` include an `address type`
- `Memory types` include an `address type`
- Operand types of `table` and `memory` instructions now depend on the subject's declared address type:
 - `table.get`
 - `table.set`
 - `table.size`
 - `table.grow`
 - `table.fill`
 - `table.copy`
 - `table.init`
 - `memory.size`
 - `memory.grow`
 - `memory.fill`
 - `memory.copy`
 - `memory.init`
 - `t.load`
 - `t.store`
 - `t.loadNsx`
 - `t.storeN`
 - `v128.loadNxM_sx`
 - `v128.loadN_zero`
 - `v128.loadN_splat`
 - `v128.loadN_lane`
 - `v128.storeN_lane`

⁶⁸ <https://github.com/WebAssembly/spec/blob/main/proposals/memory64/>

Typeful References

Added more precise types for references.⁶⁹

- New generalised form of [reference types](#):
 - `(ref null? heaptype)`
- New class of [heap types](#):
 - `func`
 - `extern`
 - `typeidx`
- Basic [subtyping](#) on [reference](#) and [value types](#)
- New [reference instructions](#):
 - `ref.as_non_null`
 - `br_on_null`
 - `br_on_non_null`
- New [control instruction](#):
 - `call_ref`
- Refined typing of [reference instruction](#):
 - `ref.func` with more precise result type
- Refined typing of [local instructions](#) and [instruction sequences](#) to track the [initialization status](#) of [locals](#) with [non-defaultable type](#)
- Extended [table definitions](#) with optional initializer expression

Garbage Collection

Added managed reference types.⁷⁰

- New forms of [heap types](#):
 - `any`
 - `eq`
 - `i31`
 - `struct`
 - `array`
 - `none`
 - `nofunc`
 - `noextern`
- New [reference type](#) short-hands:
 - `anyref`
 - `eqref`
 - `i31ref`
 - `structref`

⁶⁹ <https://github.com/WebAssembly/spec/tree/main/proposals/function-references/>

⁷⁰ <https://github.com/WebAssembly/spec/tree/main/proposals/gc/>

- arrayref
 - nullref
 - nullfuncref
 - nullexternref
- New forms of type definitions:
 - structure
 - array types
 - sub types
 - recursive types
- Enriched subtyping based on explicitly declared sub types and the new heap types
- New generic reference instructions:
 - ref.eq
 - ref.test
 - ref.cast
 - br_on_cast
 - br_on_cast_fail
- New reference instructions for unboxed scalars:
 - ref.i31
 - i31.get_sx
- New reference instructions for structure types:
 - struct.new
 - struct.new_default
 - struct.get_sx?
 - struct.set
- New reference instructions for array types:
 - array.new
 - array.new_default
 - array.new_fixed
 - array.new_data
 - array.new_elem
 - array.get_sx?
 - array.set
 - array.len
 - array.fill
 - array.copy
 - array.init_data
 - array.init_elem
- New reference instructions for converting external types:
 - any.convert_extern

- `extern.convert_any`
- Extended set of `constant` instructions with:
 - `ref.i31`
 - `struct.new`
 - `struct.new_default`
 - `array.new`
 - `array.new_default`
 - `array.new_fixed`
 - `any.convert_extern`
 - `extern.convert_any`

Relaxed Vector Instructions

Added new *relaxed* vector instructions, whose behaviour is non-deterministic and implementation-dependent.⁷¹

- New binary `vector` instruction:
 - `fNxM.relaxed_min`
 - `fNxM.relaxed_max`
 - `i16x8.relaxed_q15mulr_s`
 - `i16x8.relaxed_dot_i8x16_i7x16_s`
- New ternary `vector` instruction:
 - `fNxM.relaxed_madd`
 - `fNxM.relaxed_nmadd`
 - `iNxM.relaxed_laneselect`
 - `i32x4.relaxed_dot_i8x16_i7x16_add_s`
- New conversion `vector` instructions:
 - `i32x4.relaxed_trunc_f32x4_sx`
 - `i32x4.relaxed_trunc_f64x2_sx_zero`
- New byte reordering `vector` instruction:
 - `i8x16.relaxed_swizzle`

Profiles

Introduced the concept of `profile` for specifying language subsets.

- A new profile defining a `deterministic` mode of execution.

⁷¹ <https://github.com/WebAssembly/spec/tree/main/proposals/relaxed-simd/>

Custom Annotations

Added generic syntax for custom annotations in the text format, mirroring the role of custom sections in the binary format.⁷²

- Annotations of the form ‘(@id ...)’ are allowed anywhere in the [text format](#)
- Identifiers can be escaped as ‘@” ... ”’ with arbitrary [names](#)
- Defined [name annotations](#) ‘(@name ” ... ”)’ for:
 - module names
 - type names
 - function names
 - local names
 - field names
- Defined [custom annotation](#) ‘(@custom ” ... ”)’ to represent arbitrary [custom sections](#) in the text format

7.9 Index of Types

Category	Constructor	Binary Opcode
Type index	<i>x</i>	(positive number as s32 or u32)
Number type	i32	0x7F (-1 as s7)
Number type	i64	0x7E (-2 as s7)
Number type	f32	0x7D (-3 as s7)
Number type	f64	0x7C (-4 as s7)
Vector type	v128	0x7B (-5 as s7)
(reserved)		0x7A .. 0x79
Packed type	i8	0x78 (-8 as s7)
Packed type	i16	0x77 (-9 as s7)
(reserved)		0x78 .. 0x75
Heap type	noexn	0x74 (-14 as s7)
Heap type	nofunc	0x73 (-13 as s7)
Heap type	noextern	0x72 (-14 as s7)
Heap type	none	0x71 (-15 as s7)
Heap type	func	0x70 (-16 as s7)
Heap type	extern	0x6F (-17 as s7)
Heap type	any	0x6E (-18 as s7)
Heap type	eq	0x6D (-19 as s7)
Heap type	i31	0x6C (-20 as s7)
Heap type	struct	0x6B (-21 as s7)
Heap type	array	0x6A (-22 as s7)
Heap type	exn	0x69 (-23 as s7)
(reserved)		0x68 .. 0x65
Reference type	ref	0x64 (-28 as s7)
Reference type	ref null	0x63 (-29 as s7)
(reserved)		0x62 .. 0x61
Composite type	func [<i>valtype</i> *] → [<i>valtype</i> *]	0x60 (-32 as s7)
Composite type	struct <i>fieldtype</i> *	0x5F (-33 as s7)
Composite type	array <i>fieldtype</i>	0x5E (-34 as s7)
(reserved)		0x5D .. 0x51
Sub type	sub <i>typeid</i> x* <i>comptype</i>	0x50 (-48 as s7)

continues on next page

⁷² <https://github.com/WebAssembly/annotations/tree/main/proposals/annotations/>

Table 1 – continued from previous page

Category	Constructor	Binary Opcode
Sub type	<i>sub final typeid_x* comptype</i>	0x4F (-49 as <i>s7</i>)
Recursive type	<i>rec subtype*</i>	0x4E (-50 as <i>s7</i>)
(reserved)		0x4D .. 0x41
Result type	[<i>ε</i>]	0x40 (-64 as <i>s7</i>)
Table type	<i>limits reftype</i>	(none)
Memory type	<i>limits</i>	(none)
Global type	<i>mut valtype</i>	(none)
Tag type	<i>functype</i>	(none)

7.10 Index of Instructions

Instruction	Binary Opcode	Type	Validation	Execution
unreachable	0x00	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
nop	0x01	$[] \rightarrow []$	validation	execution
block <i>bt</i>	0x02	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
loop <i>bt</i>	0x03	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
if <i>bt</i>	0x04	$[t_1^* \text{ i32}] \rightarrow [t_2^*]$	validation	execution
else	0x05			
(reserved)	0x06			
(reserved)	0x07			
throw <i>x</i>	0x08	$[t_1^* t_x^*] \rightarrow [t_2^*]$	validation	execution
(reserved)	0x09			
throw_ref	0x0A	$[t_1^* \text{ exnref}] \rightarrow [t_2^*]$	validation	execution
end	0x0B			
br <i>l</i>	0x0C	$[t_1^* t^*] \rightarrow [t_2^*]$	validation	execution
br_if <i>l</i>	0x0D	$[t^* \text{ i32}] \rightarrow [t^*]$	validation	execution
br_table <i>l* l</i>	0x0E	$[t_1^* t^* \text{ i32}] \rightarrow [t_2^*]$	validation	execution
return	0x0F	$[t_1^* t^*] \rightarrow [t_2^*]$	validation	execution
call <i>x</i>	0x10	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
call_indirect <i>x y</i>	0x11	$[t_1^* \text{ i32}] \rightarrow [t_2^*]$	validation	execution
return_call <i>x</i>	0x12	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
return_call_indirect <i>x y</i>	0x13	$[t_1^* \text{ i32}] \rightarrow [t_2^*]$	validation	execution
call_ref <i>x</i>	0x14	$[t_1^* (\text{ref null } x)] \rightarrow [t_2^*]$	validation	execution
return_call_ref <i>x</i>	0x15	$[t_1^* (\text{ref null } x)] \rightarrow [t_2^*]$	validation	execution
(reserved)	0x16			
(reserved)	0x17			
(reserved)	0x18			
(reserved)	0x19			
drop	0x1A	$[t] \rightarrow []$	validation	execution
select	0x1B	$[t \text{ t i32}] \rightarrow [t]$	validation	execution
select <i>t</i>	0x1C	$[t \text{ t i32}] \rightarrow [t]$	validation	execution
(reserved)	0x1D			
(reserved)	0x1E			
try_table <i>bt</i>	0x1F	$[t_1^*] \rightarrow [t_2^*]$	validation	execution
local.get <i>x</i>	0x20	$[] \rightarrow [t]$	validation	execution
local.set <i>x</i>	0x21	$[t] \rightarrow []$	validation	execution
local.tee <i>x</i>	0x22	$[t] \rightarrow [t]$	validation	execution
global.get <i>x</i>	0x23	$[] \rightarrow [t]$	validation	execution
global.set <i>x</i>	0x24	$[t] \rightarrow []$	validation	execution
table.get <i>x</i>	0x25	$[\text{i32}] \rightarrow [t]$	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
<code>table.set <i>x</i></code>	0x26	$[i32\ t] \rightarrow []$	validation	execution
(reserved)	0x27			
<code>i32.load <i>x memarg</i></code>	0x28	$[i32] \rightarrow [i32]$	validation	execution
<code>i64.load <i>x memarg</i></code>	0x29	$[i32] \rightarrow [i64]$	validation	execution
<code>f32.load <i>x memarg</i></code>	0x2A	$[i32] \rightarrow [f32]$	validation	execution
<code>f64.load <i>x memarg</i></code>	0x2B	$[i32] \rightarrow [f64]$	validation	execution
<code>i32.load8_s <i>x memarg</i></code>	0x2C	$[i32] \rightarrow [i32]$	validation	execution
<code>i32.load8_u <i>x memarg</i></code>	0x2D	$[i32] \rightarrow [i32]$	validation	execution
<code>i32.load16_s <i>x memarg</i></code>	0x2E	$[i32] \rightarrow [i32]$	validation	execution
<code>i32.load16_u <i>x memarg</i></code>	0x2F	$[i32] \rightarrow [i32]$	validation	execution
<code>i64.load8_s <i>x memarg</i></code>	0x30	$[i32] \rightarrow [i64]$	validation	execution
<code>i64.load8_u <i>x memarg</i></code>	0x31	$[i32] \rightarrow [i64]$	validation	execution
<code>i64.load16_s <i>x memarg</i></code>	0x32	$[i32] \rightarrow [i64]$	validation	execution
<code>i64.load16_u <i>x memarg</i></code>	0x33	$[i32] \rightarrow [i64]$	validation	execution
<code>i64.load32_s <i>x memarg</i></code>	0x34	$[i32] \rightarrow [i64]$	validation	execution
<code>i64.load32_u <i>x memarg</i></code>	0x35	$[i32] \rightarrow [i64]$	validation	execution
<code>i32.store <i>x memarg</i></code>	0x36	$[i32\ i32] \rightarrow []$	validation	execution
<code>i64.store <i>x memarg</i></code>	0x37	$[i32\ i64] \rightarrow []$	validation	execution
<code>f32.store <i>x memarg</i></code>	0x38	$[i32\ f32] \rightarrow []$	validation	execution
<code>f64.store <i>x memarg</i></code>	0x39	$[i32\ f64] \rightarrow []$	validation	execution
<code>i32.store8 <i>x memarg</i></code>	0x3A	$[i32\ i32] \rightarrow []$	validation	execution
<code>i32.store16 <i>x memarg</i></code>	0x3B	$[i32\ i32] \rightarrow []$	validation	execution
<code>i64.store8 <i>x memarg</i></code>	0x3C	$[i32\ i64] \rightarrow []$	validation	execution
<code>i64.store16 <i>x memarg</i></code>	0x3D	$[i32\ i64] \rightarrow []$	validation	execution
<code>i64.store32 <i>x memarg</i></code>	0x3E	$[i32\ i64] \rightarrow []$	validation	execution
<code>memory.size <i>x</i></code>	0x3F	$[] \rightarrow [i32]$	validation	execution
<code>memory.grow <i>x</i></code>	0x40	$[i32] \rightarrow [i32]$	validation	execution
<code>i32.const <i>i32</i></code>	0x41	$[] \rightarrow [i32]$	validation	execution
<code>i64.const <i>i64</i></code>	0x42	$[] \rightarrow [i64]$	validation	execution
<code>f32.const <i>f32</i></code>	0x43	$[] \rightarrow [f32]$	validation	execution
<code>f64.const <i>f64</i></code>	0x44	$[] \rightarrow [f64]$	validation	execution
<code>i32.eqz</code>	0x45	$[i32] \rightarrow [i32]$	validation	execution
<code>i32.eq</code>	0x46	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.ne</code>	0x47	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.lt_s</code>	0x48	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.lt_u</code>	0x49	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.gt_s</code>	0x4A	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.gt_u</code>	0x4B	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.le_s</code>	0x4C	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.le_u</code>	0x4D	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.ge_s</code>	0x4E	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i32.ge_u</code>	0x4F	$[i32\ i32] \rightarrow [i32]$	validation	execution
<code>i64.eqz</code>	0x50	$[i64] \rightarrow [i32]$	validation	execution
<code>i64.eq</code>	0x51	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.ne</code>	0x52	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.lt_s</code>	0x53	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.lt_u</code>	0x54	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.gt_s</code>	0x55	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.gt_u</code>	0x56	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.le_s</code>	0x57	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.le_u</code>	0x58	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.ge_s</code>	0x59	$[i64\ i64] \rightarrow [i32]$	validation	execution
<code>i64.ge_u</code>	0x5A	$[i64\ i64] \rightarrow [i32]$	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
f32.eq	0x5B	[f32 f32] → [i32]	validation	execution
f32.ne	0x5C	[f32 f32] → [i32]	validation	execution
f32.lt	0x5D	[f32 f32] → [i32]	validation	execution
f32.gt	0x5E	[f32 f32] → [i32]	validation	execution
f32.le	0x5F	[f32 f32] → [i32]	validation	execution
f32.ge	0x60	[f32 f32] → [i32]	validation	execution
f64.eq	0x61	[f64 f64] → [i32]	validation	execution
f64.ne	0x62	[f64 f64] → [i32]	validation	execution
f64.lt	0x63	[f64 f64] → [i32]	validation	execution
f64.gt	0x64	[f64 f64] → [i32]	validation	execution
f64.le	0x65	[f64 f64] → [i32]	validation	execution
f64.ge	0x66	[f64 f64] → [i32]	validation	execution
i32.clz	0x67	[i32] → [i32]	validation	execution
i32.ctz	0x68	[i32] → [i32]	validation	execution
i32.popcnt	0x69	[i32] → [i32]	validation	execution
i32.add	0x6A	[i32 i32] → [i32]	validation	execution
i32.sub	0x6B	[i32 i32] → [i32]	validation	execution
i32.mul	0x6C	[i32 i32] → [i32]	validation	execution
i32.div_s	0x6D	[i32 i32] → [i32]	validation	execution
i32.div_u	0x6E	[i32 i32] → [i32]	validation	execution
i32.rem_s	0x6F	[i32 i32] → [i32]	validation	execution
i32.rem_u	0x70	[i32 i32] → [i32]	validation	execution
i32.and	0x71	[i32 i32] → [i32]	validation	execution
i32.or	0x72	[i32 i32] → [i32]	validation	execution
i32.xor	0x73	[i32 i32] → [i32]	validation	execution
i32.shl	0x74	[i32 i32] → [i32]	validation	execution
i32.shr_s	0x75	[i32 i32] → [i32]	validation	execution
i32.shr_u	0x76	[i32 i32] → [i32]	validation	execution
i32.rotl	0x77	[i32 i32] → [i32]	validation	execution
i32.rotr	0x78	[i32 i32] → [i32]	validation	execution
i64.clz	0x79	[i64] → [i64]	validation	execution
i64.ctz	0x7A	[i64] → [i64]	validation	execution
i64.popcnt	0x7B	[i64] → [i64]	validation	execution
i64.add	0x7C	[i64 i64] → [i64]	validation	execution
i64.sub	0x7D	[i64 i64] → [i64]	validation	execution
i64.mul	0x7E	[i64 i64] → [i64]	validation	execution
i64.div_s	0x7F	[i64 i64] → [i64]	validation	execution
i64.div_u	0x80	[i64 i64] → [i64]	validation	execution
i64.rem_s	0x81	[i64 i64] → [i64]	validation	execution
i64.rem_u	0x82	[i64 i64] → [i64]	validation	execution
i64.and	0x83	[i64 i64] → [i64]	validation	execution
i64.or	0x84	[i64 i64] → [i64]	validation	execution
i64.xor	0x85	[i64 i64] → [i64]	validation	execution
i64.shl	0x86	[i64 i64] → [i64]	validation	execution
i64.shr_s	0x87	[i64 i64] → [i64]	validation	execution
i64.shr_u	0x88	[i64 i64] → [i64]	validation	execution
i64.rotl	0x89	[i64 i64] → [i64]	validation	execution
i64.rotr	0x8A	[i64 i64] → [i64]	validation	execution
f32.abs	0x8B	[f32] → [f32]	validation	execution
f32.neg	0x8C	[f32] → [f32]	validation	execution
f32.ceil	0x8D	[f32] → [f32]	validation	execution
f32.floor	0x8E	[f32] → [f32]	validation	execution
f32.trunc	0x8F	[f32] → [f32]	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
f32.nearest	0x90	[f32] → [f32]	validation	execution
f32.sqrt	0x91	[f32] → [f32]	validation	execution
f32.add	0x92	[f32 f32] → [f32]	validation	execution
f32.sub	0x93	[f32 f32] → [f32]	validation	execution
f32.mul	0x94	[f32 f32] → [f32]	validation	execution
f32.div	0x95	[f32 f32] → [f32]	validation	execution
f32.min	0x96	[f32 f32] → [f32]	validation	execution
f32.max	0x97	[f32 f32] → [f32]	validation	execution
f32.copysign	0x98	[f32 f32] → [f32]	validation	execution
f64.abs	0x99	[f64] → [f64]	validation	execution
f64.neg	0x9A	[f64] → [f64]	validation	execution
f64.ceil	0x9B	[f64] → [f64]	validation	execution
f64.floor	0x9C	[f64] → [f64]	validation	execution
f64.trunc	0x9D	[f64] → [f64]	validation	execution
f64.nearest	0x9E	[f64] → [f64]	validation	execution
f64.sqrt	0x9F	[f64] → [f64]	validation	execution
f64.add	0xA0	[f64 f64] → [f64]	validation	execution
f64.sub	0xA1	[f64 f64] → [f64]	validation	execution
f64.mul	0xA2	[f64 f64] → [f64]	validation	execution
f64.div	0xA3	[f64 f64] → [f64]	validation	execution
f64.min	0xA4	[f64 f64] → [f64]	validation	execution
f64.max	0xA5	[f64 f64] → [f64]	validation	execution
f64.copysign	0xA6	[f64 f64] → [f64]	validation	execution
i32.wrap_i64	0xA7	[i64] → [i32]	validation	execution
i32.trunc_f32_s	0xA8	[f32] → [i32]	validation	execution
i32.trunc_f32_u	0xA9	[f32] → [i32]	validation	execution
i32.trunc_f64_s	0xAA	[f64] → [i32]	validation	execution
i32.trunc_f64_u	0xAB	[f64] → [i32]	validation	execution
i64.extend_i32_s	0xAC	[i32] → [i64]	validation	execution
i64.extend_i32_u	0xAD	[i32] → [i64]	validation	execution
i64.trunc_f32_s	0xAE	[f32] → [i64]	validation	execution
i64.trunc_f32_u	0xAF	[f32] → [i64]	validation	execution
i64.trunc_f64_s	0xB0	[f64] → [i64]	validation	execution
i64.trunc_f64_u	0xB1	[f64] → [i64]	validation	execution
f32.convert_i32_s	0xB2	[i32] → [f32]	validation	execution
f32.convert_i32_u	0xB3	[i32] → [f32]	validation	execution
f32.convert_i64_s	0xB4	[i64] → [f32]	validation	execution
f32.convert_i64_u	0xB5	[i64] → [f32]	validation	execution
f32.demote_f64	0xB6	[f64] → [f32]	validation	execution
f64.convert_i32_s	0xB7	[i32] → [f64]	validation	execution
f64.convert_i32_u	0xB8	[i32] → [f64]	validation	execution
f64.convert_i64_s	0xB9	[i64] → [f64]	validation	execution
f64.convert_i64_u	0xBA	[i64] → [f64]	validation	execution
f64.promote_f32	0xBB	[f32] → [f64]	validation	execution
i32.reinterpret_f32	0xBC	[f32] → [i32]	validation	execution
i64.reinterpret_f64	0xBD	[f64] → [i64]	validation	execution
f32.reinterpret_i32	0xBE	[i32] → [f32]	validation	execution
f64.reinterpret_i64	0xBF	[i64] → [f64]	validation	execution
i32.extend8_s	0xC0	[i32] → [i32]	validation	execution
i32.extend16_s	0xC1	[i32] → [i32]	validation	execution
i64.extend8_s	0xC2	[i64] → [i64]	validation	execution
i64.extend16_s	0xC3	[i64] → [i64]	validation	execution
i64.extend32_s	0xC4	[i64] → [i64]	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
(reserved)	0xC5			
(reserved)	0xC6			
(reserved)	0xC7			
(reserved)	0xC8			
(reserved)	0xC9			
(reserved)	0xCA			
(reserved)	0xCB			
(reserved)	0xCC			
(reserved)	0xCD			
(reserved)	0xCE			
(reserved)	0xCF			
<i>ref.null ht</i>	0xD0	$\emptyset \rightarrow [(\text{ref null } ht)]$	validation	execution
<i>ref.is_null</i>	0xD1	$[(\text{ref null } ht)] \rightarrow [i32]$	validation	execution
<i>ref.func x</i>	0xD2	$\emptyset \rightarrow [\text{ref } ht]$	validation	execution
<i>ref.eq</i>	0xD3	$[\text{eqref eqref}] \rightarrow [i32]$	validation	execution
<i>ref.as_non_null</i>	0xD4	$[(\text{ref null } ht)] \rightarrow [(\text{ref } ht)]$	validation	execution
<i>br_on_null l</i>	0xD5	$[t^* (\text{ref null } ht)] \rightarrow [t^* (\text{ref } ht)]$	validation	execution
<i>br_on_non_null l</i>	0xD6	$[t^* (\text{ref null } ht)] \rightarrow [t^*]$	validation	execution
(reserved)	0xD7			
(reserved)	0xD8			
(reserved)	0xD9			
(reserved)	0xDA			
(reserved)	0xDB			
(reserved)	0xDC			
(reserved)	0xDD			
(reserved)	0xDE			
(reserved)	0xDF			
(reserved)	0xE0			
(reserved)	0xE1			
(reserved)	0xE2			
(reserved)	0xE3			
(reserved)	0xE4			
(reserved)	0xE5			
(reserved)	0xE6			
(reserved)	0xE7			
(reserved)	0xE8			
(reserved)	0xE9			
(reserved)	0xEA			
(reserved)	0xEB			
(reserved)	0xEC			
(reserved)	0xED			
(reserved)	0xEE			
(reserved)	0xEF			
(reserved)	0xF0			
(reserved)	0xF1			
(reserved)	0xF2			
(reserved)	0xF3			
(reserved)	0xF4			
(reserved)	0xF5			
(reserved)	0xF6			
(reserved)	0xF7			
(reserved)	0xF8			
(reserved)	0xF9			

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
(reserved)	0xFA			
struct.new <i>x</i>	0xFB 0x00	$[t^*] \rightarrow [(\text{ref } x)]$	validation	execution
struct.new_default <i>x</i>	0xFB 0x01	$[] \rightarrow [(\text{ref } x)]$	validation	execution
struct.get <i>x y</i>	0xFB 0x02	$[(\text{ref null } x)] \rightarrow [t]$	validation	execution
struct.get_s <i>x y</i>	0xFB 0x03	$[(\text{ref null } x)] \rightarrow [i32]$	validation	execution
struct.get_u <i>x y</i>	0xFB 0x04	$[(\text{ref null } x)] \rightarrow [i32]$	validation	execution
struct.set <i>x y</i>	0xFB 0x05	$[(\text{ref null } x) t] \rightarrow []$	validation	execution
array.new <i>x</i>	0xFB 0x06	$[t] \rightarrow [(\text{ref } x)]$	validation	execution
array.new_default <i>x</i>	0xFB 0x07	$[i32] \rightarrow [(\text{ref } x)]$	validation	execution
array.new_fixed <i>x n</i>	0xFB 0x08	$[t^n] \rightarrow [(\text{ref } x)]$	validation	execution
array.new_data <i>x y</i>	0xFB 0x09	$[i32 i32] \rightarrow [(\text{ref } x)]$	validation	execution
array.new_elem <i>x y</i>	0xFB 0x0A	$[i32 i32] \rightarrow [(\text{ref } x)]$	validation	execution
array.get <i>x</i>	0xFB 0x0B	$[(\text{ref null } x) i32] \rightarrow [t]$	validation	execution
array.get_s <i>x</i>	0xFB 0x0C	$[(\text{ref null } x) i32] \rightarrow [i32]$	validation	execution
array.get_u <i>x</i>	0xFB 0x0D	$[(\text{ref null } x) i32] \rightarrow [i32]$	validation	execution
array.set <i>x</i>	0xFB 0x0E	$[(\text{ref null } x) i32 t] \rightarrow []$	validation	execution
array.len	0xFB 0x0F	$[(\text{ref null array})] \rightarrow [i32]$	validation	execution
array.fill <i>x</i>	0xFB 0x10	$[(\text{ref null } x) i32 t i32] \rightarrow []$	validation	execution
array.copy <i>x y</i>	0xFB 0x11	$[(\text{ref null } x) i32 (\text{ref null } y) i32 i32] \rightarrow []$	validation	execution
array.init_data <i>x y</i>	0xFB 0x12	$[(\text{ref null } x) i32 i32 i32] \rightarrow []$	validation	execution
array.init_elem <i>x y</i>	0xFB 0x13	$[(\text{ref null } x) i32 i32 i32] \rightarrow []$	validation	execution
ref.test (ref <i>t</i>)	0xFB 0x14	$[(\text{ref } t')] \rightarrow [i32]$	validation	execution
ref.test (ref null <i>t</i>)	0xFB 0x15	$[(\text{ref null } t')] \rightarrow [i32]$	validation	execution
ref.cast (ref <i>t</i>)	0xFB 0x16	$[(\text{ref } t')] \rightarrow [(\text{ref } t)]$	validation	execution
ref.cast (ref null <i>t</i>)	0xFB 0x17	$[(\text{ref null } t')] \rightarrow [(\text{ref null } t)]$	validation	execution
br_on_cast <i>t</i> ₁ <i>t</i> ₂	0xFB 0x18	$[t_1] \rightarrow [t_1 \setminus t_2]$	validation	execution
br_on_cast_fail <i>t</i> ₁ <i>t</i> ₂	0xFB 0x19	$[t_1] \rightarrow [t_2]$	validation	execution
any.convert_extern	0xFB 0x1A	$[(\text{ref null extern})] \rightarrow [(\text{ref null any})]$	validation	execution
extern.convert_any	0xFB 0x1B	$[(\text{ref null any})] \rightarrow [(\text{ref null extern})]$	validation	execution
ref.i31	0xFB 0x1C	$[i32] \rightarrow [(\text{ref } i31)]$	validation	execution
i31.get_s	0xFB 0x1D	$[i31\text{ref}] \rightarrow [i32]$	validation	execution
i31.get_u	0xFB 0x1E	$[i31\text{ref}] \rightarrow [i32]$	validation	execution
(reserved)	0xFB 0x1E...			
i32.trunc_sat_f32_s	0xFC 0x00	$[f32] \rightarrow [i32]$	validation	execution
i32.trunc_sat_f32_u	0xFC 0x01	$[f32] \rightarrow [i32]$	validation	execution
i32.trunc_sat_f64_s	0xFC 0x02	$[f64] \rightarrow [i32]$	validation	execution
i32.trunc_sat_f64_u	0xFC 0x03	$[f64] \rightarrow [i32]$	validation	execution
i64.trunc_sat_f32_s	0xFC 0x04	$[f32] \rightarrow [i64]$	validation	execution
i64.trunc_sat_f32_u	0xFC 0x05	$[f32] \rightarrow [i64]$	validation	execution
i64.trunc_sat_f64_s	0xFC 0x06	$[f64] \rightarrow [i64]$	validation	execution
i64.trunc_sat_f64_u	0xFC 0x07	$[f64] \rightarrow [i64]$	validation	execution
memory.init <i>x y</i>	0xFC 0x08	$[i32 i32 i32] \rightarrow []$	validation	execution
data.drop <i>x</i>	0xFC 0x09	$[] \rightarrow []$	validation	execution
memory.copy <i>x y</i>	0xFC 0x0A	$[i32 i32 i32] \rightarrow []$	validation	execution
memory.fill <i>y</i>	0xFC 0x0B	$[i32 i32 i32] \rightarrow []$	validation	execution
table.init <i>x y</i>	0xFC 0x0C	$[i32 i32 i32] \rightarrow []$	validation	execution
elem.drop <i>x</i>	0xFC 0x0D	$[] \rightarrow []$	validation	execution
table.copy <i>x y</i>	0xFC 0x0E	$[i32 i32 i32] \rightarrow []$	validation	execution
table.grow <i>x</i>	0xFC 0x0F	$[t i32] \rightarrow [i32]$	validation	execution
table.size <i>x</i>	0xFC 0x10	$[] \rightarrow [i32]$	validation	execution
table.fill <i>x</i>	0xFC 0x11	$[i32 t i32] \rightarrow []$	validation	execution
(reserved)	0xFC 0x1E...			
v128.load <i>x memarg</i>	0xFD 0x00	$[i32] \rightarrow [v128]$	validation	execution

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Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
<code>v128.load8x8_s x memarg</code>	<code>0xFD 0x01</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load8x8_u x memarg</code>	<code>0xFD 0x02</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load16x4_s x memarg</code>	<code>0xFD 0x03</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load16x4_u x memarg</code>	<code>0xFD 0x04</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load32x2_s x memarg</code>	<code>0xFD 0x05</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load32x2_u x memarg</code>	<code>0xFD 0x06</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load8_splat x memarg</code>	<code>0xFD 0x07</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load16_splat x memarg</code>	<code>0xFD 0x08</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load32_splat x memarg</code>	<code>0xFD 0x09</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.load64_splat x memarg</code>	<code>0xFD 0x0A</code>	<code>[i32] → [v128]</code>	validation	execution
<code>v128.store x memarg</code>	<code>0xFD 0x0B</code>	<code>[i32 v128] → []</code>	validation	execution
<code>v128.const i128</code>	<code>0xFD 0x0C</code>	<code>[] → [v128]</code>	validation	execution
<code>i8x16.shuffle laneidx¹⁶</code>	<code>0xFD 0x0D</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.swizzle</code>	<code>0xFD 0x0E</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.splat</code>	<code>0xFD 0x0F</code>	<code>[i32] → [v128]</code>	validation	execution
<code>i16x8.splat</code>	<code>0xFD 0x10</code>	<code>[i32] → [v128]</code>	validation	execution
<code>i32x4.splat</code>	<code>0xFD 0x11</code>	<code>[i32] → [v128]</code>	validation	execution
<code>i64x2.splat</code>	<code>0xFD 0x12</code>	<code>[i64] → [v128]</code>	validation	execution
<code>f32x4.splat</code>	<code>0xFD 0x13</code>	<code>[f32] → [v128]</code>	validation	execution
<code>f64x2.splat</code>	<code>0xFD 0x14</code>	<code>[f64] → [v128]</code>	validation	execution
<code>i8x16.extract_lane_s laneidx</code>	<code>0xFD 0x15</code>	<code>[v128] → [i32]</code>	validation	execution
<code>i8x16.extract_lane_u laneidx</code>	<code>0xFD 0x16</code>	<code>[v128] → [i32]</code>	validation	execution
<code>i8x16.replace_lane laneidx</code>	<code>0xFD 0x17</code>	<code>[v128 i32] → [v128]</code>	validation	execution
<code>i16x8.extract_lane_s laneidx</code>	<code>0xFD 0x18</code>	<code>[v128] → [i32]</code>	validation	execution
<code>i16x8.extract_lane_u laneidx</code>	<code>0xFD 0x19</code>	<code>[v128] → [i32]</code>	validation	execution
<code>i16x8.replace_lane laneidx</code>	<code>0xFD 0x1A</code>	<code>[v128 i32] → [v128]</code>	validation	execution
<code>i32x4.extract_lane laneidx</code>	<code>0xFD 0x1B</code>	<code>[v128] → [i32]</code>	validation	execution
<code>i32x4.replace_lane laneidx</code>	<code>0xFD 0x1C</code>	<code>[v128 i32] → [v128]</code>	validation	execution
<code>i64x2.extract_lane laneidx</code>	<code>0xFD 0x1D</code>	<code>[v128] → [i64]</code>	validation	execution
<code>i64x2.replace_lane laneidx</code>	<code>0xFD 0x1E</code>	<code>[v128 i64] → [v128]</code>	validation	execution
<code>f32x4.extract_lane laneidx</code>	<code>0xFD 0x1F</code>	<code>[v128] → [f32]</code>	validation	execution
<code>f32x4.replace_lane laneidx</code>	<code>0xFD 0x20</code>	<code>[v128 f32] → [v128]</code>	validation	execution
<code>f64x2.extract_lane laneidx</code>	<code>0xFD 0x21</code>	<code>[v128] → [f64]</code>	validation	execution
<code>f64x2.replace_lane laneidx</code>	<code>0xFD 0x22</code>	<code>[v128 f64] → [v128]</code>	validation	execution
<code>i8x16.eq</code>	<code>0xFD 0x23</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.ne</code>	<code>0xFD 0x24</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.lt_s</code>	<code>0xFD 0x25</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.lt_u</code>	<code>0xFD 0x26</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.gt_s</code>	<code>0xFD 0x27</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.gt_u</code>	<code>0xFD 0x28</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.le_s</code>	<code>0xFD 0x29</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.le_u</code>	<code>0xFD 0x2A</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.ge_s</code>	<code>0xFD 0x2B</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i8x16.ge_u</code>	<code>0xFD 0x2C</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.eq</code>	<code>0xFD 0x2D</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.ne</code>	<code>0xFD 0x2E</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.lt_s</code>	<code>0xFD 0x2F</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.lt_u</code>	<code>0xFD 0x30</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.gt_s</code>	<code>0xFD 0x31</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.gt_u</code>	<code>0xFD 0x32</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.le_s</code>	<code>0xFD 0x33</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.le_u</code>	<code>0xFD 0x34</code>	<code>[v128 v128] → [v128]</code>	validation	execution
<code>i16x8.ge_s</code>	<code>0xFD 0x35</code>	<code>[v128 v128] → [v128]</code>	validation	execution

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Instruction	Binary Opcode	Type	Validation	Execution
i16x8.ge_u	0xFD 0x36	[v128 v128] → [v128]	validation	execution
i32x4.eq	0xFD 0x37	[v128 v128] → [v128]	validation	execution
i32x4.ne	0xFD 0x38	[v128 v128] → [v128]	validation	execution
i32x4.lt_s	0xFD 0x39	[v128 v128] → [v128]	validation	execution
i32x4.lt_u	0xFD 0x3A	[v128 v128] → [v128]	validation	execution
i32x4.gt_s	0xFD 0x3B	[v128 v128] → [v128]	validation	execution
i32x4.gt_u	0xFD 0x3C	[v128 v128] → [v128]	validation	execution
i32x4.le_s	0xFD 0x3D	[v128 v128] → [v128]	validation	execution
i32x4.le_u	0xFD 0x3E	[v128 v128] → [v128]	validation	execution
i32x4.ge_s	0xFD 0x3F	[v128 v128] → [v128]	validation	execution
i32x4.ge_u	0xFD 0x40	[v128 v128] → [v128]	validation	execution
f32x4.eq	0xFD 0x41	[v128 v128] → [v128]	validation	execution
f32x4.ne	0xFD 0x42	[v128 v128] → [v128]	validation	execution
f32x4.lt	0xFD 0x43	[v128 v128] → [v128]	validation	execution
f32x4.gt	0xFD 0x44	[v128 v128] → [v128]	validation	execution
f32x4.le	0xFD 0x45	[v128 v128] → [v128]	validation	execution
f32x4.ge	0xFD 0x46	[v128 v128] → [v128]	validation	execution
f64x2.eq	0xFD 0x47	[v128 v128] → [v128]	validation	execution
f64x2.ne	0xFD 0x48	[v128 v128] → [v128]	validation	execution
f64x2.lt	0xFD 0x49	[v128 v128] → [v128]	validation	execution
f64x2.gt	0xFD 0x4A	[v128 v128] → [v128]	validation	execution
f64x2.le	0xFD 0x4B	[v128 v128] → [v128]	validation	execution
f64x2.ge	0xFD 0x4C	[v128 v128] → [v128]	validation	execution
v128.not	0xFD 0x4D	[v128] → [v128]	validation	execution
v128.and	0xFD 0x4E	[v128 v128] → [v128]	validation	execution
v128.andnot	0xFD 0x4F	[v128 v128] → [v128]	validation	execution
v128.or	0xFD 0x50	[v128 v128] → [v128]	validation	execution
v128.xor	0xFD 0x51	[v128 v128] → [v128]	validation	execution
v128.bitselect	0xFD 0x52	[v128 v128 v128] → [v128]	validation	execution
v128.any_true	0xFD 0x53	[v128] → [i32]	validation	execution
v128.load8_lane <i>memarg laneidx</i>	0xFD 0x54	[i32 v128] → [v128]	validation	execution
v128.load16_lane <i>memarg laneidx</i>	0xFD 0x55	[i32 v128] → [v128]	validation	execution
v128.load32_lane <i>memarg laneidx</i>	0xFD 0x56	[i32 v128] → [v128]	validation	execution
v128.load64_lane <i>memarg laneidx</i>	0xFD 0x57	[i32 v128] → [v128]	validation	execution
v128.store8_lane <i>memarg laneidx</i>	0xFD 0x58	[i32 v128] → []	validation	execution
v128.store16_lane <i>memarg laneidx</i>	0xFD 0x59	[i32 v128] → []	validation	execution
v128.store32_lane <i>memarg laneidx</i>	0xFD 0x5A	[i32 v128] → []	validation	execution
v128.store64_lane <i>memarg laneidx</i>	0xFD 0x5B	[i32 v128] → []	validation	execution
v128.load32_zero <i>memarg</i>	0xFD 0x5C	[i32] → [v128]	validation	execution
v128.load64_zero <i>memarg</i>	0xFD 0x5D	[i32] → [v128]	validation	execution
f32x4.demote_f64x2_zero	0xFD 0x5E	[v128] → [v128]	validation	execution
f64x2.promote_low_f32x4	0xFD 0x5F	[v128] → [v128]	validation	execution
i8x16.abs	0xFD 0x60	[v128] → [v128]	validation	execution
i8x16.neg	0xFD 0x61	[v128] → [v128]	validation	execution
i8x16.popcnt	0xFD 0x62	[v128] → [v128]	validation	execution
i8x16.all_true	0xFD 0x63	[v128] → [i32]	validation	execution
i8x16.bitmask	0xFD 0x64	[v128] → [i32]	validation	execution
i8x16.narrow_i16x8_s	0xFD 0x65	[v128 v128] → [v128]	validation	execution
i8x16.narrow_i16x8_u	0xFD 0x66	[v128 v128] → [v128]	validation	execution
f32x4.ceil	0xFD 0x67	[v128] → [v128]	validation	execution
f32x4.floor	0xFD 0x68	[v128] → [v128]	validation	execution
f32x4.trunc	0xFD 0x69	[v128] → [v128]	validation	execution
f32x4.nearest	0xFD 0x6A	[v128] → [v128]	validation	execution

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Instruction	Binary Opcode	Type	Validation	Execution
i8x16.shl	0xFD 0x6B	[v128 i32] → [v128]	validation	execution
i8x16.shr_s	0xFD 0x6C	[v128 i32] → [v128]	validation	execution
i8x16.shr_u	0xFD 0x6D	[v128 i32] → [v128]	validation	execution
i8x16.add	0xFD 0x6E	[v128 v128] → [v128]	validation	execution
i8x16.add_sat_s	0xFD 0x6F	[v128 v128] → [v128]	validation	execution
i8x16.add_sat_u	0xFD 0x70	[v128 v128] → [v128]	validation	execution
i8x16.sub	0xFD 0x71	[v128 v128] → [v128]	validation	execution
i8x16.sub_sat_s	0xFD 0x72	[v128 v128] → [v128]	validation	execution
i8x16.sub_sat_u	0xFD 0x73	[v128 v128] → [v128]	validation	execution
f64x2.ceil	0xFD 0x74	[v128] → [v128]	validation	execution
f64x2.floor	0xFD 0x75	[v128] → [v128]	validation	execution
i8x16.min_s	0xFD 0x76	[v128 v128] → [v128]	validation	execution
i8x16.min_u	0xFD 0x77	[v128 v128] → [v128]	validation	execution
i8x16.max_s	0xFD 0x78	[v128 v128] → [v128]	validation	execution
i8x16.max_u	0xFD 0x79	[v128 v128] → [v128]	validation	execution
f64x2.trunc	0xFD 0x7A	[v128] → [v128]	validation	execution
i8x16.avgr_u	0xFD 0x7B	[v128 v128] → [v128]	validation	execution
i16x8.extadd_pairwise_i8x16_s	0xFD 0x7C	[v128] → [v128]	validation	execution
i16x8.extadd_pairwise_i8x16_u	0xFD 0x7D	[v128] → [v128]	validation	execution
i32x4.extadd_pairwise_i16x8_s	0xFD 0x7E	[v128] → [v128]	validation	execution
i32x4.extadd_pairwise_i16x8_u	0xFD 0x7F	[v128] → [v128]	validation	execution
i16x8.abs	0xFD 0x80 0x01	[v128] → [v128]	validation	execution
i16x8.neg	0xFD 0x81 0x01	[v128] → [v128]	validation	execution
i16x8.q15mulr_sat_s	0xFD 0x82 0x01	[v128 v128] → [v128]	validation	execution
i16x8.all_true	0xFD 0x83 0x01	[v128] → [i32]	validation	execution
i16x8.bitmask	0xFD 0x84 0x01	[v128] → [i32]	validation	execution
i16x8.narrow_i32x4_s	0xFD 0x85 0x01	[v128 v128] → [v128]	validation	execution
i16x8.narrow_i32x4_u	0xFD 0x86 0x01	[v128 v128] → [v128]	validation	execution
i16x8.extend_low_i8x16_s	0xFD 0x87 0x01	[v128] → [v128]	validation	execution
i16x8.extend_high_i8x16_s	0xFD 0x88 0x01	[v128] → [v128]	validation	execution
i16x8.extend_low_i8x16_u	0xFD 0x89 0x01	[v128] → [v128]	validation	execution
i16x8.extend_high_i8x16_u	0xFD 0x8A 0x01	[v128] → [v128]	validation	execution
i16x8.shl	0xFD 0x8B 0x01	[v128 i32] → [v128]	validation	execution
i16x8.shr_s	0xFD 0x8C 0x01	[v128 i32] → [v128]	validation	execution
i16x8.shr_u	0xFD 0x8D 0x01	[v128 i32] → [v128]	validation	execution
i16x8.add	0xFD 0x8E 0x01	[v128 v128] → [v128]	validation	execution
i16x8.add_sat_s	0xFD 0x8F 0x01	[v128 v128] → [v128]	validation	execution
i16x8.add_sat_u	0xFD 0x90 0x01	[v128 v128] → [v128]	validation	execution
i16x8.sub	0xFD 0x91 0x01	[v128 v128] → [v128]	validation	execution
i16x8.sub_sat_s	0xFD 0x92 0x01	[v128 v128] → [v128]	validation	execution
i16x8.sub_sat_u	0xFD 0x93 0x01	[v128 v128] → [v128]	validation	execution
f64x2.nearest	0xFD 0x94 0x01	[v128] → [v128]	validation	execution
i16x8.mul	0xFD 0x95 0x01	[v128 v128] → [v128]	validation	execution
i16x8.min_s	0xFD 0x96 0x01	[v128 v128] → [v128]	validation	execution
i16x8.min_u	0xFD 0x97 0x01	[v128 v128] → [v128]	validation	execution
i16x8.max_s	0xFD 0x98 0x01	[v128 v128] → [v128]	validation	execution
i16x8.max_u	0xFD 0x99 0x01	[v128 v128] → [v128]	validation	execution
(reserved)	0xFD 0x9A 0x01			
i16x8.avgr_u	0xFD 0x9B 0x01	[v128 v128] → [v128]	validation	execution
i16x8.extmul_low_i8x16_s	0xFD 0x9C 0x01	[v128 v128] → [v128]	validation	execution
i16x8.extmul_high_i8x16_s	0xFD 0x9D 0x01	[v128 v128] → [v128]	validation	execution
i16x8.extmul_low_i8x16_u	0xFD 0x9E 0x01	[v128 v128] → [v128]	validation	execution
i16x8.extmul_high_i8x16_u	0xFD 0x9F 0x01	[v128 v128] → [v128]	validation	execution

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Instruction	Binary Opcode	Type	Validation	Execution
i32x4.abs	0xFD 0xA0 0x01	[v128] → [v128]	validation	execution
i32x4.neg	0xFD 0xA1 0x01	[v128] → [v128]	validation	execution
(reserved)	0xFD 0xA2 0x01			
i32x4.all_true	0xFD 0xA3 0x01	[v128] → [i32]	validation	execution
i32x4.bitmask	0xFD 0xA4 0x01	[v128] → [i32]	validation	execution
(reserved)	0xFD 0xA5 0x01			
(reserved)	0xFD 0xA6 0x01			
i32x4.extend_low_i16x8_s	0xFD 0xA7 0x01	[v128] → [v128]	validation	execution
i32x4.extend_high_i16x8_s	0xFD 0xA8 0x01	[v128] → [v128]	validation	execution
i32x4.extend_low_i16x8_u	0xFD 0xA9 0x01	[v128] → [v128]	validation	execution
i32x4.extend_high_i16x8_u	0xFD 0xAA 0x01	[v128] → [v128]	validation	execution
i32x4.shl	0xFD 0xAB 0x01	[v128 i32] → [v128]	validation	execution
i32x4.shr_s	0xFD 0xAC 0x01	[v128 i32] → [v128]	validation	execution
i32x4.shr_u	0xFD 0xAD 0x01	[v128 i32] → [v128]	validation	execution
i32x4.add	0xFD 0xAE 0x01	[v128 v128] → [v128]	validation	execution
(reserved)	0xFD 0xAF 0x01			
(reserved)	0xFD 0xB0 0x01			
i32x4.sub	0xFD 0xB1 0x01	[v128 v128] → [v128]	validation	execution
(reserved)	0xFD 0xB2 0x01			
(reserved)	0xFD 0xB3 0x01			
(reserved)	0xFD 0xB4 0x01			
i32x4.mul	0xFD 0xB5 0x01	[v128 v128] → [v128]	validation	execution
i32x4.min_s	0xFD 0xB6 0x01	[v128 v128] → [v128]	validation	execution
i32x4.min_u	0xFD 0xB7 0x01	[v128 v128] → [v128]	validation	execution
i32x4.max_s	0xFD 0xB8 0x01	[v128 v128] → [v128]	validation	execution
i32x4.max_u	0xFD 0xB9 0x01	[v128 v128] → [v128]	validation	execution
i32x4.dot_i16x8_s	0xFD 0xBA 0x01	[v128 v128] → [v128]	validation	execution
i32x4.extmul_low_i16x8_s	0xFD 0xBC 0x01	[v128 v128] → [v128]	validation	execution
i32x4.extmul_high_i16x8_s	0xFD 0xBD 0x01	[v128 v128] → [v128]	validation	execution
i32x4.extmul_low_i16x8_u	0xFD 0xBE 0x01	[v128 v128] → [v128]	validation	execution
i32x4.extmul_high_i16x8_u	0xFD 0xBF 0x01	[v128 v128] → [v128]	validation	execution
i64x2.abs	0xFD 0xC0 0x01	[v128] → [v128]	validation	execution
i64x2.neg	0xFD 0xC1 0x01	[v128] → [v128]	validation	execution
(reserved)	0xFD 0xC2 0x01			
i64x2.all_true	0xFD 0xC3 0x01	[v128] → [i32]	validation	execution
i64x2.bitmask	0xFD 0xC4 0x01	[v128] → [i32]	validation	execution
(reserved)	0xFD 0xC5 0x01			
(reserved)	0xFD 0xC6 0x01			
i64x2.extend_low_i32x4_s	0xFD 0xC7 0x01	[v128] → [v128]	validation	execution
i64x2.extend_high_i32x4_s	0xFD 0xC8 0x01	[v128] → [v128]	validation	execution
i64x2.extend_low_i32x4_u	0xFD 0xC9 0x01	[v128] → [v128]	validation	execution
i64x2.extend_high_i32x4_u	0xFD 0xCA 0x01	[v128] → [v128]	validation	execution
i64x2.shl	0xFD 0xCB 0x01	[v128 i32] → [v128]	validation	execution
i64x2.shr_s	0xFD 0xCC 0x01	[v128 i32] → [v128]	validation	execution
i64x2.shr_u	0xFD 0xCD 0x01	[v128 i32] → [v128]	validation	execution
i64x2.add	0xFD 0xCE 0x01	[v128 v128] → [v128]	validation	execution
(reserved)	0xFD 0xCF 0x01			
(reserved)	0xFD 0xD0 0x01			
i64x2.sub	0xFD 0xD1 0x01	[v128 v128] → [v128]	validation	execution
(reserved)	0xFD 0xD2 0x01			
(reserved)	0xFD 0xD3 0x01			
(reserved)	0xFD 0xD4 0x01			
i64x2.mul	0xFD 0xD5 0x01	[v128 v128] → [v128]	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
i64x2.eq	0xFD 0xD6 0x01	[v128 v128] → [v128]	validation	execution
i64x2.ne	0xFD 0xD7 0x01	[v128 v128] → [v128]	validation	execution
i64x2.lt_s	0xFD 0xD8 0x01	[v128 v128] → [v128]	validation	execution
i64x2.gt_s	0xFD 0xD9 0x01	[v128 v128] → [v128]	validation	execution
i64x2.le_s	0xFD 0xDA 0x01	[v128 v128] → [v128]	validation	execution
i64x2.ge_s	0xFD 0xDB 0x01	[v128 v128] → [v128]	validation	execution
i64x2.extmul_low_i32x4_s	0xFD 0xDC 0x01	[v128 v128] → [v128]	validation	execution
i64x2.extmul_high_i32x4_s	0xFD 0xDD 0x01	[v128 v128] → [v128]	validation	execution
i64x2.extmul_low_i32x4_u	0xFD 0xDE 0x01	[v128 v128] → [v128]	validation	execution
i64x2.extmul_high_i32x4_u	0xFD 0xDF 0x01	[v128 v128] → [v128]	validation	execution
f32x4.abs	0xFD 0xE0 0x01	[v128] → [v128]	validation	execution
f32x4.neg	0xFD 0xE1 0x01	[v128] → [v128]	validation	execution
(reserved)	0xFD 0xE2 0x01			
f32x4.sqrt	0xFD 0xE3 0x01	[v128] → [v128]	validation	execution
f32x4.add	0xFD 0xE4 0x01	[v128 v128] → [v128]	validation	execution
f32x4.sub	0xFD 0xE5 0x01	[v128 v128] → [v128]	validation	execution
f32x4.mul	0xFD 0xE6 0x01	[v128 v128] → [v128]	validation	execution
f32x4.div	0xFD 0xE7 0x01	[v128 v128] → [v128]	validation	execution
f32x4.min	0xFD 0xE8 0x01	[v128 v128] → [v128]	validation	execution
f32x4.max	0xFD 0xE9 0x01	[v128 v128] → [v128]	validation	execution
f32x4.pmin	0xFD 0xEA 0x01	[v128 v128] → [v128]	validation	execution
f32x4.pmax	0xFD 0xEB 0x01	[v128 v128] → [v128]	validation	execution
f64x2.abs	0xFD 0xEC 0x01	[v128] → [v128]	validation	execution
f64x2.neg	0xFD 0xED 0x01	[v128] → [v128]	validation	execution
f64x2.sqrt	0xFD 0xEF 0x01	[v128] → [v128]	validation	execution
f64x2.add	0xFD 0xF0 0x01	[v128 v128] → [v128]	validation	execution
f64x2.sub	0xFD 0xF1 0x01	[v128 v128] → [v128]	validation	execution
f64x2.mul	0xFD 0xF2 0x01	[v128 v128] → [v128]	validation	execution
f64x2.div	0xFD 0xF3 0x01	[v128 v128] → [v128]	validation	execution
f64x2.min	0xFD 0xF4 0x01	[v128 v128] → [v128]	validation	execution
f64x2.max	0xFD 0xF5 0x01	[v128 v128] → [v128]	validation	execution
f64x2.pmin	0xFD 0xF6 0x01	[v128 v128] → [v128]	validation	execution
f64x2.pmax	0xFD 0xF7 0x01	[v128 v128] → [v128]	validation	execution
i32x4.trunc_sat_f32x4_s	0xFD 0xF8 0x01	[v128] → [v128]	validation	execution
i32x4.trunc_sat_f32x4_u	0xFD 0xF9 0x01	[v128] → [v128]	validation	execution
f32x4.convert_i32x4_s	0xFD 0xFA 0x01	[v128] → [v128]	validation	execution
f32x4.convert_i32x4_u	0xFD 0xFB 0x01	[v128] → [v128]	validation	execution
i32x4.trunc_sat_f64x2_s_zero	0xFD 0xFC 0x01	[v128] → [v128]	validation	execution
i32x4.trunc_sat_f64x2_u_zero	0xFD 0xFD 0x01	[v128] → [v128]	validation	execution
f64x2.convert_low_i32x4_s	0xFD 0xFE 0x01	[v128] → [v128]	validation	execution
f64x2.convert_low_i32x4_u	0xFD 0xFF 0x01	[v128] → [v128]	validation	execution
i8x16.relaxed_swizzle	0xFD 0x80 0x02	[v128 v128] → [v128]	validation	execution
i32x4.relaxed_trunc_f32x4_s	0xFD 0x81 0x02	[v128] → [v128]	validation	execution
i32x4.relaxed_trunc_f32x4_u	0xFD 0x82 0x02	[v128] → [v128]	validation	execution
i32x4.relaxed_trunc_f64x2_s	0xFD 0x83 0x02	[v128] → [v128]	validation	execution
i32x4.relaxed_trunc_f64x2_u	0xFD 0x84 0x02	[v128] → [v128]	validation	execution
f32x4.relaxed_madd	0xFD 0x85 0x02	[v128 v128 v128] → [v128]	validation	execution
f32x4.relaxed_nmadd	0xFD 0x86 0x02	[v128 v128 v128] → [v128]	validation	execution
f64x2.relaxed_madd	0xFD 0x87 0x02	[v128 v128 v128] → [v128]	validation	execution
f64x2.relaxed_nmadd	0xFD 0x88 0x02	[v128 v128 v128] → [v128]	validation	execution
i8x16.relaxed_laneselect	0xFD 0x89 0x02	[v128 v128 v128] → [v128]	validation	execution
i16x8.relaxed_laneselect	0xFD 0x8A 0x02	[v128 v128 v128] → [v128]	validation	execution
i32x4.relaxed_laneselect	0xFD 0x8B 0x02	[v128 v128 v128] → [v128]	validation	execution

continues on

Table 2 – continued from previous page

Instruction	Binary Opcode	Type	Validation	Execution
i64x2.relaxed_laneselect	0xFD 0x8C 0x02	$[v128\ v128\ v128] \rightarrow [v128]$	validation	execution
f32x4.relaxed_min	0xFD 0x8D 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
f32x4.relaxed_max	0xFD 0x8E 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
f64x2.relaxed_min	0xFD 0x8F 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
f64x2.relaxed_max	0xFD 0x90 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
i16x8.relaxed_q15mulr_s	0xFD 0x91 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
i16x8.relaxed_dot_i8x16_i7x16_s	0xFD 0x92 0x02	$[v128\ v128] \rightarrow [v128]$	validation	execution
i32x4.relaxed_dot_i8x16_i7x16_add_s	0xFD 0x93 0x02	$[v128\ v128\ v128] \rightarrow [v128]$	validation	execution
(reserved)	0xFE			
(reserved)	0xFF			

Note: Multi-byte opcodes are given with the shortest possible encoding in the table. However, what is following the first byte is actually a [u32](#) with variable-length encoding and consequently has multiple possible representations.

7.11 Index of Semantic Rules

7.11.1 Well-formedness of Types

Construct	Judgement
Numeric type	$C \vdash \text{numtype ok}$
Vector type	$C \vdash \text{vectype ok}$
Heap type	$C \vdash \text{heapttype ok}$
Reference type	$C \vdash \text{reftype ok}$
Value type	$C \vdash \text{valtype ok}$
Packed type	$C \vdash \text{packedtype ok}$
Storage type	$C \vdash \text{storagetype ok}$
Field type	$C \vdash \text{fieldtype ok}$
Result type	$C \vdash \text{resulttype ok}$
Instruction type	$C \vdash \text{instrtype ok}$
Function type	$C \vdash \text{functype ok}$
Structure type	$C \vdash \text{structtype ok}$
Array type	$C \vdash \text{arraytype ok}$
Composite type	$C \vdash \text{comptype ok}$
Sub type	$C \vdash \text{subtype ok}$
Recursive type	$C \vdash \text{rectype ok}$
Defined type	$C \vdash \text{deftype ok}$
Block type	$C \vdash \text{blocktype} : \text{instrtype}$
Table type	$C \vdash \text{tabletype ok}$
Memory type	$C \vdash \text{memtype ok}$
Global type	$C \vdash \text{globaltype ok}$
Tag type	$C \vdash \text{tagtype ok}$
External type	$C \vdash \text{externtype ok}$
Type definitions	$C \vdash \text{type}^* \text{ ok}$

7.11.2 Typing of Static Constructs

Construct	Judgement
Instruction	$S; C \vdash instr : functype$
Instruction sequence	$S; C \vdash instr^* : functype$
Catch clause	$C \vdash catch \text{ ok}$
Expression	$C \vdash expr : resulttype$
Function	$C \vdash func : functype$
Local	$C \vdash local : localtype$
Table	$C \vdash table : tabletype$
Memory	$C \vdash mem : memtype$
Limits	$C \vdash limits : k$
Global	$C \vdash global : globaltype$
Tag	$C \vdash tag : tagtype$
Element segment	$C \vdash elem : reftype$
Element mode	$C \vdash elemmode : reftype$
Data segment	$C \vdash data \text{ ok}$
Data mode	$C \vdash datamode \text{ ok}$
Start function	$C \vdash start \text{ ok}$
Export	$C \vdash export : externtype$
Export description	$C \vdash exportdesc : externtype$
Import	$C \vdash import : externtype$
Import description	$C \vdash importdesc : externtype$
Module	$\vdash module : externtype^* \rightarrow externtype^*$

7.11.3 Typing of Runtime Constructs

Construct	Judgement
Value	$S \vdash val : valtype$
Result	$S \vdash result : resulttype$
Packed value	$S \vdash packedval : packedtype$
Field value	$S \vdash fieldval : storagetype$
External value	$S \vdash externval : externtype$
Function instance	$S \vdash funcinst : functype$
Table instance	$S \vdash tableinst : tabletype$
Memory instance	$S \vdash meminst : memtype$
Global instance	$S \vdash globalinst : globaltype$
Tag instance	$S \vdash taginst : tagtype$
Element instance	$S \vdash eleminst : t$
Data instance	$S \vdash datainst \text{ ok}$
Structure instance	$S \vdash structinst \text{ ok}$
Array instance	$S \vdash arrayinst \text{ ok}$
Export instance	$S \vdash exportinst \text{ ok}$
Module instance	$S \vdash moduleinst : C$
Store	$\vdash store \text{ ok}$
Configuration	$\vdash config \text{ ok}$
Thread	$S; resulttype^? \vdash thread : resulttype$
Frame	$S \vdash frame : C$

7.11.4 Defaultability

Construct	Judgement
Defaultable value type	$C \vdash \text{valtype defaultable}$

7.11.5 Constantness

Construct	Judgement
Constant expression	$C \vdash \text{expr const}$
Constant instruction	$C \vdash \text{instr const}$

7.11.6 Matching

Construct	Judgement
Number type	$C \vdash \text{numtype}_1 \leq \text{numtype}_2$
Vector type	$C \vdash \text{vectype}_1 \leq \text{vectype}_2$
Heap type	$C \vdash \text{heaptypes}_1 \leq \text{heaptypes}_2$
Reference type	$C \vdash \text{reftype}_1 \leq \text{reftype}_2$
Value type	$C \vdash \text{valtype}_1 \leq \text{valtype}_2$
Packed type	$C \vdash \text{packedtype}_1 \leq \text{packedtype}_2$
Storage type	$C \vdash \text{storagetype}_1 \leq \text{storagetype}_2$
Field type	$C \vdash \text{fieldtype}_1 \leq \text{fieldtype}_2$
Result type	$C \vdash \text{resulttype}_1 \leq \text{resulttype}_2$
Instruction type	$C \vdash \text{instrtype}_1 \leq \text{instrtype}_2$
Function type	$C \vdash \text{functype}_1 \leq \text{functype}_2$
Structure type	$C \vdash \text{structtype}_1 \leq \text{structtype}_2$
Array type	$C \vdash \text{arraytype}_1 \leq \text{arraytype}_2$
Composite type	$C \vdash \text{comptype}_1 \leq \text{comptype}_2$
Defined type	$C \vdash \text{deftype}_1 \leq \text{deftype}_2$
Table type	$C \vdash \text{tabletype}_1 \leq \text{tabletype}_2$
Memory type	$C \vdash \text{memtype}_1 \leq \text{memtype}_2$
Global type	$C \vdash \text{globaltype}_1 \leq \text{globaltype}_2$
Tag type	$C \vdash \text{tagtype}_1 \leq \text{tagtype}_2$
External type	$C \vdash \text{externtype}_1 \leq \text{externtype}_2$
Limits	$C \vdash \text{limits}_1 \leq \text{limits}_2$

7.11.7 Store Extension

Construct	Judgement
Function instance	$\vdash \text{funcinst}_1 \preceq \text{funcinst}_2$
Table instance	$\vdash \text{tableinst}_1 \preceq \text{tableinst}_2$
Memory instance	$\vdash \text{meminst}_1 \preceq \text{meminst}_2$
Global instance	$\vdash \text{globalinst}_1 \preceq \text{globalinst}_2$
Tag instance	$\vdash \text{taginst}_1 \preceq \text{taginst}_2$
Element instance	$\vdash \text{eleminst}_1 \preceq \text{eleminst}_2$
Data instance	$\vdash \text{datainst}_1 \preceq \text{datainst}_2$
Structure instance	$\vdash \text{structinst}_1 \preceq \text{structinst}_2$
Array instance	$\vdash \text{arrayinst}_1 \preceq \text{arrayinst}_2$
Store	$\vdash \text{store}_1 \preceq \text{store}_2$

7.11.8 Execution

Construct	Judgement
Instruction	$S; F; instr^* \hookrightarrow S'; F'; instr'^*$
Expression	$S; F; expr \hookrightarrow S'; F'; expr'$

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