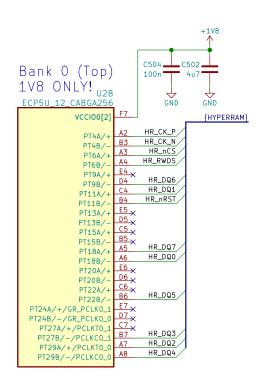
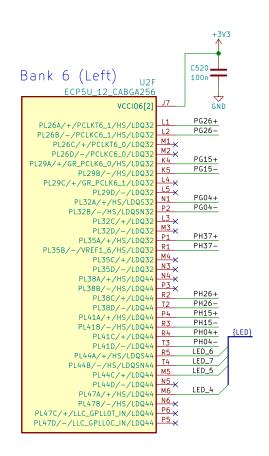
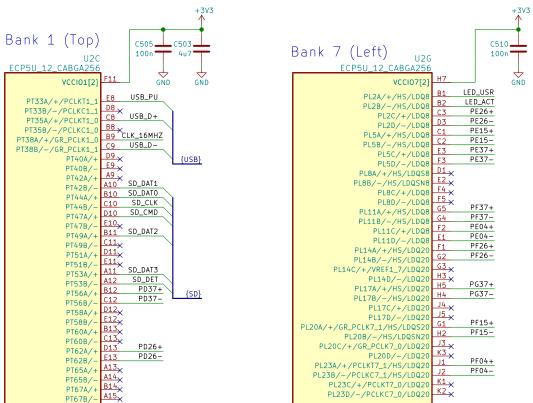
CP5 Min

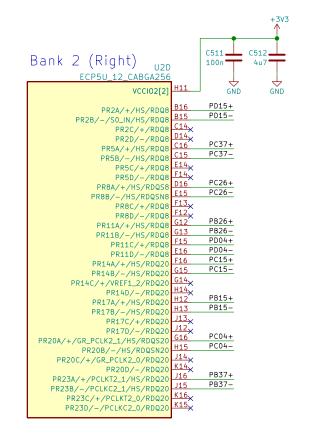
Following Black Mesa Lab's "S7 Mini" form factor

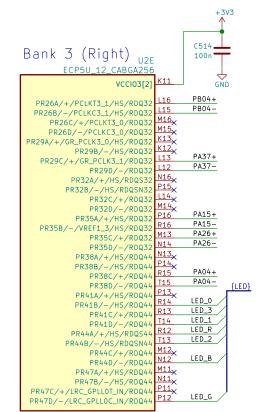
10 Banks



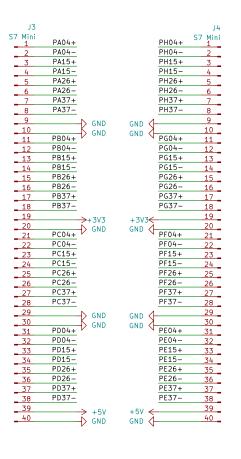




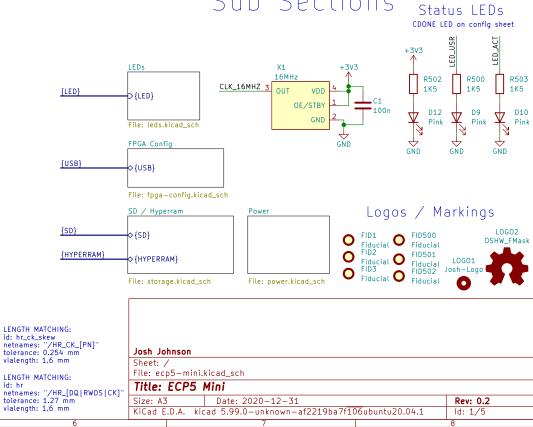


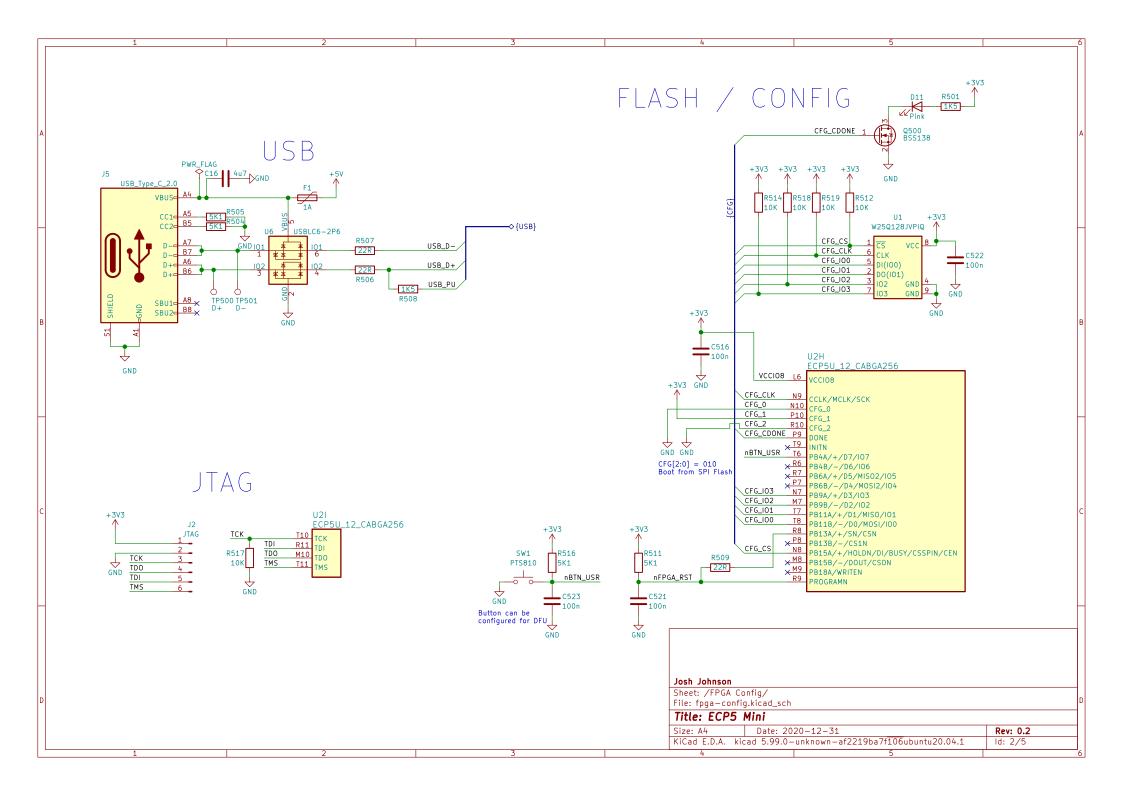


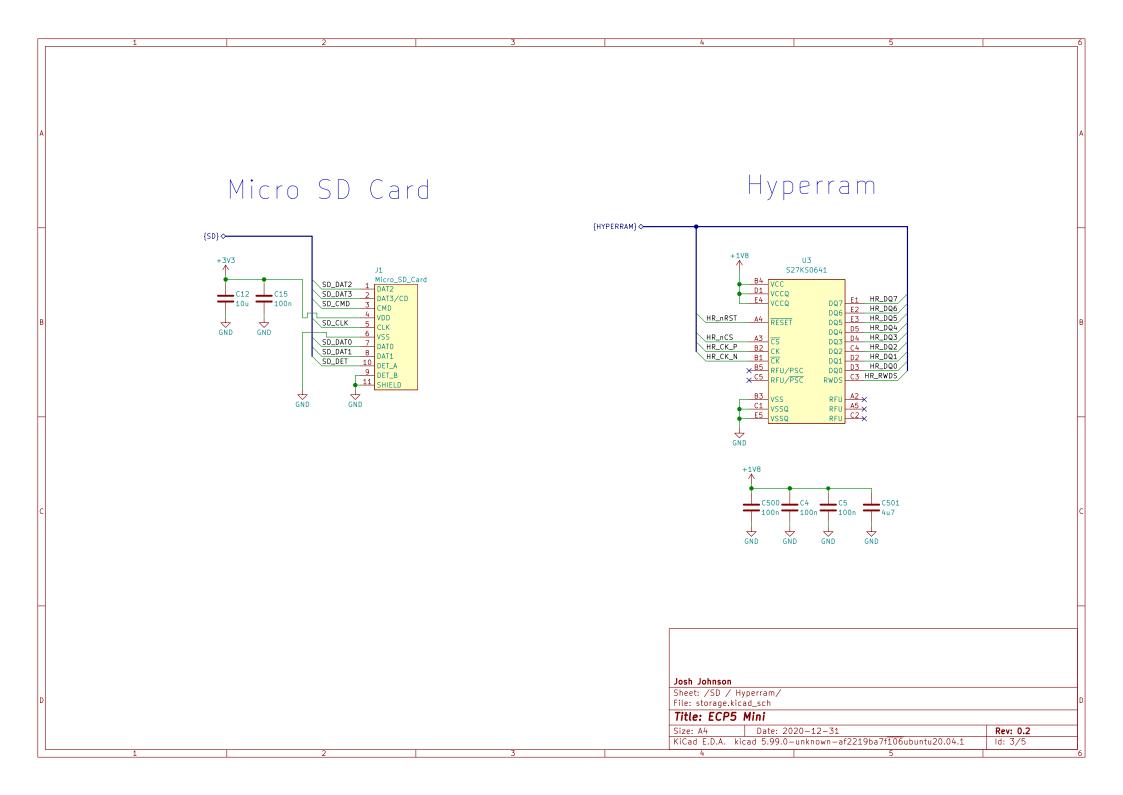
Breakout Pins

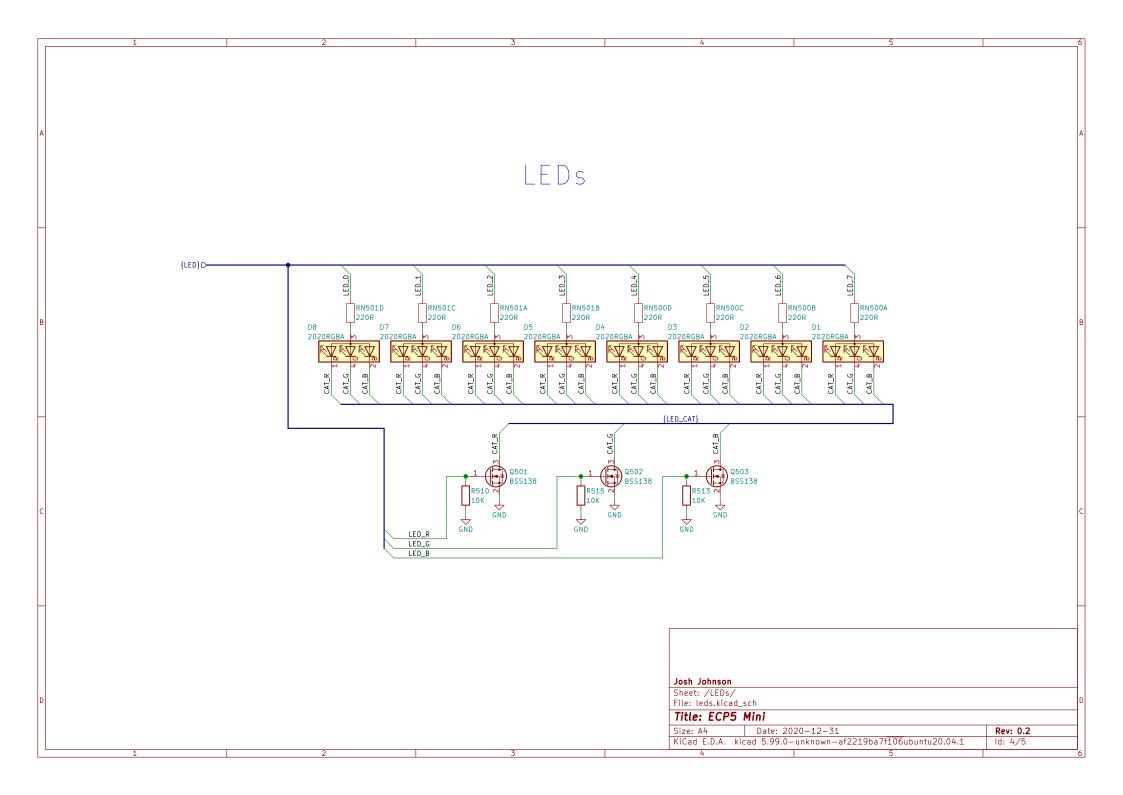












Power Supply VCore L1 2u2 U2A ECP5U_12_CABGA256 $V_{out} = 0.6V * (1 + R1/R2)$ 3v3 = 0.6 * (1 + 120k/26.7k) $3v3 \otimes 1A$ L10 VCC[6] R1 120K C13 6p8 G11 VCCAUX[2] 10u GND GND A16 GND[27] GND GND L2 2u2 Vout = 0.6V * (1 + R1/R2) 1v1 = 0.6 * (1 + 100k/120k) 1v1 @ 1A _C515 ____C518 ____C507 ____C517 R2 100K 1% GND GND GND C506 ____ C519 U4 GND TLV75525PDQNR IN OUT 3 EN GND 2 GND GND TLV75518PDQNR IN OUT 1 3 EN GND 2 GND GND Josh Johnson Sheet: /Power/ PWR_FLAG +1V1 File: power.kicad_sch Title: ECP5 Mini GND PWR_FLAG PWR_FLAG PWR_FLAG Date: 2020-12-31 Size: A4 KiCad E.D.A. kicad 5.99.0-unknown-af2219ba7f106ubuntu20.04.1 Id: 5/5