

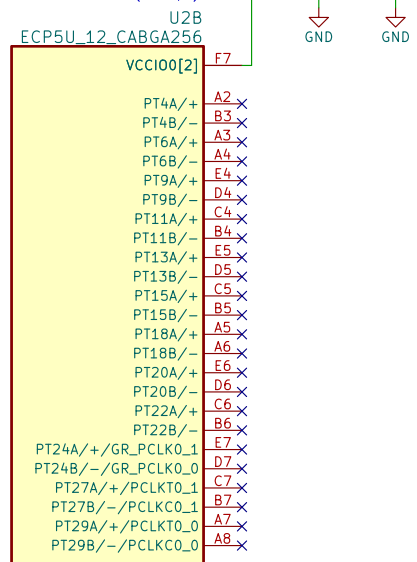
# ECP5 Mini

Following Black Mesa Lab's "S7 Mini" form factor

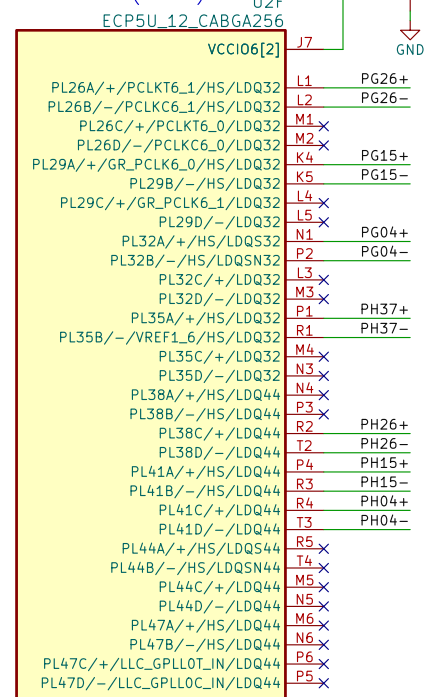
## IO Banks

## Breakout Pins

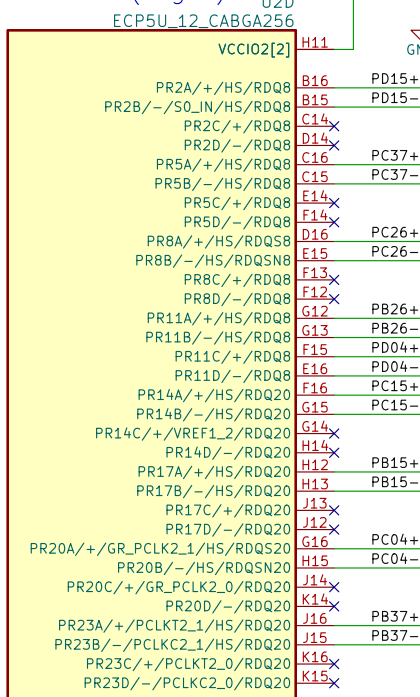
### Bank 0 (Top)



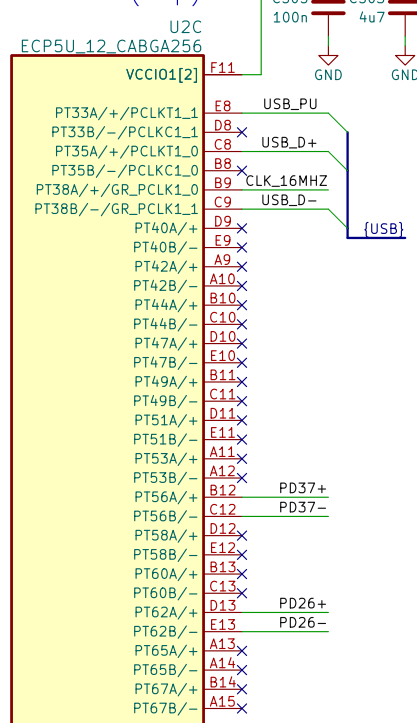
### Bank 6 (Left)



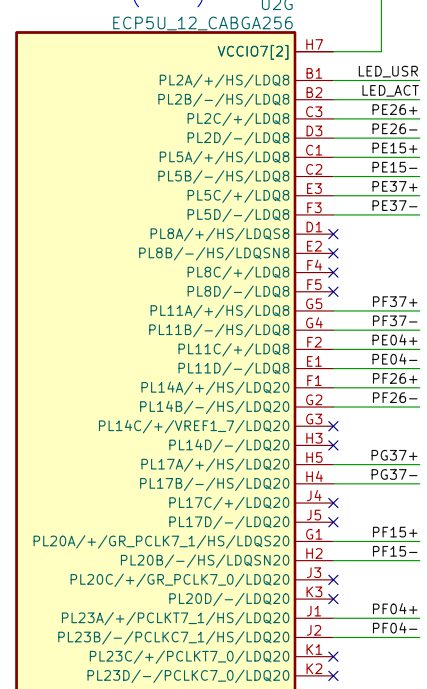
### Bank 2 (Right)



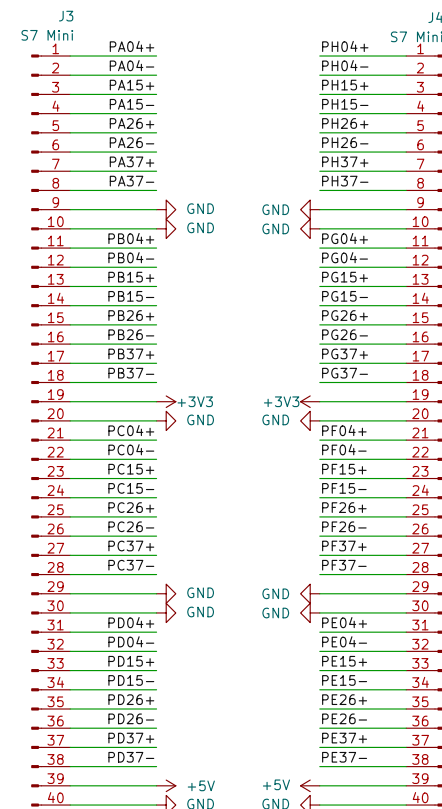
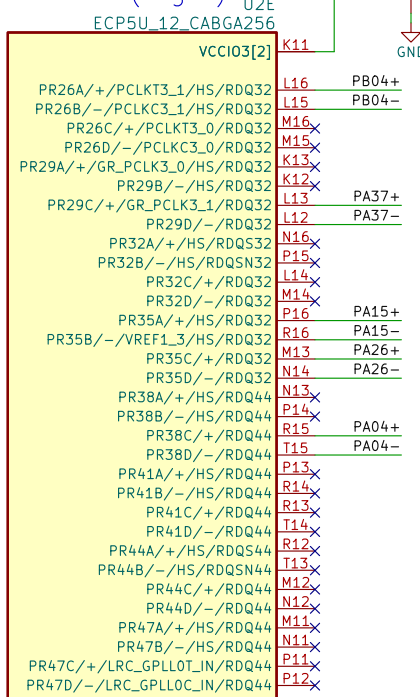
### Bank 1 (Top)



### Bank 7 (Left)



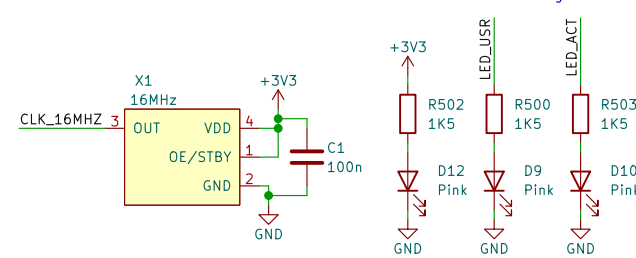
### Bank 3 (Right)



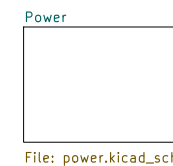
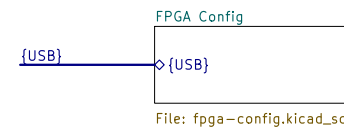
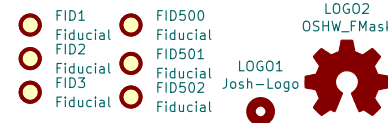
## Sub Sections

### Status LEDs

CDONE LED on config sheet



### Logos / Markings



LENGTH MATCHING:  
id: hr\_ck\_skew  
netnames: "/HR\_CK\_[PN]"  
tolerance: 0.254 mm  
viallength: 1.6 mm

LENGTH MATCHING:  
id: hr  
netnames: "/HR\_[DQ|RWDS|CK]"  
tolerance: 1.27 mm  
viallength: 1.6 mm

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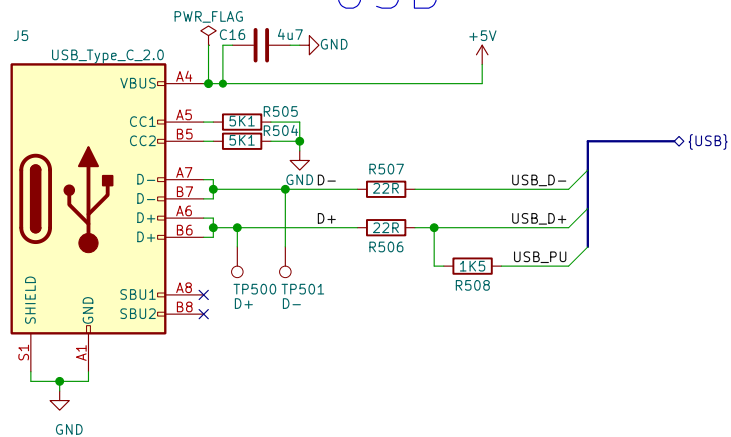
Sheet: /  
File: ecp5-mini.kicad\_sch

Title: ECP5 Mini

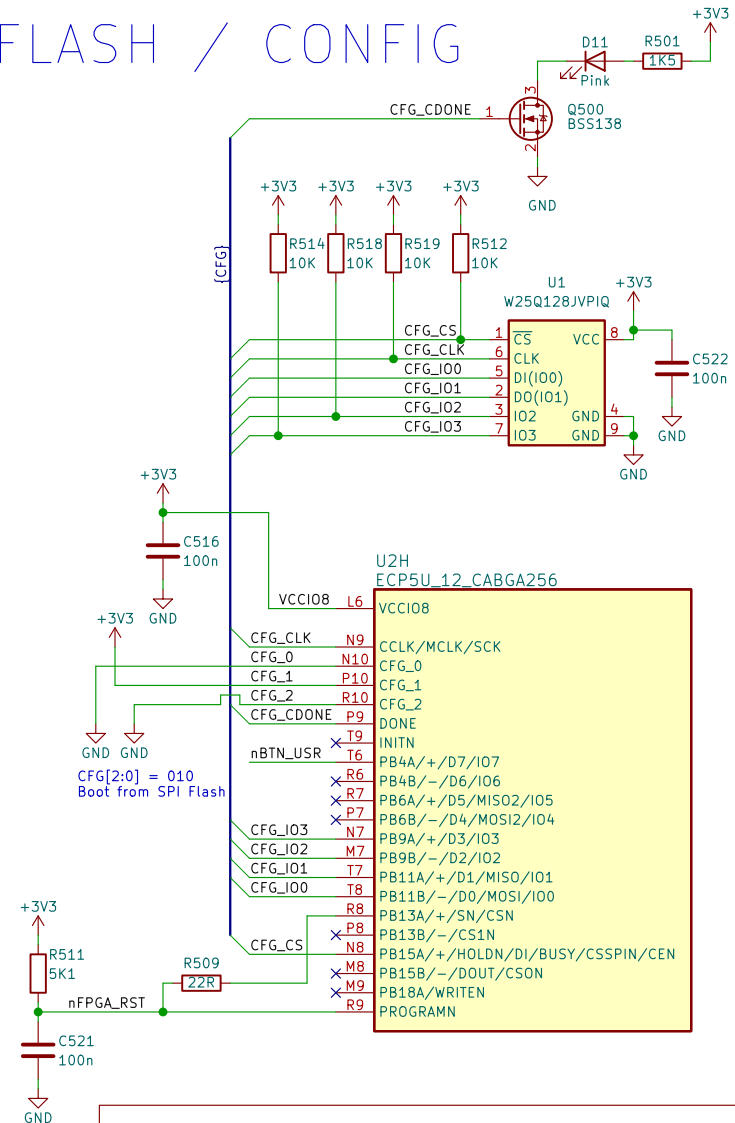
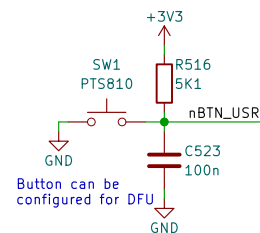
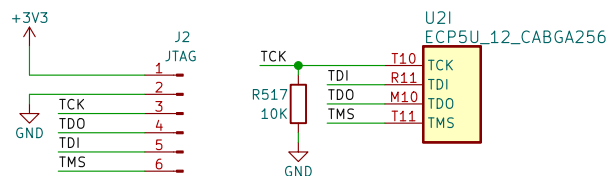
Size: A3 Date: 2020-12-31  
KiCad E.D.A. kicad 5.99.0-unknown-bbe2ee39a0117ubuntu20.04.1

Rev: 0.2  
Id: 1/3

## FLASH / CONFIG



## JTAG



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Sheet: /FPGA Config/

File: fpga-config.kicad\_sch

**Title:** ECP5 Mini

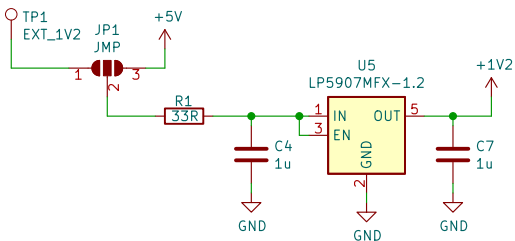
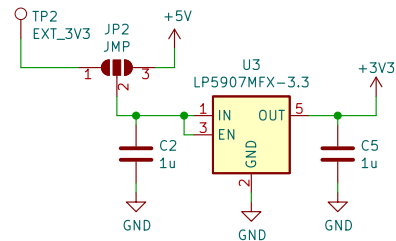
Size: A4	Date: 2020-12-31
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KiCad E.D.A.	5.99.0-unknown-bbe2ee39a0117ubuntu20.04.1
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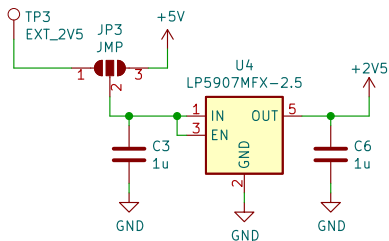
Rev: 0.2

Id: 2/3

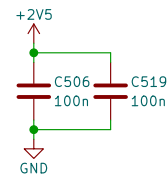
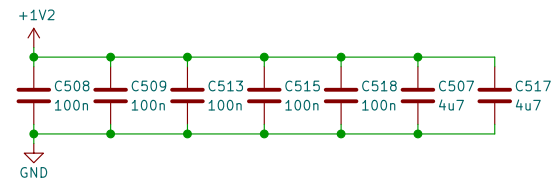
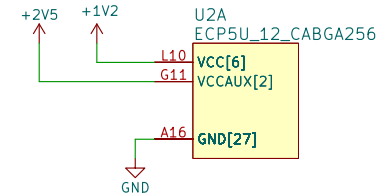
# Power Supply



NOTE: 1V2 VCORE instead of 1V1 as specified for non 5G parts!



# VCore



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Sheet: /Power/  
File: power.kicad\_sch

Title: ECP5 Mini

Size: A4 Date: 2020-12-31

KiCad E.D.A. kicad 5.99.0-unknown-bbe2ee39a0117ubuntu20.04.1

Rev: 0.2

Id: 5/3