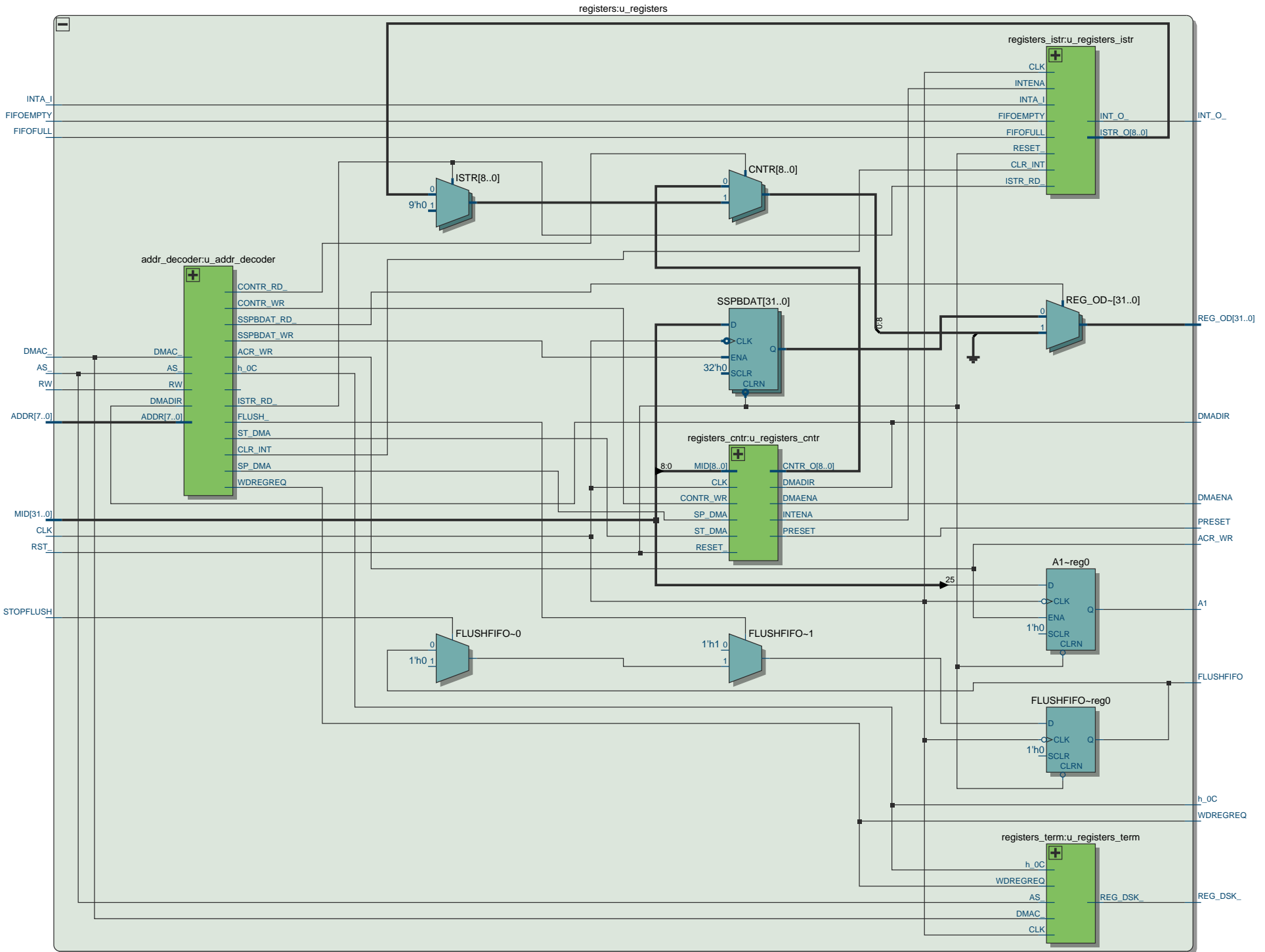
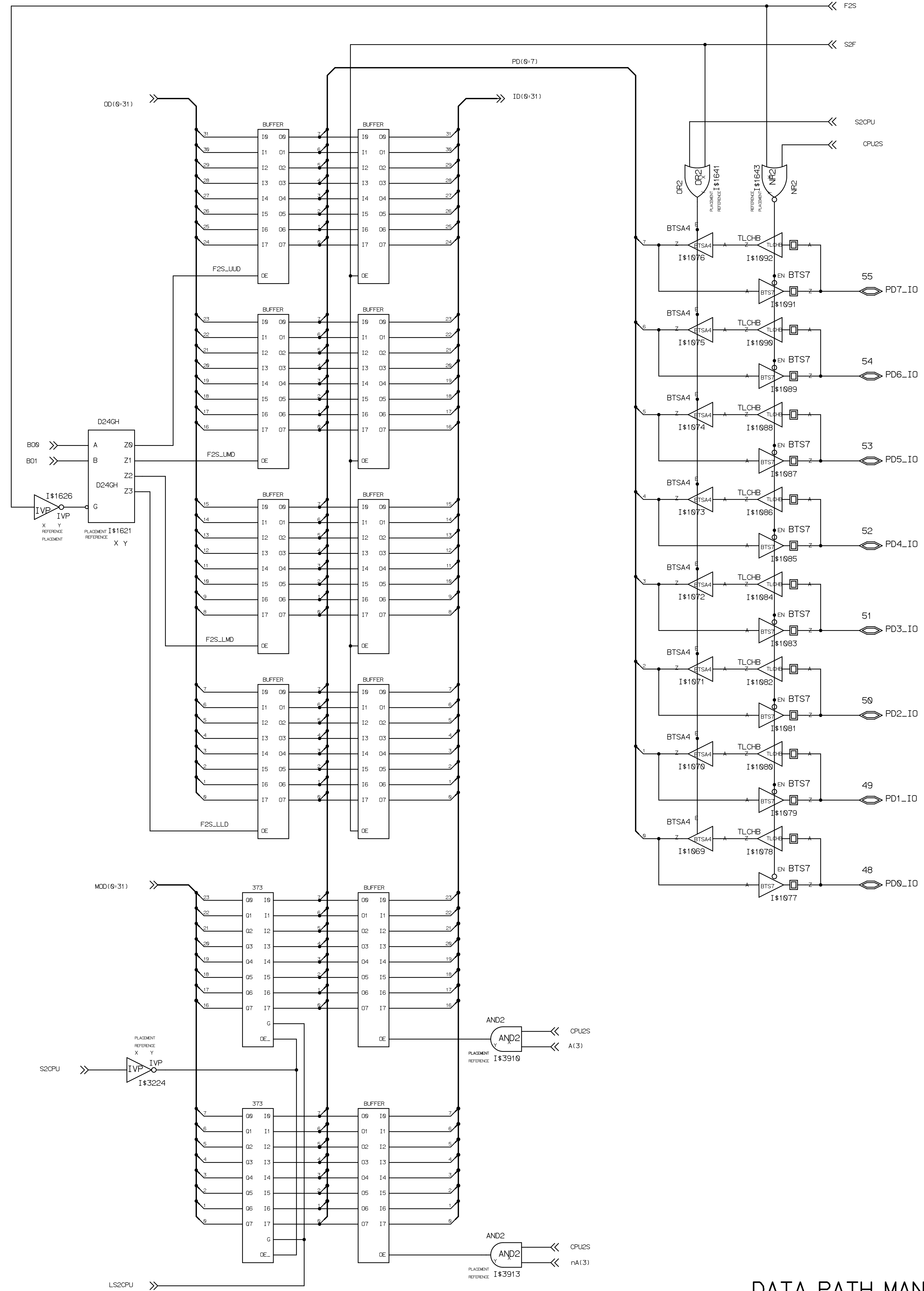
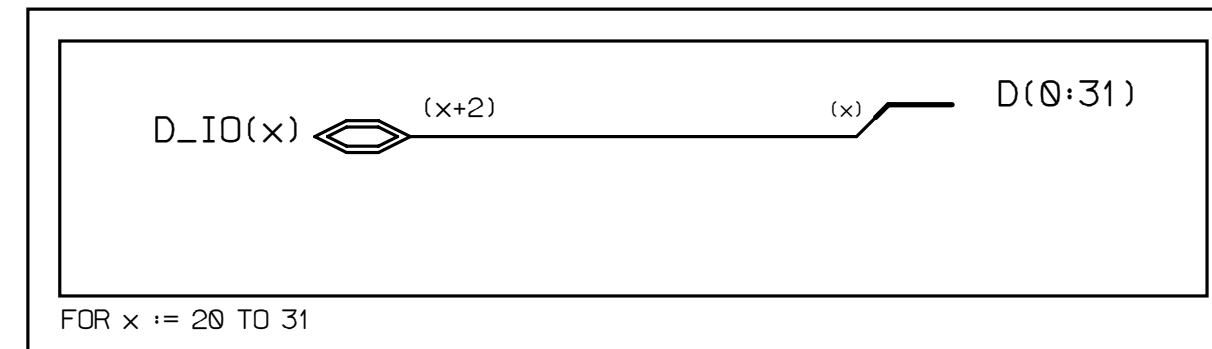
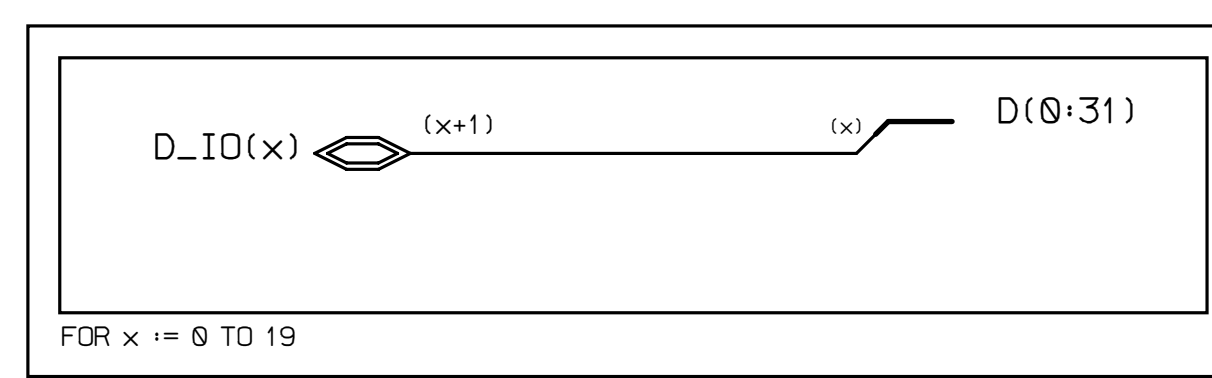
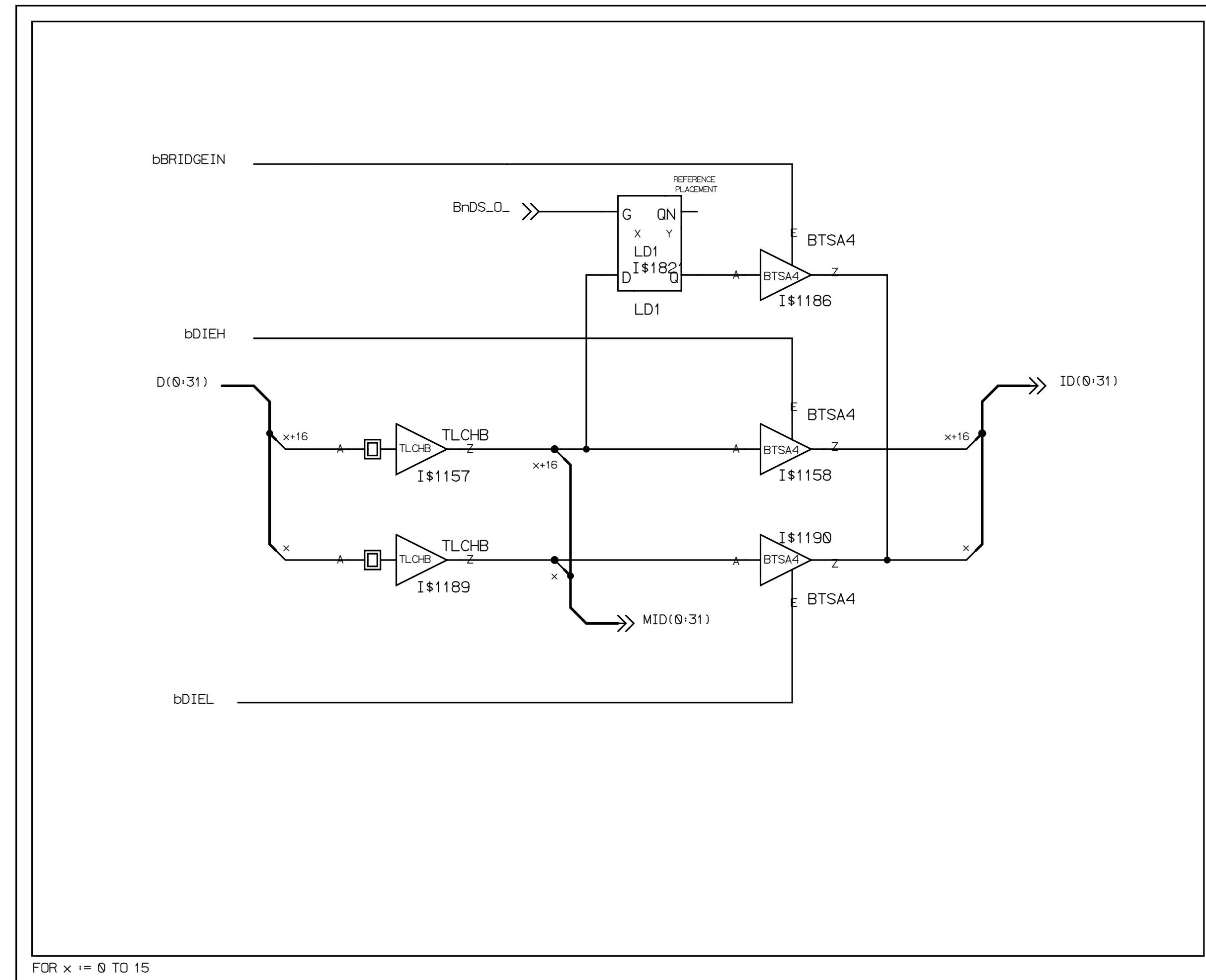
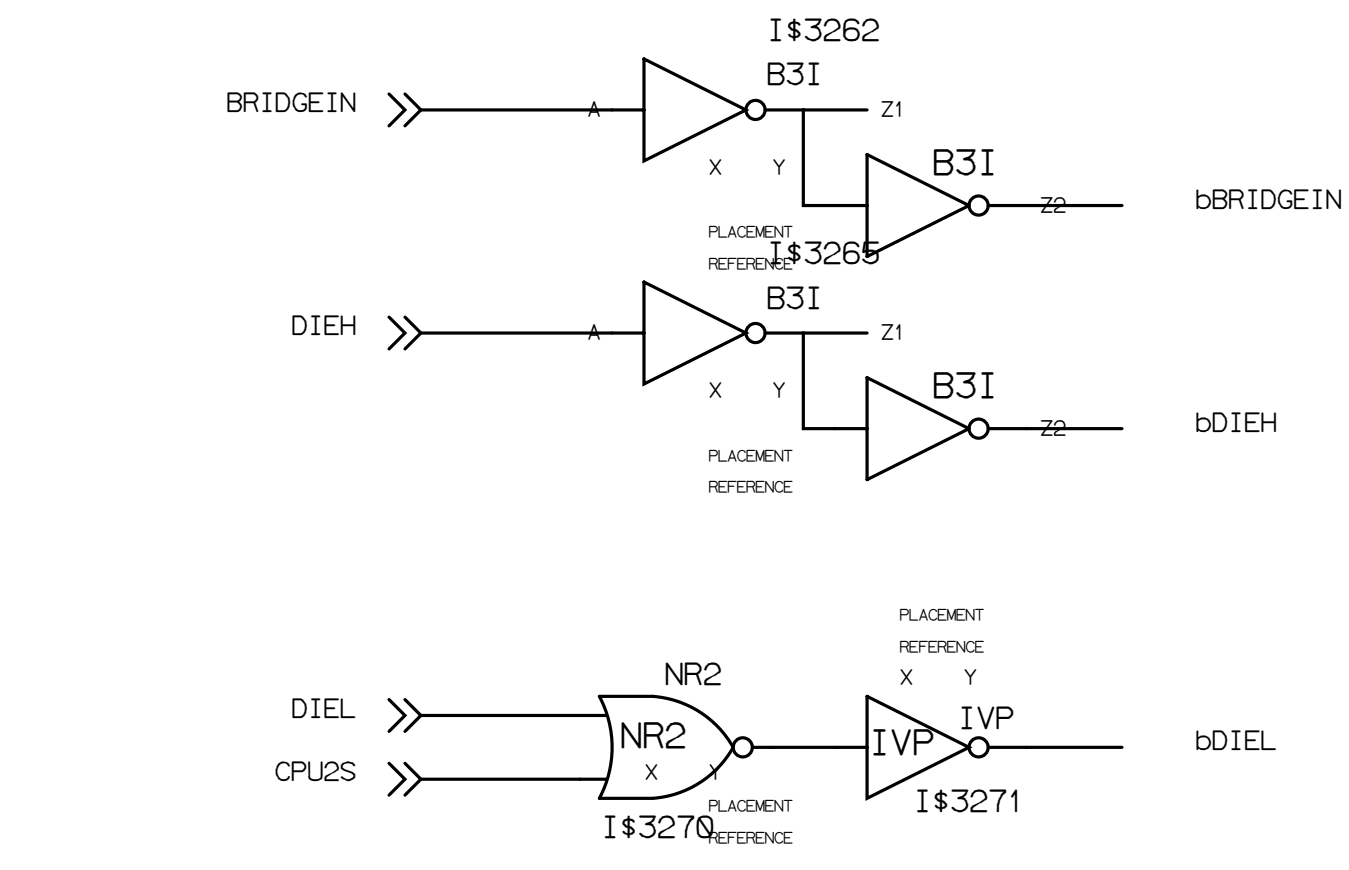
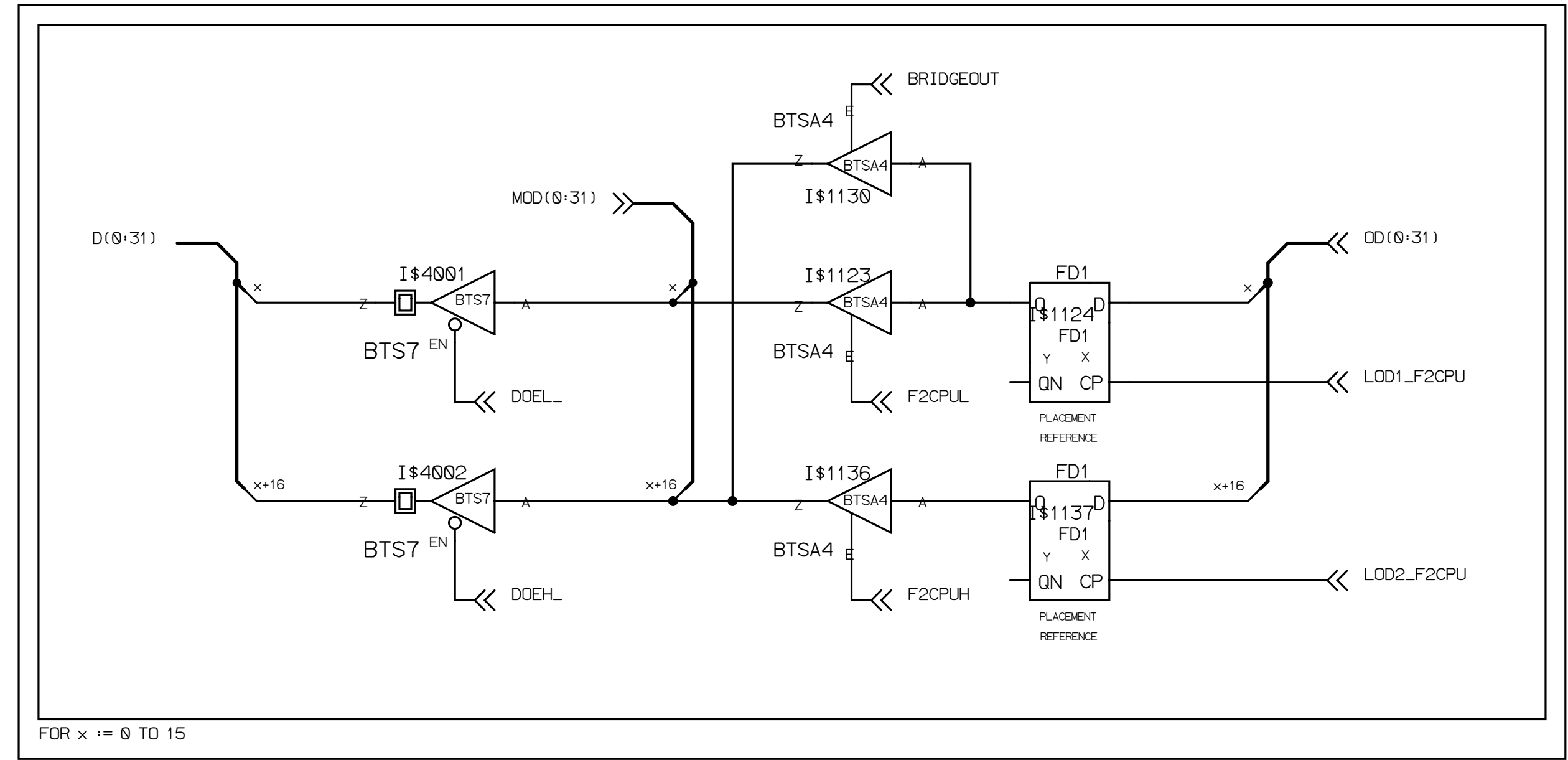
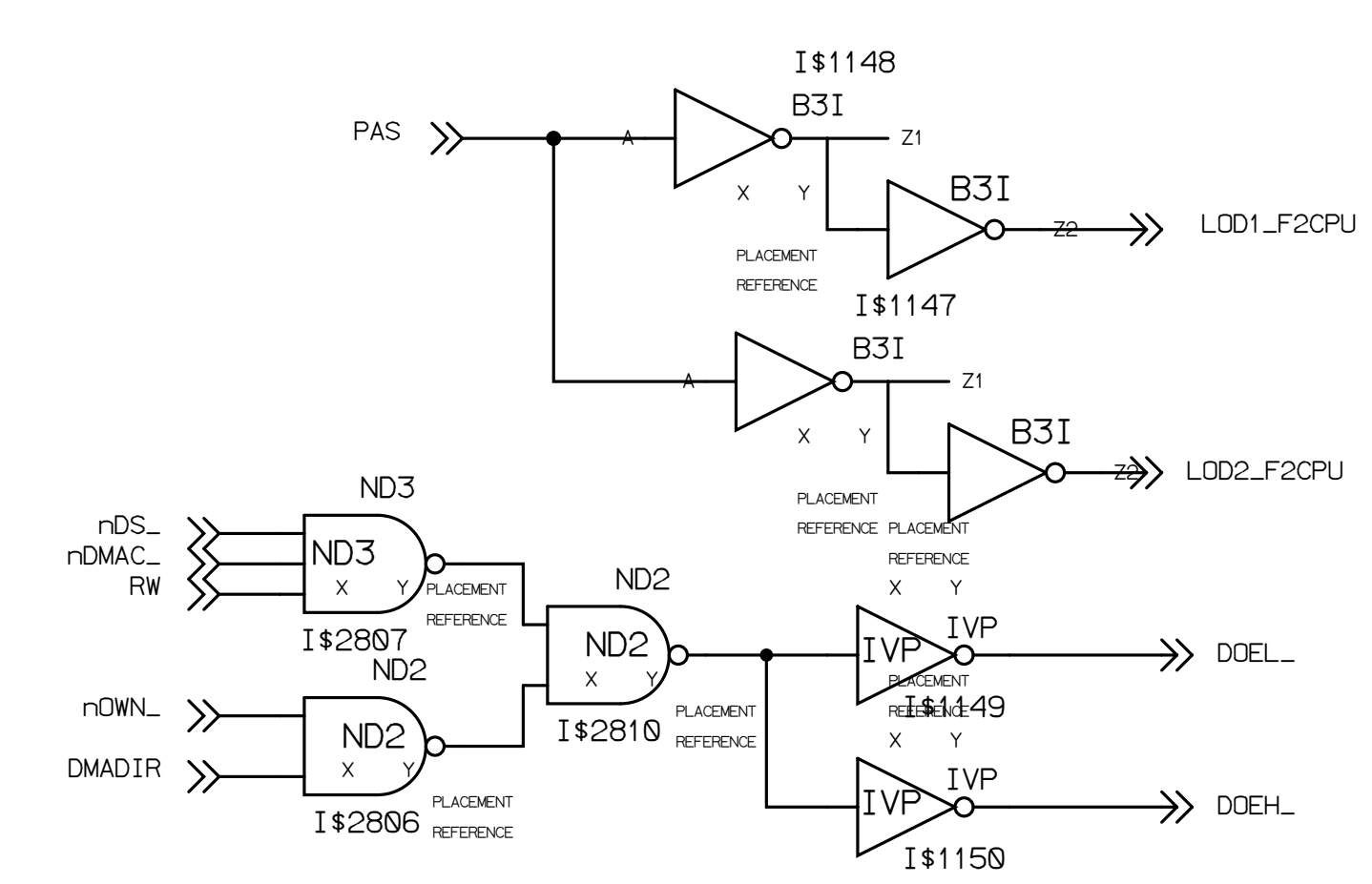
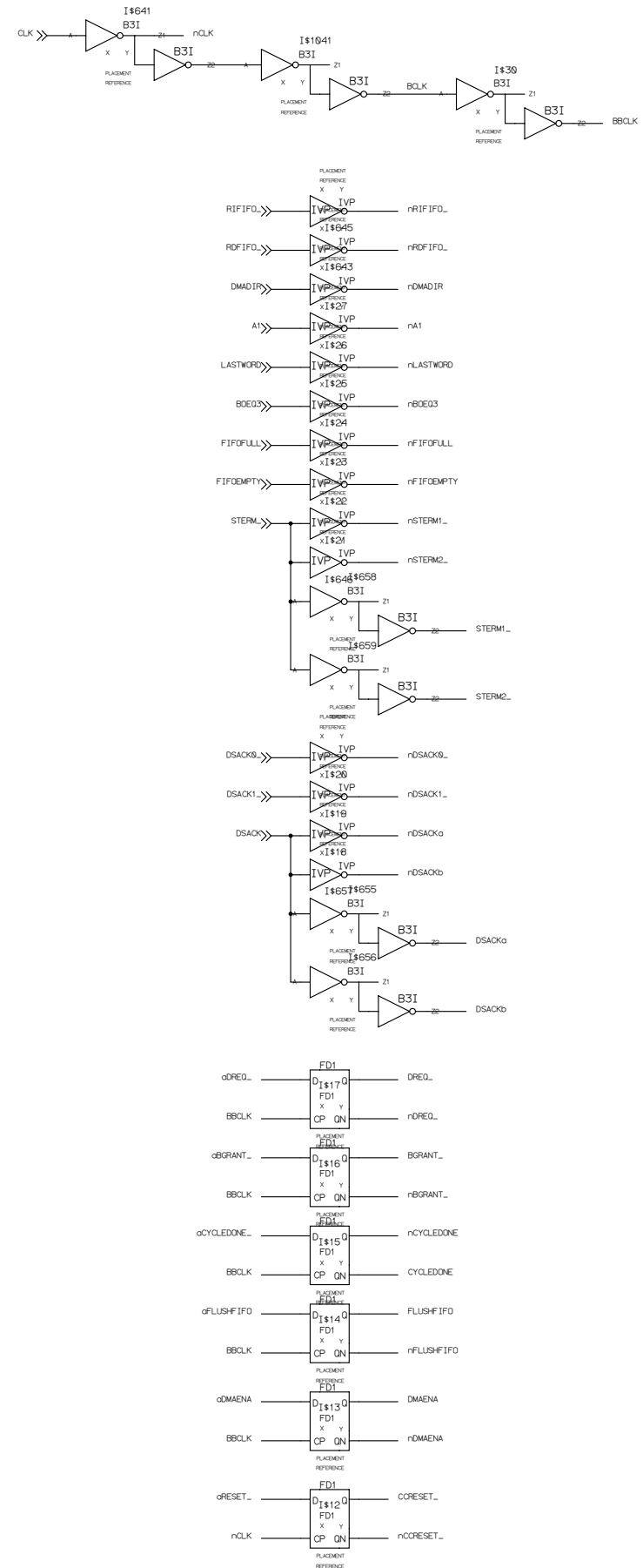
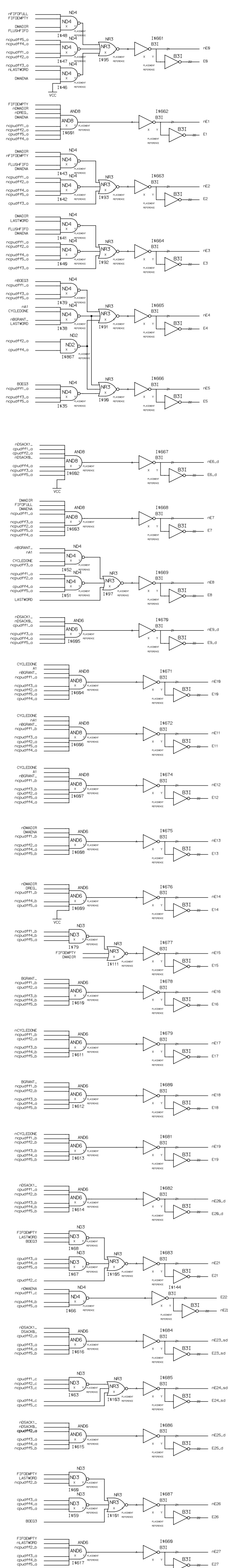
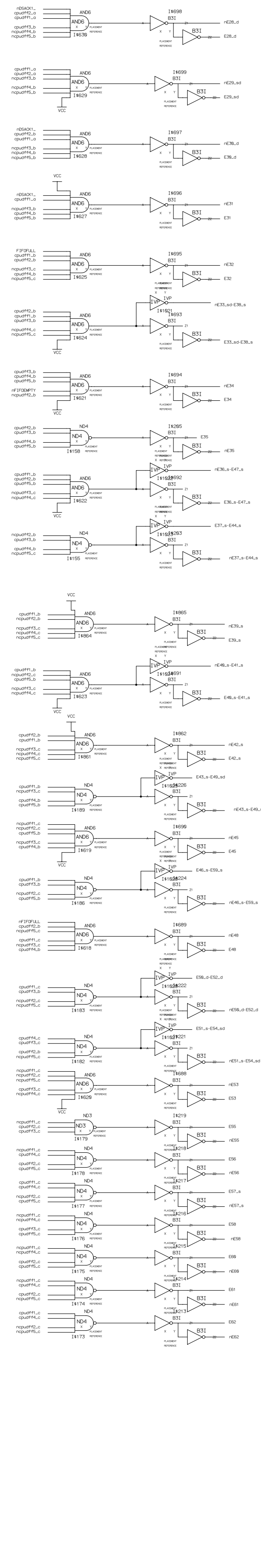
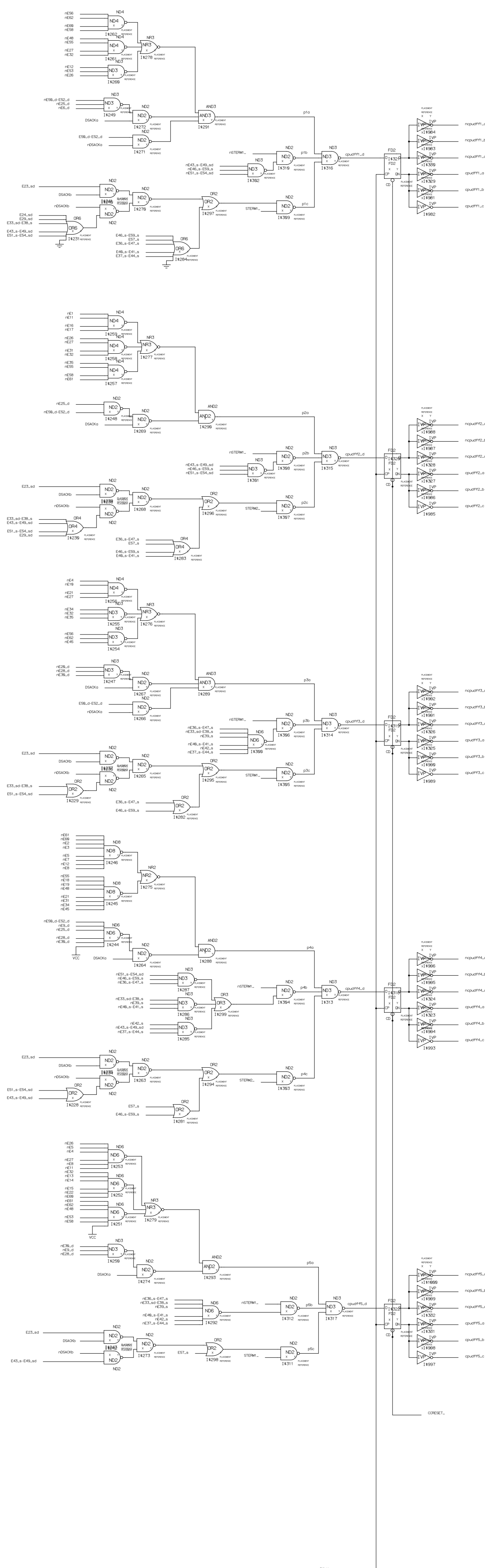
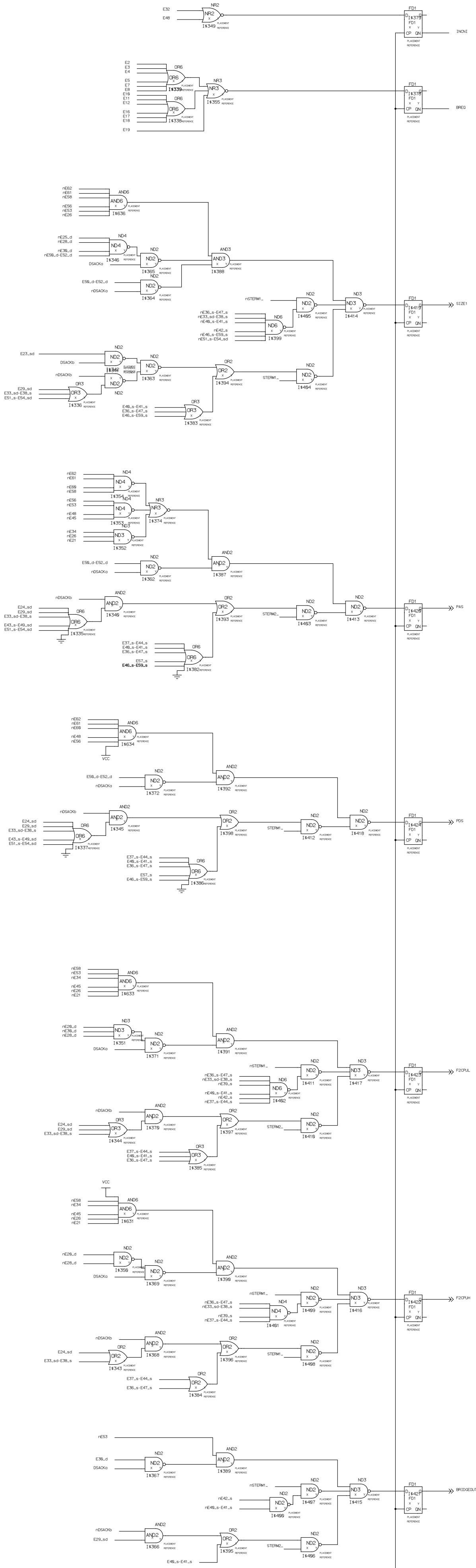
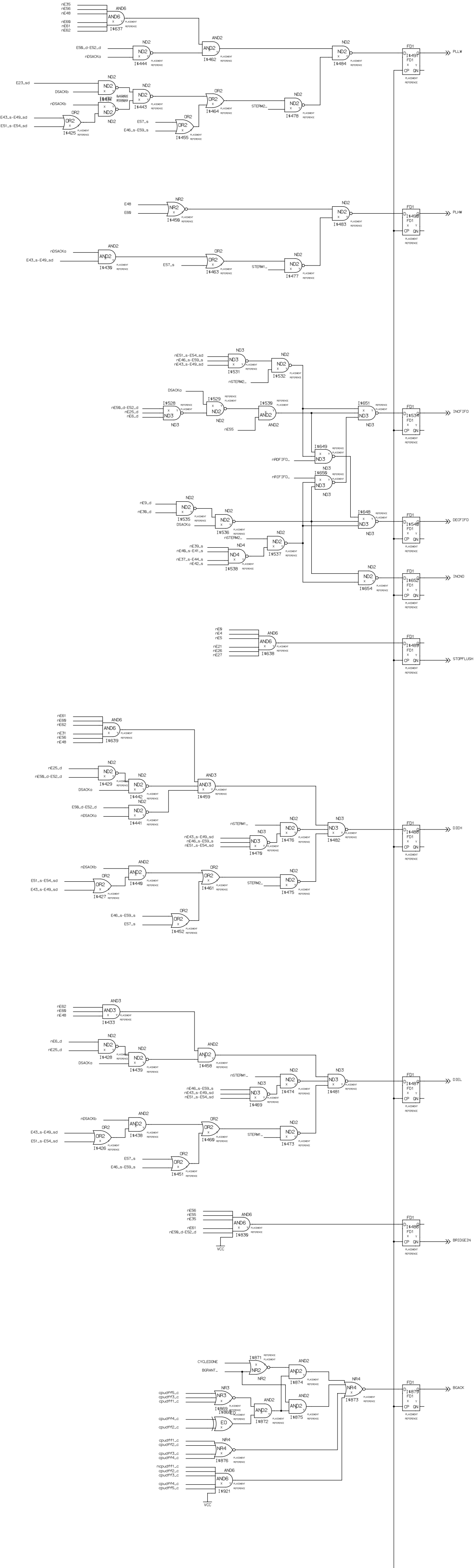
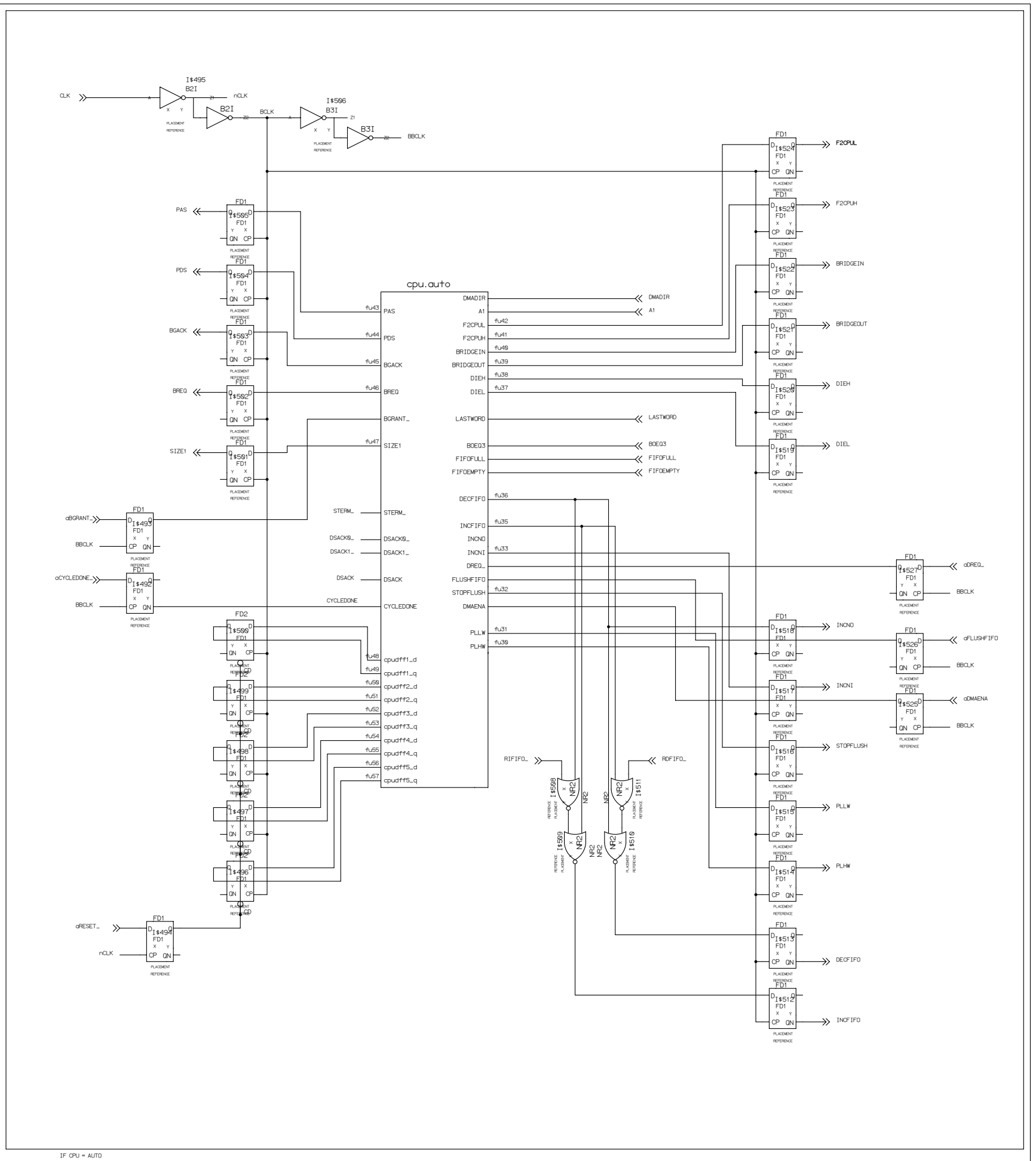


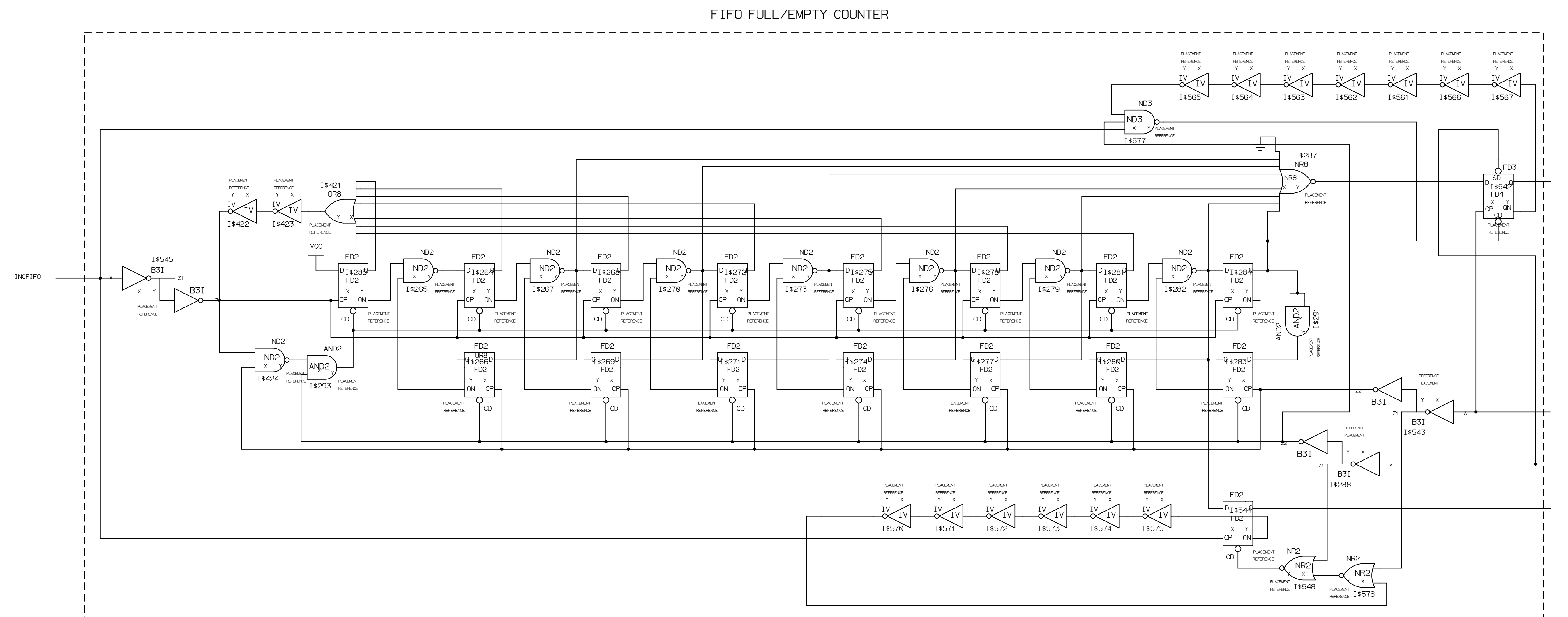
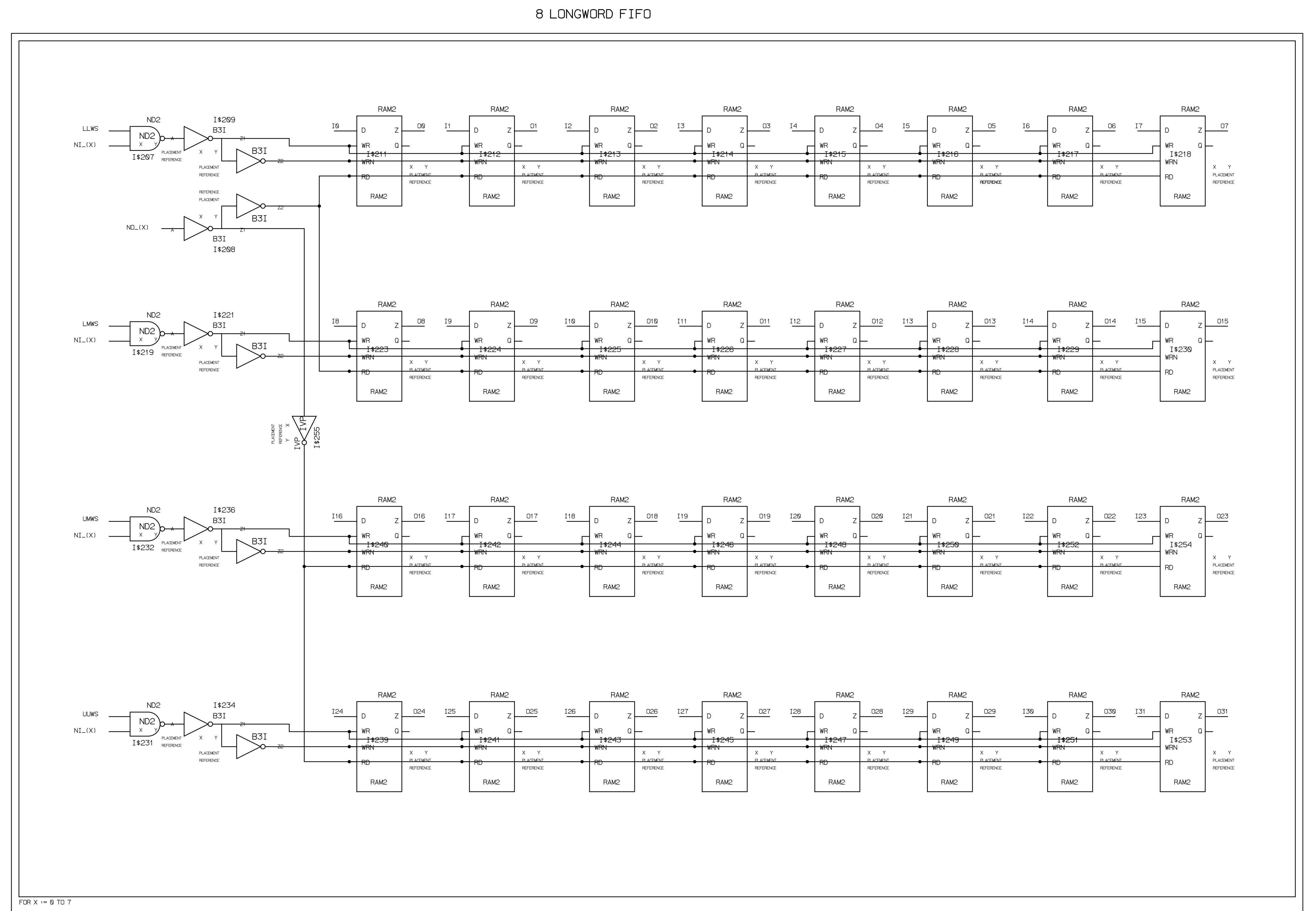
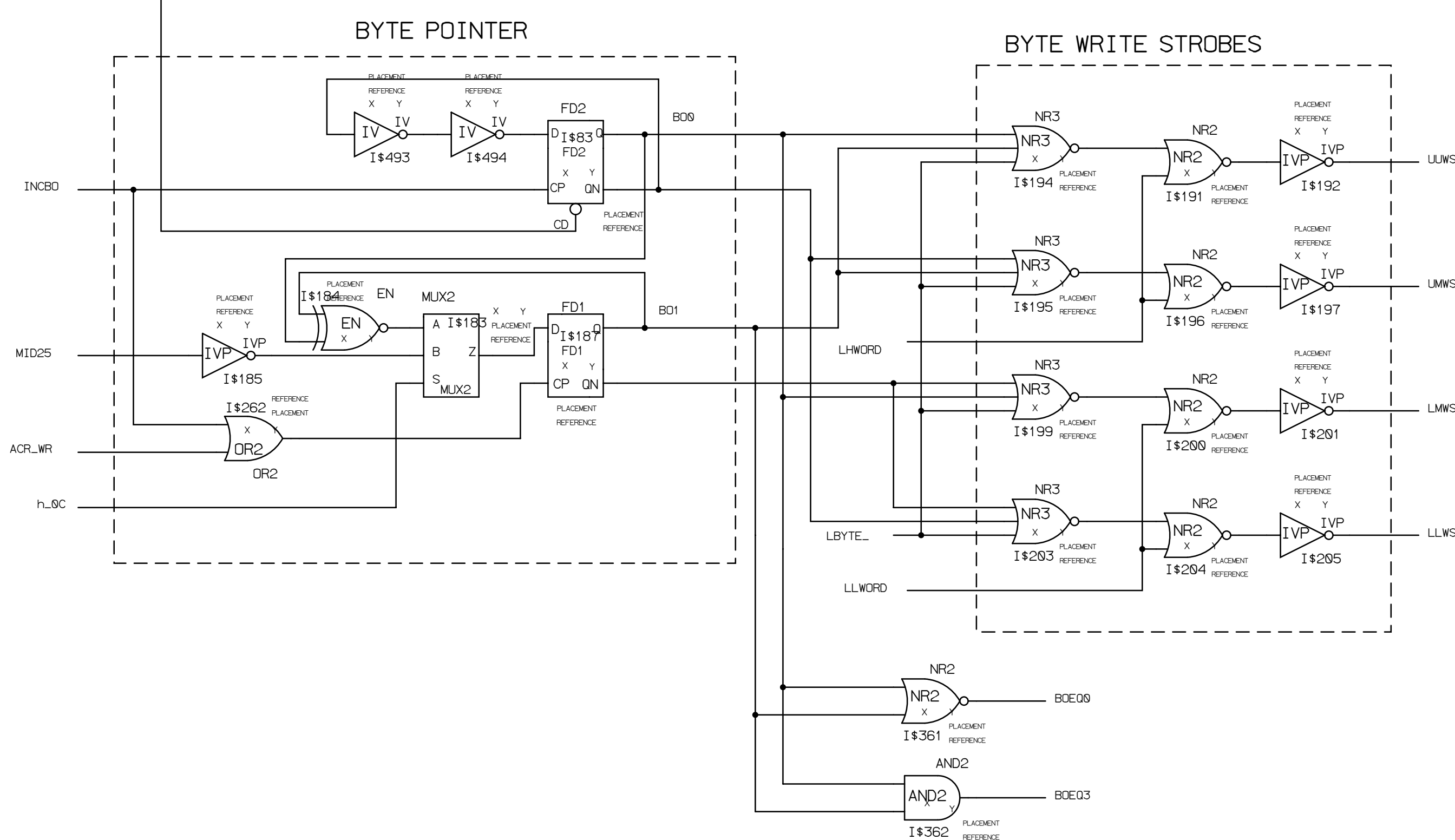
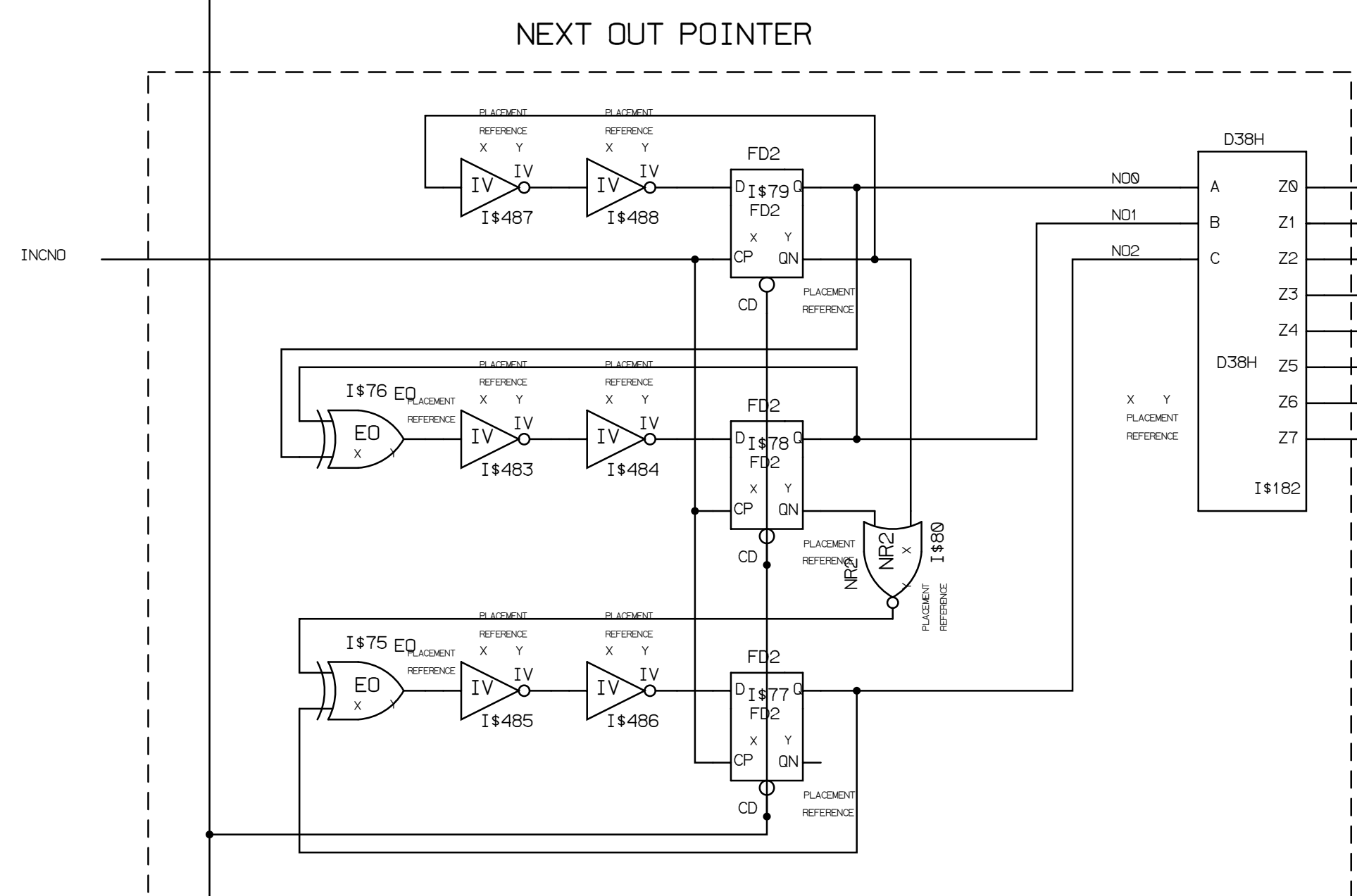
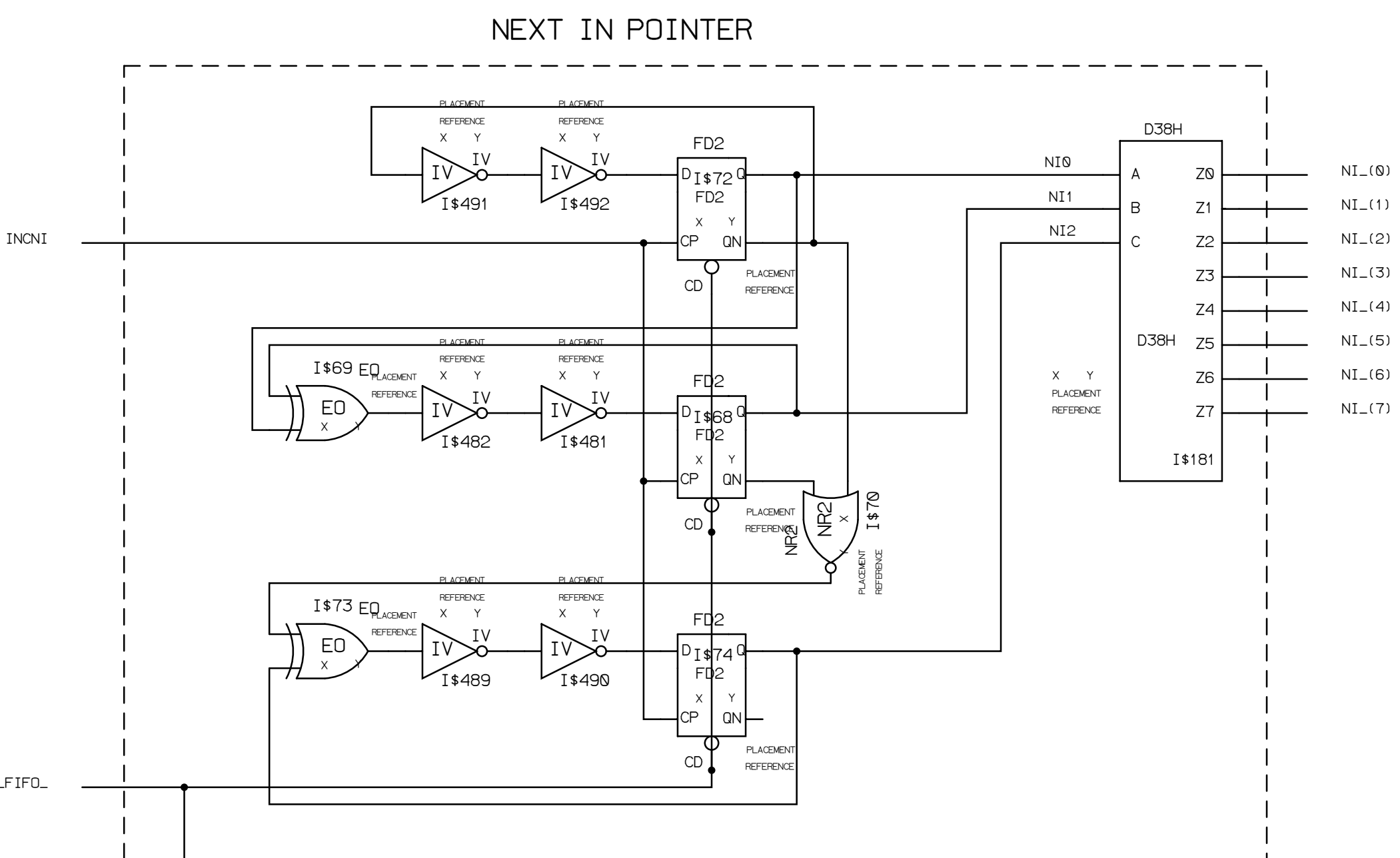
CPU Interface State Machine,
SCSI Controller Interface State Machine
& FIFO.



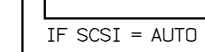
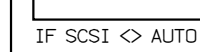


DATA PATH MANAGEMENT

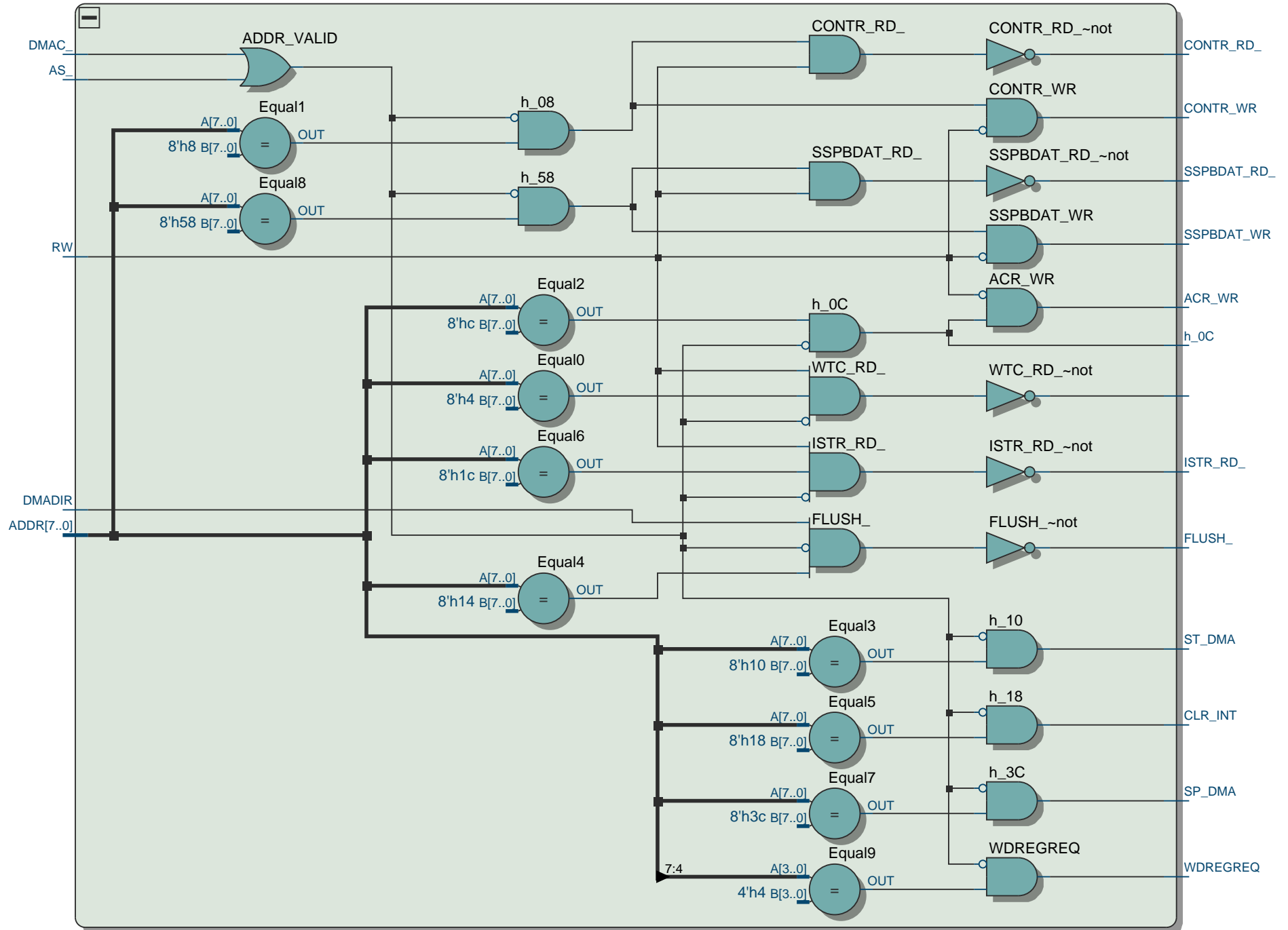




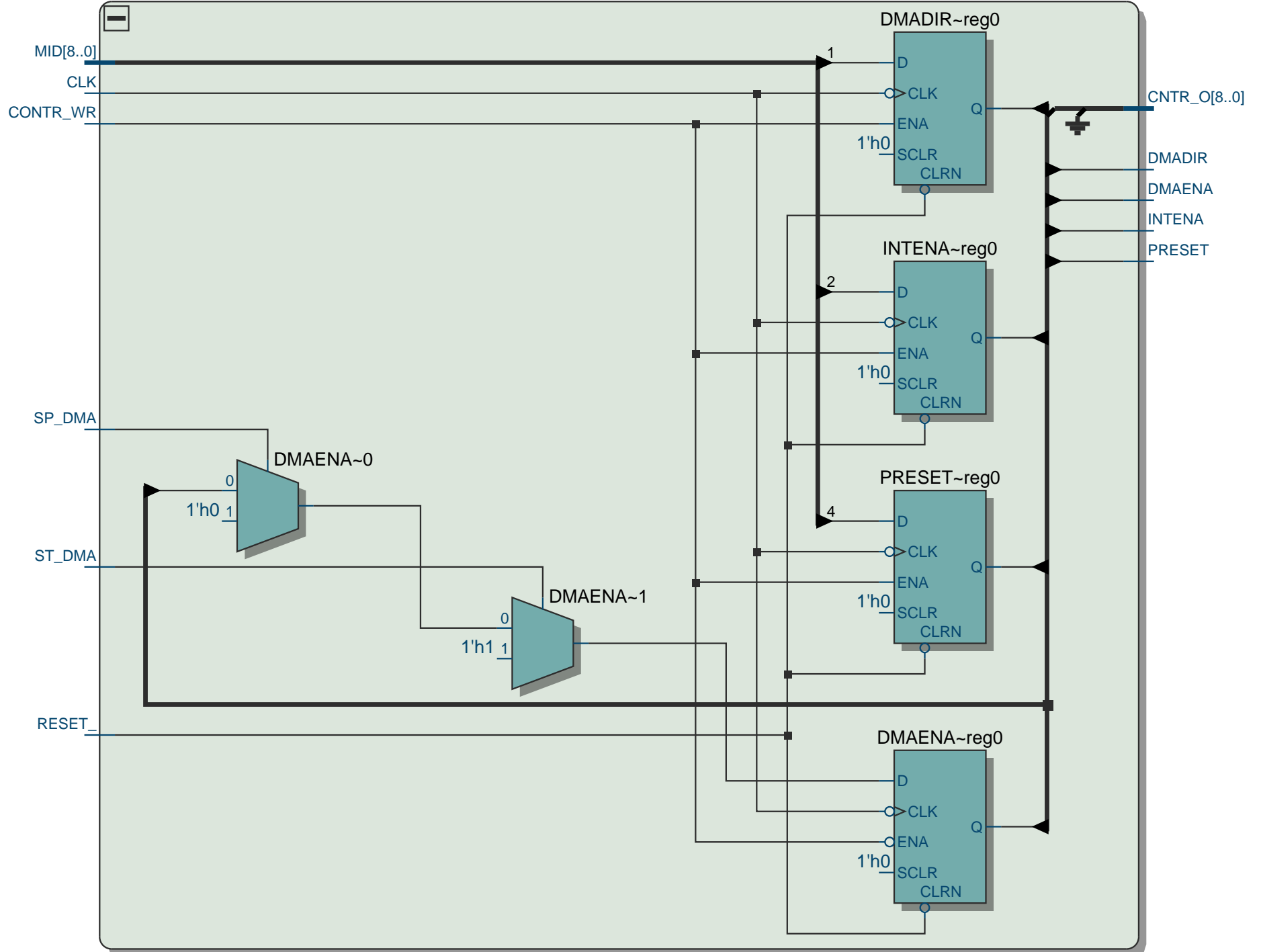
FIFO LOGIC BLOCK

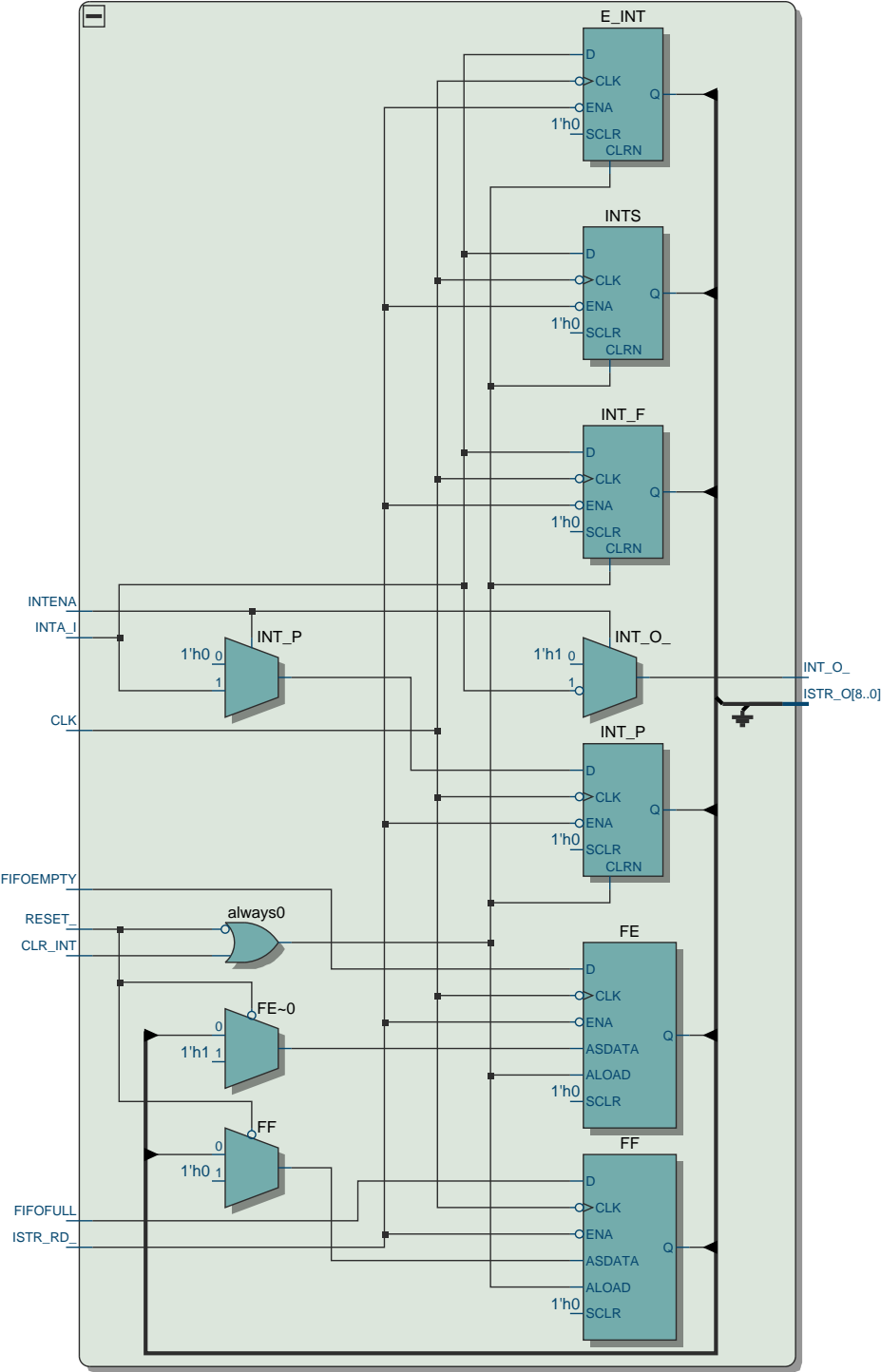


addr_decoder:u_addr_decoder



registers_cntr:u_registers_cntr





registers_term:u_registers_term

