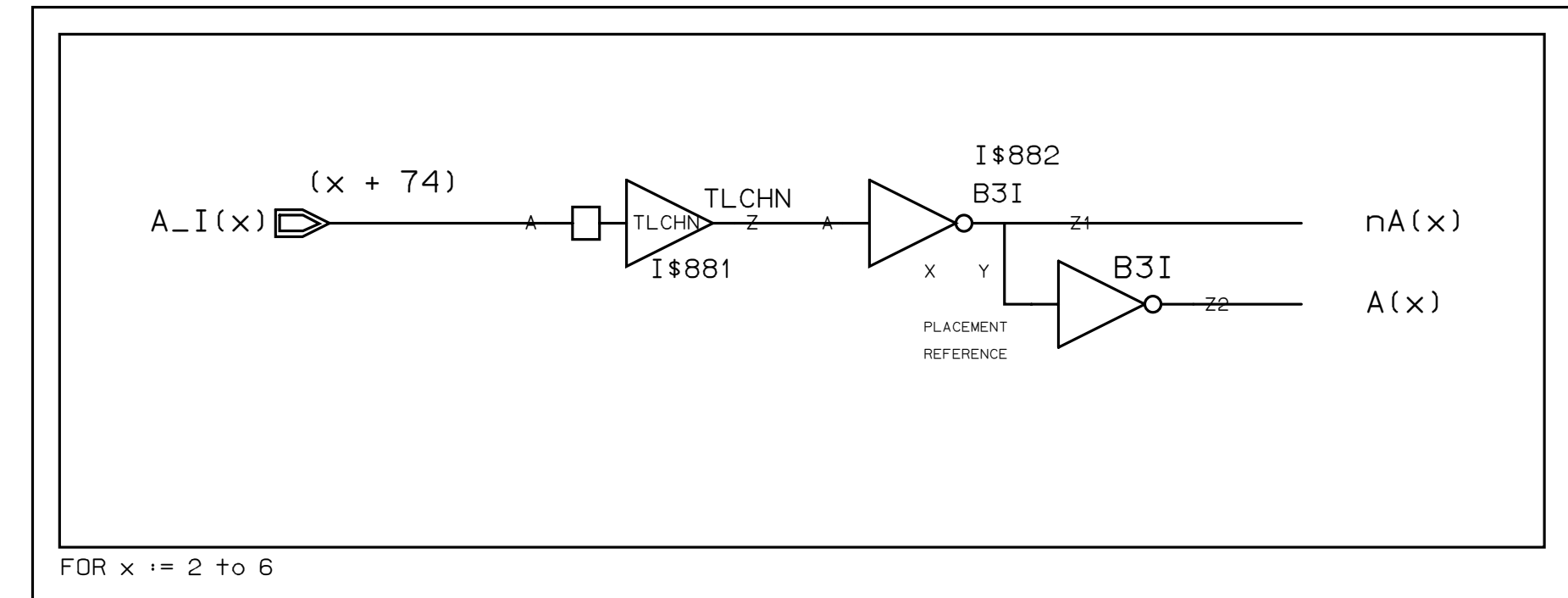
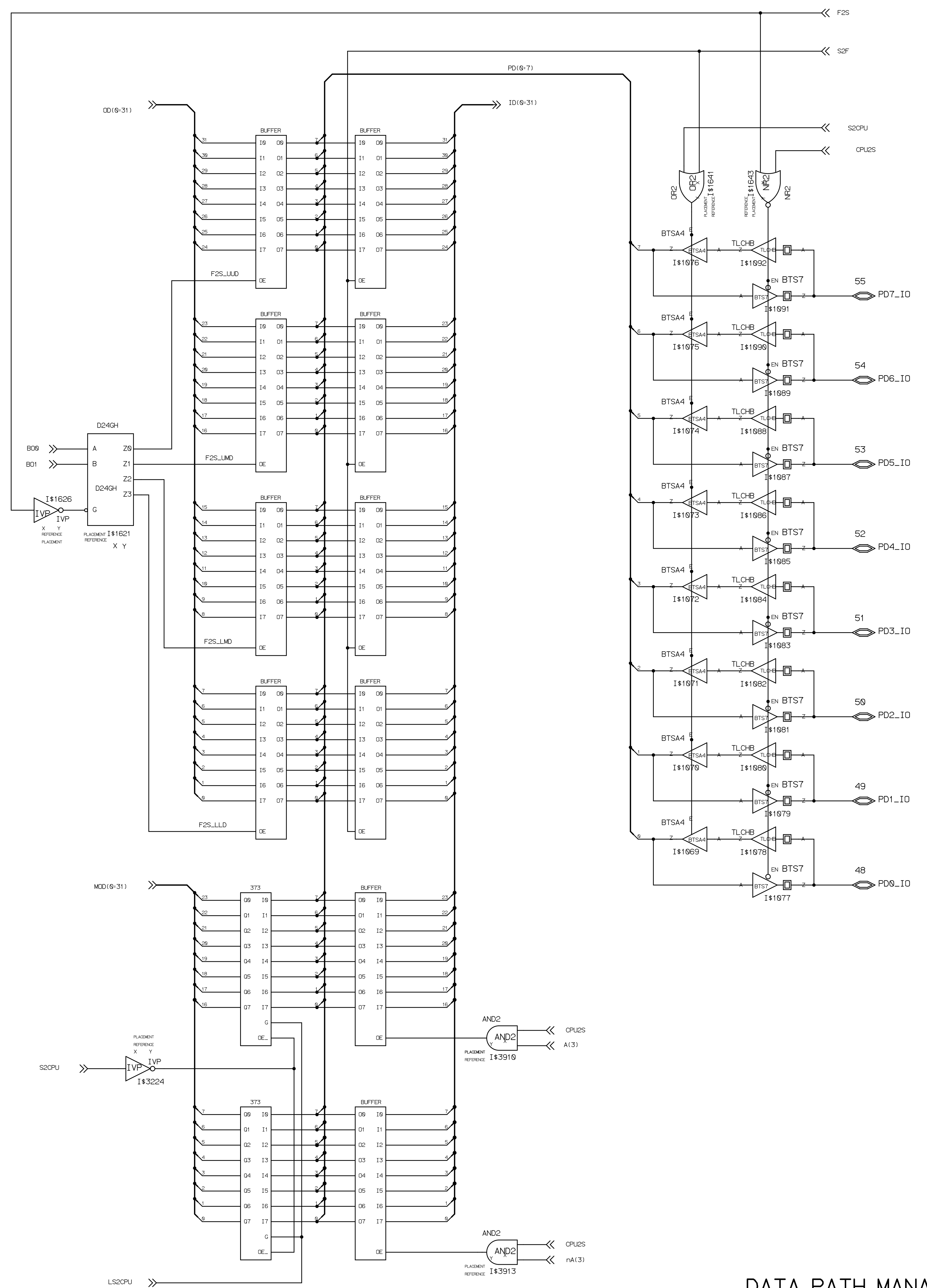
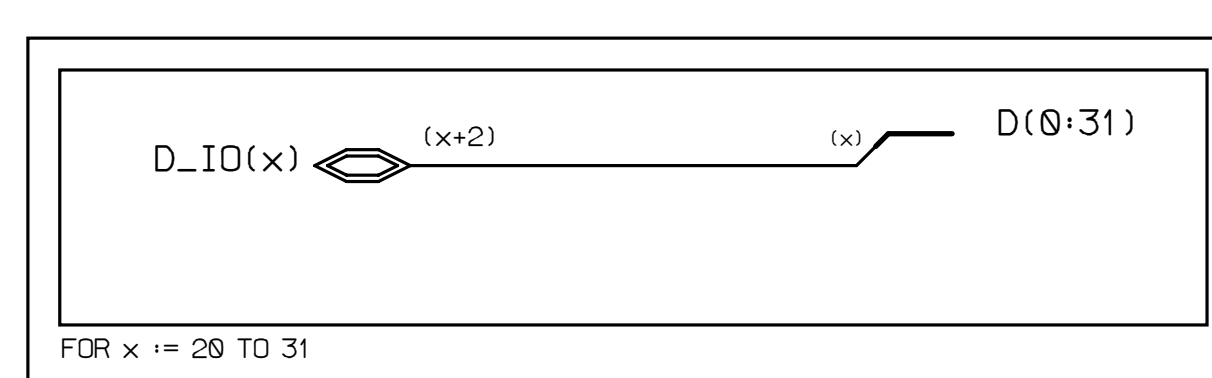
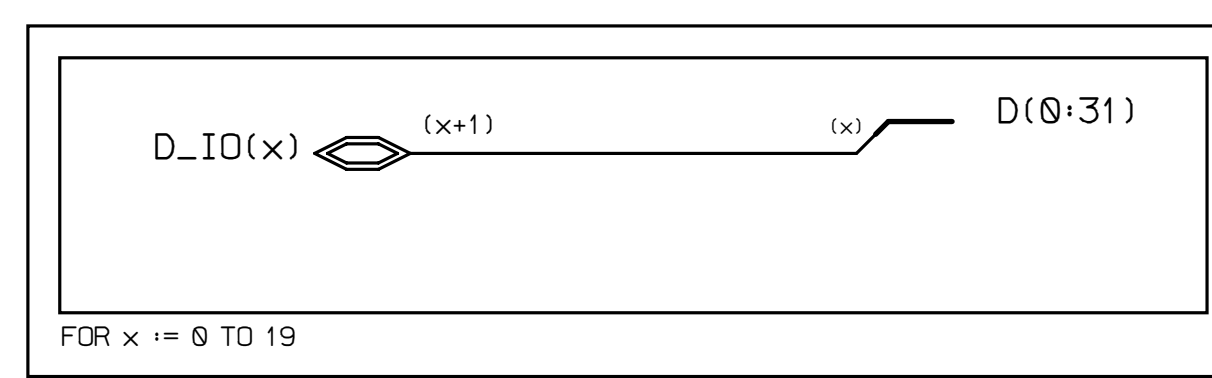
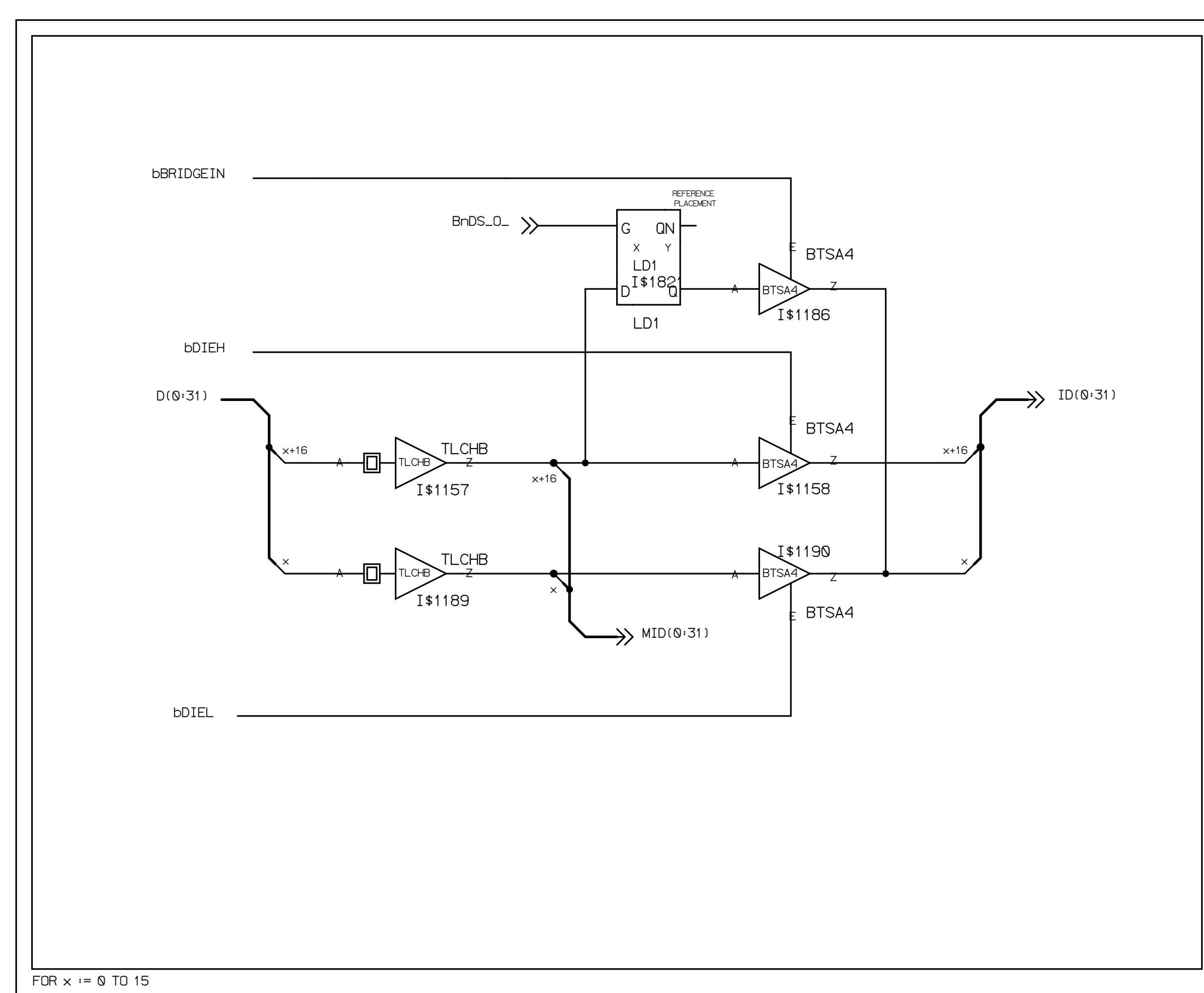
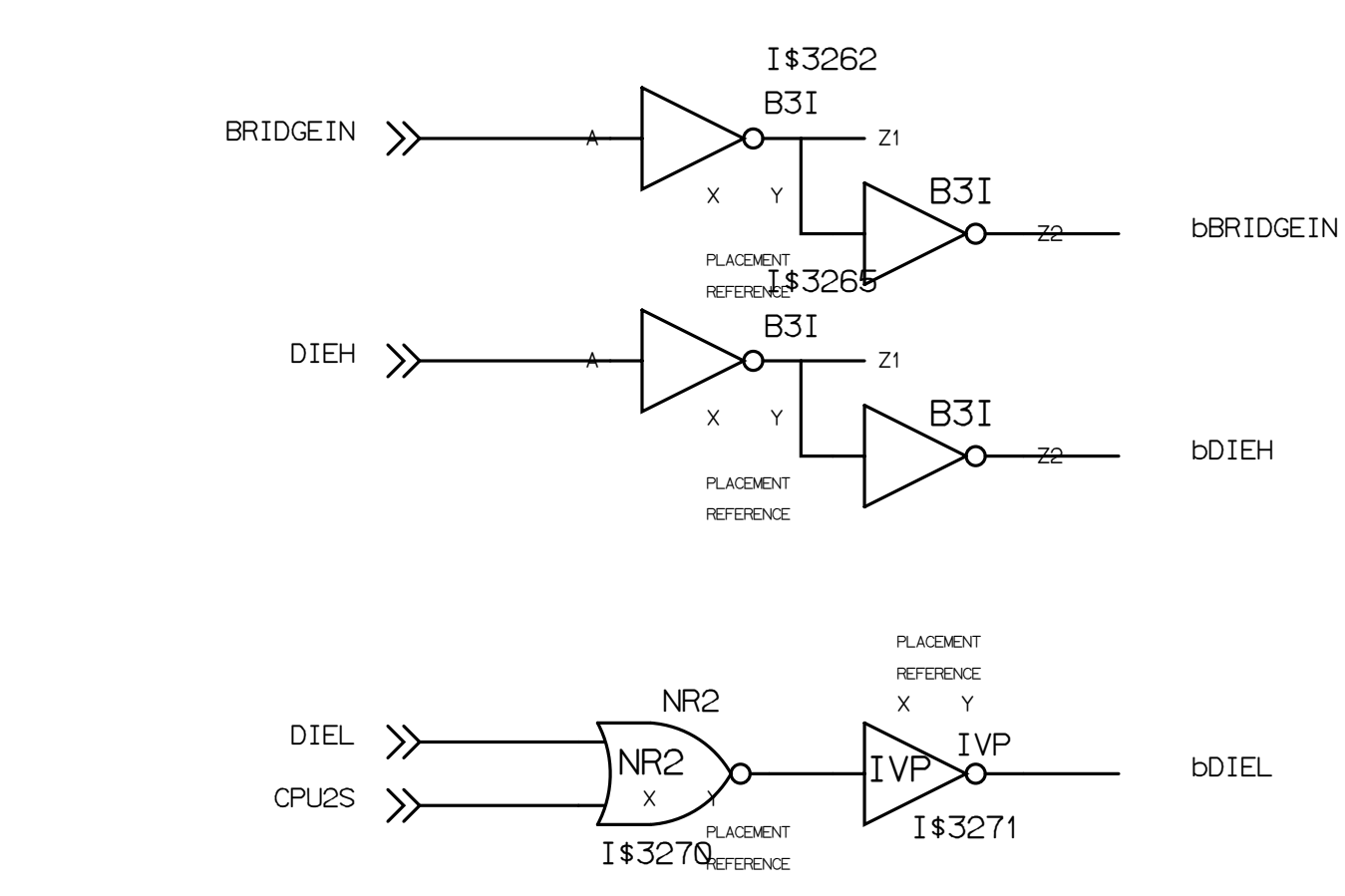
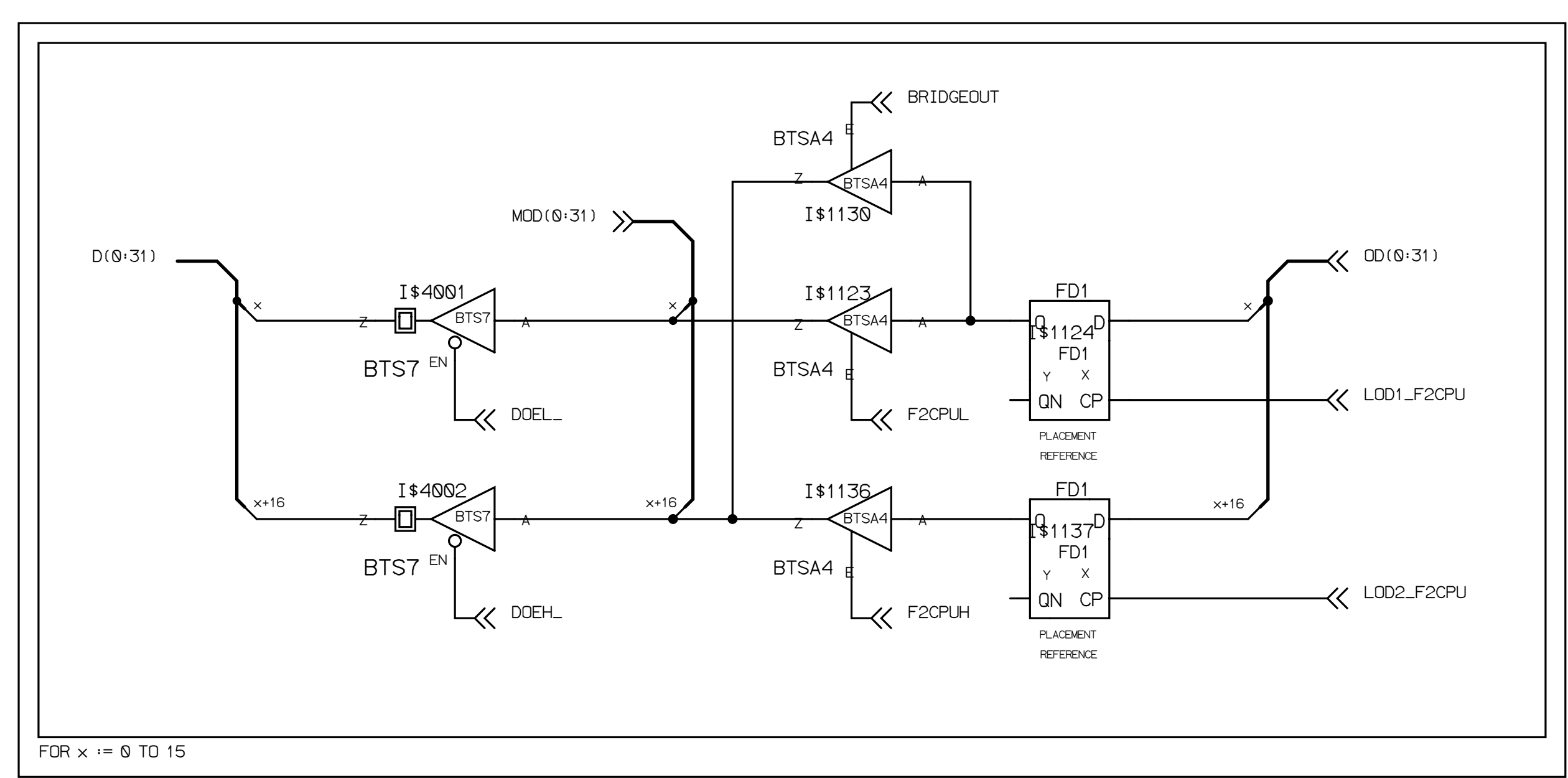
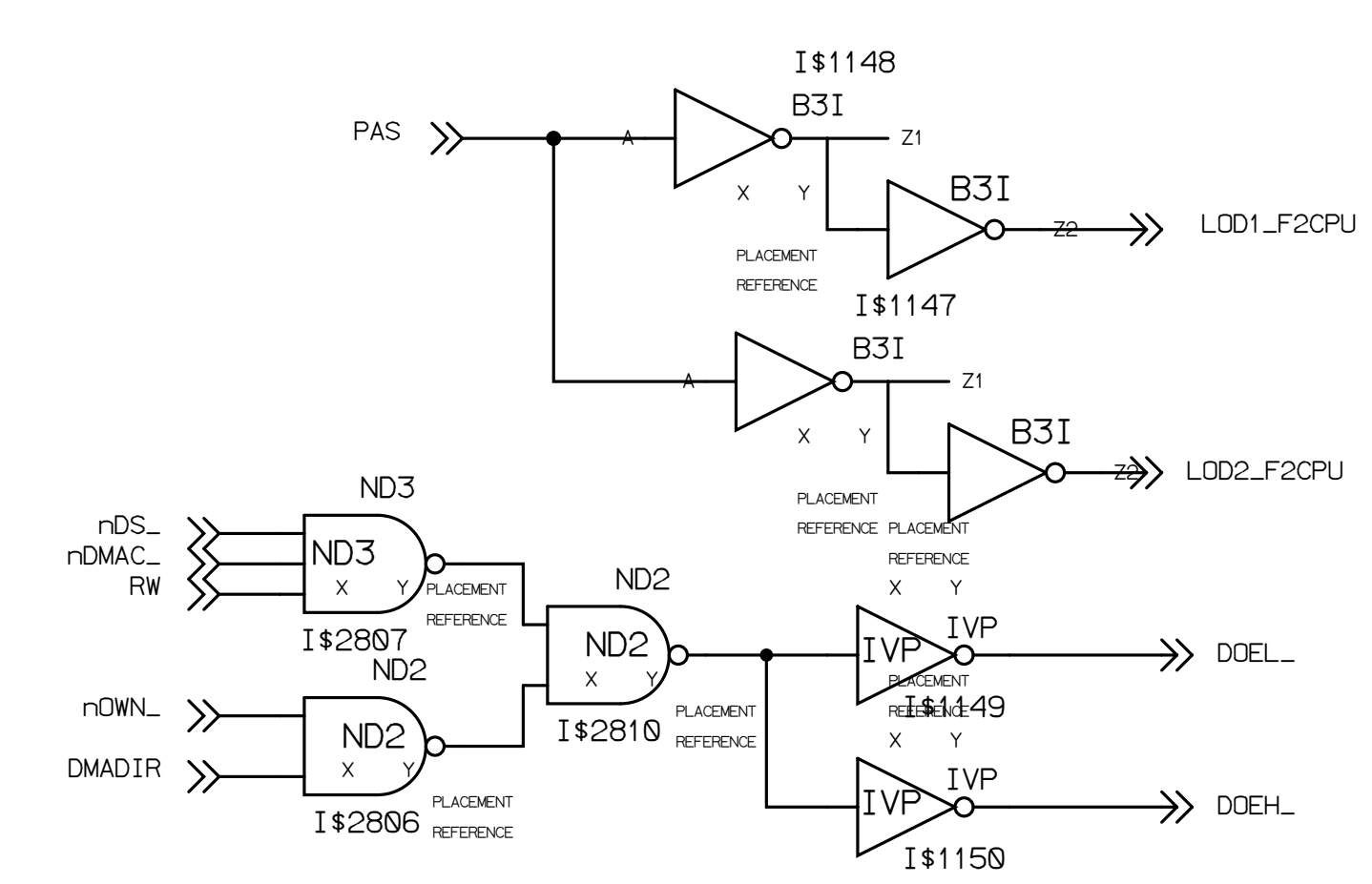


CPU Interface State Machine,  
SCSI Controller Interface State Machine,  
& FIFO.

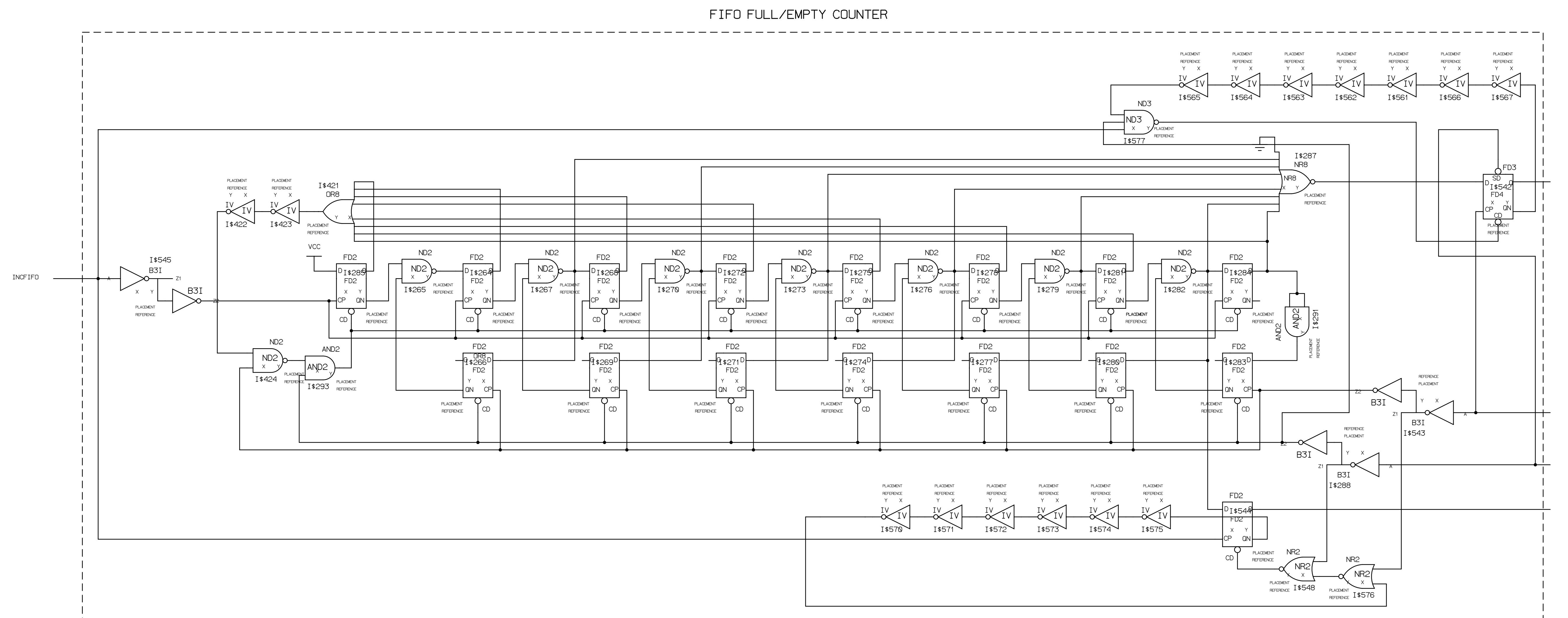
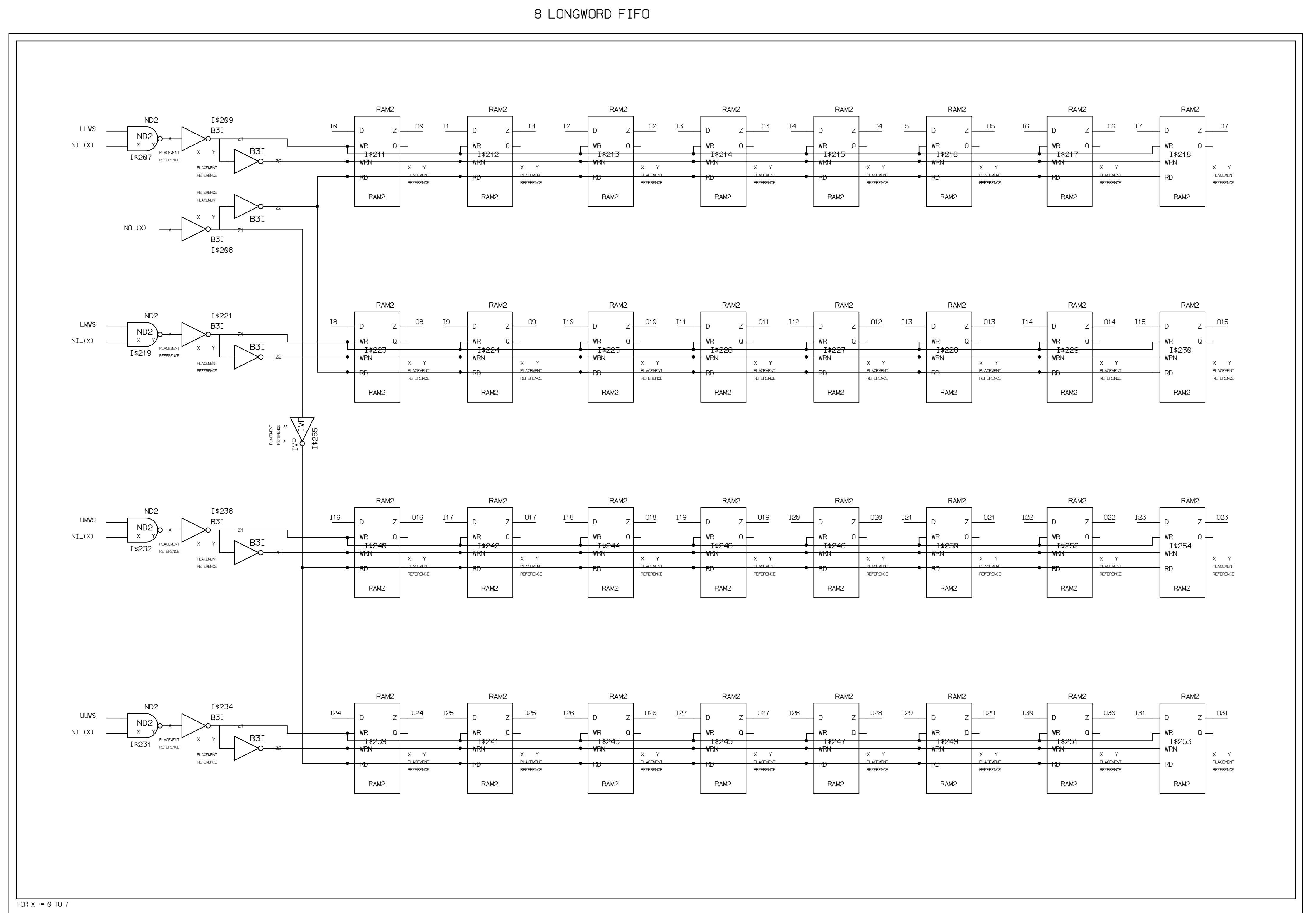
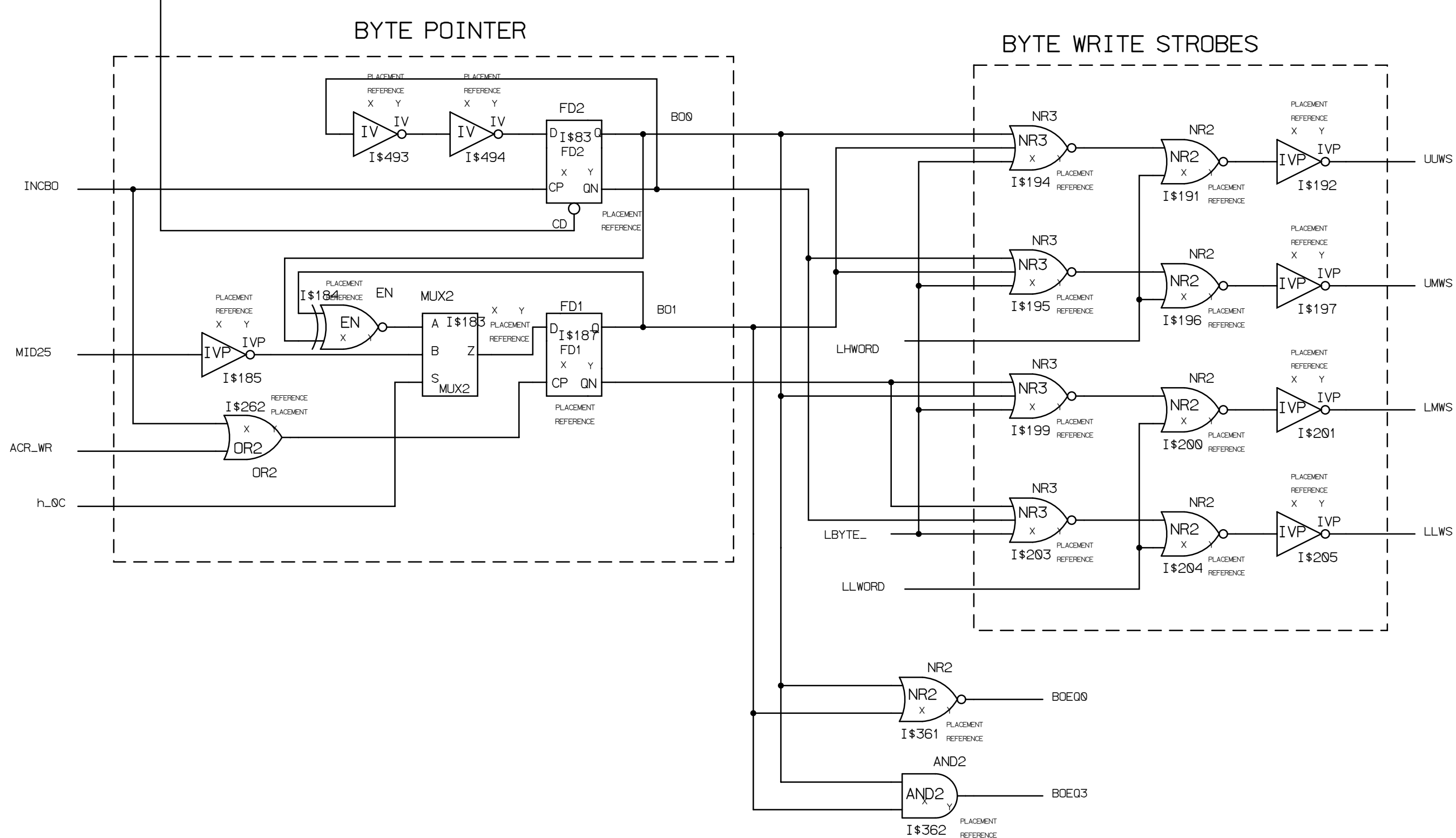
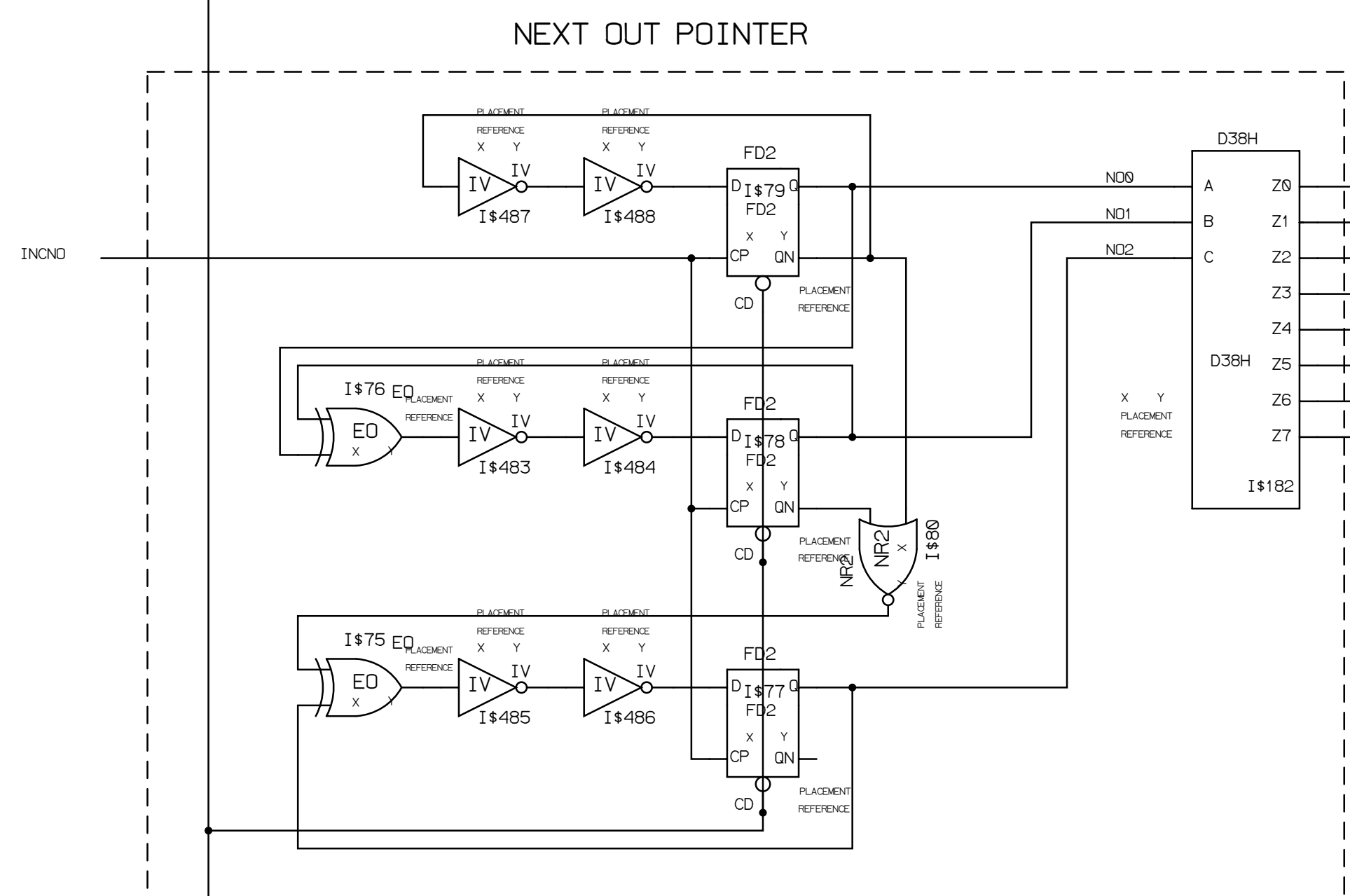
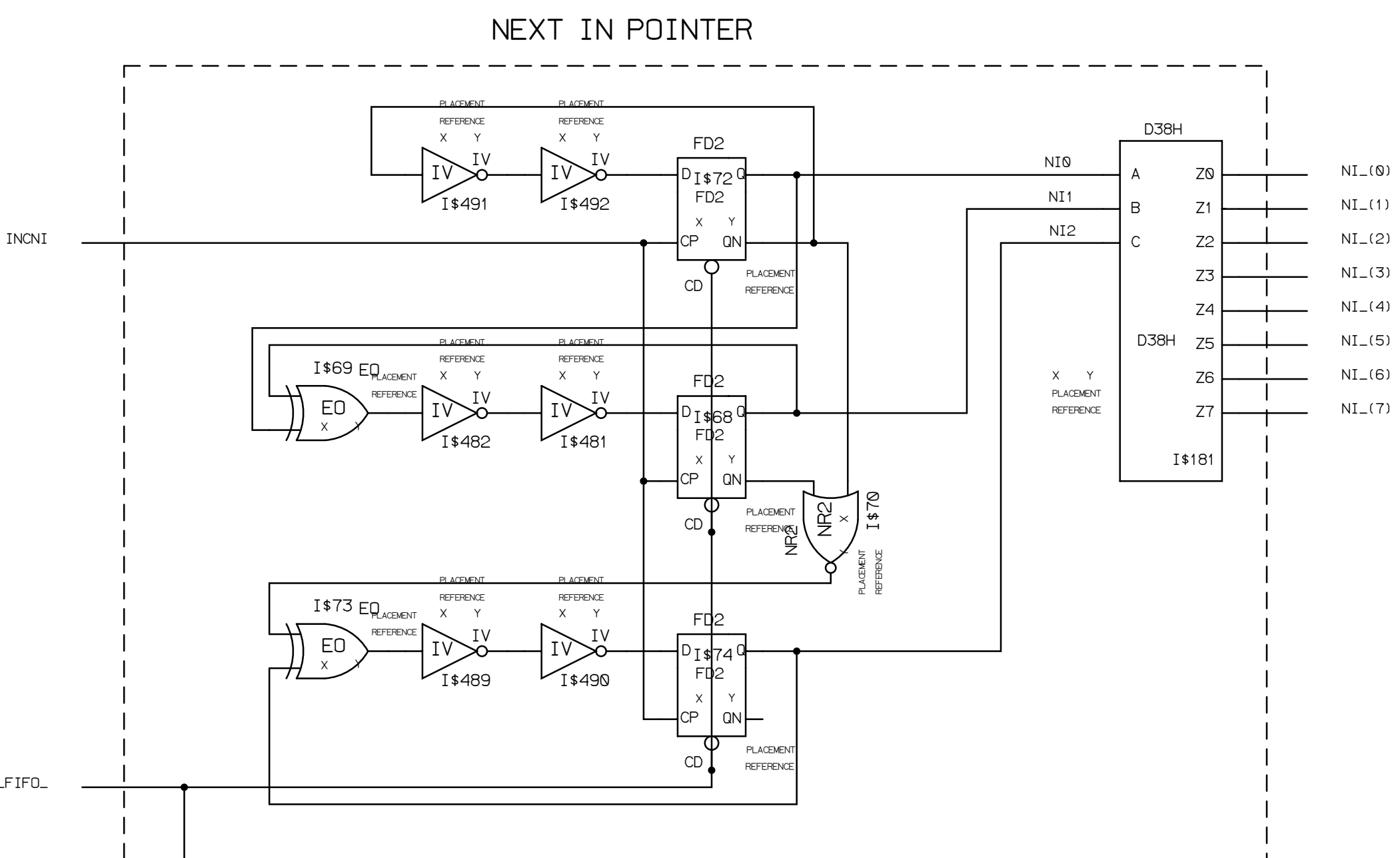


## CONTROL REGISTER





DATA PATH MANAGEMENT



FIFO LOGIC BLOCK

