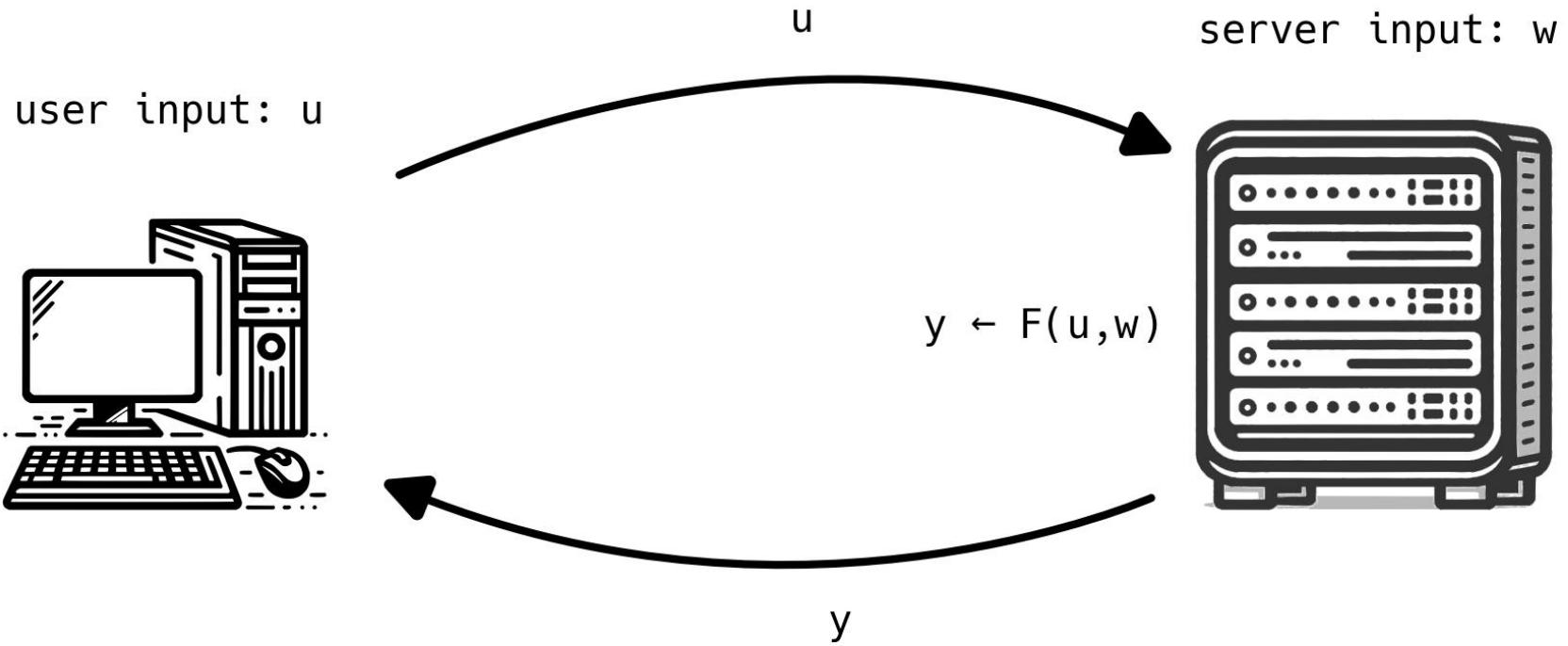


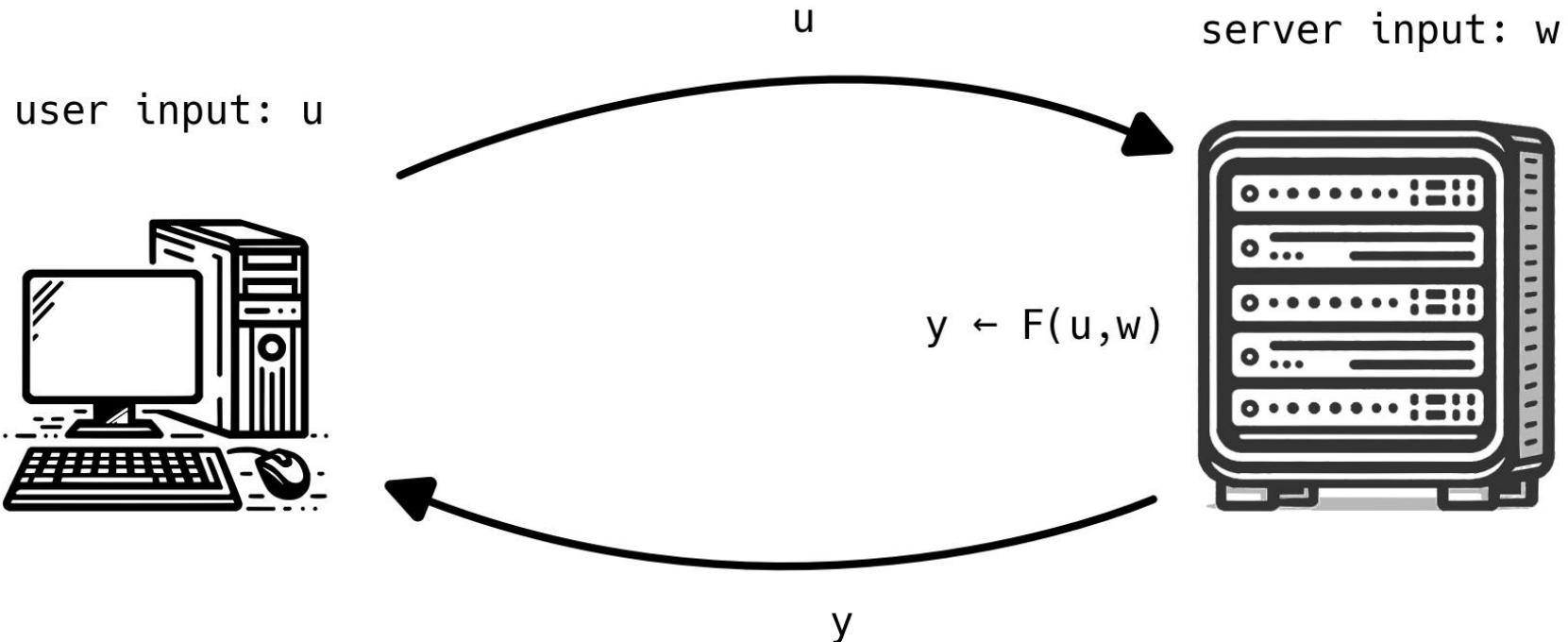
Blind zkSNARKs

Private Proof Delegation and Verifiable Computation over Encrypted Data

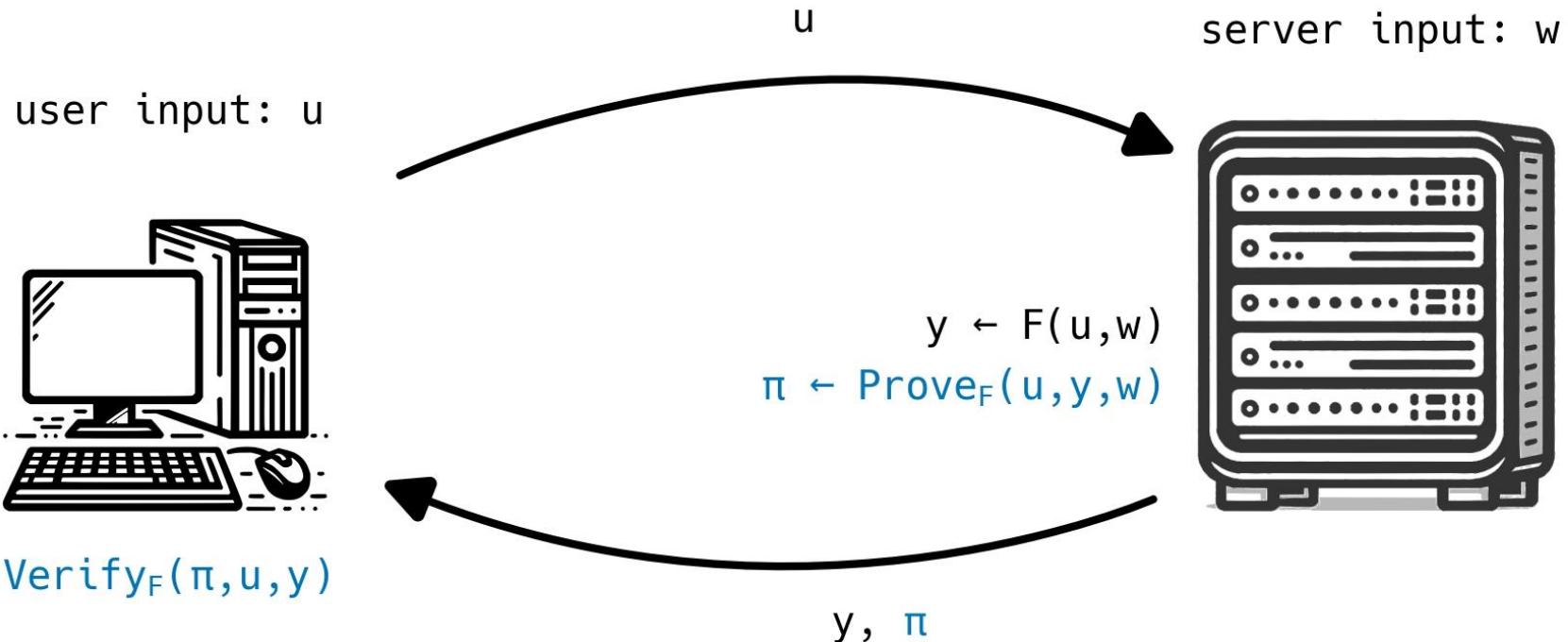
Mariana Gama¹ Emad Heydari Beni^{1,2} Jiayi Kang¹
Jannik Spiessens¹ Frederik Vercauteren¹

¹COSIC, KU Leuven ²Nokia Bell Labs

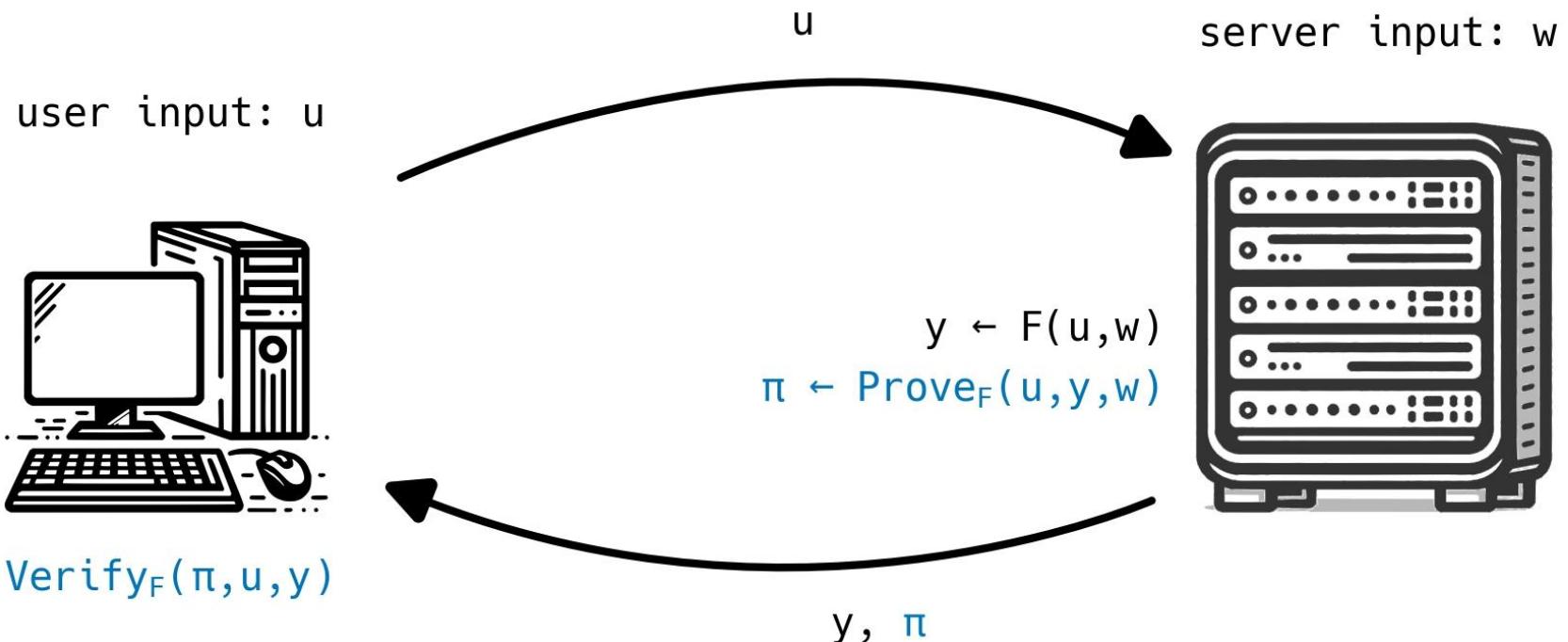




What if server is dishonest?



Verifiable Computation (VC)



Intermezzo: Homomorphic Encryption (HE)

> Encryption scheme

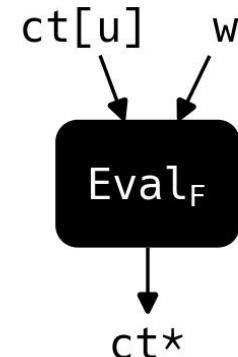
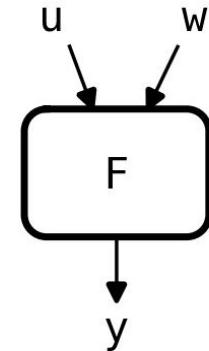
$$\begin{aligned} \text{ct}[u] &\leftarrow \text{Enc}(u) \\ u &\leftarrow \text{Dec}(\text{ct}[u]) \end{aligned}$$

> Homomorphic

$$\begin{aligned} u + w &= \text{Dec}(\text{ct}[u] + \text{ct}[w]) \\ u * w &= \text{Dec}(\text{ct}[u] \times \text{ct}[w]) \end{aligned}$$

Intermezzo: Homomorphic Encryption (HE)

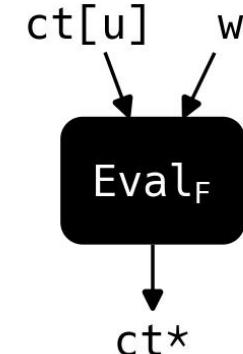
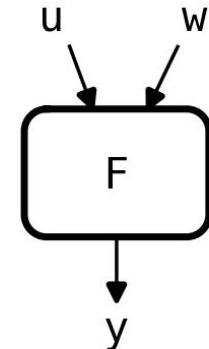
- > Transforms arithmetic circuit F into homomorphic circuit Eval_F



Intermezzo: Homomorphic Encryption (HE)

- > Transforms arithmetic circuit F into homomorphic circuit Eval_F
- > Correctness:

$$\left. \begin{array}{l} y \\ \text{ct}[u] \end{array} \right\} \begin{array}{l} \xleftarrow{\quad F(u, w) \quad} \\ \xleftarrow{\quad \text{Enc}(u) \quad} \end{array} \Rightarrow y \leftarrow \text{Dec}(\text{ct}[y])$$

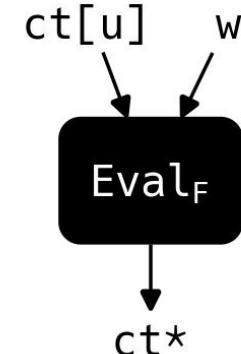
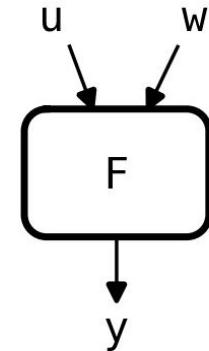


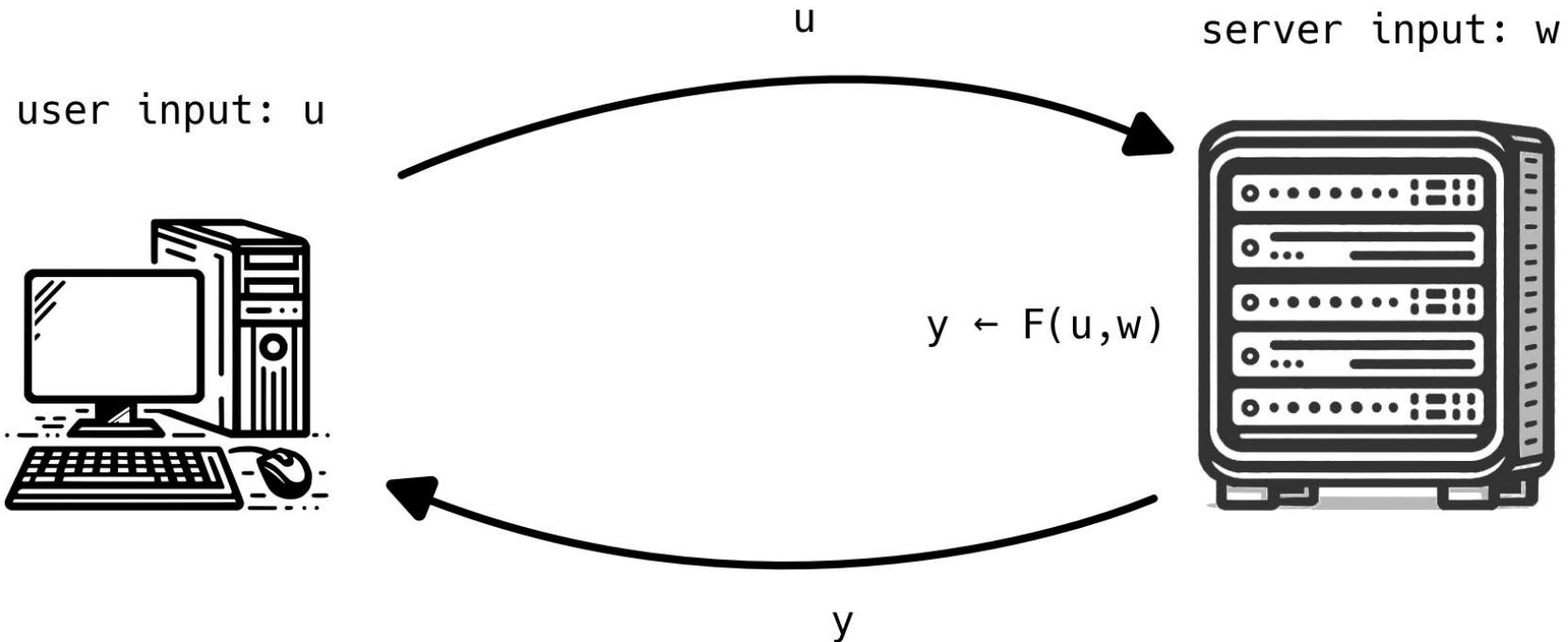
Intermezzo: Homomorphic Encryption (HE)

- > Transforms arithmetic circuit F into homomorphic circuit Eval_F
- > Correctness:

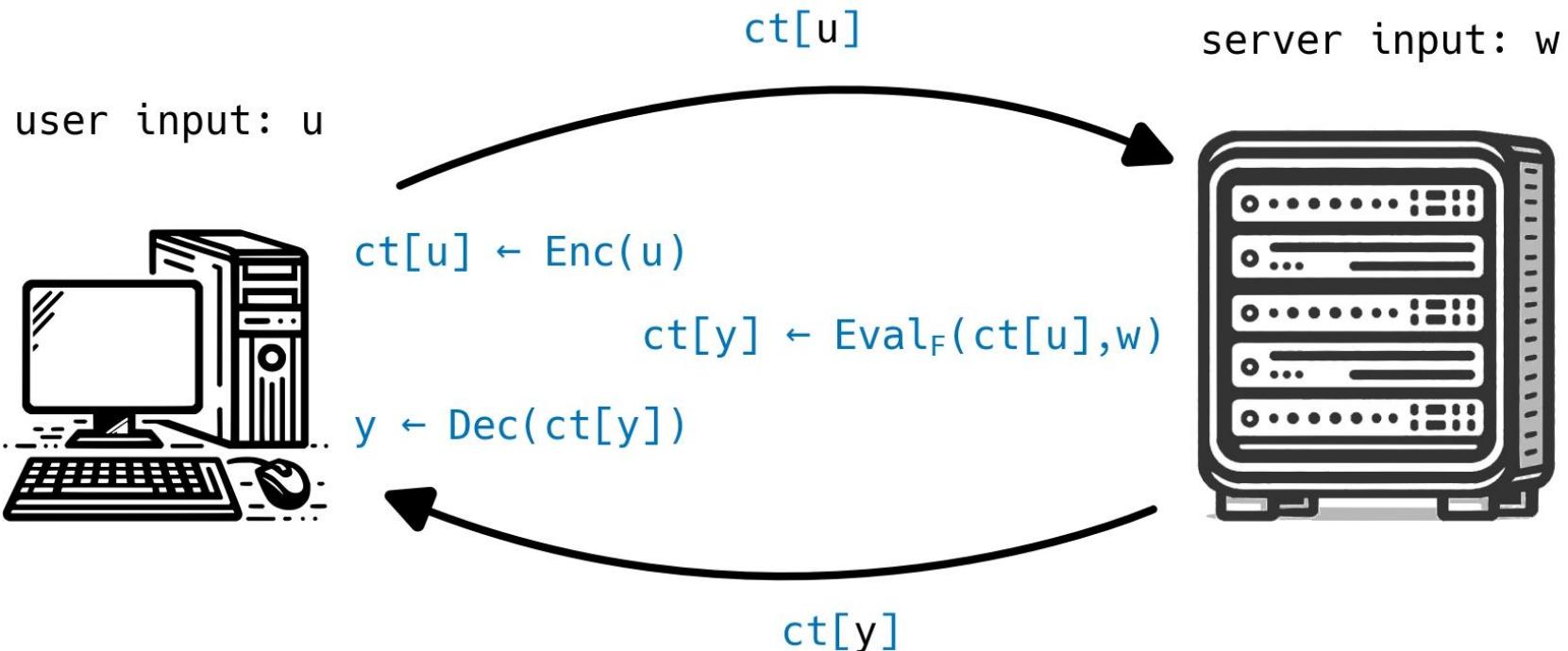
$$\left. \begin{array}{l} y \leftarrow F(u, w) \\ ct[u] \leftarrow \text{Enc}(u) \end{array} \right\} \Rightarrow y \leftarrow \text{Dec}(ct[y])$$

- > Encryption/Decryption using secret key sk

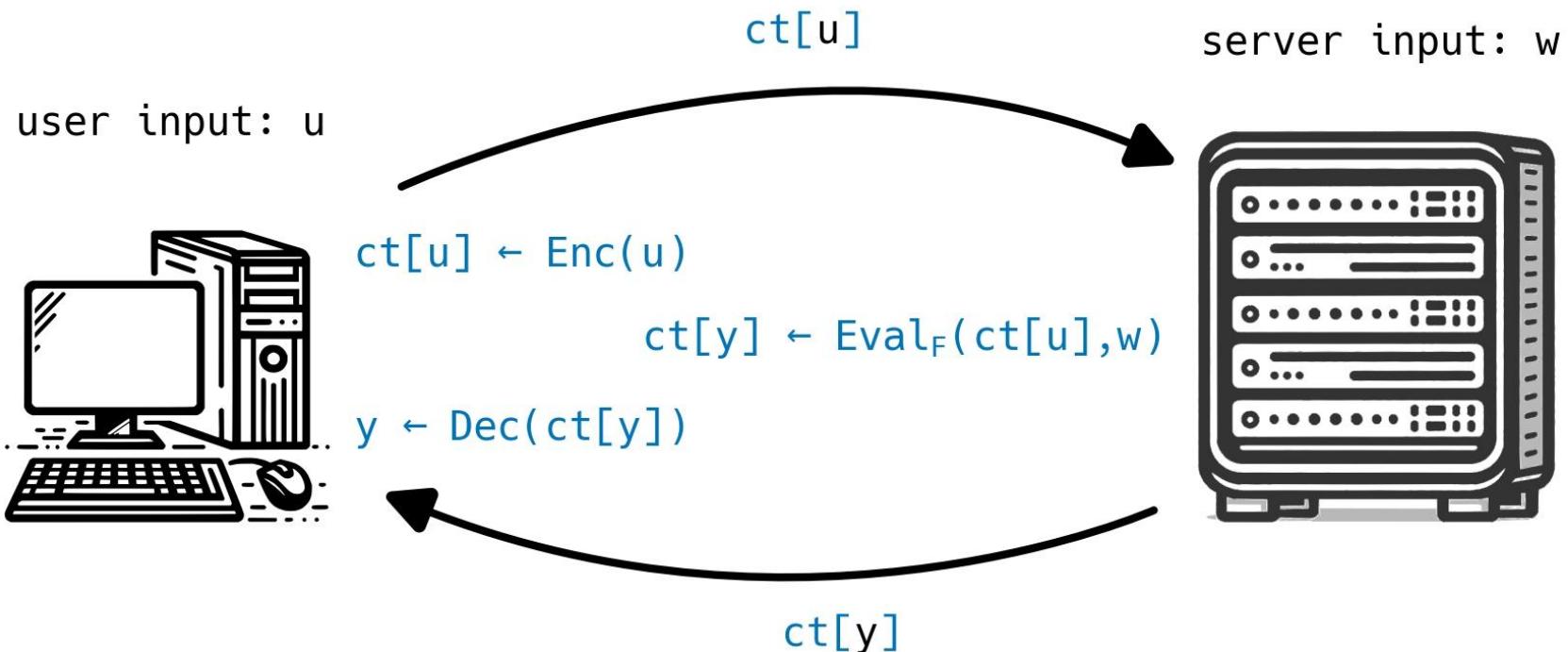


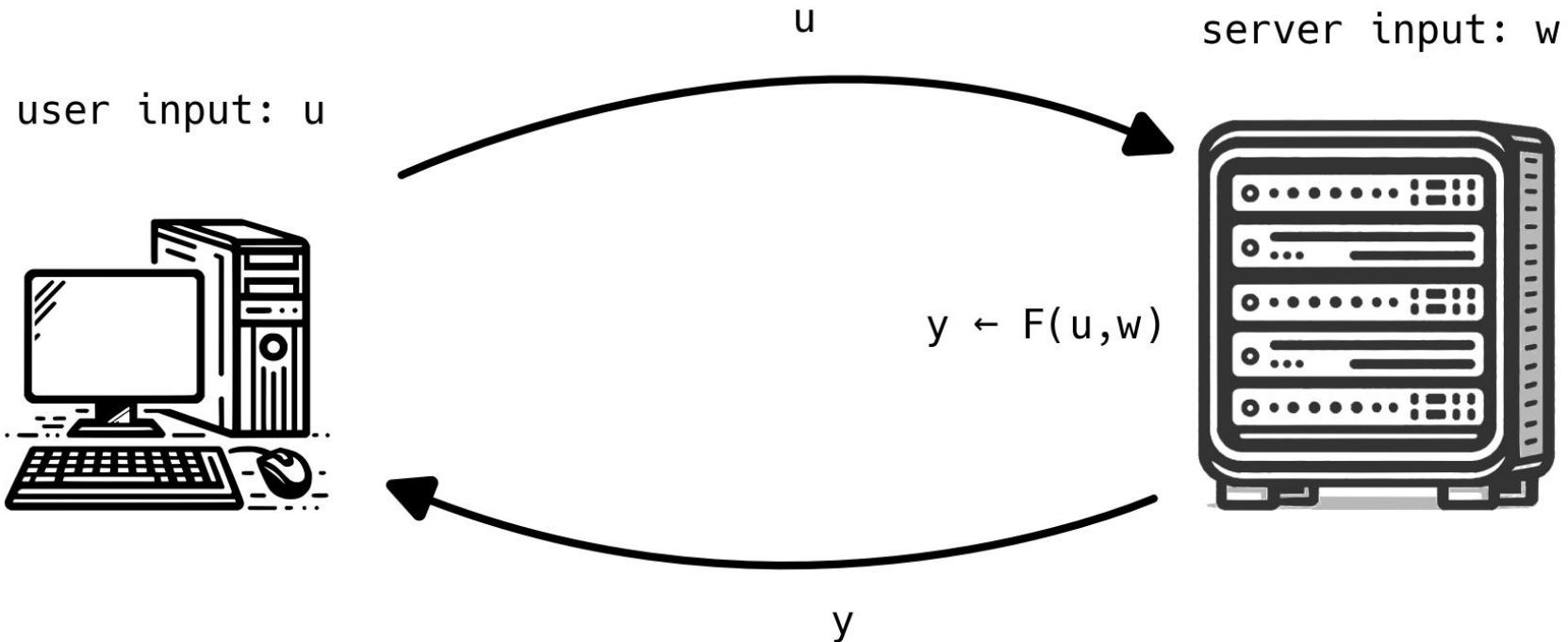


What if server is curious?

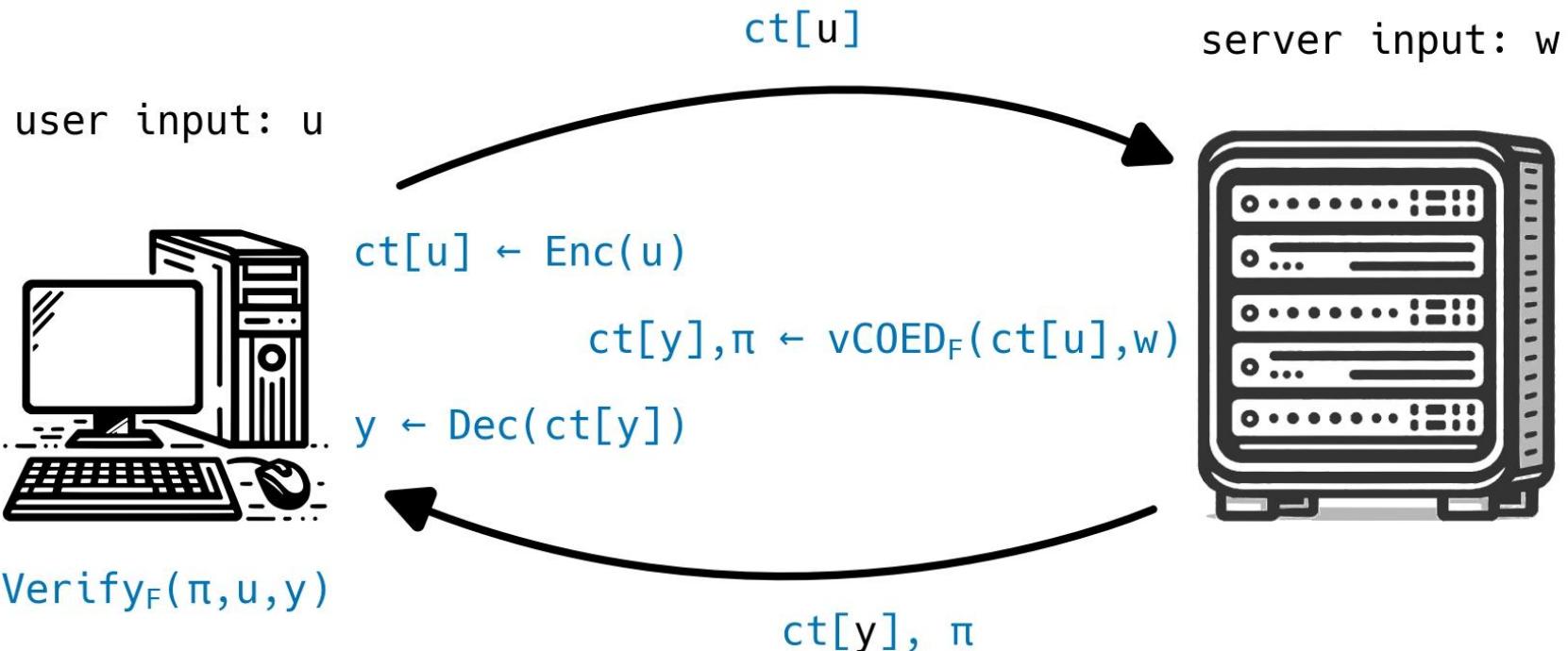


Computation Over Encrypted Data (COED)

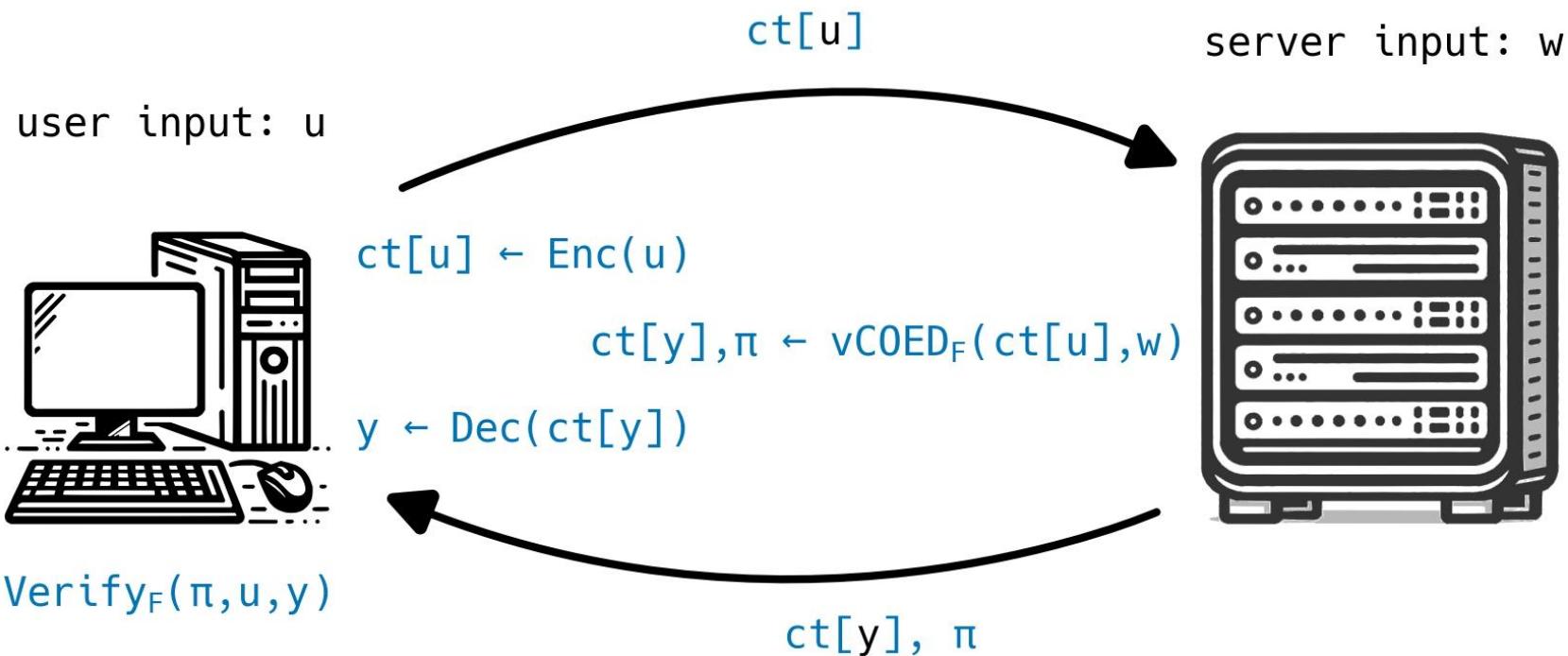




What if server is dishonest and curious?

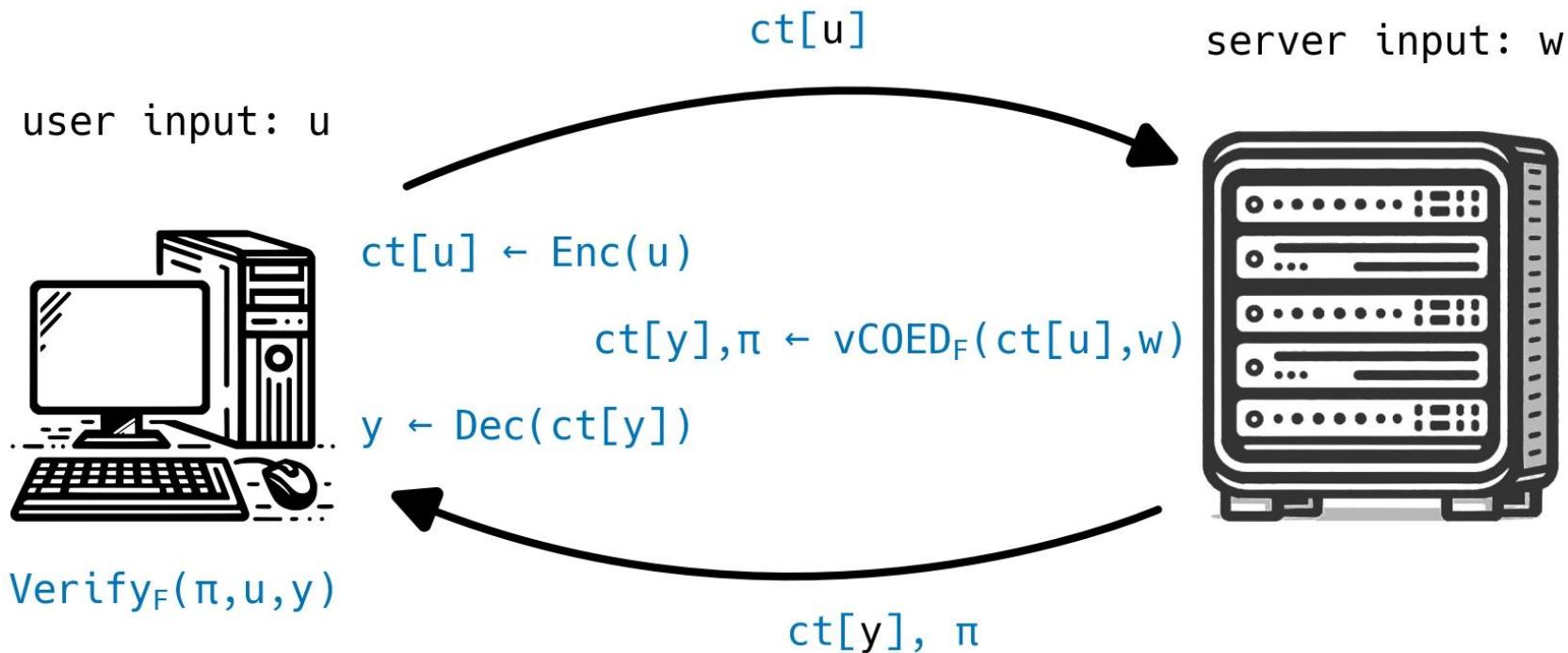


Verifiable COED (vCOED)



Verifiable COED (vCOED)

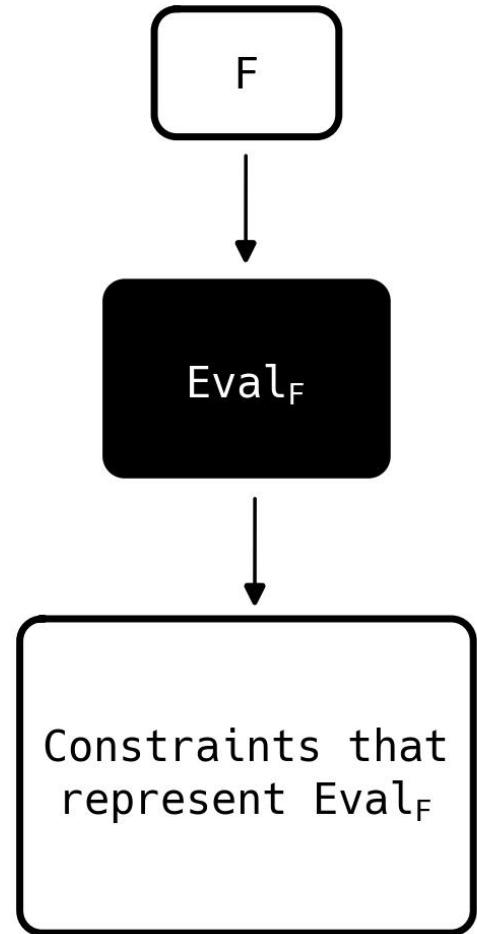
- > introduced in [FGP14]
- > Cryptographic TEE



vCOED from Verifiable FHE (vFHE)

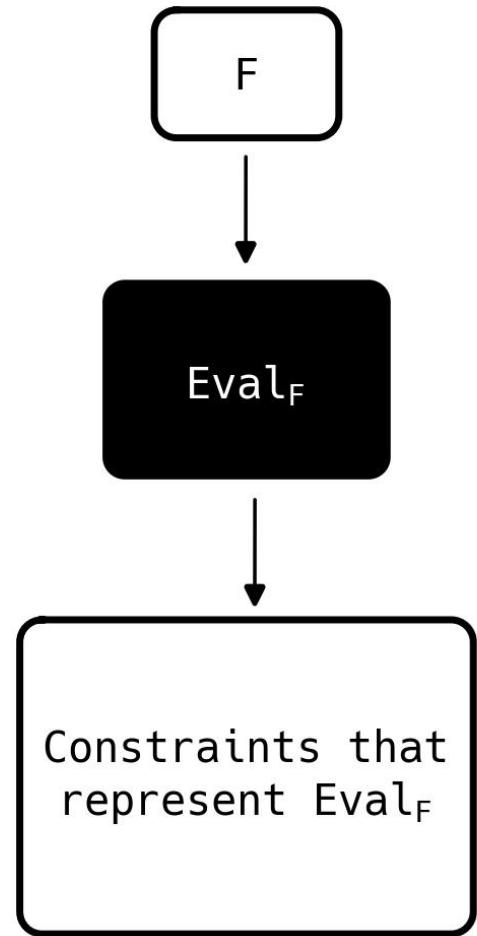
- > Prove homomorphic computations
i.e. $\text{vCOED}_F \leftarrow \text{Prove}_{\text{Eval}_F}$

1. Construct homomorphic circuit Eval_F
2. Represent Eval_F as a constraint system
3. Prove that constraint system using VC



Why vFHE is so hard

- > Circuit size blowup in step 1
 - >> Operations in \mathbb{F}_p become operations in $\mathbb{Z}_Q/(X^N + 1)$
- > Circuit size blowup in step 2
 - >> Maintenance operations require proving non-native arithmetic
- > State-of-the-art:
 - >> F is a small circuit and/or has a small plaintext space



[VKH23][Ata+24][Wal24][Liu+25][Cas+25]

The opposite approach

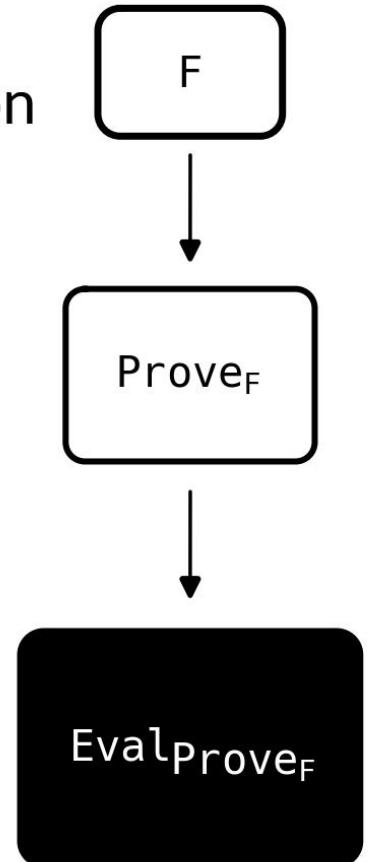
- > Homomorphically compute proving computation
i.e. $\text{vCOED}_F \leftarrow \text{Eval}_{\text{Prove}_F}$
- > Only proves plaintext operations

e.g. $F : y = u + w$

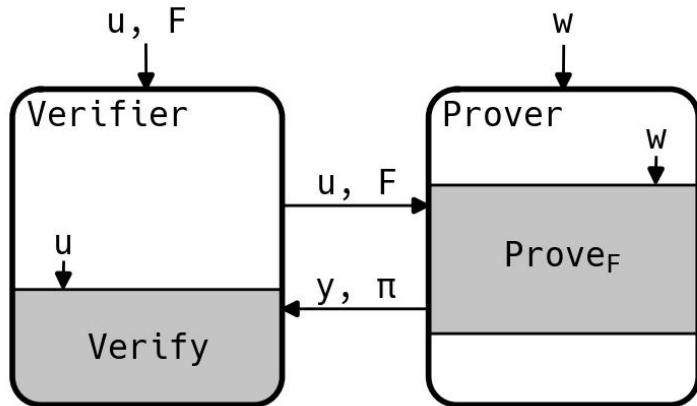
$$\text{ct}[y] = \begin{cases} \text{ct}[u] + \text{ct}[w] \\ \text{ct}[u] + \text{ct}[w] + \text{ct}[0] \end{cases} \quad \text{OR}$$

can both construct valid proof!

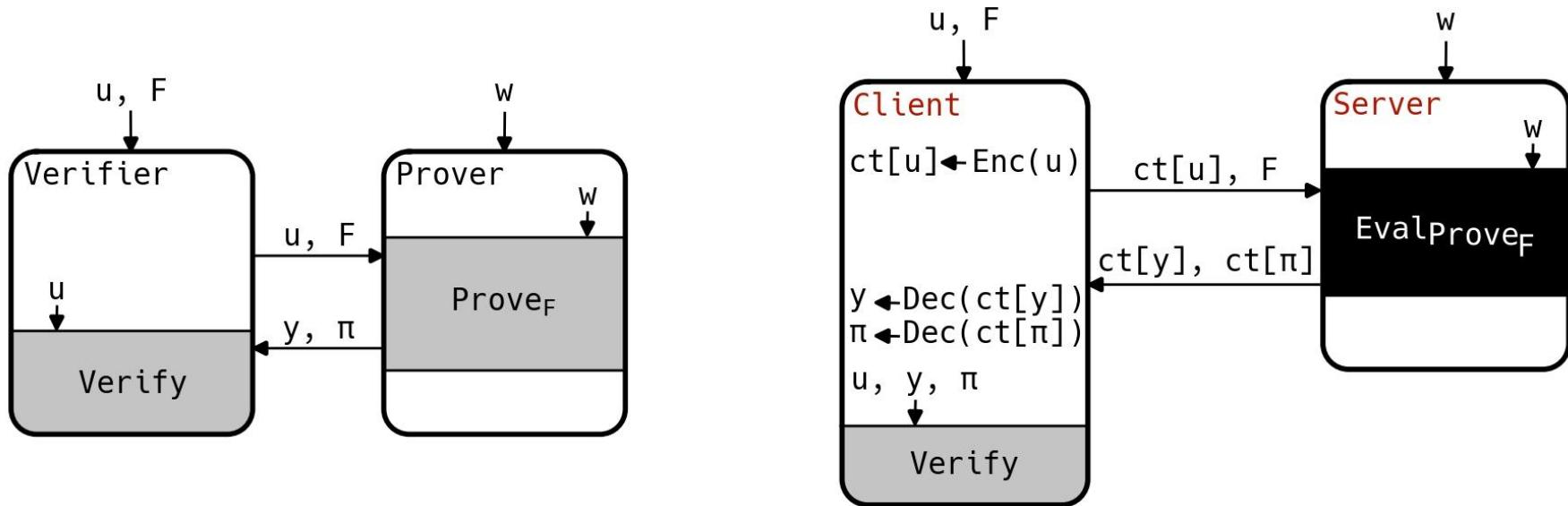
- > Related work: [GGW24][Ara+24][Zha+25]



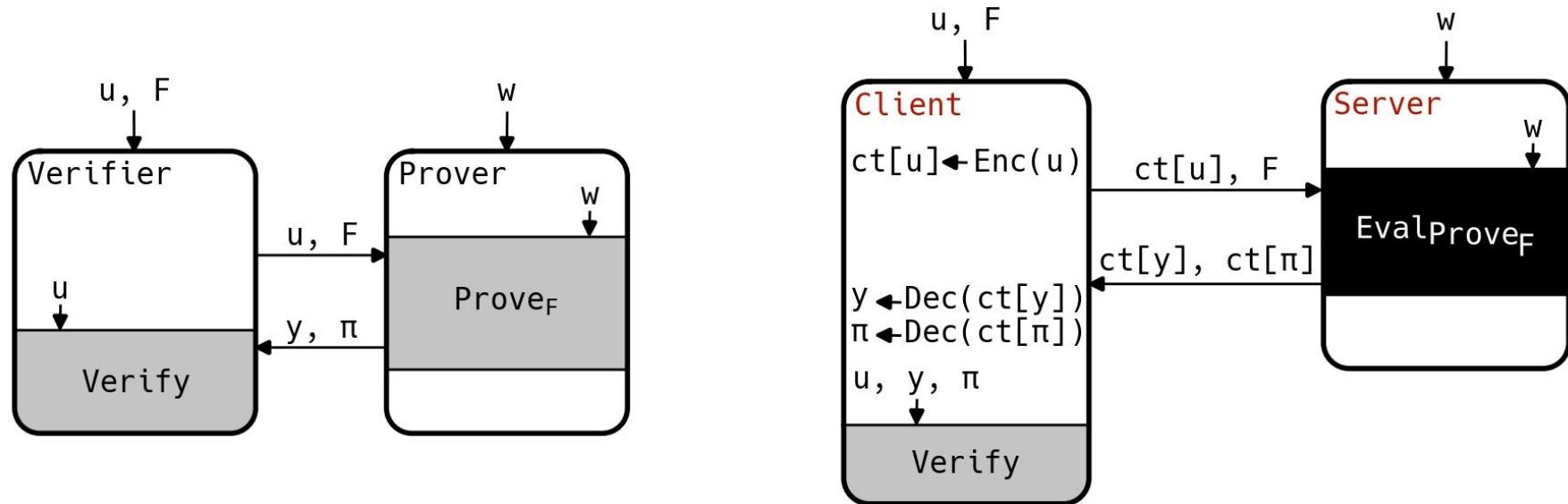
Blind zkSNARK for vCOED



Blind zkSNARK for vCOED



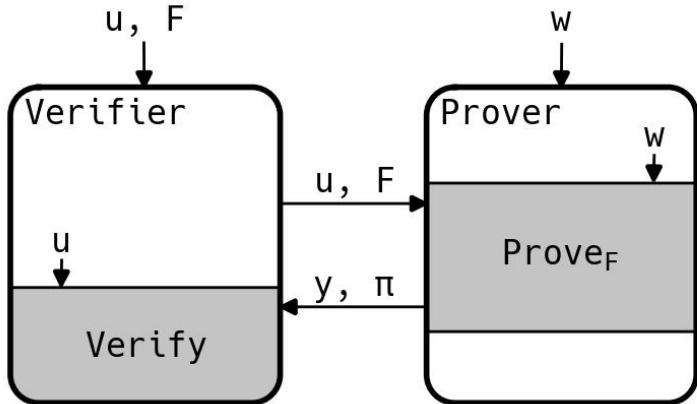
Blind zkSNARK for vCOED



- > Server returns partly encrypted proof $ct[\pi]$
- > Designated-verifier

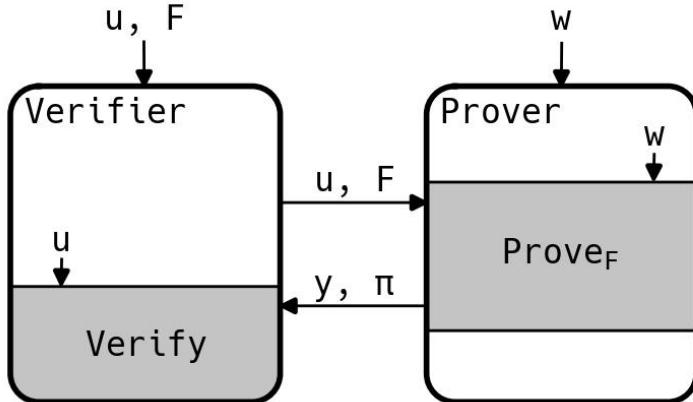
Blind zkSNARK for zkDel

zkDel =
Private Proof Delegation
to a single party



Blind zkSNARK for zkDel

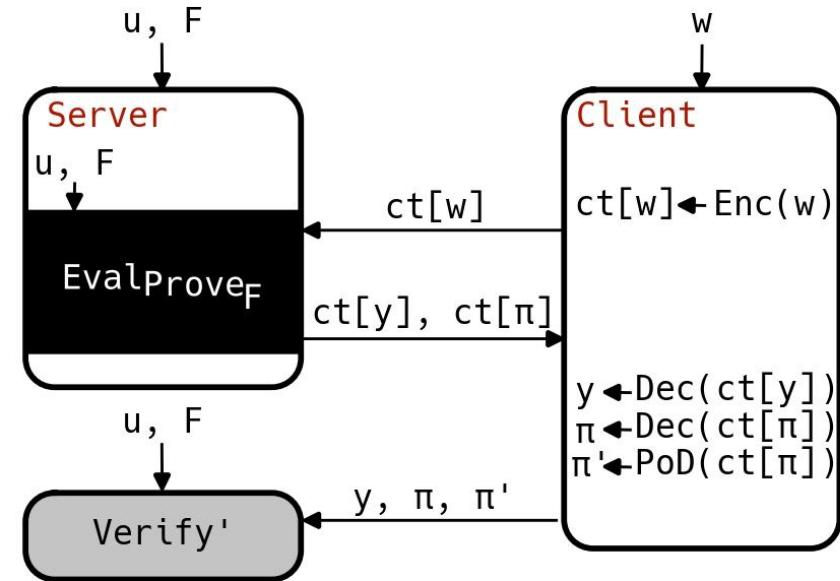
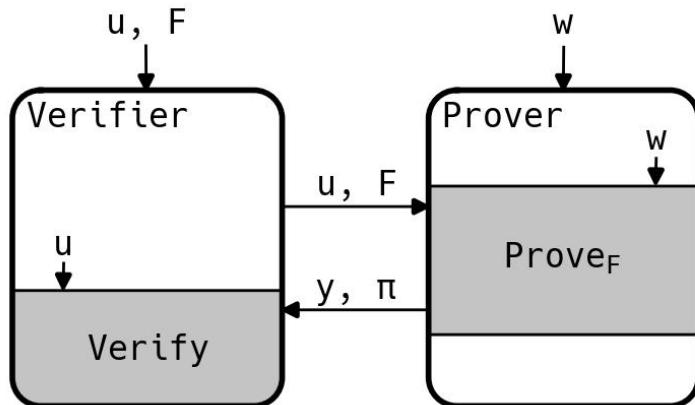
zkDel =
Private Proof Delegation
to a single party



- > What if the client is the prover?
- > Privacy Enhancing Technologies (PETs)
 - >> anonymous credentials
 - >> private cryptocurrencies
 - >> ...
- > Computing Prove_F can be expensive

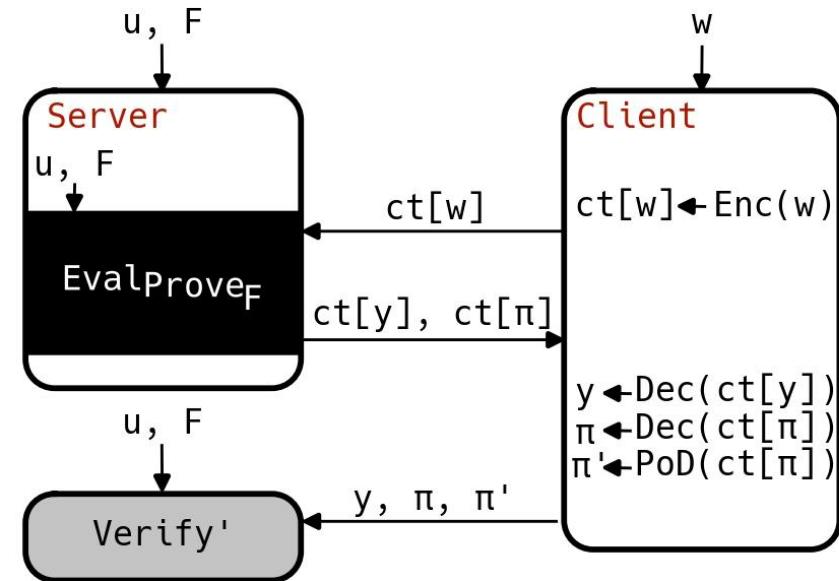
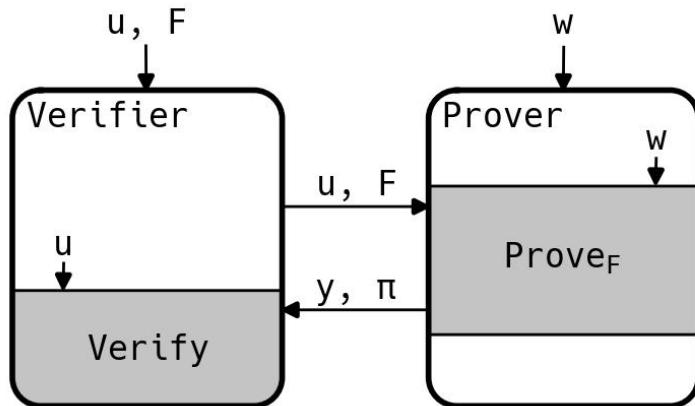
Blind zkSNARK for zkDel

zkDel =
Private Proof Delegation
to a single party



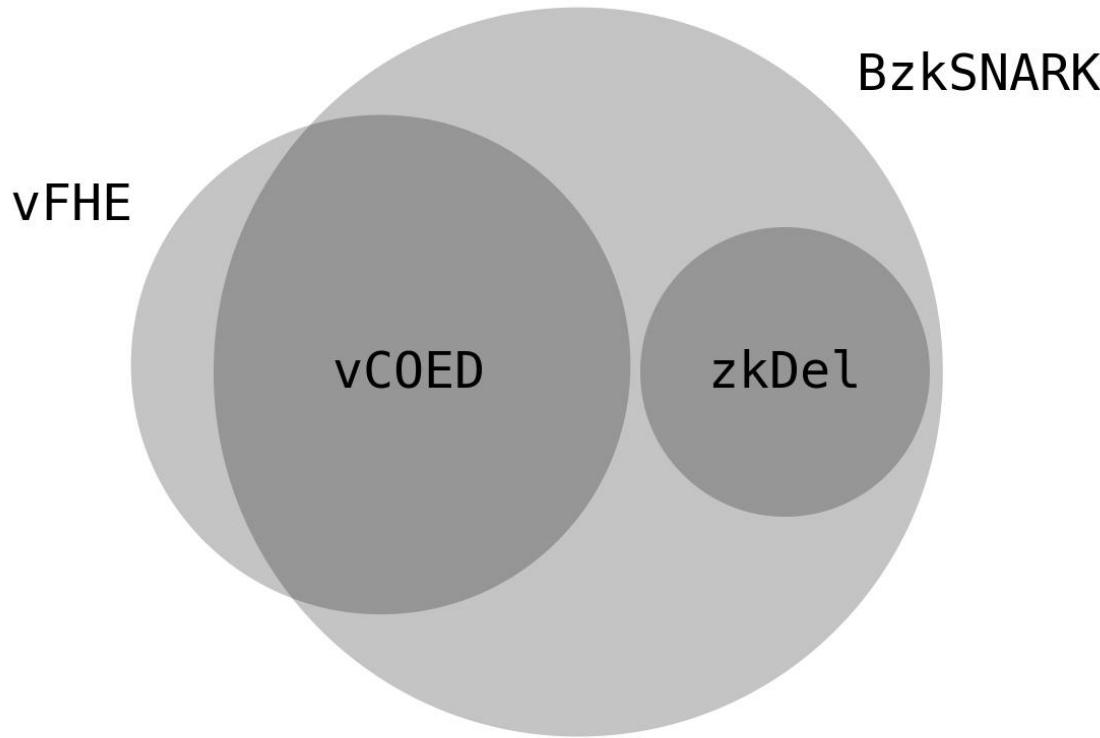
Blind zkSNARK for zkDel

zkDel =
Private Proof Delegation
to a single party



- > Server returns partly encrypted proof $ct[\pi]$
- > Client transforms into publicly verifiable proof
 - > by appending a **Proof of Decryption (PoD)**

A summary of BzkSNARK applications



Making proof systems blind

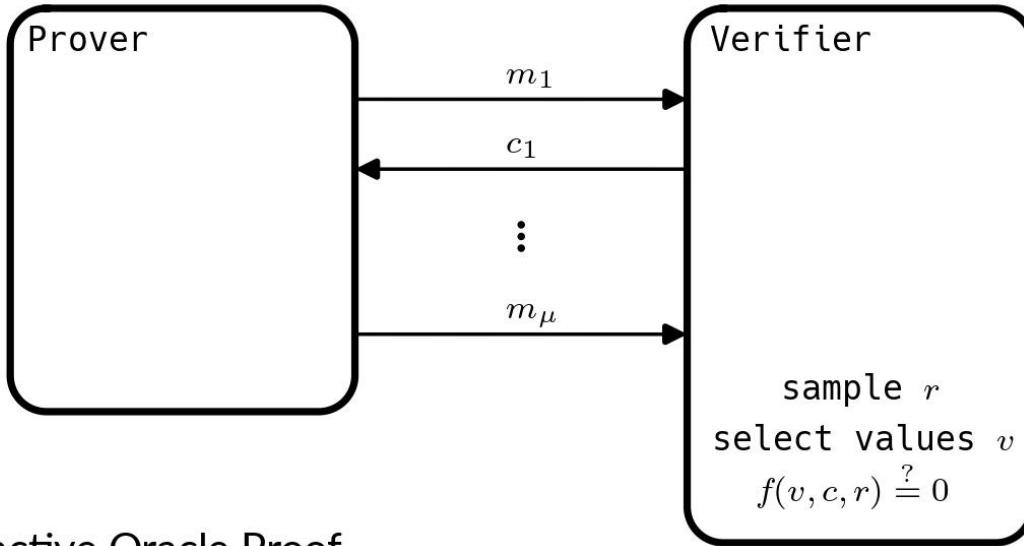
Proof

- > proves that $(x, w) \in R$
- > implies knowledge
- > native server-privacy

Blind proof

- > proves $(C_{\text{sk}}, \text{ct}[x], \text{ct}[w]) \in \mathcal{E}[R]$
 $\Rightarrow (\text{Dec}_{\text{sk}}(x), \text{Dec}_{\text{sk}}(w)) \in R$
- > implies *plaintext* knowledge
- > server-privacy requires circuit-private HE scheme

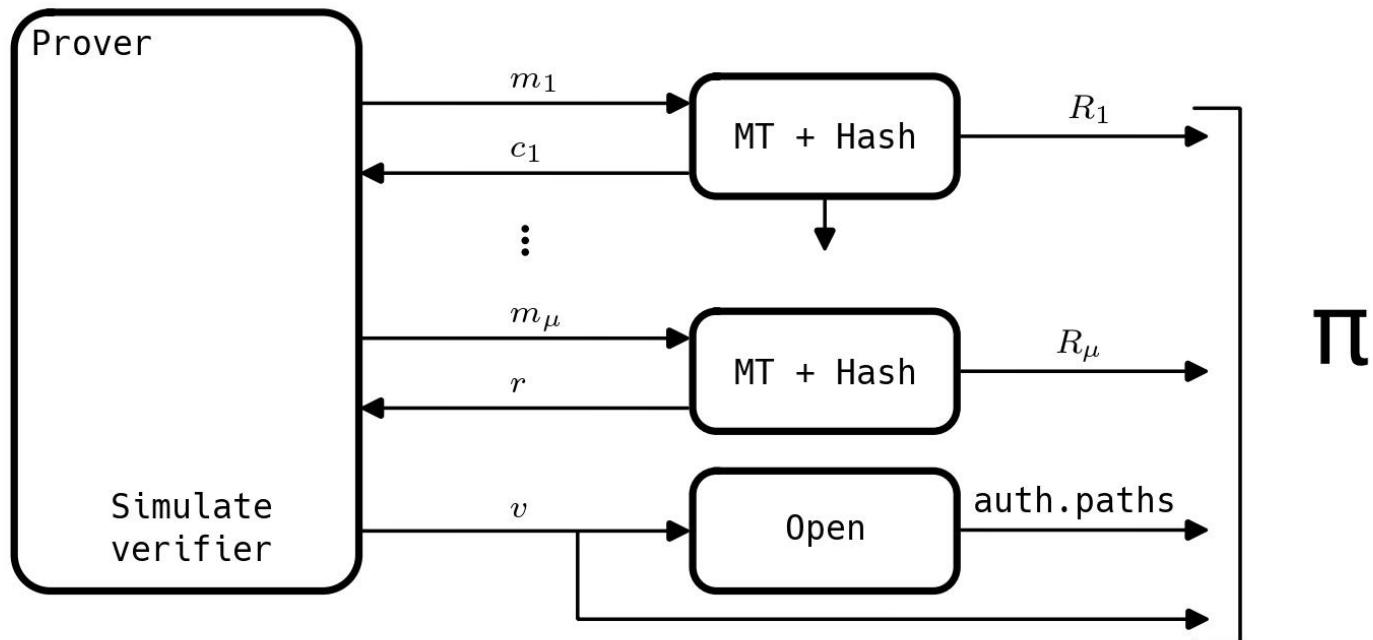
From hIOP to zkSNARK



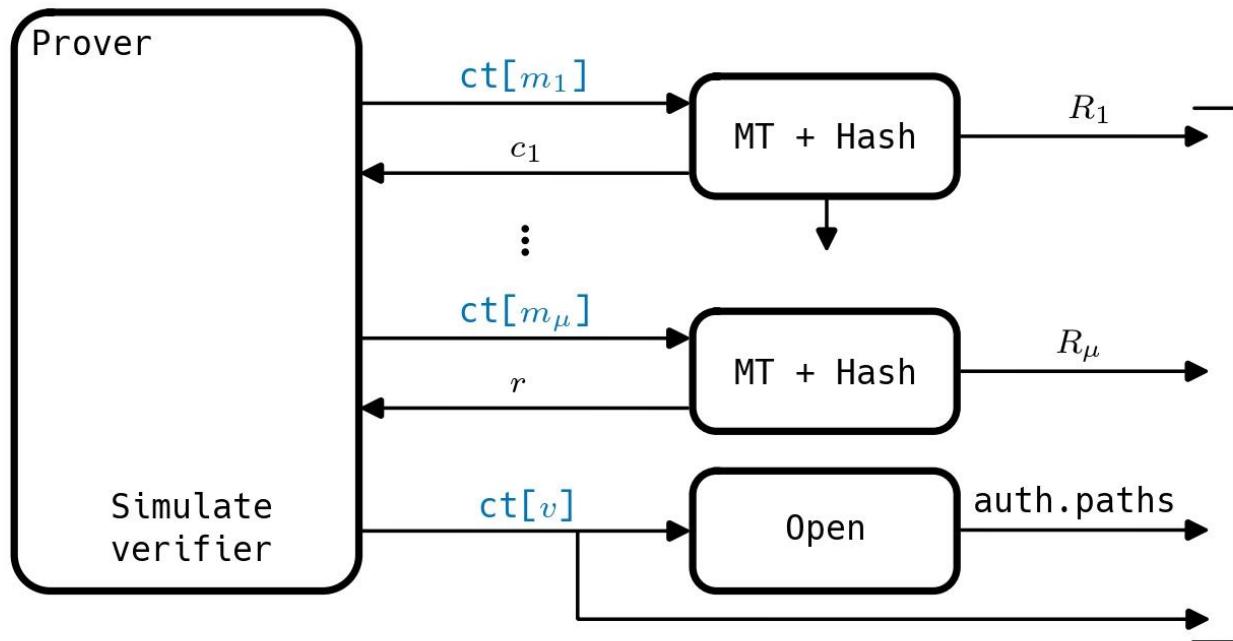
- > (holographic) Interactive Oracle Proof
- > public-coin
- > μ rounds

From hIOP to zkSNARK

BCS compilation [BCS16]

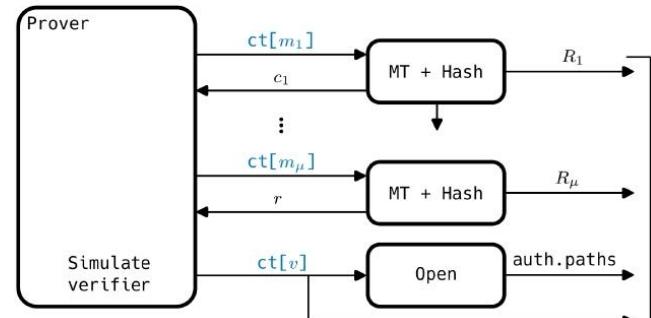


From BhIOP to BzkSNARK



From BhIOP to BzkSNARK

- > BhIOP inherits round-by-round (knowledge) soundness
- > BCS compilation applies if hIOP is non-adaptive
- > Designated-verifier:
 1. Decrypt $\text{ct}[v]$
 2. Check identity $f(v, c, r) \stackrel{?}{=} 0$
 3. Check openings and Fiat-Shamir
- > Make proof publicly verifiable:
 1. append plaintexts v to proof
 2. append Proof of Decryption (PoD)



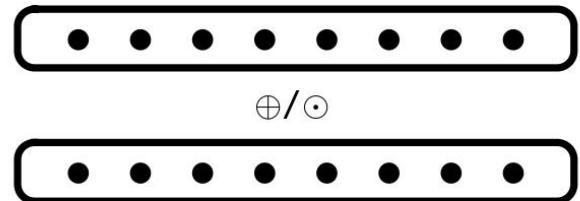
π

Constructing an efficient Blind zkSNARK

- > Select a linear hIOP and high-precision HE scheme
 - >> as hIOP: Fractal [COS20] + FRI [Ben+18]
- > Optimize the hIOP proving algorithm for homomorphic computation in the HE scheme
- > Construct an efficient PoD for the selected HE scheme

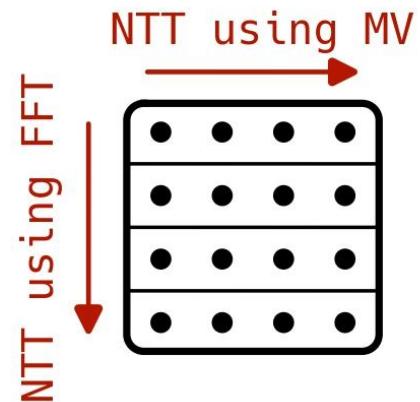
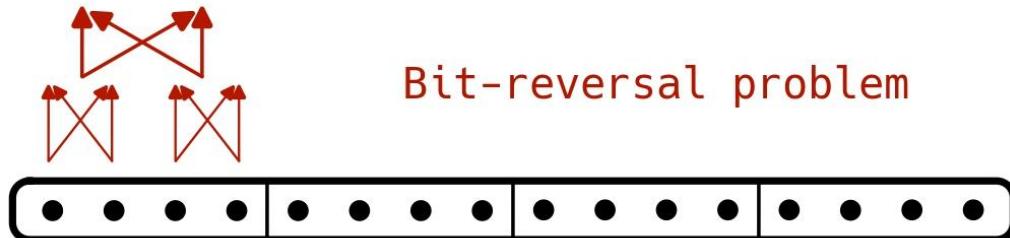
Computational interface: HE scheme

- > Ciphertext space $\mathbb{Z}_Q[X]/\Phi(X)$ homomorphic to plaintext space \mathcal{P} : vectors on finite field \mathbb{F}
- > Operations:
 - >> elt-wise add pt or ct
 - >> elt-wise multiply by pt or ct
 - >> permutation in vector
- > Noise growth
- > Generalized BFV (GBFV) [GV24]
 - >> supports SIMD slots (cf. BGV/BFV [FV12])
 - >> supports high-precision arithmetic (cf. CLPX [Che+18])
 - >> we select $\mathcal{P} = \mathbb{F}_{p^2}^{256}$ for $p = 2^{64} - 2^{32} + 1$



Modifying the hIOP

- > Generally a trade-off between number of operations and noise depth
 - >> e.g. domain extensions: compute $f|_L$ from $f|_H$
 - Min. number of operations: $f|_L = \text{NTT}(\text{iNTT}(f|_H))$ using FFT
 - Min. noise growth: $f|_L = V_L V_H^{-1} f|_H$ using matrix-vector product
- Solution: 2D NTT*



Modifying the hIOP

- > Example estimate: R1CS with 2^{20} constraints

Computation	Noise (bits)	C_{add}	C_{ptct}	C_{aut}	C_{ctct}
Unpacking	9	0	4096	4096	0
Computing $\text{ct}[Mz]$	14	9421459	9433747	2978354	0
Computing $\text{ct}[\vec{f}_z]/\text{ct}[\vec{f}_{Mz}]$	64	4636672	4653056	491520	0
Computing $\text{ct}[\vec{g}]$	138	6762496	6782976	552960	0
Computing $\text{ct}[\vec{f}_{\text{FRI}}]$	0	65536	65536	0	8192
Computing FRI	16	98305	106496	0	0
Ringswitching	9	0	196604	589812	0

Operation count and noise estimates for computing blind Fractal.

Proof of Decryption

- > Proves that $\|c_0 + c_1 \cdot \text{sk} - [\Delta \cdot m]\|_{\infty} \leq B$ w.r.t. committed sk
- > Based on [LNP22] Approximate Range Proofs
 - > work over $\mathbb{Z}_{q'}[X]/(X^d + 1)$ with $q' - 1 \equiv 4 \pmod{8}$ such that

$$X^d + 1 = (X^{d/2} - r)(X^{d/2} + r)$$

- > large set of invertible challenges $c \in \mathcal{C}$ s.t. $\sigma_{-1}(c) = c$
- > use identity $\widetilde{\sigma_{-1}(r)} \cdot s = \langle \vec{r}, \vec{s} \rangle$ to prove norms
- > For $\vec{y} \in [-Q/2, Q/2]^l$ and $B \leq Q/41l$, random $R \leftarrow \text{Bin}_2^{256 \times l}$ and $\vec{v} = R\vec{y} \pmod{Q}$

$$\|\vec{v}\| < \sqrt{26}B \Rightarrow \|\vec{y}\| < B$$

except with probability 2^{-256} .

Proof of Decryption

- > Proves that $\|c_0 + c_1 \cdot \text{sk} - \lfloor \Delta \cdot m \rceil\|_{\infty} \leq B$ w.r.t. committed sk
- > translate into $\|\vec{v}_{inh}\|_{\infty} < B_{q'}$ where

$$\vec{v}_{inh} = \text{Rot}_m(c_1) \cdot \vec{\text{sk}} + \vec{c}_0 - \overrightarrow{\lfloor \Delta \cdot m \rceil}$$

- > First modswitch from $\mathbb{Z}_q[X]/\Phi_m(X)$ to $\mathbb{Z}_{q'}[X]/\Phi_m(X)$
- > Then represent $\mathbb{Z}_{q'}[X]/\Phi_m(X)$ as $\mathbb{Z}_{q'}^n$ operations
- > Approx. norm proof only complete for $\|\vec{v}_{inh}\|_{\infty} < B_{\text{PoD}}$
 - > Relaxation factor $\Phi_r := B_{q'}/B_{\text{PoD}} \approx 31$ bits

Proof of Decryption

- > Batching naively scales as $\mathcal{O}(rn^2)$

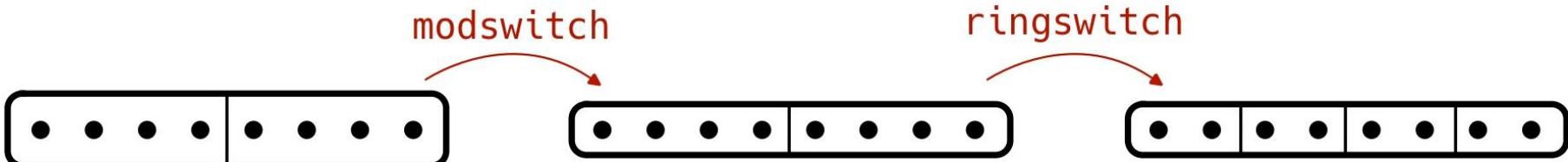
r	$\Pi_{\text{vec-ANP}}$ w/out $\Pi_{\text{eval}}^{(2)}$	$\Pi_{\text{eval}}^{(2)}$		Total runtime	
		single thread	8 threads	single thread	8 threads
1	0.04	1.15	0.45	1.19	0.49
8	0.14	6.92	1.26	7.06	1.40
64	0.93	53.01	8.09	53.94	9.02
512	7.28	424.17	64.30	431.45	71.58
1024	14.68	846.59	126.89	861.27	141.57
2048	29.40	1688.15	253.55	1717.55	282.95
4096	58.81	3407.10	516.12	3465.91	574.93

Proof of Decryption

- > Batching naively scales as $\mathcal{O}(rn^2)$
- > Introduce new protocol for batching r PoDs
 - > for some random linear combination $f : \vec{m} \mapsto \sum_{i \in [r]} \alpha_i m_i$ prove that $f(\vec{m}) = \text{Dec} \left(\text{Eval} \left(f, \overrightarrow{\text{ct}[m]} \right) \right)$
 - > based on homomorphic property and Schwartz-Zippel lemma
- > reduces proves cost $\mathcal{O}(rn^2) \rightarrow \mathcal{O}(n^2 + rn \log n)$
- > at cost of $\approx 6 + \log r$ bits of noise

Proof of Decryption

- > Optimized using HE operations
 - > Modswitch
From “FHE-friendly” modulus q to “LNP22-friendly” q'
i.e. from 398 bits to 97 bits
 - > Ringswitch
From “efficient” $\mathbb{Z}_{q'} / \Phi_{2^{11}.3.7}(X)$ to “small” $\mathbb{Z}_{q'} / \Phi_{2^8.3.7}(X)$
i.e. from 96 slots to 24 slots
- > MS and RS performed again inside batching protocol



Proof of Decryption

- > Implemented in C
- > Built upon the LaZer library [LSS24]
- > Our parameters: blind zkSNARK for 2^{20} R1CS gates
 - >> Proof size: 12 kB
 - >> Prover runtime: 1.7s (1 thread) or 0.65s (8 threads)

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Q&A

