

# SNARKs from the sum-check protocol

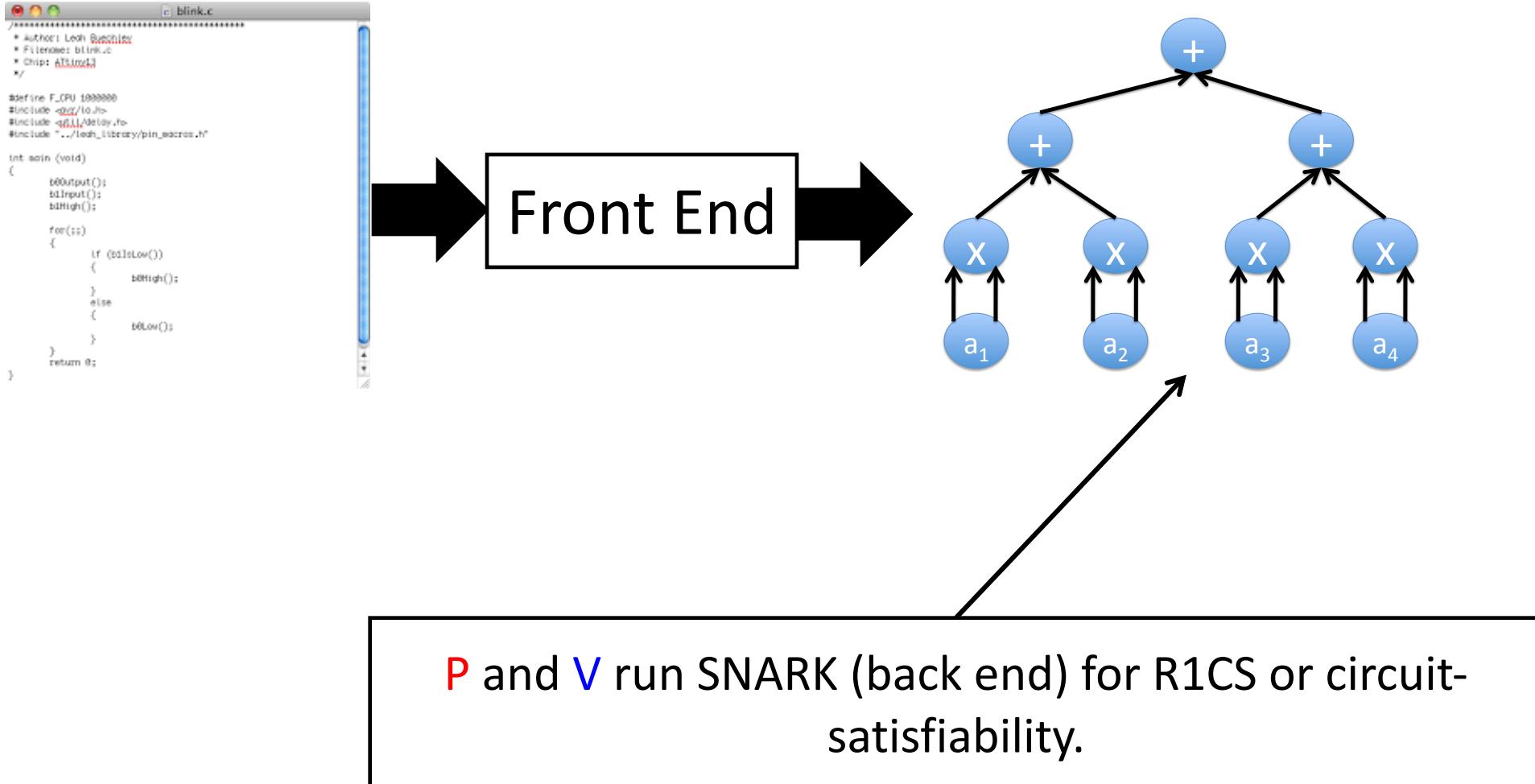
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# General-Purpose SNARKs

- Prover  $P$  claims to know witness  $w$  satisfying some property.

# General-Purpose SNARKs

- Prover **P** claims to know witness  $w$  satisfying some property.
- Start with a computer program written in high-level programming language (C, Java, etc.).
  - Takes  $w$  as input and checks it satisfies the claimed property.
- Step 1: Turn the program into an equivalent model amenable to probabilistic checking.
  - Typically some type of circuit-satisfiability or R1CS problem.
  - Called the **Front End** of the system.
- Step 2: Run an interactive proof on the circuit/R1CS.
  - Called the **Back End** of the system.



# Key Paradigm for Backend Design

1. Give a “polynomial IOP” for R1CS or circuit-satisfiability.
2. Combine with polynomial commitment scheme to get succinct interactive argument.
3. Apply Fiat-Shamir transformation to render non-interactive.

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- The sum-check protocol is a key technique for Step 1.

# Building Block: Interactive Proofs (IPs)

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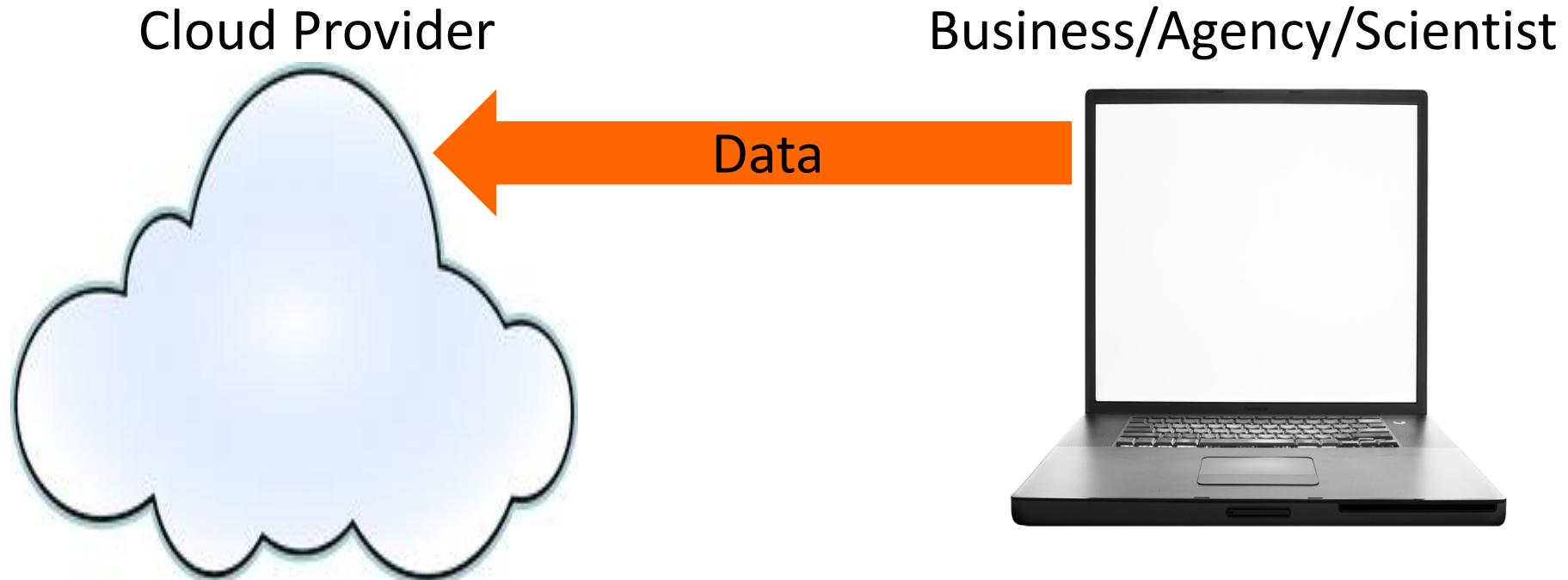
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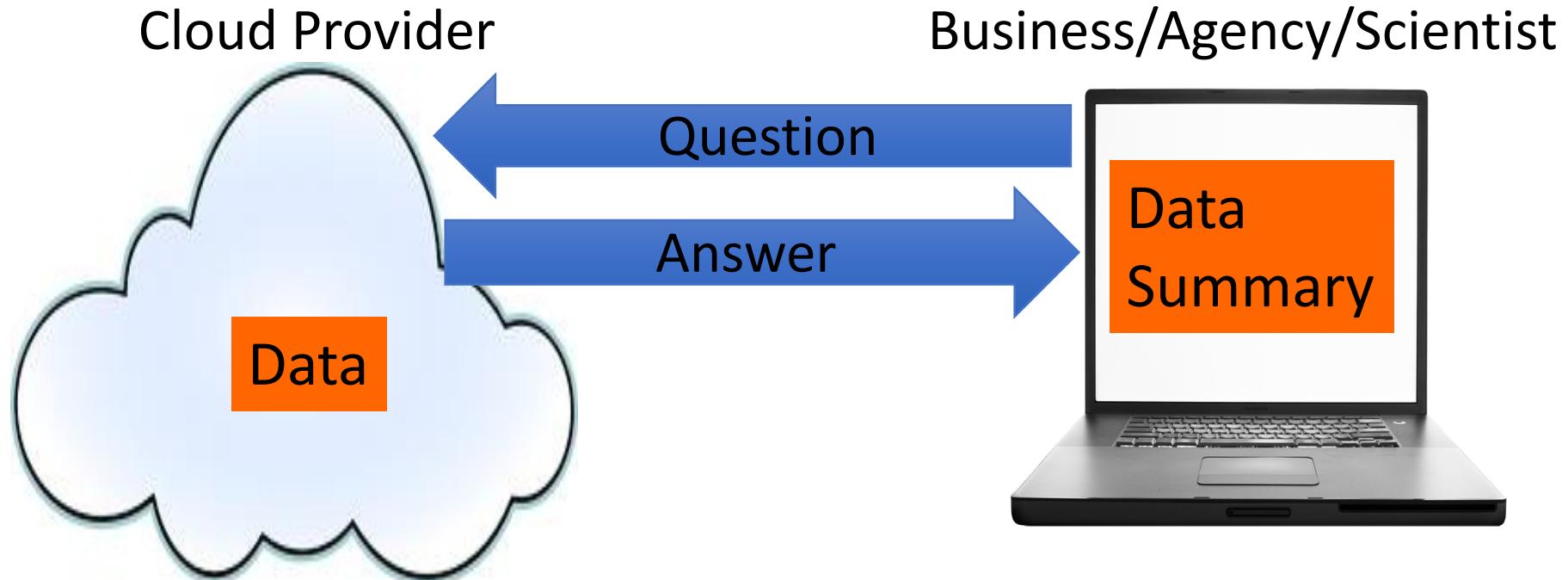
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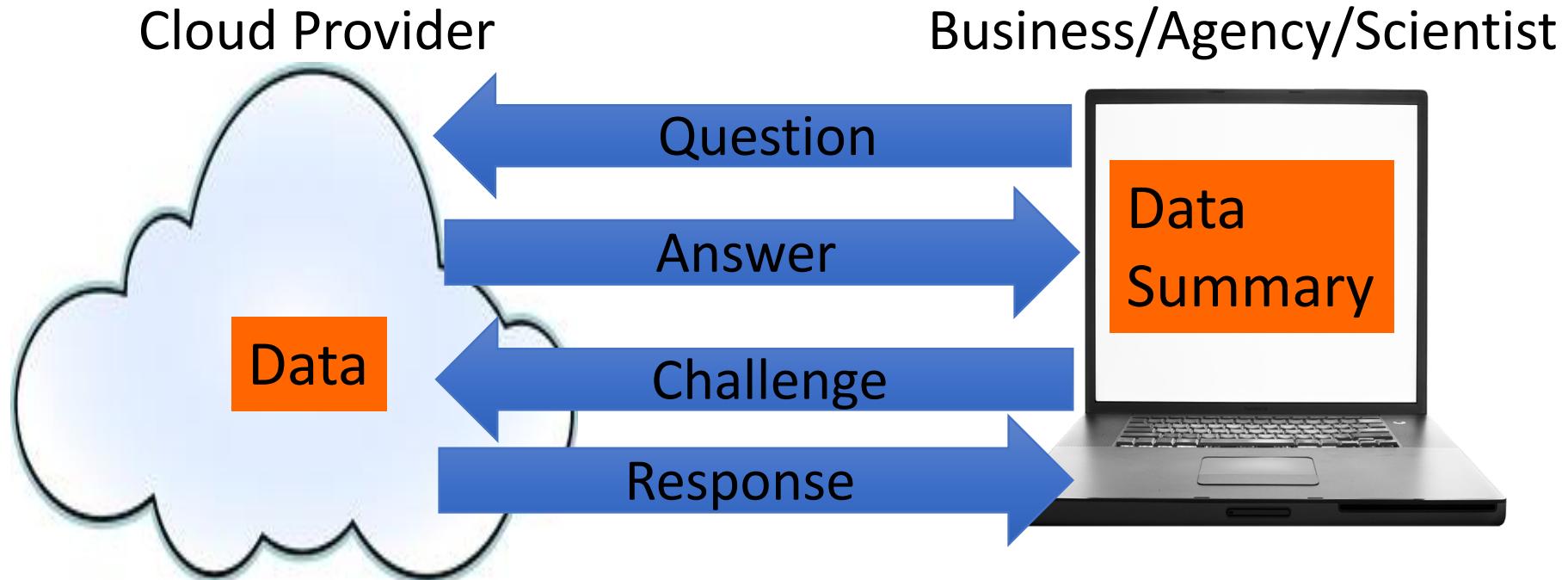
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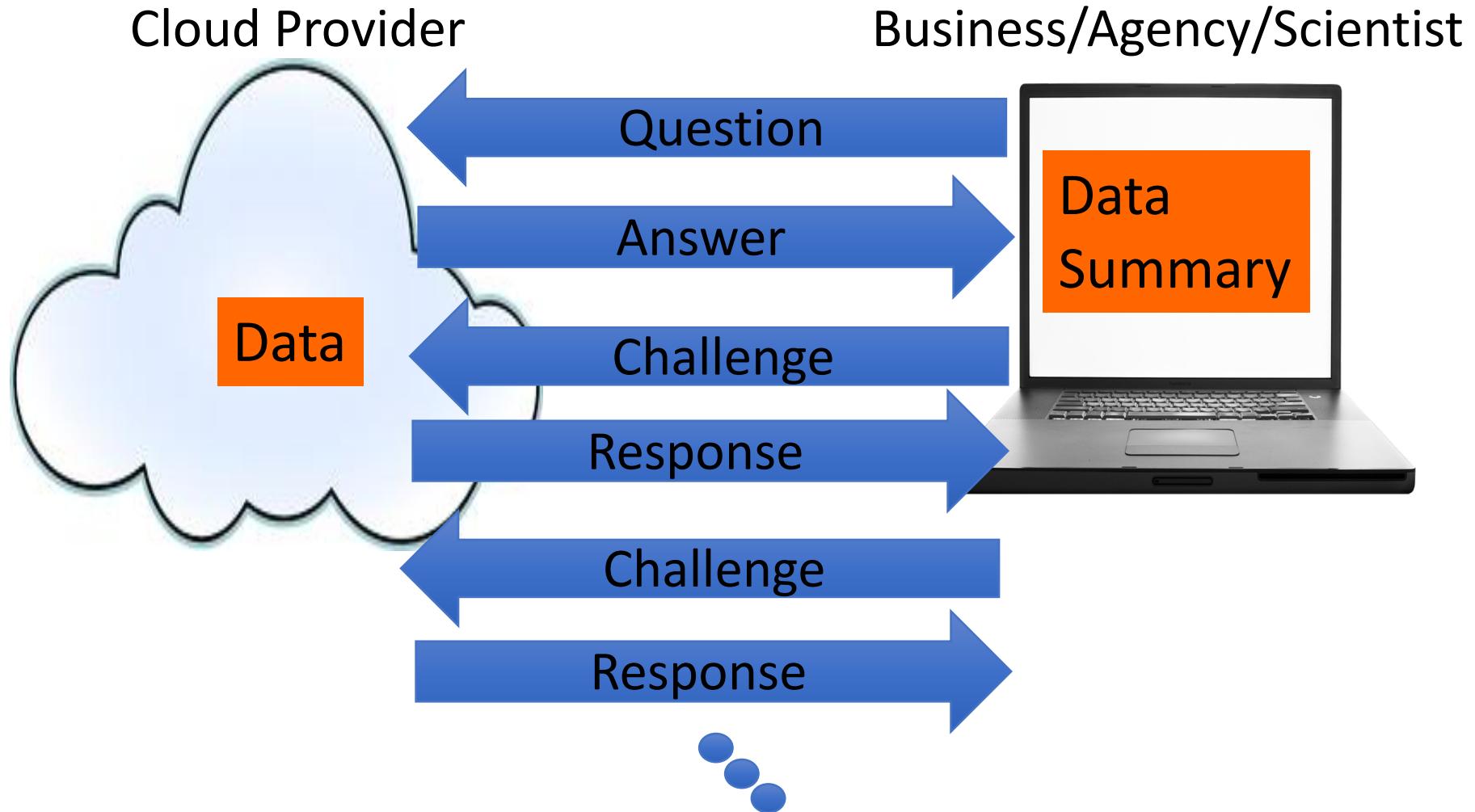
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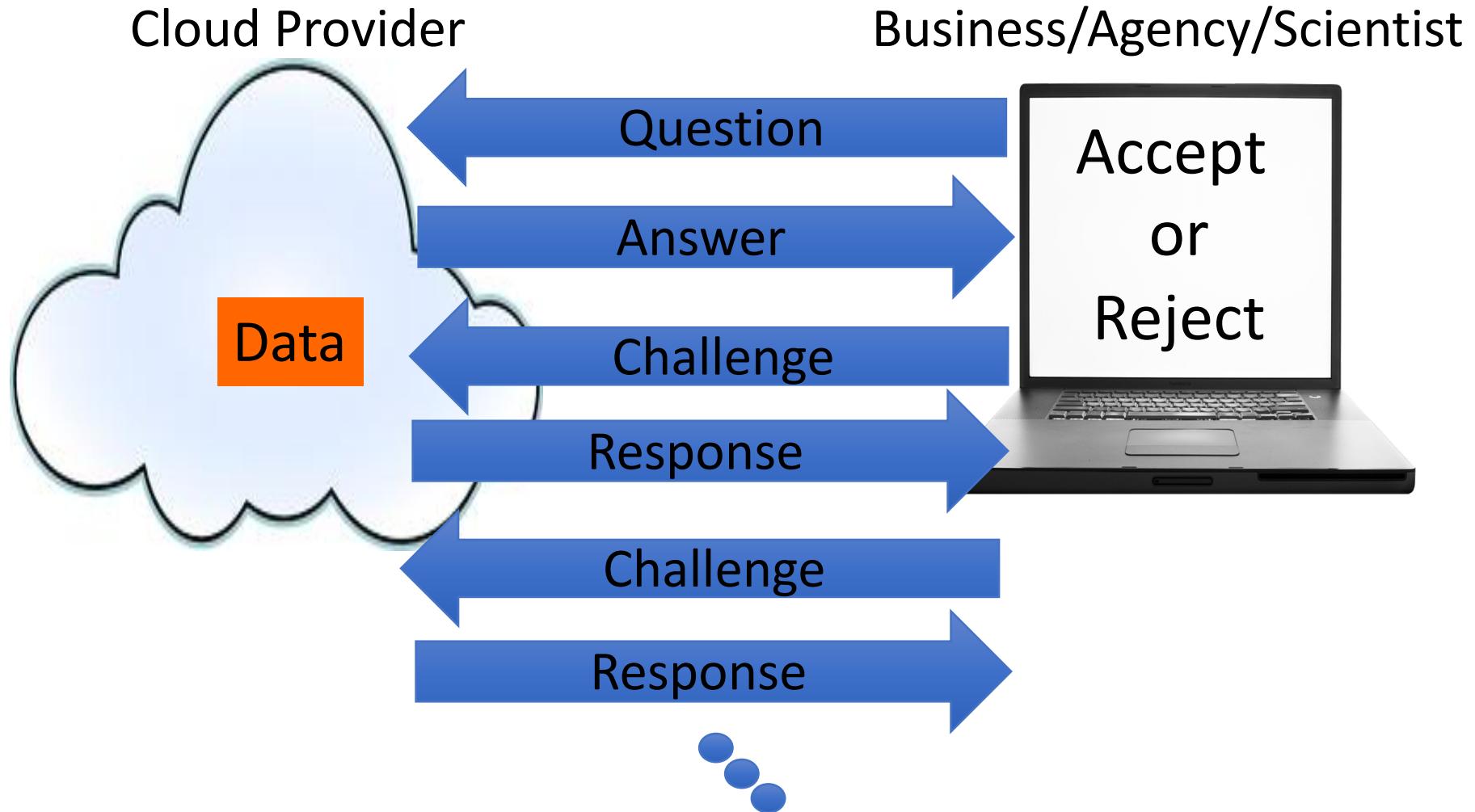
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# Doubly-Efficient Interactive Proof

- A doubly-efficient interactive proof for a problem is one where:
  - $V$  runs in time linear in the input size.
  - $P$  runs in polynomial time.



# The Sum-Check Protocol: Technical Ideas

# Checking if two polynomials are equal

- Let  $g$  and  $h$  be two univariate degree- $d$  polynomials over field  $F$ .
- Goal: quickly determine if  $g$  and  $h$  are the **same** polynomial.

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- Let  $g$  and  $h$  be two univariate degree- $d$  polynomials over field  $F$ .
- Goal: quickly determine if  $g$  and  $h$  are the **same** polynomial.
- **Fact:** If  $g$  and  $h$  are not the same polynomial, then:

$$\Pr_{r \sim F}[g(r) \neq h(r)] \geq 1 - \frac{d}{|F|}.$$

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- Here's the (non-interactive!) proof:
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  - $\text{V}$  picks a random input  $r$  and confirms that  $h(r) = g_x(r)$ , rejecting if not.
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- [AW 2008]: MA communication complexity of Disjointness.
- [T16]: doubly-efficient MA protocol for counting triangles.

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  - This can be logarithmic in the size of a complete description of  $g_x$ .
    - E.g.,  $p(x_1, x_2, x_3, x_4, x_5) = 1 + x_1 + x_2 + 3x_3 + 7x_4 + 2x_1x_2 + 5x_3x_4 - x_3x_4x_5 + x_2x_3x_4 + 3x_1x_3x_4$ .
    - Total degree of  $p$  is only 3, but description is longer.

# Sum-Check Protocol [LFKN90]

- Goal: compute the quantity:

$$\sum_{b_1 \in \{0,1\}} \sum_{b_2 \in \{0,1\}} \dots \sum_{b_\ell \in \{0,1\}} g(b_1, \dots, b_\ell).$$

- Proof length is roughly the total degree of  $g$ .
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- $\textcolor{blue}{V}$  time is roughly the time to evaluate  $g$  at a single randomly chosen input.
- [CTY12, CMT12, T13, XZZPS19]: if  $g$  is “structured” then  $\textcolor{red}{P}$  can compute all of its messages during the sum-check protocol quickly.
  - Almost as fast as just solving the problem in the first place.

# A Polynomial-IP for Circuit-SAT [V<sub>T</sub>BW14]

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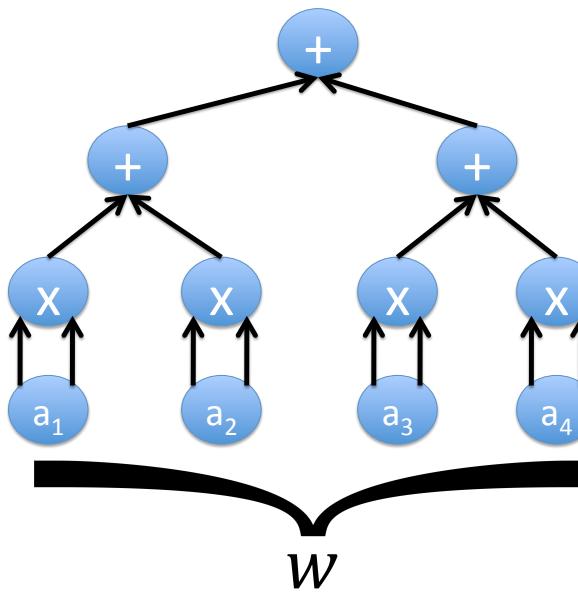
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- After that,  $\textcolor{red}{P}$  and  $\textcolor{blue}{V}$  execute a standard IP.
- Turn the polynomial-IP into a SNARK by having  $\textcolor{red}{P}$  commit to  $h$  via a polynomial commitment, then use Fiat-Shamir to remove interaction.

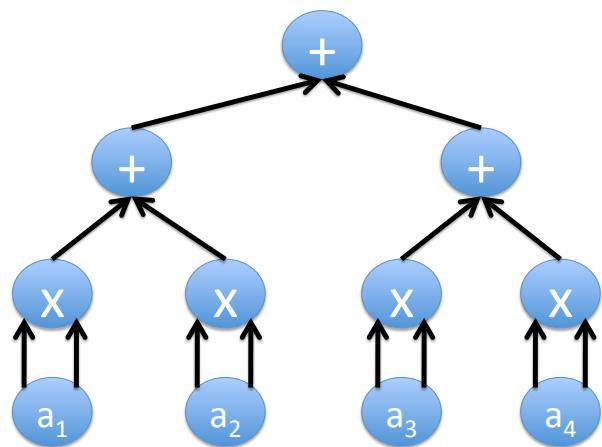
# Arithmetic Circuit Satisfiability

- Given: An arithmetic circuit  $C$  over  $F$  of size  $S$ .
- Goal: Determine if there exists a  $w$  such that  $C(w) = 1$ .

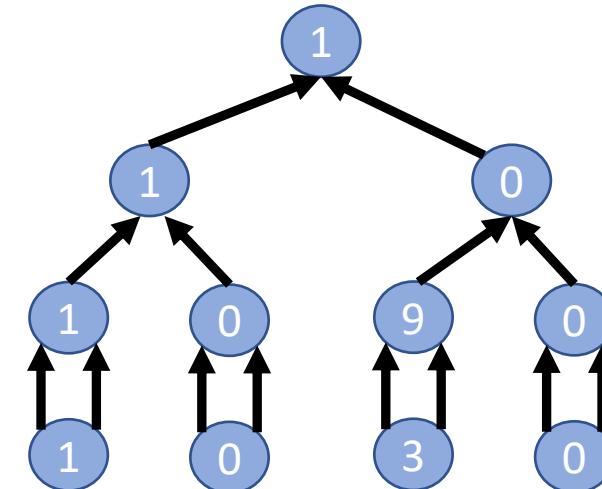


# Arithmetic Circuit Satisfiability

- A **transcript**  $T$  for  $C$  is an assignment of a value to every gate.
  - $T$  is a **correct** transcript if assigns the gate values obtained by evaluating  $C$  on a valid witness  $w$ .



Circuit-SAT instance  $C$



Correct transcript for  $C$

# The Start of the Polynomial-IP

- Assign each gate in  $C$  a  $(\log S)$ -bit label and view  $T$  as a function mapping gate labels to  $F$ .
- $\mathbf{P}$ 's first message is a  $(\log S)$ -variate polynomial  $h$  claimed to **extend** a correct transcript  $T$ , which means:

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- $\text{V}$  needs to check this, but is only able to learn a few evaluations of  $h$ .

**Key Lemma:** Given any  $(\log S)$ -variate polynomial  $h$ , there is a related  $(3\log S)$ -variate polynomial  $g_h$  such that:

$h$  **extends** a correct transcript  $T \Leftrightarrow g_h(a, b, c) = 0 \forall (a, b, c) \in \{0,1\}^{3\log S}$ .  
Moreover, to evaluate  $g_h(r)$  at any input  $r$ , suffices to evaluate  $h$  at only 3 inputs.

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- Proof sketch (simplification):

$$g_h(a, b, c) := \widetilde{\text{add}}(a, b, c) \cdot \left( h(a) - (h(b) + h(c)) \right) + \widetilde{\text{mult}}(a, b, c) \cdot \left( h(a) - h(b) \cdot h(c) \right).$$

- Ensures that:
  1.  $g_h(a, b, c) = h(a) - (h(b) + h(c))$  if  $a$  is the label of a gate that computes the **sum** of gates  $b$  and  $c$ .
  2.  $g_h(a, b, c) = h(a) - h(b) \cdot h(c)$  if  $a$  is the label of a gate that computes the **product** of gates  $b$  and  $c$ .
  3.  $g_h(a, b, c) = 0$  otherwise.

# The Polynomial IP for Circuit-SAT

- Recall: P's first message is a polynomial  $h$  claimed to **extend** a correct transcript T.
- By key lemma:  $h$  **extends** a correct transcript  $\Leftrightarrow g_h(a, b, c) = 0 \forall (a, b, c) \in \{0,1\}^{3 \log S}$ .

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- $\text{V}$  checks this by running sum-check protocol with  $\text{P}$  to compute:

$$\sum_{a,b,c \in \{0,1\}^{\log S}} g_h(a, b, c)^2.$$

- If all terms in the sum are 0, the sum is 0.
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  - To do so, suffices to evaluate  $h(r_1), h(r_2), h(r_3)$ .
  - Outside of these evaluations,  $\text{V}$  runs in time  $O(\log S)$ .
  - $\text{P}$  performs  $O(S)$  field operations given a witness  $w$ .

# Trustless Setup SNARKs in One Slide

# Trustless Setup SNARKs

1. Spartan [Setty 20] extends the polynomial IP from circuit-SAT to R1CS, among other improvements.
  - See Srinath's talk later today!
  - Other recent papers (Quarks [SL21], Cerberus [LSTW21]) plug in different polynomial commitment schemes to get SNARKs with different performance profiles.

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3. Marlin/Aurora/Fractal: use univariate rather than multivariate polynomials.
  - They give a “univariate sum-check protocol”.
  - Pro: univariate sum-check is only one round, rather than logarithmically many.
    - So if compiled to a SNARK using a polynomial commitment scheme with constant proof size, the resulting proofs are constant-sized (see Marlin, Plonk, etc.)
    - But such polynomial commitments (e.g., KZG) require a trusted setup, aren't post-quantum.
  - Con:  $P$  costs tend to be higher.

Thank you!

# Univariate Sum-Check

- Univariate sum-check:
  - Let  $H$  be a multiplicative subgroup of  $\mathbf{F}$ ,  $g$  be a low-degree univariate polynomial, and  $\mathbf{Z}_H$  is the “vanishing polynomial” of  $H$ .
  - Lemma:  $\sum_{a \in H} g(a) = 0$  if and only if there exists polynomials  $p$  and  $q$  of degree  $d - n$  and  $n - 1$  such that:

$$g(X) = p(X) \cdot \mathbf{Z}_H(X) - X \cdot q(X).$$

- To prove that  $\sum_{a \in H} g(a) = 0$ ,  $\textcolor{red}{P}$  can send two low-degree polynomials  $p$  and  $q$  such that  $g(X) = p(X) \cdot \mathbf{Z}_H(X) - X \cdot q(X)$ .
  - $\textcolor{blue}{V}$  can pick a random  $r$  and check that  $g(r) = p(r) \cdot \mathbf{Z}_H(r) - r \cdot q(r)$ .