

Exercises

Page Replacement Algorithms

E-1: Compute page faults with a memory of 3 frames for the page trace listed below by using LRU (Stack) replacement policy.

Page Trace: 1, 2, 3, 1, 4, 3, 2, 3, 5, 2, 6

E-2: Compute page faults with a memory of 3 frames for the page trace of **E-1** by using optimal replacement policy.

E-3: Possibility of having same page faults for each replacement policy (FIFO, Optimal, LRU)

E-4: Compute page faults with a memory of 3 frames for the following page trace by using additional reference bits (4 bit shift counter) replacement policy.

Page Trace: 1, 3, 1, 4, 5, 2, 4, 3, 2, 6

E-5: Compute page faults with a memory of 4 frames for the page trace listed below by using LRU (Stack) replacement policy.

Page Trace: 1, 2, 4, 1, 2, 3, 6, 1, 4, 3, 1, 4, 7

E-6: Compute page faults with a memory of 3 frames for the page trace in E-5 by using FIFO replacement policy.