

TO SYNC PULSE GENERATOR TO DI GENERATOR —0 xJ1 1 LO_{XF1} 2 O XJ1 2 —О хЈ1 з -0 xF1 O XFI O XJ1 4 O XFI O xJ1 6 INT_IØMHZ_SEL FH_G > 0 XJ1 7 G_GENLOCK ——O XF1 В FH_M > O XJ1 8 SH-4 OVENTEMP > —0 xF1 9 O xJ1 9 O xF1 10 SH-5 BB9_OK > -0 x.11 10 O XF1 11 F8_G >----—0 xJ1 11 SH-4 FREQ_SEL > —0 xJ1 12 F4_M > TCODECLK X O XF1 13 —0 xJ1 13 DICLK > SH-4 | SCL1 | O XF1 | 13 | O XF1 | 14 | SDA1 | O XF1 | 15 ----O ×J1 TX_COM > O XJ1 MRES XFI 16 MREST > O XJ 1 16 SH-4 | TX_COM > O XF1 17 TDI X O XJ1 17 RX_COM > —0 xF1 18 TMS7 > 0 XJ1 18 ——O XJ1 19 O xF1 19 тск > TDO > 0 XJ1 20 O xF1 20 TO SYNC PULSE GENERATOR TO TIMECODE O XK1 1 —0 xG1 1 O XGI 2 ——O хк1 з —0 хG1 з 0 XK1 4 O XG1 4

BB7_OK →

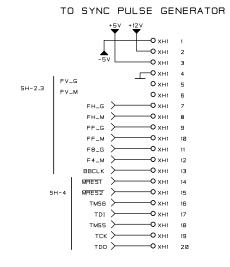
888_OK >

IØMHZ_SEL

—O xG1 9

—0 xei ia

—0 xg1 11



FIRST USED IN: PT5210 PCB REF.: 4008 117 0626

O XK1 6

—0 xk1

—0 xк1

—0 xк1

O XK1

O XK1

TDI > 0 XK1 17

TMSØ > O XK1 18

TCK > 0 XK1 19

O XK1 16

——O xк1 2∅

FH_M > O XK1 B
FF_G > O XK1 9

TCODECLK > O XK1

FF_M >

FB_G >

F4_M >

SCL1 >

SDA1 >

тро >

MREST >

		MAIN BOARD						4	97-87	2-10
			400	4008 109 82			90	4	98-06	6 - 15
								3	96-11-	18
CHECK:		NAME: SNJ/SMa/JML/SMa		6 sr	SH	130	- 6	3	97-81	-21
FABCDE82904.SCM A06265.PCE		J					DAT. S	96-	03-28	AЗL