## TO SYNC PULSE GENERATOR TO DI GENERATOR —0 xJ1 1 LO<sub>XF1</sub> 2 O XJ1 2 —О хЈ1 з -0 xF1 O XFI O XJ1 4 O XFI O xJ1 6 INT\_IØMHZ\_SEL FH\_G > 0 XJ1 7 G\_GENLOCK ——O XF1 В FH\_M > O XJ1 8 SH-4 OVENTEMP > —0 xF1 9 O xJ1 9 O xF1 10 SH-5 BB9\_OK > -0 x.11 10 O XF1 11 FB\_G > —O XJ1 11 SH-4 FREQ\_SEL > —0 xJ1 12 F4\_M > TCODECLK X O XF1 13 —0 xJ1 13 DICLK > SH-4 | SCL1 | O XF1 | 13 | O XF1 | 14 | SDA1 | O XF1 | 15 ----O ×J1 TX\_COM > O XJ1 MRES XFI 16 MREST > ----O ×J1 SH-4 | TX\_COM > O XF1 17 TDI X O XJ1 17 RX\_COM > —0 xF1 18 TMS7 > 0 XJ1 18 ——O XJ1 19 O xF1 19 тск >-TDO > 0 XJ1 20 O xF1 20 TO SYNC PULSE GENERATOR TO TIMECODE +12V O XK1 1 —0 xG1 1 O XK1 2 O XGI 2 ——O хк1 з —0 хG1 з 0 XG1 4 0 XK1 4 O XK1 6

## TO SYNC PULSE GENERATOR

BB4\_OK > O XG1 6 BB5\_OK > O XG1 7 886\_OK > O XGI 8

DICLK > O XGI 13

SCL2 O XGI 14

5DA2 > 0 XG1 15

MRES3 > O XG1 16

TMS1 > 0 XG1 17

TMS2 > 0 XG1 18

TMS3 > O XG1 19

TM54 > O XG1 2Ø

BB7\_OK ➤

888\_OK >

IØMHZ\_SEL

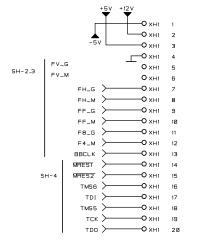
DI\_SELECT

—O xG1 9

—0 xei ia

—0 xg1 11

—O xG1 12



FIRST USED IN: PT5210 PCB REF.: 4008 117 0626

FH\_M > O XK1 B FF\_G > O XK1 9

TCODECLK > O XK1

—0 xk1

—0 xк1

—0 xк1

O XK1

——O XK1 16

O XK1 17

O XK1 19

——O xк1 2∅

O XK1

TMSØ > O XK1 18

FF\_M >

FB\_G >

F4\_M >

SCL1 >

SDA1 >

трі >—

тск >-

тро >

MREST >

											4	97-97	-10
			MAIN BOARD			8	109		8290		4	98-06	i - 15
								3	96-11-	18			
CHECK:		NAME: SI	NJ/SMa/JML/SN	/la		6	SH	SH	130	- 6	3	97-01	-21
FABCDE82904.SC A06265.PC		J Pro	TELEVISION	TECHNOLO	GIES A/	'S	DK-26	605 BI	RØNDBY	DAT. S	96 -	03-28	AЗL