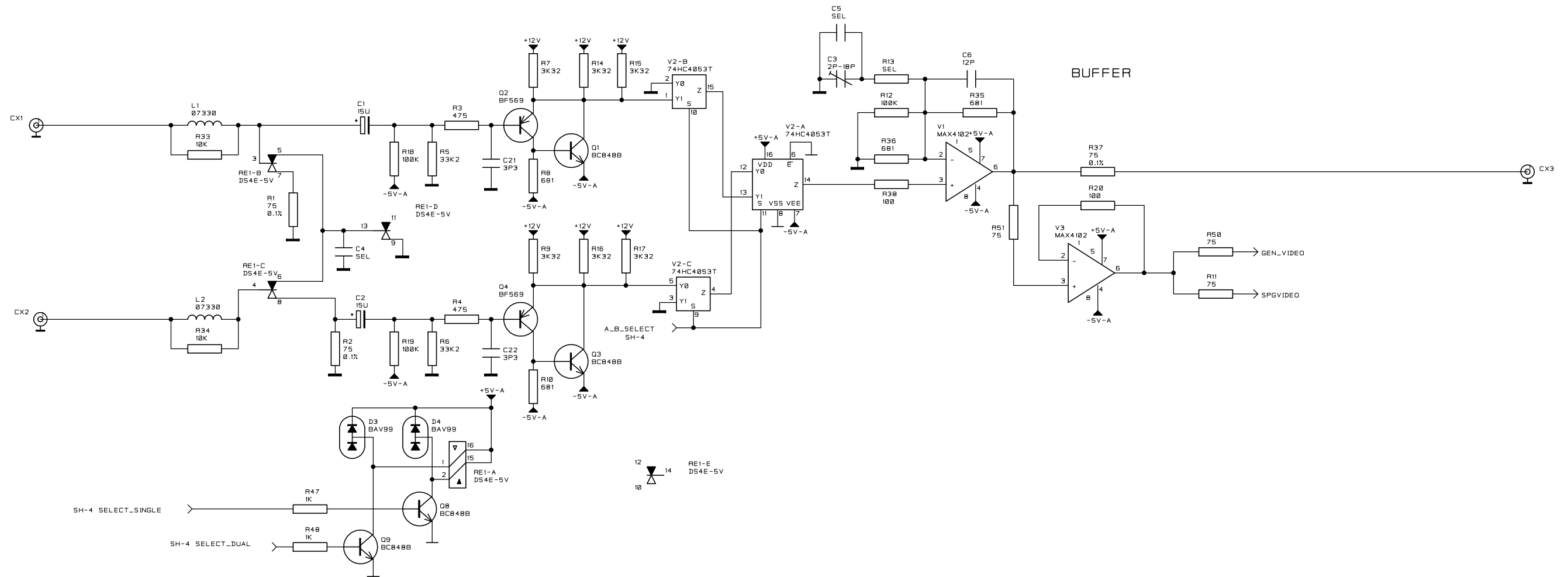
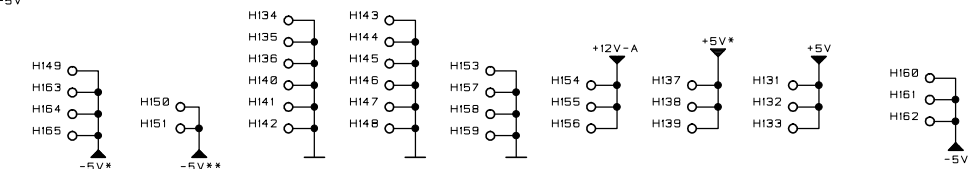
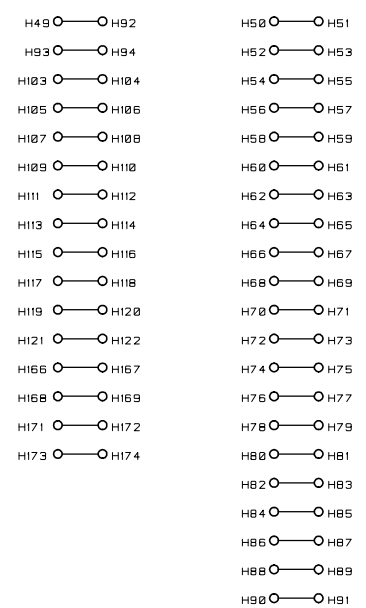
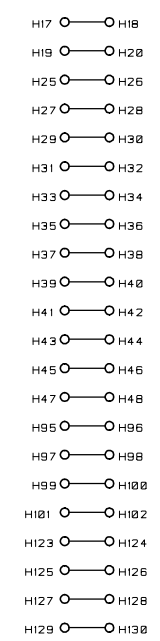
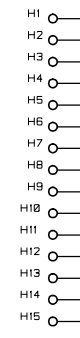
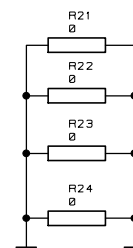
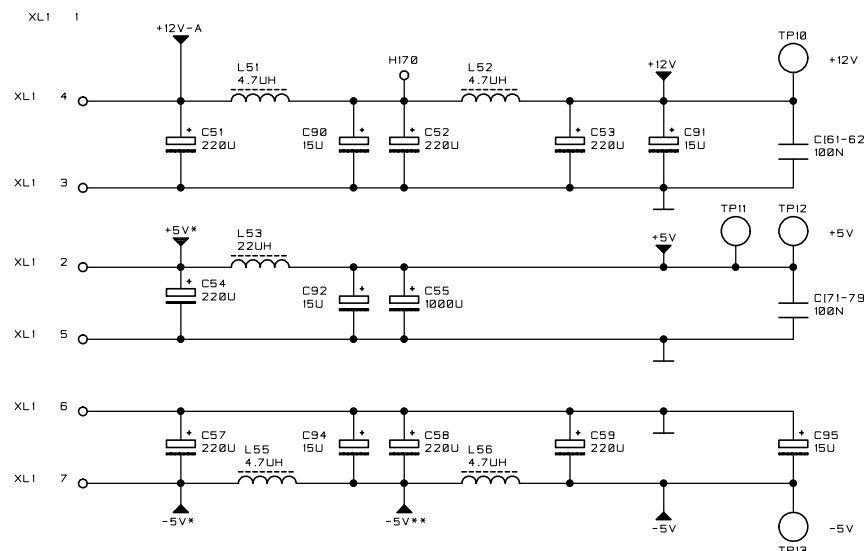


INPUT SELECTOR

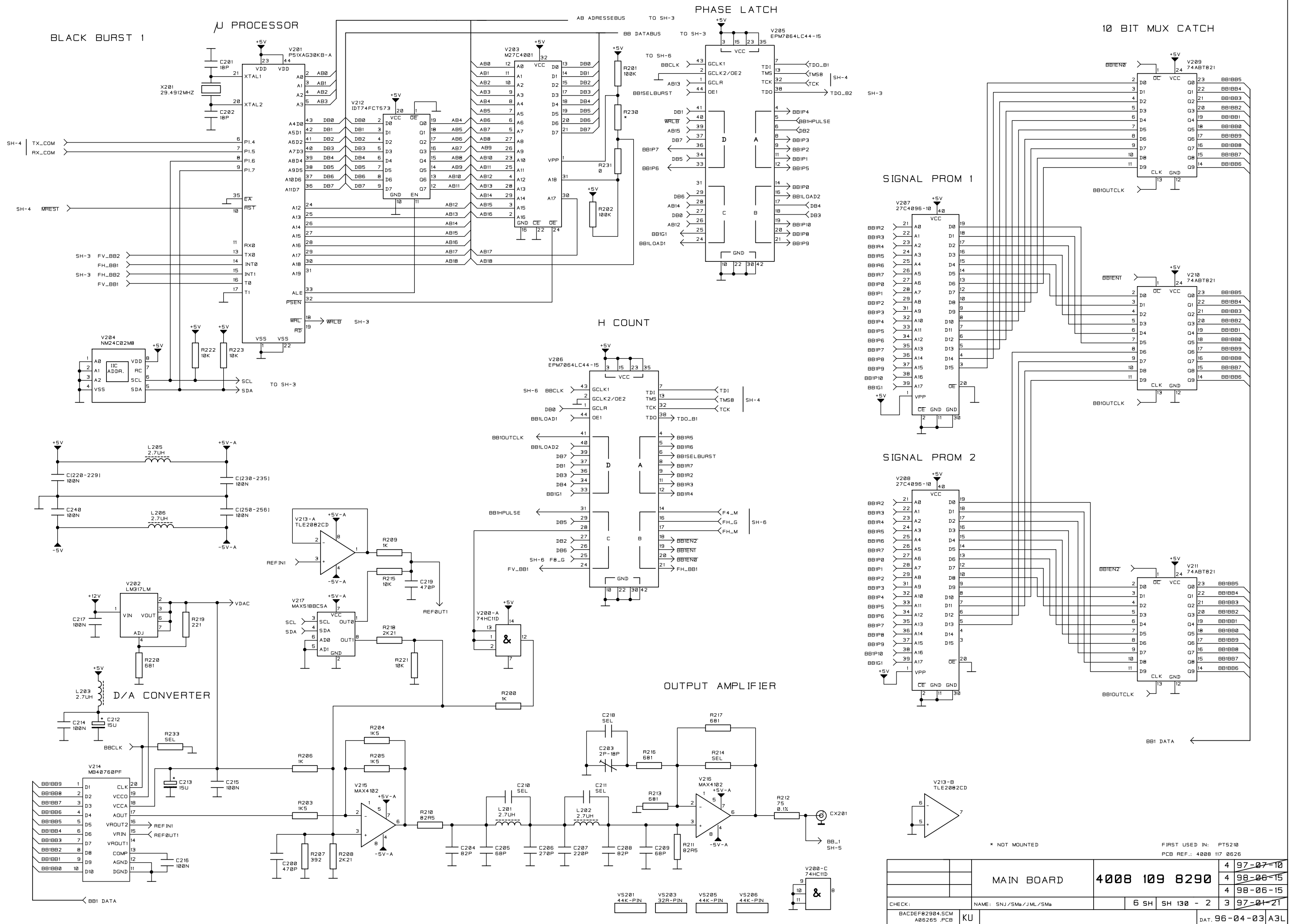


BUFFER



FIRST USED IN: PT5210
PCB REF.: 4008 117 0626

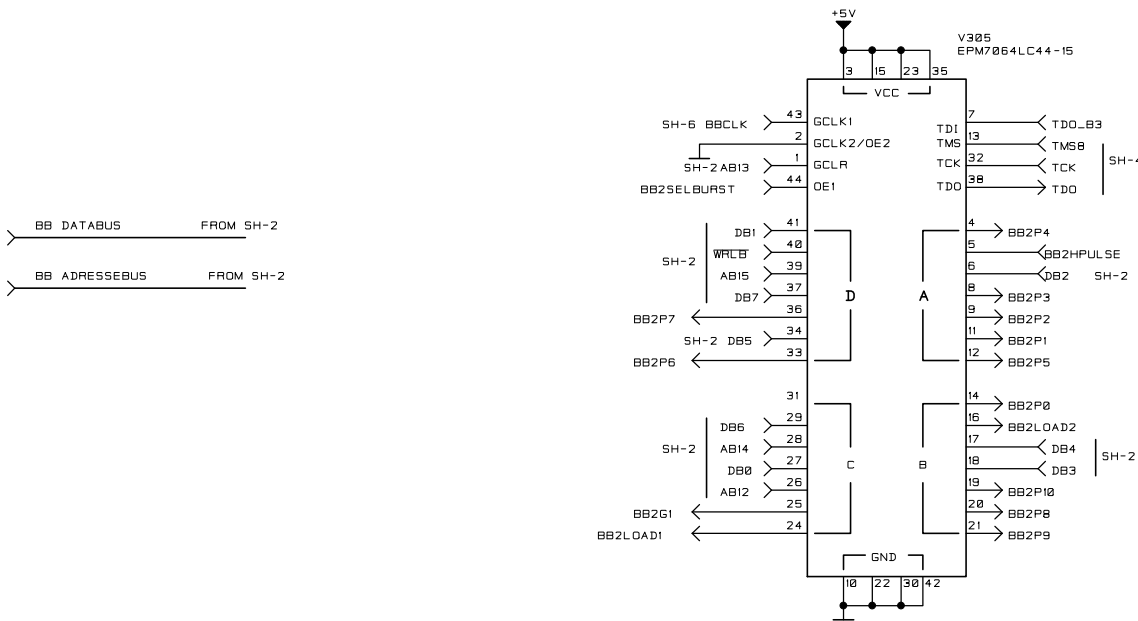
		MAIN BOARD		4008 109 8290		3	97-08-27
						4	97-07-10
						4	98-06-15
CHECK:		NAME: PF/SMa/JML/SMa		6 SH	SH 130 - 1	3	97-01-21
ABCDEF82904.SCM		A06265.PCB		KU		DAT. 96-04-03 A3L	



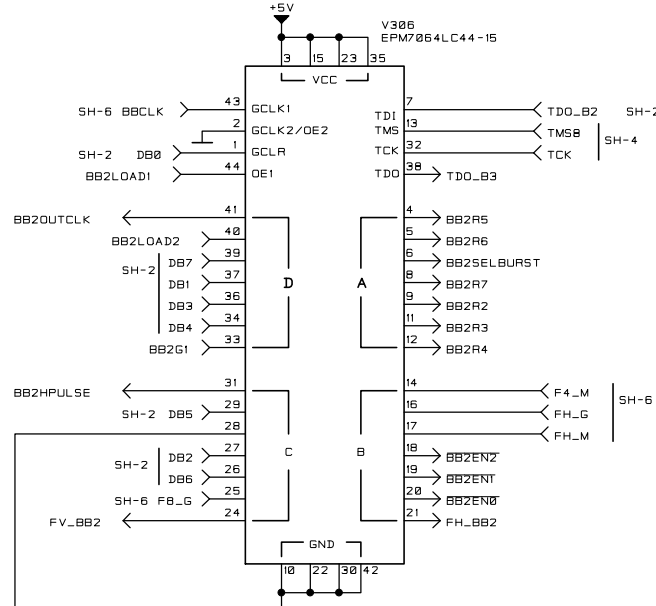
		MAIN BOARD		4008 109 8290		4 97-07-10	
						4 98-06-15	
						4 98-06-15	
CHECK:		NAME: SNJ / Sma / J.ML / Sma		6 SH		SH 130 - 2	
BACDEF82904.SCM A06265 .PCB		KU				3 97-01-21	
						DAT. 96-04-03 A3L	

BLACK BURST 2

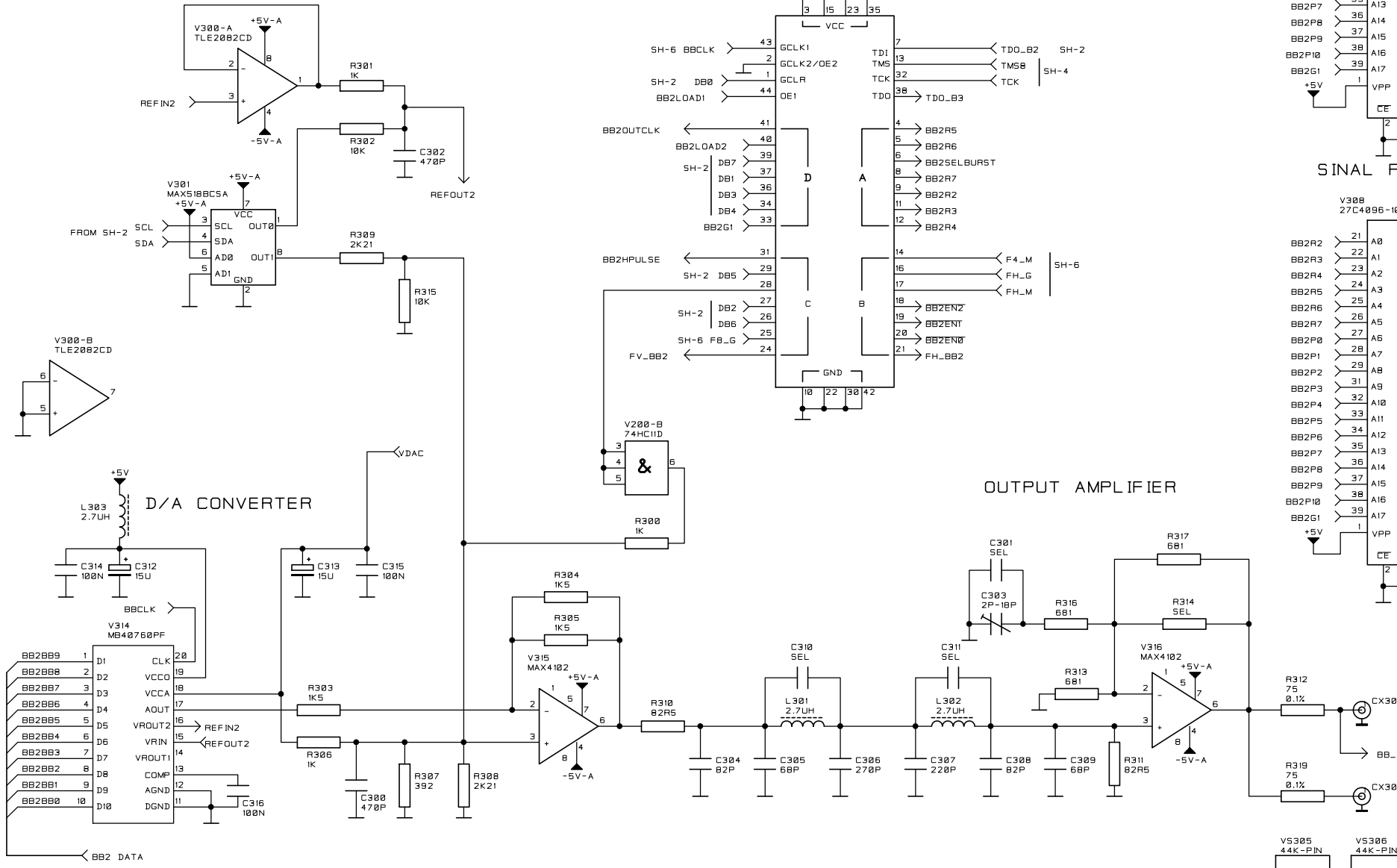
PHASE LATCH



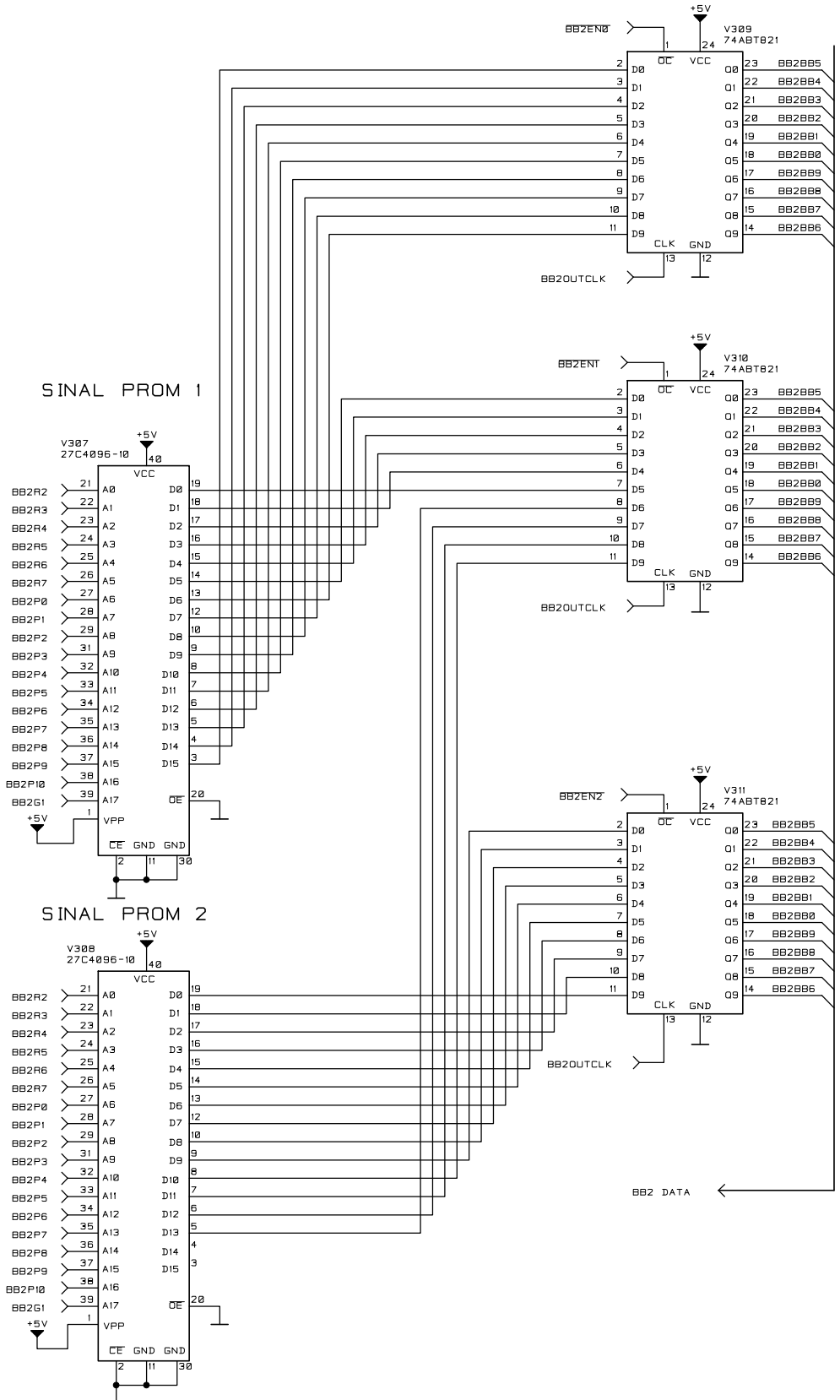
H COUNT



OUTPUT AMPLIFIER

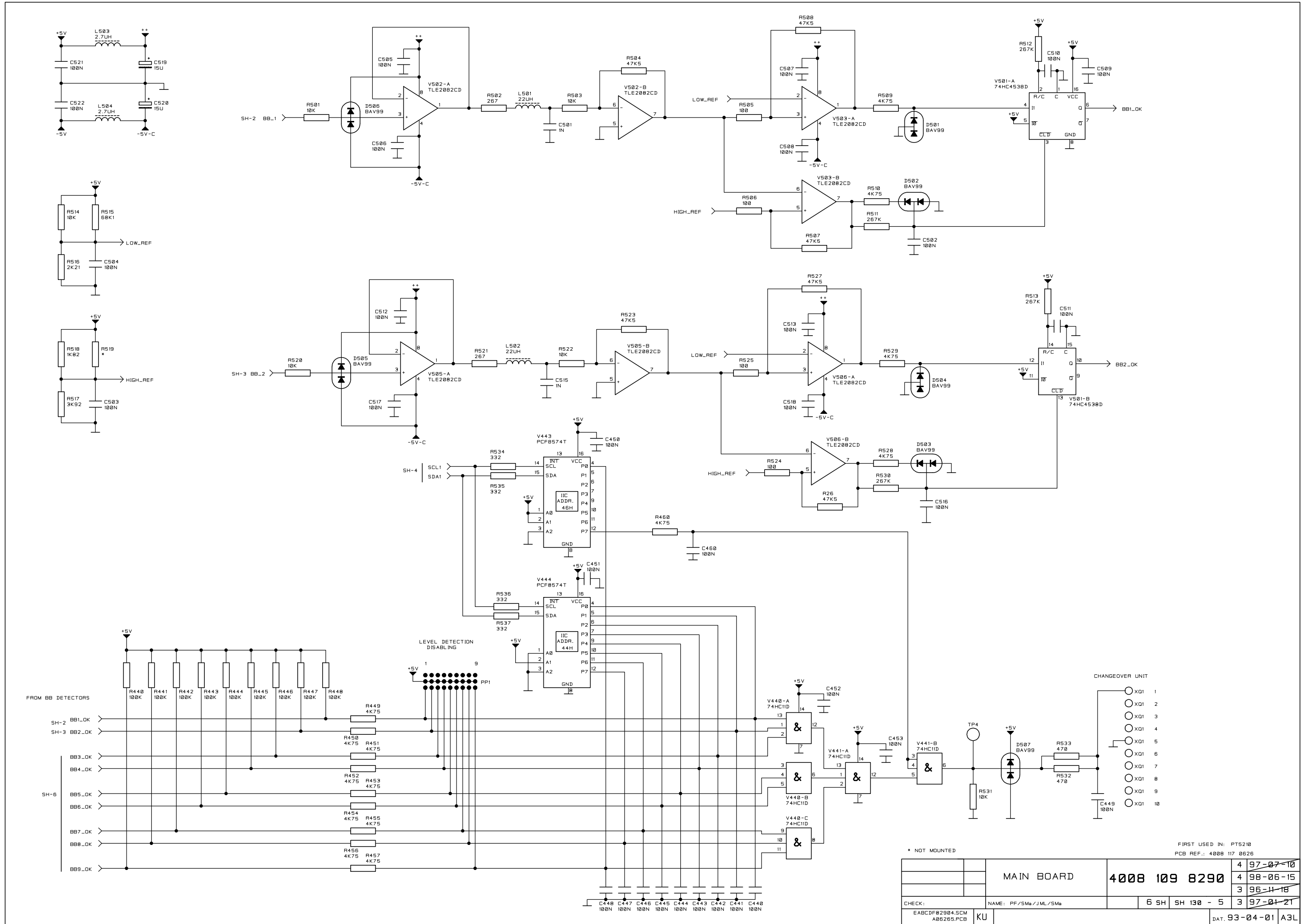


10 BIT MUX LATCH



FIRST USED IN: PT5210
PCB REF.: 4008 117 0626

		MAIN BOARD	4008 109 8290		4	97-07-10	
					4	98-06-15	
					3	96-11-21	
CHECK:		NAME: SNJ/SMa/JML/SMa		6 SH	SH 130 - 3	3	97-01-21
CABDEF029045CM A06265.PCB		KU				DAT. 96-04-03	A3L

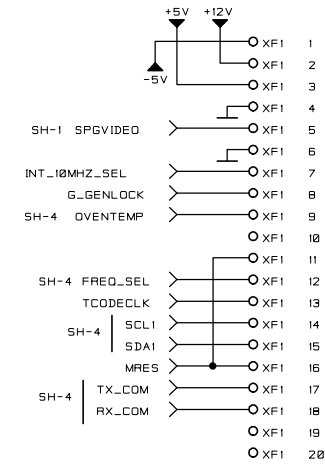


* NOT MOUNTED

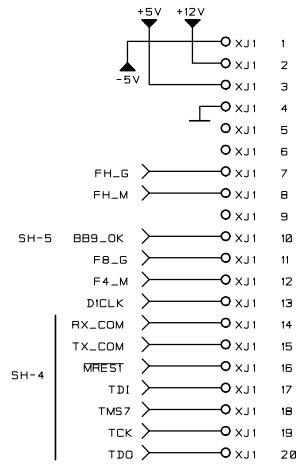
FIRST USED IN: PT5210
PCB REF.: 4008 117 0626

		MAIN BOARD		4008 109 8290		4	97-07-10
						4	98-06-15
						3	96-11-18
CHECK:		NAME: PF/SMa/JML/SMa		6 SH SH 130 - 5		3	97-01-21
EABCD F82004.SCM A06265.PCB		KU				DAT. 93-04-01 A3L	

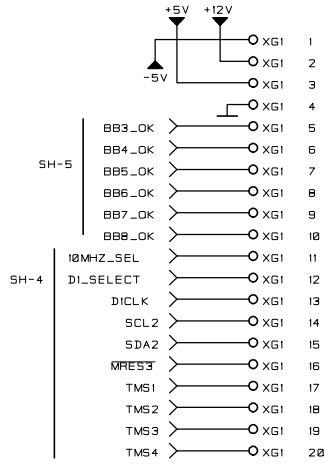
TO SYNC PULSE GENERATOR



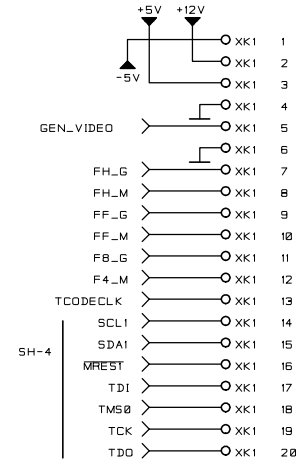
TO D1 GENERATOR



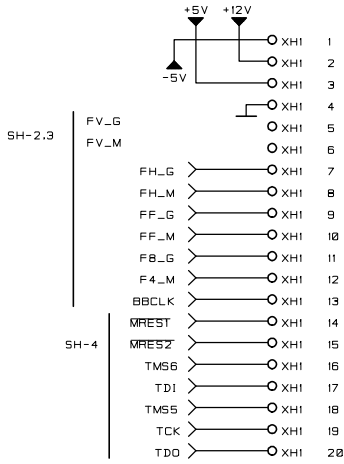
TO SYNC PULSE GENERATOR



TO TIMECODE



TO SYNC PULSE GENERATOR



FIRST USED IN: PT5210
PCB REF.: 4008 117 0626

		MAIN BOARD	4008 109 8290	4	97-07-10
				4	98-06-15
				3	96-11-18
CHECK:		NAME: SNJ/SMa/JML/SMa	6 SH	SH 130 - 6	3 97-01-21
FABCD82904.SCM A06265.PCB		KU	DAT. 96-03-28 A3L		