

# KEVIN PRAKASH

kevinprakash30@gmail.com ◇ [kevinprakash.github.io](https://github.com/kevinprakash) ◇ +91-9482180235


## OBJECTIVE

---

Reliable and resourceful verification engineer seeking a challenging role to hone my skills. Bringing new ideas to the organisation while expanding my thinking. With a deep passion for developing innovative solutions for complex engineering problems. Focus on collaborative problem solving to enhance team productivity.

## WORK EXPERIENCE

---

 <b>Synopsys Inc, Bangalore [5 Years]</b>	Jan 2020 - Present
<i>ASIC Digital Design, Staff Engineer</i>	<i>Jan 2024</i>
<i>ASIC Digital Design, Engineer II</i>	<i>Nov 2021</i>
<i>ASIC Digital Design, Engineer I</i>	<i>Nov 2020</i>
<i>Intern(Technical Engineer)</i>	<i>Jan 2020</i>

### USB IP

- Planning and development of EUSB2V2 features.
- Handled porting of VMM testbench to UVM methodology.
- Implemented logic for data throughput testing of USB 3.X controller.
- Developing and verifying latest xHCI feature.
- Enhancing AXI implementation to handle delay modelling.
- Adapting to latest internal methodology to maintain compliance with latest industry standards.
- Integrated AI feature to improve functional coverage closure time.
- Writing and maintaining scripts across the IP.
- Created functional coverage and assertions for testing the design.
- Worked on updating the RTL codebase to the latest methodology in terms of LINT, CDC and RDC.

## EDUCATION

---

<b>M.Tech Microelectronics (ME)</b> BITS (Autonomous), Pilani	<i>August 2022 - May 2024</i> CGPA : 8.05
<b>B.E. Electronics and Communication (ECE)</b> BMS College of Engineering(Autonomous), Bangalore	<i>August 2016 - May 2020</i> CGPA : 8.84
<b>KPUC Physics Chemistry Math Electronics (PCME)</b> CMR Pre University College, Bangalore	<i>July 2014 - June 2016</i> Overall Percentage: 85.5
<b>CBSE 10th Standard</b> SJR Public School, Bangalore	<i>June 2006 - July 2014</i> CGPA: 9.4

## PROJECTS

---

### Central Regression

Created a website to record all the regression details across all USB IPs. This provides a one point access to latest information with the ability to sort records based on various parameters. Created redundancies to retrieve database incase of failure. Was able to improve the teams productivity

### V.I.E.W- Visual Interpreter of Environment Wizard

Created a device that is capable of assisting the visually impaired in their day to day task using Machine Learning. Provides assistance in reading text, describing environment, navigation using maps. Using a Resnet 50, a CNN model was trained that could run fast and accurately on a low power device such as RPi. The user is given the option to provide voice input to activate these features. [\[Code\]](#)

### Module replacer

Script to replace a given module with a new module while mapping the signals based on user input provided across the entire design.

### Result Parser

Tools to quickly analyse FC results and generate a HTML page for easy accessibility

### ML Workshop

Conducted Workshop and Competition on Image Classification as a part of UTSAV (BMSCE Cultural Fest). Contestants were taught the concepts of CNN and then had to create a CNN model with high accuracy [\[Website\]](#)

## TECHNICAL STRENGTHS

---

<b>Programming</b>	System Verilog, VHDL, C++, Python, Shell, Perl, TCL, Assembly C, HTML, CSS, JS
<b>Methodologies</b>	UVM, VMM
<b>Software &amp; Tools</b>	VCS, Verdi, Spyglass, MS Office, L <sup>A</sup> T <sub>E</sub> X

## CERTIFICATIONS

---

Verification of methodology with System Verilog and UVM (Verikwest)	<a href="#">[Certificate]</a>
Machine Learning by Stanford University (Andrew Ng):	<a href="#">[Certificate]</a>
Computer Vision (The School of AI)	<a href="#">[Certificate]</a>
Matlab Certification from BMSCE	