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M17 Protocol Specification

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Contents

Licenses	7
Introduction	8
Glossary	9
1 Physical Layer	10
1.1 4-level Frequency-shift Keying Modulation (4FSK)	10
1.2 Dibit, Symbol, and Frequency-shift	10
1.3 4FSK Generation	10
1.4 Transmission	11
1.4.1 Preamble	11
1.4.2 Synchronization Burst (Sync Burst)	11
1.4.3 Payload	11
1.4.4 Randomizer	11
1.4.5 End of Transmission marker (EoT)	12
1.4.6 Carrier-sense Multiple Access (CSMA)	12
1.5 Physical Layer Flow Summary	13
2 Data Link Layer	14
2.1 Frame	14
2.2 Forward Error Correction (FEC)	14
2.3 Modes	15
2.4 Synchronization Burst (Sync Burst)	15
2.5 Link Setup Frame (LSF)	16
2.5.1 LSF DST and SRC	16
2.5.2 LSF TYPE	16
2.5.3 LSF META	17
2.5.4 LSF CRC	17
2.5.5 LSF Contents ECC/FEC	18
2.6 Stream Mode	19
2.6.1 Stream Frames	20
2.6.2 Stream Superframes	22
2.7 Packet Mode	23
2.7.1 Packet Frames	23
2.7.2 Packet Superframes	25
2.8 BERT Mode	25
2.8.1 BERT Frames	26
3 Application Layer	28
3.1 M17 Amateur Radio Voice Application	28

3.1.1	LSF	28
3.1.2	Encryption Types	29
3.1.3	Channel Access Number (CAN)	34
3.1.4	Stream Frames	34
3.1.5	Digital Signatures	35
3.2	Packet Application	35
4	IP Networking	37
4.1	Standard IP Framing	37
A	Address Encoding	38
A.1	Callsign Encoding	38
A.2	Encoded Addresses	39
A.3	Encoder Example	40
A.4	Decoder Example	41
B	Randomizer Sequence	42
C	Convolutional Encoder	43
D	Golay Encoder	44
E	Code Puncturing	46
F	Interleaving	48
F.1	References	51
G	BERT Details	52
G.1	PRBS Generation	52
G.2	PRBS Receiver	53
G.2.1	Synchronization	53
G.2.2	Counting Bit Errors	54
G.2.3	Resynchronization	55
G.3	References	55
H	File Formats	56
H.1	Glossary	56
H.1.1	Bit numbering, Bit order, Most significant bit (MSB), Least significant bit (LSB)	56
H.1.2	Deviation, Frequency Deviation	56
H.1.3	Deviation Function (Transmit)	56
H.1.4	Deviation Function (Receive)	56
H.1.5	Dibit	56
H.1.6	Endianness, Byte order, Big-endian (BE), Little-endian (LE)	57
H.1.7	RF Sample Rate	57
H.1.8	Root-raised-cosine (RRC) Filter	57
H.1.9	Symbol	57
H.1.10	Symbol Rate	57
H.2	File Extensions	57
H.2.1	aud	57
H.2.2	sym	58
H.2.3	bin	58
H.2.4	rrc	58

H.2.5 dev	58
H.3 Example file flows	58
H.3.1 Transmit	58
H.3.2 Receive	58
H.4 To-Do	58
H.5 References	59
I GNU Free Documentation License	60
1. APPLICABILITY AND DEFINITIONS	60
2. VERBATIM COPYING	62
3. COPYING IN QUANTITY	62
4. MODIFICATIONS	62
5. COMBINING DOCUMENTS	64
6. COLLECTIONS OF DOCUMENTS	64
7. AGGREGATION WITH INDEPENDENT WORKS	64
8. TRANSLATION	65
9. TERMINATION	65
10. FUTURE REVISIONS OF THIS LICENSE	65
11. RELICENSING	66
ADDENDUM: How to use this License for your documents	66
J GNU General Public License, version 2	67

List of Tables

1.1	Dibit symbol mapping to 4FSK deviation	10
1.2	Physical Layer Transmission	11
1.3	Physical Layer Transmission with Multiple Synchronization Bursts	11
2.1	Frame	14
2.2	Bit Types	15
2.3	Frame Specific Sync Bursts	16
2.4	Link Setup Frame Contents	16
2.5	LSF TYPE layout	16
2.6	Packet/Stream indicator	17
2.7	Data type	17
2.8	Encryption type	17
2.9	Key lengths for encryption subtypes	17
2.10	CRC Test Vectors	18
2.11	Stream Mode	19
2.12	Link Information Channel Contents	20
2.13	LICH_CNT and LSF bits	20
2.14	Stream Contents	20
2.15	STREAM Payload Examples	21
2.16	LICH and Stream Combined	21
2.17	Single Packet	23
2.18	Packet Mode	23
2.19	Packet Contents	23
2.20	Packet Metadata Field with EOF = 0	24
2.21	Packet Metadata Field with EOF = 1	24
2.22	Packet Mode	26
2.23	BERT Contents	26
3.1	Link Setup Frame Contents	28
3.2	M17 Voice LSF TYPE definition	29
3.3	Null encryption subtype bits	29
3.5	GNSS Data encoding	31
3.6	Extended Callsign Data encoding	32
3.7	Scrambling	32
3.8	AES key lengths	34
3.9	AES counter	34
3.10	Packet protocol identifiers	36
A.1	M17 Callsign Alphabet	38
A.2	M17 Addresses	39

B.1	Randomizer values	42
D.1	Golay encoder details	45
H.1	File extensions	57

List of Figures

1.1	4FSK Generation	10
1.2	Physical Layer Flow	13
2.1	Transmit Contents to Payload	15
2.2	Receive Payload to Contents	15
2.3	LSF Construction	19
2.4	Stream Frame Construction	22
2.5	Stream Superframes	23
2.6	Packet Frame Construction	25
2.7	BERT Frame Construction	27
3.1	8-bit LFSR taps	33
3.2	16-bit LFSR taps	33
3.3	24-bit LFSR taps	33
C.1	Convolutional encoder	43
G.1	Traditional form LFSR	52
G.2	M17 LFSR	52
G.3	M17 PRBS9 Generator	53
G.4	M17 PRBS9 Synchronization	53
G.5	M17 PRBS9 Validation	54

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Introduction

M17 is an RF protocol that is:

- Completely open: open specification, open source code, open source hardware, open algorithms. Anyone must be able to build an M17 radio and interoperate with other M17 radios without having to pay anyone else for the right to do so.
- Optimized for amateur radio use.
- Simple to understand and implement.
- Capable of doing the things hams expect their digital protocols to do:
 - Voice (eg: DMR, D-Star, etc)
 - Point to point data (eg: Packet, D-Star, etc)
 - Broadcast telemetry (eg: APRS, etc)
 - Extensible, so more capabilities can be added over time.

To do this, the M17 protocol is broken down into three protocol layers, like a network:

1. Physical Layer: How to encode 1s and 0s into RF. Specifies RF modulation, symbol rates, bits per symbol, etc.
2. Data Link Layer: How to packetize those 1s and 0s into usable data. Packet vs Stream modes, headers, addressing, etc.
3. Application Layer: Accomplishing activities. Voice and data streams, control packets, beacons, etc.

This document attempts to document these layers.

Glossary

Common terms used in M17

BER Bit Error Rate

ECC Error Correcting Code

FEC Forward Error Correction

Frame The individual components of a stream, each of which contains payload data interleaved with frame signalling.

Link Setup Frame (LSF) The first data frame of any transmission. It contains full link information data.

LICH Link Information Channel. The LICH carries all information of an M17 link. The first frame of a transmission contains full link setup data, and subsequent frames each contain one sixth of this data, so that late-joiners can obtain the full link setup data information.

Packet A single burst of data transmitted in packet mode.

Superframe A set of six consecutive frames which collectively contain full LICH data are grouped into a superframe.

Chapter 1

Physical Layer

This section describes the M17 standard radio physical layer suitable for use where a transmission bandwidth of 9 kHz is permitted.

1.1 4-level Frequency-shift Keying Modulation (4FSK)

The M17 standard uses 4FSK at 4800 symbols/s (9600 bits/s) with a deviation index $h=1/3$ for transmission in a 9 kHz channel bandwidth. Minimum channel spacing is 12.5 kHz.

1.2 Dibit, Symbol, and Frequency-shift

Each of the 4-level frequency-shifts can be represented by dibits (2-bit values) or symbols, as shown in Table 1 below.

In the case of dibits, the most significant bit is sent first. When four dibits are grouped into a byte, the most significant dibit of the byte is sent first. For example, the four dibits contained in the byte 0xB4 (0b 10 11 01 00) would be sent as the symbols (-1, -3, +3, +1).

Dibit		Symbol	Deviation
MSB	LSB		
0	1	+3	+2.4 kHz
0	0	+1	+0.8 kHz
1	0	-1	-0.8 kHz
1	1	-3	-2.4 kHz

Table 1.1: Dibit symbol mapping to 4FSK deviation

1.3 4FSK Generation



Figure 1.1: 4FSK Generation

Dibits are converted to symbols. The symbol stream is upsampled to a series of impulses which pass through a root-raised-cosine ($\alpha=0.5$) shaping filter before frequency modulation at the transmitter and again after frequency demodulation at the receiver.

Upsampling by a factor of 10 is recommended (48000 samples/s).

The root-raised-cosine filter should span at least 8 symbols (81 taps at the recommended up-sample rate).

1.4 Transmission

A complete transmission shall consist of a Preamble, a Synchronization Burst, Payload, and an End of Transmission marker.

PREAMBLE	SYNC BURST	PAYLOAD	EoT
40ms	16 bits	Multiples of 2 bits	40ms
(192 symbols)	(8 symbols)	(multiples of 1 symbol)	(192 symbols)

Table 1.2: Physical Layer Transmission

Transmissions may include more than one synchronization burst followed by a payload.

PREAMBLE	SYNC BURST	PAYLOAD	...	SYNC BURST	PAYLOAD	EoT
----------	------------	---------	-----	------------	---------	-----

Table 1.3: Physical Layer Transmission with Multiple Synchronization Bursts

1.4.1 Preamble

Every transmission shall start with a preamble, which shall consist of 40 ms (192 symbols) of alternating outer symbols (+3, -3) or (-3, +3), see section 2.4 for details. To ensure a zero crossing prior to a synchronization burst, the last symbol transmitted within the preamble shall be opposite the first symbol transmitted in the synchronization burst.

1.4.2 Synchronization Burst (Sync Burst)

A sync burst of 16 bits (8 symbols) shall be sent immediately after the preamble. The sync burst is constructed using only outer symbols, with codings based on Barker codes. Properly chosen sync burst coding assists in symbol clocking and alignment. Different sync burst codes may also be used by the Data Link Layer to identify the type of payload to follow.

1.4.3 Payload

Payload shall be transmitted in multiples of 2 bits (1 symbol).

1.4.4 Randomizer

To avoid transmitting long sequences of constant symbols (e.g. +3, +3, +3, ...), a simple randomizing algorithm is used. At the transmitter, all payload bits shall be XORed with a pseudorandom predefined sequence before being converted to symbols. At the receiver, the ran-

domized payload symbols are converted to bits and are again passed through the same XOR algorithm to obtain the original payload bits.

The pseudorandom sequence is composed of the 46 bytes (368 bits) found in the appendix Randomizer Sequence.

Before each bit of payload is converted to symbols for transmission, it is XORed with a bit from the pseudorandom sequence. The first payload bit is XORed with most significant bit (bit 7) of sequence byte 0 (0xD6), second payload bit with bit 6 of sequence byte 0, continuing to the eighth payload bit and bit 0 of sequence byte 0. The ninth payload bit is XORed with bit 7 of sequence byte 1 (0xB5), tenth payload bit with bit 6 of sequence byte 1, etc.

When payload bits have XORed through sequence byte 45 (0xC3), the pseudorandom sequence is restarted at sequence byte 0 (0xD6).

On the receive side, symbols are converted to randomized payload bits. Each randomized payload bit is converted back to a payload bit by once again XORing each randomized bit with the corresponding pseudorandom sequence bit.

1.4.5 End of Transmission marker (EoT)

Every transmission ends with a distinct symbol stream, which shall consist of 40 ms (192 symbols) of a repeating (0x55) (0x5D) (+3, +3, +3, +3, +3, +3, -3, +3) pattern.

1.4.6 Carrier-sense Multiple Access (CSMA)

CSMA may be used to minimize collisions on a shared radio frequency by having the sender ensure the frequency is clear before transmitting. Higher layers (Data Link and Application) may require the use of CSMA, and may specify parameters other than the defaults.

P-persistent access is used with a default probability of $p = 0.25$ and default slot time of 40 ms.

1.5 Physical Layer Flow Summary



Figure 1.2: Physical Layer Flow

Chapter 2

Data Link Layer

2.1 Frame

A Frame shall be composed of a frame type specific Synchronization Burst (Sync Burst) followed by 368 bits (184 symbols) of Payload. The combination of Sync Burst plus Payload results in a constant 384 bit (192 symbol) Frame. At the M17 data rate of 4800 symbols/s (9600 bits/s), each Frame is exactly 40ms in duration.

There are four frame types each with their own specific Sync Burst: Link Setup Frames (LSF), Bit Error Rate Test (BERT) Frames, Stream Frames, and Packet Frames.

SYNC BURST	PAYLOAD
16 bits (8 symbols)	368 bits (184 symbols)

Table 2.1: Frame

2.2 Forward Error Correction (FEC)

The Data Link Layer Contents of a specific frame are modified using various Error Correction Code (ECC) methods. Applying these codes at the transmitter allows the receiver to correct some amount of induced errors in a Forward Error Correction (FEC) process. It is this ECC/FEC data that is inserted into the Payload portion of the Frame. The exact ECC/FEC techniques used vary by frame type.

Applying ECC/FEC may be a multi-step process. To distinguish data bits at the various stages of the process, Bit Types are defined as shown in the following table. It is important to note that not all ECC/FEC processes utilize both Type 2 and Type 3 bits. Prior to decoding Data Link Layer contents, a receiver would need to convert incoming bits from Type 4 back to Type 1 bits, which may also include conversion through Type 3 and/or Type 2 bits. The exact ECC/FEC methods and Bit Types utilized will be indicated for each frame type.

Type	Description
Type 1	Data link layer content bits
Type 2	Bits after appropriate encoding
Type 3	Bits after puncturing
Type 4	Interleaved (re-ordered) bits

Table 2.2: Bit Types

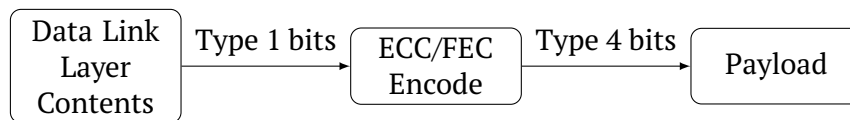


Figure 2.1: Transmit Contents to Payload

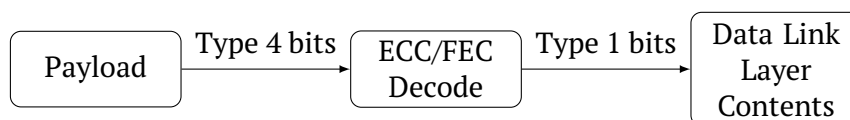


Figure 2.2: Receive Payload to Contents

2.3 Modes

The Data Link layer shall operate in one of three modes during a Transmission.

- Stream Mode Data are sent in a continuous stream for an indefinite amount of time, with no break in physical layer output, until the stream ends. e.g. voice data, bulk data transfers, etc. Stream Mode shall start with an LSF and is followed by one or more Stream Frames.
- Packet Mode Data are sent in small bursts, up to 823 bytes at a time, after which the physical layer stops sending data. e.g. messages, beacons, etc. Packet Mode shall start with an LSF and is followed by one to 33 Packet Frames.
- BERT Mode PRBS9 is used to fill frames with a deterministic bit sequence. Frames are sent in a continuous sequence. Bert Mode shall start with a BERT frame, and is followed by one or more BERT Frames.

NOTE As is the convention with other networking protocols, all values and data structures are encoded in big endian byte order.

2.4 Synchronization Burst (Sync Burst)

All frames shall be preceded by 16 bits (8 symbols) of Sync Burst. The Sync Burst definition straddles both the Physical Layer and the Data Link Layer.

Only LSF and BERT Sync Bursts may immediately follow the Preamble, and each requires a different Preamble symbol pattern as shown in the table below.

During a Transmission, only one LSF Sync Burst may be present, and if present, it shall immediately follow the Preamble.

BERT Sync Bursts, if present, may only follow the Preamble or other BERT frames.

Multiple Stream or Packet Sync Bursts may be present during a Transmission, depending on the mode.

Frame Type	Preamble	Sync Burst Bytes	Sync Burst Symbols
LSF	+3, -3	0x55 0xF7	+3, +3, +3, +3, -3, -3, +3, -3
BERT	-3, +3	0xDF 0x55	-3, +3, -3, -3, +3, +3, +3, +3
Stream	None	0xFF 0x5D	-3, -3, -3, -3, +3, +3, -3, +3
Packet	None	0x75 0xFF	+3, -3, +3, +3, -3, -3, -3, -3

Table 2.3: Frame Specific Sync Bursts

2.5 Link Setup Frame (LSF)

The LSF is the initial frame for both Stream and Packet Modes and contains information needed to establish a link.

Field	Length	Description
DST	48 bits	Destination address - Encoded callsign or a special number (eg. a group)
SRC	48 bits	Source address - Encoded callsign of the originator or a special number (eg. a group)
TYPE	16 bits	Information about the incoming data stream
META	112 bits	Metadata field, suitable for cryptographic metadata like IVs or single-use numbers, or non-crypto metadata like the sender's GNSS position.
CRC	16 bits	CRC for the link setup data

Table 2.4: Link Setup Frame Contents

Total: 240 Type 1 bits

2.5.1 LSF DST and SRC

Destination and source addresses may be encoded amateur radio callsigns, or special numbers. See the Address Encoding Appendix for details.

2.5.2 LSF TYPE

The TYPE field contains information about the frames to follow LSF. The Packet/Stream indicator bit determines which mode (Packet or Stream) will be used during the transmission. The remaining field meanings are defined by the specific mode and application.

Byte \ Bit	7	6	5	4	3	2	1	0
0	Reserved				Signed Stream	Channel Access Number...		
1	...	Encryption Subtype		Encryption Type		Data Type		Packet/Stream

Table 2.5: LSF TYPE layout

Value	Mode
0	Packet mode
1	Stream mode

Table 2.6: Packet/Stream indicator

Value	Content
00 ₂	Reserved
01 ₂	Data
10 ₂	Voice
11 ₂	Voice+Data

Table 2.7: Data type

Value	Encryption
00 ₂	None
01 ₂	Scrambler
10 ₂	AES
11 ₂	Other/reserved

Table 2.8: Encryption type

For the encryption subtype, meaning of values depends on encryption type.

Value	Scrambler	AES
00 ₂	8-bit	128-bit
01 ₂	16-bit	192-bit
10 ₂	24-bit	256-bit
11 ₂	reserved	reserved

Table 2.9: Key lengths for encryption subtypes

2.5.3 LSF META

The LSF META field is defined by the specific application.

2.5.4 LSF CRC

M17 uses a non-standard version of 16-bit CRC with polynomial $x^{16} + x^{14} + x^{12} + x^{11} + x^8 + x^5 + x^4 + x^2 + 1$ or 0×5935 and initial value of $0 \times \text{FFFF}$. This polynomial allows for detecting all errors up to hamming distance of 5 with payloads up to 241 bits, which is less than the amount of data in each frame.

As M17's native bit order is most significant bit first, neither the input nor the output of the CRC algorithm gets reflected.

The CRC field enables verification of the other 28 bytes forming the LSF: 6-byte DST, 6-byte SRC, 2-byte TYPE, and 14-byte META fields. Data integrity of an LSF frame is established by computing the CRC of the first 28 bytes and storing the resulting checksum in the trailing 2-byte CRC field, which can be compared by a recipient after repeating the same checksum process. Alternatively, a CRC computed over the entire 30-byte LSF frame, including a valid CRC field, will always equal zero.

The test vectors in the following table are calculated by feeding the given message to the CRC algorithm.

Message	CRC Output
(empty string)	0xFFFF
ASCII string "A"	0x206E
ASCII string "123456789"	0x772B
Bytes 0x00 to 0xFF	0x1C31

Table 2.10: CRC Test Vectors

2.5.5 LSF Contents ECC/FEC

The 240 Type 1 bits of the Link Setup Frame Contents along with 4 flush bits are convolutionally coded using a rate 1/2 coder with constraint $K=5$. 244 bits total are encoded resulting in 488 Type 2 bits.

Type 3 bits are computed by P_1 puncturing the Type 2 bits, resulting in 368 Type 3 bits.

Interleaving the Type 3 bits produces 368 Type 4 bits that are ready to be passed to the Physical Layer.

Within the Physical Layer, the 368 Type 4 bits are randomized and combined with the 16-bit LSF Sync Burst, which results in a complete frame of 384 bits ($384 \text{ bits} / 9600\text{bps} = 40 \text{ ms}$).

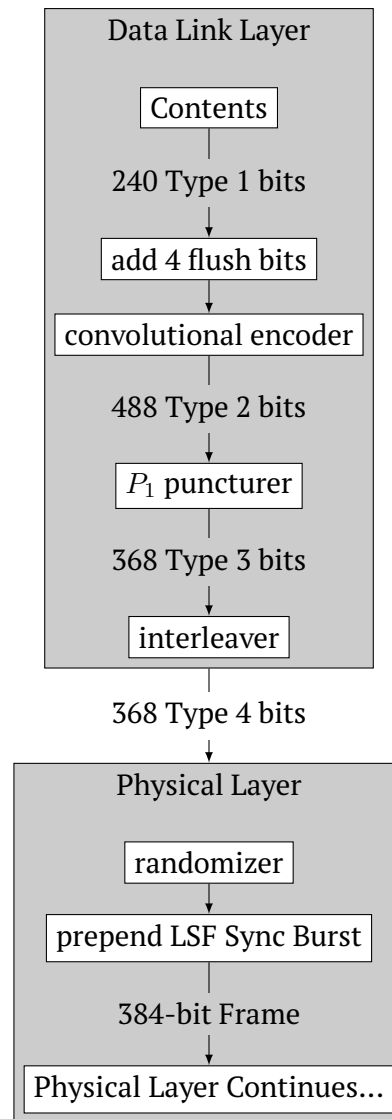


Figure 2.3: LSF Construction

2.6 Stream Mode

In Stream Mode, an *indefinite* amount of data is sent continuously without breaks in the physical layer. Stream Mode shall always start with an LSF that has the LSF TYPE Packet/Stream indicator bit set to 1 (Stream Mode). Other valid LSF TYPE parameters are selected per application.

Following the LSF, one or more Stream Frames may be sent.

PREAMBLE	LSF SYNC BURST	LSF FRAME	STREAM SYNC BURST	STREAM FRAME	...	STREAM SYNC BURST	STREAM FRAME	EoT
----------	----------------------	--------------	-------------------------	-----------------	-----	-------------------------	-----------------	-----

Table 2.11: Stream Mode

2.6.1 Stream Frames

Stream Frames are composed of frame signalling information contained within the Link Information Channel (LICH) combined with Stream Contents. Both the LICH and Stream Contents utilize different ECC/FEC mechanisms, and are combined at the bit level in a Frame Combiner.

Link Information Channel (LICH) The LICH allows for late listening and independent decoding to check destination address if the LSF for the current transmission was missed.

Each Stream Frame contains a 48-bit Link Information Channel (LICH). Each LICH within a Stream Frame includes a 40-bit chunk of the 240-bit LSF frame that was used to establish the stream. A 3-bit modulo 6 counter (LICH_CNT) is used to indicate which chunk of the LSF is present in the current Stream Frame. LICH_CNT starts at 0, increments to 5, then wraps back to 0.

Byte \ Bit	7	6	5	4	3	2	1	0
0	40-bit chunk of full LSF Contents (Type 1 bits)							
...								
4								
5	LICH_CNT				Reserved			

Table 2.12: Link Information Channel Contents

Total: 48 bits

The 40-bit chunks start from the beginning of the LSF.

LICH_CNT	LSF bits
0	0:39
1	40:79
2	80:119
3	120:159
4	160:199
5	200:239

Table 2.13: LICH_CNT and LSF bits

LICH Contents ECC/FEC The 48-bit LICH Contents is partitioned into 4 12-bit parts and encoded using Golay (24, 12) code. This produces 96 encoded Type 2 bits that are fed into the Frame Combiner.

Field	Length	Description
FN	16 bits	Frame Number
STREAM	128 bits	Stream data, can contain arbitrary data

Table 2.14: Stream Contents

Total: 144 Type 1 bits

The Frame Number (FN) starts from 0 and increments every frame to a maximum of $0 \times 7fff$ where it will then wrap back to 0. The most significant bit in the FN is used for transmission end signaling. When transmitting the last frame, it shall be set to 1 (one), and 0 (zero) in all other frames.

Stream data (STREAM) is obtained by extracting 128 bits at a time from the continuous stream of application layer data. If the last frame will contain less than 128 bits of valid data, the remaining bits should be set to zero. The stream may end at the frame boundary.

Mode	Codec 2 rate	Frame t + 0	Frame t + 1...
Voice	3200	128 bits encoded speech	128 bits encoded speech
Voice + Data	1600	64 bits encoded speech + 64 bits arbitrary data	64 bits encoded speech + 64 bits arbitrary data

Table 2.15: STREAM Payload Examples

Stream Contents ECC/FEC The 144 Type 1 bits of Stream Contents along with 4 flush bits are convolutionally coded using a rate 1/2 coder with constraint K=5. 148 bits total are encoded resulting in 296 Type 2 bits.

These bits are P_2 punctured to generate 272 Type 3 bits that are fed into the Frame Combiner.

Frame Combiner The 96 Type 2 bits of the ECC/FEC LICH Contents are concatenated with 272 Type 3 bits of the ECC/FEC Stream Contents resulting in 368 of combined Type 2/3 bits.

Field	Length	Description
LICH	96 bits	ECC/FEC LICH Contents Type 2 bits
STREAM	272 bits	ECC/FEC STREAM Contents Type 3 bits

Table 2.16: LICH and Stream Combined

Total: 368 Type 2/3 bits

Interleaving the Combined Type 2/3 bits produces 368 Type 4 bits that are ready to be passed to the Physical Layer.

Within the Physical Layer, the 368 Type 4 bits are randomized and combined with the 16-bit Stream Sync Burst, which results in a complete frame of 384 bits ($384 \text{ bits} / 9600\text{bps} = 40 \text{ ms}$).

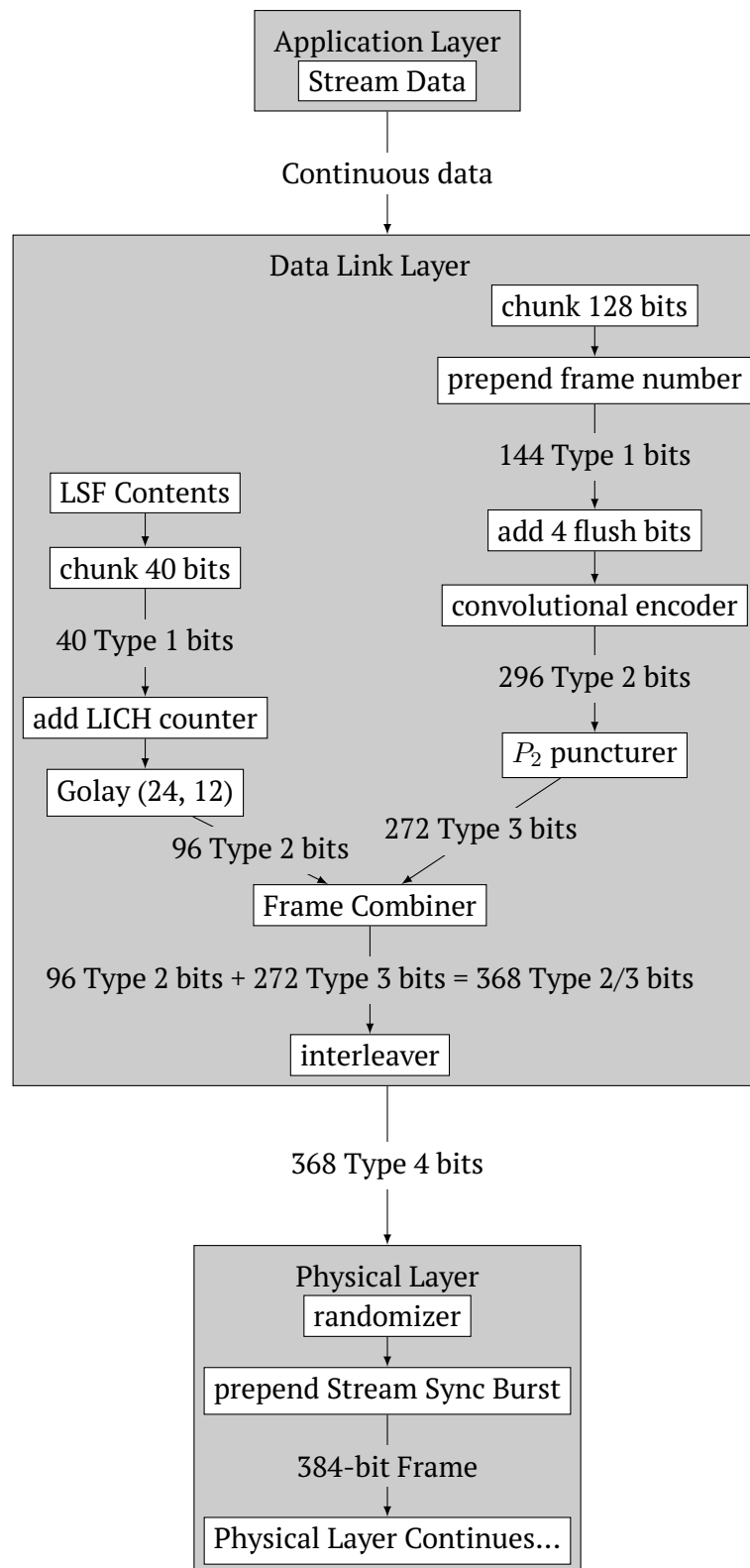


Figure 2.4: Stream Frame Construction

2.6.2 Stream Superframes

Stream Frames are grouped into Stream Superframes, which is the group of 6 frames that contain everything needed to rebuild the original LSF packet, so that the user who starts listening in the middle of a stream (late-joiner) is eventually able to reconstruct the LSF message and

understand how to receive the in-progress stream.

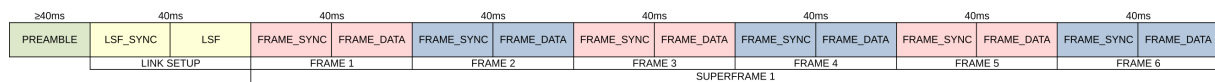


Figure 2.5: Stream Superframes

2.7 Packet Mode

In Packet Mode, a Single Packet with up to 823 bytes of Application Packet Data along with an appended two byte CRC may be sent over the physical layer during one Transmission. The total number of bytes is 825 (33×25).

Bytes	Meaning
0..n-1	Application Packet Data
n..n+1	CRC

Table 2.17: Single Packet

n is the number of bytes of the Application Packet Data. The CRC calculation used here is the same as described for LSF CRC.

Packet Mode shall always start with an LSF that has the LSF TYPE Packet/Stream indicator bit set to 0 (Packet Mode). Following the LSF, 1 to 33 Packet Frames may be sent.

Packet Mode achieves a base throughput of 5 kbps, a net throughput of approximately 4.7 kbps for the largest data payload, and over 3 kbps for 100-byte payloads. Net throughput takes into account preamble and link setup overhead. (TODO: recompute this)

PREAMBLE	LSF Sync Burst	LSF Frame	Packet Sync Burst	Packet Frame	...	Packet Sync Burst	Packet Frame	EoT
----------	----------------	-----------	-------------------	--------------	-----	-------------------	--------------	-----

Table 2.18: Packet Mode

2.7.1 Packet Frames

Packet Frames contain Packet Contents after ECC/FEC is applied.

Byte \ Bit	7	6	5	4	3	2	1	0
0	200-bit chunk of Single Packet							
...								
24								
25	End of Frame	Packet Frame/Byte Counter						

Table 2.19: Packet Contents

Packet Contents Total: 206 Type 1 bits

The packet metadata field contains the 1-bit End of Frame (EOF) indicator, and the 5-bit Packet Frame/Byte Counter. This is *NOT* to be confused with the LSF's 112-bit metadata field.

Data starting with the first byte of the Packet Data, and ending with 2 computed and appended CRC bytes (big-endian) is split in groups of 25 bytes (chunks). The CRC value is calculated over the whole Packet Data, including the terminating null-byte in the case of text. Each Packet Frame payload contains up to a 25-byte chunk of the Data. If fewer than 25 bytes can be extracted from the Data (i.e. for the last Packet Frame), the Data chunk is padded with zero bytes (after the terminating CRC) to reach 25 bytes total.

The Packet Frame Counter is reset to zero at the start of Packet Mode. For each Packet Frame where there is at least 1 byte remaining in the Packet Data after removing a 25-byte chunk, the EOF metadata bit is set to zero, the Packet Frame Counter value is inserted into the Packet Frame/Byte Counter metadata field, and the Packet Frame Counter is incremented afterwards.

When there are no bytes remaining in the Packet Data after removing a 25-byte (or less) chunk, the EOF bit is set to one, the Packet Byte Counter is set to the number of valid bytes present in the current frame (1 to 25) and both fields are concatenated into the Packet Frame/Byte Counter metadata field. This results in a minimum of 1 to a maximum of 33 Packet Frames per transmission. Packet Mode is ended with an End of Transmission frame.

Byte \ Bit	7	6	5	4	3	2	1	0
0	0	Frame number, 0..31						

Table 2.20: Packet Metadata Field with EOF = 0

Byte \ Bit	7	6	5	4	3	2	1	0
0	1	Number of bytes in frame, 1..25						

Table 2.21: Packet Metadata Field with EOF = 1

Packet Contents ECC/FEC The 206 Type 1 bits of the Packet Contents along with 4 flush bits are convolutionally coded using a rate 1/2 coder with constraint K=5. 210 bits total are encoded resulting in 410 Type 2 bits.

These bits are P_3 punctured to generate 368 Type 3 bits.

Interleaving the Type 3 bits produces 368 Type 4 bits that are ready to be passed to the Physical Layer.

Within the Physical Layer, the 368 Type 4 bits are randomized and combined with the 16-bit Packet Sync Burst, which results in a complete frame of 384 bits (384 bits / 9600bps = 40 ms).

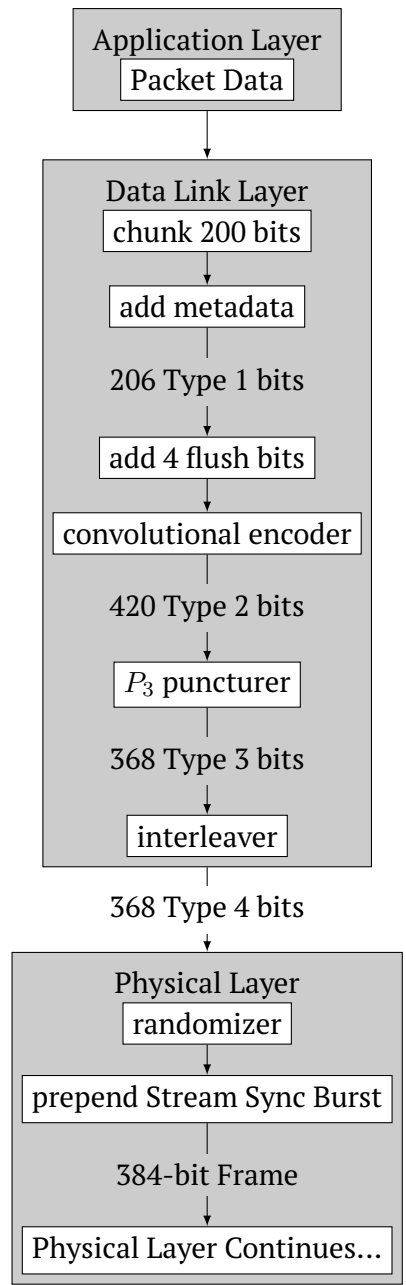


Figure 2.6: Packet Frame Construction

2.7.2 Packet Superframes

A Packet Superframe consists of up to the 33 Packet Frames used to reconstruct the original Single Packet.

2.8 BERT Mode

BERT mode is a standardized, interoperable mode for bit error rate testing. The preamble is sent, followed by an indefinite sequence of BERT frames. Notably, an LSF is not sent in BERT mode.

The primary purpose of defining a bit error rate testing standard for M17 is to enhance interoperability testing across M17 hardware and software implementations, and to aid in the

configuration and tuning of ad hoc communications equipment common in amateur radio.

PREAMBLE	BERT Sync Burst	BERT Frame	...	BERT Sync Burst	BERT Frame	EoT
----------	-----------------	------------	-----	-----------------	------------	-----

Table 2.22: Packet Mode

2.8.1 BERT Frames

BERT Frames contain BERT Contents after ECC/FEC is applied.

BERT Contents The BERT Contents consists of 197 bits from a PRBS9 generator. This is 24 bytes and 5 bits of data. The next BERT Contents starts with the 198th bit from the PRBS9 generator. The same generator is used for each subsequent BERT Contents without being reset. The number of bits pulled from the generator, 197, is a prime number. This will produce a reasonably large number of unique frames even with a PRBS generator with a relatively short period.

See the Appendix for BERT generation and reception details.

Bits	Meaning
0-196	BERT PRBS9 Payload

Table 2.23: BERT Contents

Total: 197 Type 1 bits

BERT Contents ECC/FEC The 197 Type 1 bits of the Packet Contents along with 4 flush bits are convolutionally coded using a rate 1/2 coder with constraint K=5. 201 bits total are encoded resulting in 402 Type 2 bits.

These bits are P_2 punctured to generate 368 Type 3 bits.

Interleaving the Type 3 bits produces 368 Type 4 bits that are ready to be passed to the Physical Layer.

This provides the same error ECC/FEC used for Stream Frames.

Within the Physical Layer, the 368 Type 4 bits are randomized and combined with the 16-bit BERT Sync Burst, which results in a complete frame of 384 bits (384 bits / 9600bps = 40 ms).

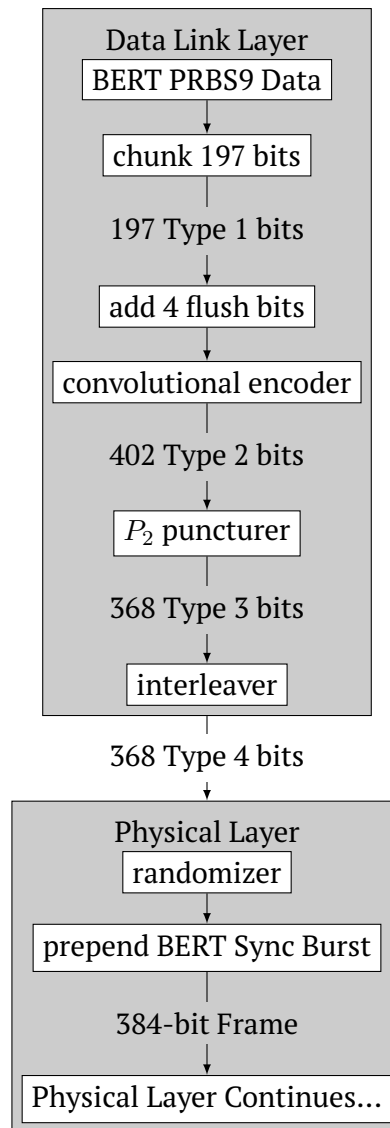


Figure 2.7: BERT Frame Construction

Chapter 3

Application Layer

3.1 M17 Amateur Radio Voice Application

This section defines the application layer parameters for an audio stream containing low bit rate speech encoded using the open source Codec 2 codec. It is intended to be used over the air by amateur radio operators worldwide. Implementation details for M17 clients, repeaters, and gateways ensure that an M17 Amateur Radio Voice Application is legal under all licensing regimes.

There are many applications and devices provided by various developers that support M17: clients, repeaters and hot-spots, and reflectors. An updated list of third-party applications is available [here](#). The use of these applications are not discussed in this document but can be found in various manuals and README files of those applications.

A Stream Mode Transmission begins with an Link Setup Frame, LSF.

3.1.1 LSF

Field	Length	Description
DST	48 bits	Destination address
SRC	48 bits	Source address
TYPE	16 bits	Information about the incoming data stream
META	112 bits	Metadata field

Table 3.1: Link Setup Frame Contents

Address fields Destination (DST) and source (SRC) addresses may be encoded amateur radio callsigns, or special identifiers. See the Address Encoding Appendix for details on how up to 9 characters of text can be encoded into the 6-byte address value.

The source address is always the callsign of the station transmitting, be it a client, repeater, or gateway. This is not a problem for a client, but for a repeater/gateway this raises issues about identifying the original source of a transmission. Having a repeater/gateway always use its own callsign for the source field does ensure that there are no issues with licensing authorities. To retain identification of the original source for a voice stream, an extended callsign data field will be encoded in the LSF META field.

The destination address used by a client may simply be a callsign or reflector designation for a point to point contact, or may be a special identifier. Special identifiers are 6-byte addresses that can't be encoded in the standard way. For an explanation, see the Address Encoding Appendix.

TYPE field The TYPE field contains information about the frames to follow LSF. For full definition see Section 2.5.2.

Packet/Stream	1 = Stream Mode
Data Type	10 ₂ = Voice only (3200 bps)
Encryption Type	00 ₂ = None 01 ₂ = Scrambling 10 ₂ = AES
Encryption Subtype	Depends on Encryption Type
Channel Access Number (CAN)	0..15

Table 3.2: M17 Voice LSF TYPE definition

This application requires Stream Mode.

The Voice only Data type indicator specifies voice data encoded at 3200 bps using Codec 2.

3.1.2 Encryption Types

Encryption is **optional**. The use of it may be restricted within some radio services and countries, and should only be used if legally permissible.

Null Encryption Encryption type = 00₂

When no encryption is used, the 14-byte (112-bit) META field of the LSF and corresponding LICH of the stream can be used for transmitting relatively small amounts of extended data without affecting the bandwidth available for the audio. The full 14 bytes of META extended data is potentially decodable every six stream frames, at a 240 ms update rate. The extended data is transmitted in a simple round robin manner, with the only exception being GPS data which should be transmitted as soon as possible after the GPS data is received from its source.

The “Encryption subtype” bits in the Stream Type field indicate what extended data is stored in the META field.

Encryption subtype bits	LSF META data contents
00 ₂	Text Data
01 ₂	GNSS Position Data
10 ₂	Extended Callsign Data
11 ₂	Reserved

Table 3.3: Null encryption subtype bits

Text Data The first byte of the Text Data is a Control Byte. To maintain backward compatibility, a Control Byte of 0x00 indicates that no Text Data is included.

Up to four Text Data blocks compose a complete message with a maximum length of 52 bytes. Each block may contain up to 13 bytes of UTF-8 encoded text, and is padded with space characters to fill any unused space at the end of the last used Text Data block.

The Control Byte is split into two 4-bit fields. The most significant four bits are a bit map of the message length indicating how many Text Data blocks are required for a complete message. There is one bit per used Text Data block, with 0001_2 used for one block, 0011_2 for the two, 0111_2 for three, and 1111_2 for four.

The least significant four bits indicate which of the Text Data blocks this text corresponds to. It is 0001_2 for the first, 0010_2 for the second, 0100_2 for the third, and 1000_2 for the fourth. Any received Control Byte is OR-ed together by the receiving station, and once the most significant and least significant four bits are the same, a complete message has been received.

It is up to the receiver to decide how to display this message. It may choose to wait for all of the Text Data to be received, or display the parts as they are received. It is not expected that the data in the text field changes during the course of a transmission.

GNSS Data Unlike Text and Extended Callsign Data, GNSS data is expected to be dynamic during the course of a transmission and to be transmitted quickly after the GNSS data becomes available. To stop the LSF/LICH data stream from being overrun with GNSS data relative to other data types, a throttle on the amount of GNSS data transmitted is needed. It is recommended that GNSS data be sent at an update rate no faster than once every five seconds.

The GNSS data fits within one 14-byte META field, which equates to six audio frames, and takes 240ms to transmit. This is a simple format of the GNSS data which does not require too much work to convert into, and provides enough flexibility for most cases. This has been tested on-air and successfully gated to APRS-IS, showing a location very close to the position reported by the GPS receiver.

GNSS Position Data stores the 112 bit META field as follows:

Size in bits	Format	Contents
8	unsigned integer	Data Source Used to modify the message added to the APRS message sent to APRS-IS 0x00 : M17 Client 0x01 : OpenRTX 0x02..0xFE : reserved 0xFF : other data source
8	unsigned integer	Station Type Translated into suitable APRS symbols when gated to APRS-IS 0x00 : Fixed Station 0x01 : Mobile Station 0x02 : Handheld
8	unsigned integer	Whole number absolute value of degrees latitude
16	unsigned integer	Decimal degrees of latitude multiplied by 65535, MSB first
8	unsigned integer	Whole number absolute value of degrees longitude
16	unsigned integer	Decimal degrees of longitude multiplied by 65535, MSB first
8	unsigned integer	Latitude N/S, Longitude E/W, Altitude, Speed and Bearing bit fields $xxxxxx0_2$ North Latitude $xxxxxx1_2$ South Latitude $xxxxxx0x_2$ East Longitude $xxxxxx1x_2$ West Longitude $xxxxx0xx_2$ Altitude data invalid $xxxxx1xx_2$ Altitude data valid $xxxx0xxx_2$ Speed and Bearing data invalid $xxxx1xxx_2$ Speed and Bearing data valid
16	unsigned integer	Altitude above sea level in feet + 1500 (if valid), MSB first
16	unsigned integer	Whole number of bearing in degrees between 0 and 360 (if valid), MSB first
8	unsigned integer	Whole number of speed in miles per hour (if valid)

Table 3.5: GNSS Data encoding

Extended Callsign Data This is only transmitted from repeaters/gateways and not from clients, who only receive and display this data. These fields should not appear over M17 Internet links as they should only be used over the air from a repeater/gateway.

The META field is split into two callsign fields. The first is always present, and the second is optional. The callsign data is encoded using the standard M17 callsign Address Encoding which takes six bytes to encode a nine character callsign. Any unused space in the META field contains 0x00 bytes. The first callsign field starts at offset zero in the META field, and the second callsign if present starts immediately after the first. There are two unused bytes at the end of the META field.

The use of these two callsign fields is as follows:

Source	Callsign Field 1	Callsign Field 2
Locally Repeated RF	Originator	Unused
ECHO Reply	Originator	Unused
Reflector Traffic	Originator	Reflector Name

Table 3.6: Extended Callsign Data encoding

The extended callsign data is not used under any other circumstances than the above currently. It is not expected that the data in the extra callsign fields change during the course of a transmission.

Scrambling Encryption type = 01₂

Scrambling is an encryption by bit inversion using a bitwise exclusive-or (XOR) operation between the bit sequence of data and a pseudorandom bit sequence.

Pseudorandom bit sequence is generated using a Fibonacci-topology Linear- Feedback Shift Register (LFSR). Three different LFSR sizes are available: 8, 16 and 24-bit. Each shift register has an associated polynomial. The polynomials are listed in Table 7. The LFSR is initialized with a seed value of the same length as the shift register. The seed value acts as an encryption key for the scrambler algorithm. Figures 16 to 18 show block diagrams of the algorithm.

Encryption subtype	LFSR polynomial	Seed length	Sequence period
00 ₂	$x^8 + x^6 + x^5 + x^4 + 1$	8 bits	255
01 ₂	$x^{16} + x^{15} + x^{13} + x^4 + 1$	16 bits	65,535
10 ₂	$x^{24} + x^{23} + x^{22} + x^{17} + 1$	24 bits	16,777,215

Table 3.7: Scrambling

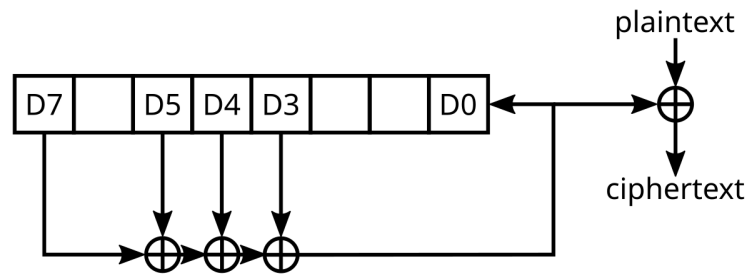


Figure 3.1: 8-bit LFSR taps

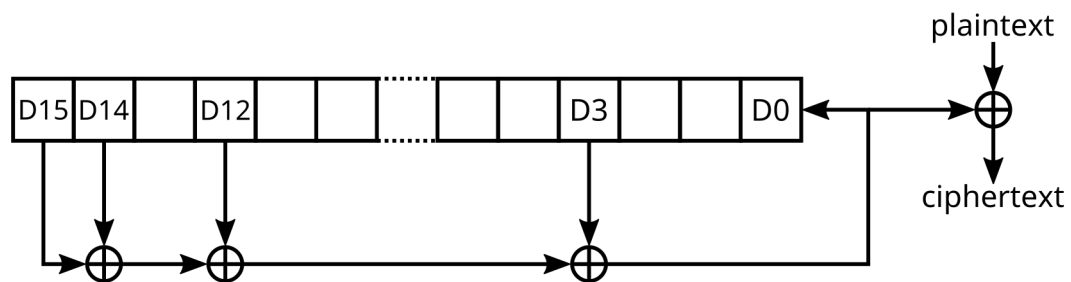


Figure 3.2: 16-bit LFSR taps

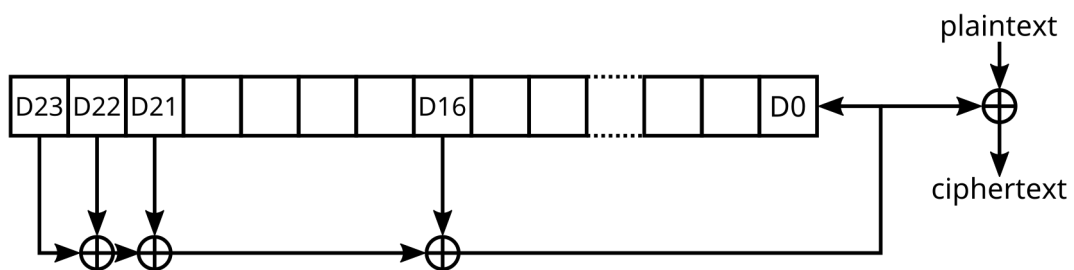


Figure 3.3: 24-bit LFSR taps

Advanced Encryption Standard (AES) Encryption type = 10_2

This method uses AES block cipher in counter mode (AES-CTR), with a 112-bit nonce that should never be used for more than one stream (transmission) and a 16-bit counter.

Key length is defined by the encryption subtype field.

Encryption subtype	Key length
00 ₂	128 bits
01 ₂	192 bits
10 ₂	256 bits
11 ₂	reserved

Table 3.8: AES key lengths

The 112-bit nonce value is stored in the META field. The FN (Frame Number) value is then used to fill out the remaining 16 bits of the counter, totalling to 128 bits, and always starts from 0 (zero) in a new voice stream.

NOTE The effective capacity of the frame counter is 15 bits, as its most significant bit is used for transmission end signalling. At 25 frames per second and 2^{15} frames, the transmission can last up to 2^{15} frames / 25 frames per second = 1310 seconds, or almost 22 minutes, without rolling over the counter.

The random part of the nonce value should be generated with a hardware random number generator or any other cryptographically secure method of generating random values.

To combat replay attacks, a 32-bit timestamp shall be embedded into the cryptographic nonce field. The field structure of the 128 bit counter is shown in Table 9. Timestamp is the number of seconds that elapsed since January 1, 2020, 00:00:00 UTC, minus leap seconds.

128 bit counter structure FN field sets the most significant 16 bits of the counter, with the 32-bit least significant part holding the timestamp. The remaining 80-bit portion is filled with random data, re-generated per transmission.

Timestamp	Random Data	FN
32	80	16

Table 3.9: AES counter

WARNING In CTR mode, AES encryption is malleable. That is, an attacker can change the contents of the encrypted message without decrypting it. This means that recipients of AES-encrypted data must not trust that the data is authentic. Users who require that received messages are proven to be exactly as-sent by the sender should use an appropriate digital signature algorithm, as described below.

3.1.3 Channel Access Number (CAN)

The Channel Access Number (CAN) is a four bit code that may be used to filter received audio, text, and GNSS data. A receiver may optionally allow reception from sources only if their transmitted CAN value matches the receiver's own specified CAN value.

3.1.4 Stream Frames

Stream Frames will contain chunked LSF contents (in the LICH field). The Stream Contents will include the incrementing 16-bit Frame Number, and 128 bits of data (unencrypted or encrypted).

3.1.5 Digital Signatures

M17 protocol provides a stream authentication method through Elliptic Curve Digital Signature Algorithm (ECDSA). The curve used is *secp256r1*. Signature availability is signalled with a specific bit in the TYPE field. Signature use reduces the maximum length of the stream by 4 frames.

Message Digest Algorithm for Voice Streams

At the beginning of the transmission, a *digest* byte array of size 16 is initialized with zeros. After every stream frame (starting at frame 0) an exclusive or (XOR) operation is performed over the contents of the *digest* array and the frame's payload. The *digest* array is then rotated left by 1 byte. The result shall be retained in the array.

$$\begin{aligned} \text{digest} &:= \text{digest} \oplus \text{payload} \\ \text{digest} &:= \text{rol}(\text{digest}, 8) \end{aligned}$$

This process is repeated until there is no more data to transmit. In case there is any encryption enabled, the *payload* input shall be the encrypted stream. This ensures the possibility of verification, even if the encryption details are not known to the receiving parties. Frame Numbers of the frames carrying the signature should follow a succession of $\{7FFC_{16}, 7FFD_{16}, 7FFE_{16}, 7FFF_{16}\}$.

NOTE The Frame Number's most significant bit of the last speech payload stream shall not be set, since it is not the last frame to be transmitted.

Signature Generation and Transmission

At the transmitter-side, the stream digest is signed with a 256-bit private key. The resulting 512-bit signature is split into 4 chunks and sent as additional payload at the end of the transmission. To keep the reassembled LSF data consistent, the LICH counter shall advance normally. The most significant bit of the Frame Number (signalling end of transmission) shall be set only in the last frame carrying the signature.

Signature Verification

At the receiver-side, the 512-bit signature is retrieved from the last 4 frames' contents, if the appropriate TYPE bit is set. The signature is then checked using a 512-bit public key.

NOTE The verification process will work if and only if all the data is received successfully (without transmission errors or dropped frames).

3.2 Packet Application

ATTENTION This is work in progress.

A single packet of up to 823 bytes of data may be sent in one transmission.

Packets are sent using Packet Mode.

A Stream Mode Transmission begins with an LSF.

Packets are composed of a 1..n byte data type specifier and up to $823 - n$ bytes of payload data. The data type specifier is encoded in the same way as UTF-8. It provides efficient coding of

common data types. And it can be extended to include a very large number of distinct packet data type codes.

The data type specifier can also be used as a protocol specifier. For example, the following protocol identifiers are reserved in the M17 packet spec:

Identifier	Protocol
0x00	RAW
0x01	AX.25
0x02	APRS
0x03	6LoWPAN
0x04	IPv4
0x05	SMS (null-terminated, UTF-8 encoded string)
0x06	Winlink

Table 3.10: Packet protocol identifiers

The data type specifier is used to compute the CRC, along with the payload.

Chapter 4

IP Networking

Digital modes are commonly networked together through linked repeaters using IP networking.

For commercial protocols like DMR, this is meant for linking metropolitan and state networks together and allows for easy interoperability between radio users. Amateur Radio uses this capability for creating global communications networks for all imaginable purposes, and makes ‘working the world’ with an HT possible.

M17 is designed with this use in mind, and has native IP framing to support it.

In competing radio protocols, a repeater or some other RF to IP bridge is required for linking, leading to the use of hotspots (tiny simplex RF bridges).

4.1 Standard IP Framing

M17 over IP is big endian, consistent with other IP protocols. We have standardized on UDP port 17000, this port is recommended but not required. Later specifications may require this port.

Field	Size	Description
MAGIC	32 bits	Magic bytes 0x4d313720 (“M17 ”)
StreamID (SID)	16 bits	Random bits, changed for each PTT or stream, but consistent from frame to frame within a stream
LICH	224 bits	The meaningful contents of a LICH frame (dst, src, streamtype, META field) as defined earlier.
FN	16 bits	Frame number (exactly as would be transmitted as an RF stream frame, including the last frame indicator at (FN & 0x8000))
Payload	128 bits	Payload (exactly as would be transmitted in an RF stream frame)
CRC16	16 bits	CRC for the entire packet, as defined earlier CRC definition

The CRC checksum must be recomputed after modification or re-assembly of the packet, such as when translating from RF to IP framing.

Appendix A

Address Encoding

M17 uses a 48-bit (6-byte) address to represent the characters that define a source and destination. M17 uses a 40-character alphabet. Encoded, up to nine characters can be used to encode a source or destination address that will still fit in a 48-bit address field. These nine characters will usually, but not necessarily be an amateur radio callsign.

In nearly all circumstances, the source address will decode to an amateur callsign. But frequently, the destination address will not decode to an amateur radio callsign. Typically it will be a unit command, like ECHO, or UNLINK, or the module of a reflector, like M17-M17 C.

In order to define how encoding and decoding are done, here are 40 characters used in M17 ordered by their value:

Value	Character	Name	ASCII	Note
0	' '	Space	0x20	Also, any invalid character
1 - 26	'A' - 'Z'	Letter	0x41 - 0x5A	Uppercase
27 - 36	'0' - '9'	Digit	0x30 - 0x39	Decimal
37	'-'	Hyphen	0x2D	Dash
38	'/'	Slash	0x3F	Forward slash
39	'.'	Dot	0x3E	Period

Table A.1: M17 Callsign Alphabet

A.1 Callsign Encoding

Here are some facts and rules about the encoding an address from a callsign:

- A callsign is encoded backwards, from the last character to the first character. This means that the first character of the callsign is in the least significant bits of the address, while the last character is encoded into the most significant bits of the address.
- Since the space character has a value of zero, trailing spaces will not affect the encoded value. For example the calculated address of 'ABC' is the same as 'ABC ', or 'ABC '.
- If an uncoded address represents an amateur radio callsign it should be left-justified. That means that the first character will always be a digit or letter.

- Over 262 trillion address can be encoded from 0x1 (A) to 0xEE6B27FFFFFF (.....) and only a fraction of these callsign actually look like an amateur radio callsign. Those encodable base-40 text strings that don't look like an amateur radio callsign can be used by applications for triggering events and features that their programs offer.
- A callsign consisting of only spaces is invalid, because it would have a corresponding address of zero. That address is defined to be invalid.
- Using this scheme, there are over 19 trillion 48-bit addresses that can't be encoded by nine characters. Only one of these non-encodable addresses ($2^{48} - 1$) has a specified use.
- After the base-40 value is calculated, the final 6-byte address is the big endian encoded representation of the base-40 value. This is also called network byte order.

As an example, the address of AB1CD would be calculated as:

$$('A': 1) + ('B': 2 \times 40) + ('1': 28 \times 40^2) + ('C': 3 \times 40^3) + ('D': 4 \times 40^4)$$

or, after refactoring and reordering:

$$((((4) \times 40 + 3) \times 40 + 28) \times 40 + 2) \times 40 + 1$$

producing the resulting address:

0x9fdd51 (base-16), 10476881 (base-10).

A.2 Encoded Addresses

Because 40^9 is less than 2^{48} , there are some 48-bit addresses that can't be accessed. Here is a map of the address space:

Address Range	Category	Number of Addresses	Remarks
0x000000000000	INVALID	1	Forbidden
0x000000000001 0xEE6B27FFFFFF	Codable	~262 trillion	"A" to "....."
0xEE6B28000000 0xFFFFFFFFFFFFE	Uncodable	~19 trillion	for application use
0xFFFFFFFFFFFFF	BROADCAST	1	valid only for a destination

Table A.2: M17 Addresses

The BROADCAST address should only be used in an RF transmission. As a destination address, it means that the RF stream is intended for any capable RF receivers.

The Uncodable addresses can be used by applications for their own purposes and encoding/decoding algorithms for these addresses are left to the developer.

For Codable addresses, the following encoding and decoding examples written in C will not treat the BROADCAST address. This is an implementation detail left to the developers.

A.3 Encoder Example

```

void Encode(const char *callsign, uint8_t *pUChar)
{
    uint64_t address = 0; // the calculate address in host byte order

    if (pUChar && callsign && *callsign) // make sure we can return a non-zero
        address
    {
        const char *p = callsign;

        // find the last char, but don't select more than 9 characters
        while (*p++ && (p-callsign < 9)) ;

        // process each char from the end to the beginning
        for (p--; p>=callsign; p--)
        {
            unsigned val = 0; // the default value of the character
            if ('A' <= *p && *p <= 'Z') val = *p - 'A' + 1;
            else if ('0' <= *p && *p <= '9') val = *p - '0' + 27;
            else if ('-' == *p) val = 37;
            else if ('/' == *p) val = 38;
            else if ('.' == *p) val = 39;
            else if ('a' <= *p && *p <= 'z') val = *p - 'a' + 1;

            address = 40u * address + val; // increment and add
        }
    }

    for (int i=5; i>=0; i--) // put it in network byte order
    {
        pUChar[i] = address & 0xffu;
        address /= 0x100u;
    }
}

```

A.4 Decoder Example

```
char *Decode(const uint8_t* pUChar)
{
    static char cs[10];    // this is the return value
    memset(cs, NULL, 10); // initialize it to nothing

    if (NULL == pUChar) // nothing in, nothing out
        return cs;

    // calculate the address in host byte order
    uint64_t address = 0;
    for (int i=0; i<6; i++)
        address = address * 0x100u + pUChar[i];

    if (address >= 0xee6b28000000u) // is it in the undecodable range?
        return cs; // practical applications will do something here

    // the M17 alphabet, ordered by value
    const char *m17chars = " ABCDEFGHIJKLMNOPQRSTUVWXYZ0123456789-./.";

    unsigned i = 0; // index for the current character

    while (address)
    {
        // the current character is the address modulus 40
        cs[i++] = m17chars[address % 40u];
        address /= 40u; // keep dividing the address until there's nothing left
    }

    return cs;
}
```

For an example of how to encode and decode BROADCAST, or how to use part of the Uncodable address space, see <https://github.com/M17-Project/libm17>.

Appendix B

Randomizer Sequence

Seq. number	Value	Seq. number	Value
00	0xD6	23	0x6E
01	0xB5	24	0x68
02	0xE2	25	0x2F
03	0x30	26	0x35
04	0x82	27	0xDA
05	0xFF	28	0x14
06	0x84	29	0xEA
07	0x62	30	0xCD
08	0xBA	31	0x76
09	0x4E	32	0x19
10	0x96	33	0x8D
11	0x90	34	0xD5
12	0xD8	35	0x80
13	0x98	36	0xD1
14	0xDD	37	0x33
15	0x5D	38	0x87
16	0x0C	39	0x13
17	0xC8	40	0x57
18	0x52	41	0x18
19	0x43	42	0x2D
20	0x91	43	0x29
21	0x1D	44	0x78
22	0xF8	45	0xC3

Table B.1: Randomizer values

Appendix C

Convolutional Encoder

The convolutional code shall encode the input bit sequence after appending 4 tail bits at the end of the sequence. Rate of the coder is $R=1/2$ with constraint length $K=5$. The encoder diagram and generating polynomials are shown below.

$$G_1(D) = 1 + D^3 + D^4$$
$$G_2(D) = 1 + D + D^2 + D^4$$

The output from the encoder must be read alternately.

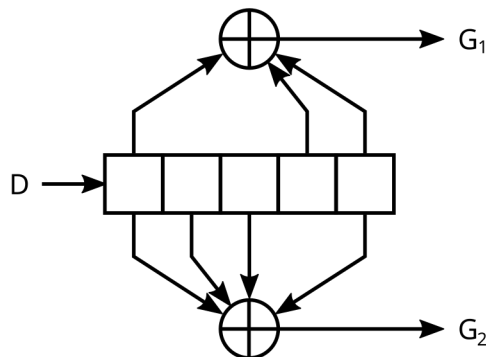


Figure C.1: Convolutional encoder

Appendix D

Golay Encoder

The extended Golay(24,12) encoder uses generating polynomial $g(x)$ given below to generate the 11 check bits. The check bits and an additional parity bit are appended to the 12 bit data, resulting in a 24 bit codeword. The resulting code is systematic, meaning that the input data (message) is embedded in the codeword.

$$g(x) = x^{11} + x^{10} + x^6 + x^5 + x^4 + x^2 + 1$$

This is equivalent to 0xC75 in hexadecimal notation. Both the generating matrix G and parity check matrix H are shown below.

$$G = [I_{12}|P] = \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 1 \\ 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & 0 & 1 & 1 & 0 & 0 & 1 & 1 & 1 \\ 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 1 & 1 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 1 & 1 & 1 \\ 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 & 1 & 0 \\ 1 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 & 1 & 1 \end{bmatrix} \quad (\text{D.1})$$

$$H = [P^T | I_{12}] = \begin{bmatrix} 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 1 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 1 \\ 1 & 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 1 \\ 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 0 & 1 & 1 \\ 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 & 1 & 0 \\ 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 1 & 1 & 1 & 1 \\ 1 & 1 & 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 & 1 \end{bmatrix} I_{12} \quad (D.2)$$

The output of the Golay encoder is shown in the table below.

Field	Data	Check bits	Parity
Position	23..12	11..1	0 (LSB)
Length	12	11	1

Table D.1: Golay encoder details

Four of these 24-bit blocks are used to reconstruct the LSF.

Sample MATLAB/Octave code snippet for generating G and H matrices is shown below.

```

1 P = hex2poly('0xC75');
2 [H,G] = cyclgen(23, P);
3
4 G_P = G(1:12, 1:11);
5 I_K = eye(12);
6 G = [I_K G_P P.'];
7 H = [transpose([G_P P.']) I_K];

```

Appendix E

Code Puncturing

Removing some of the bits from the convolutional coder's output is called code puncturing. The nominal coding rate of the encoder used in M17 is $\frac{1}{2}$. This means the encoder outputs two bits for every bit of the input data stream. To get other (higher) coding rates, a puncturing scheme has to be used.

Two different puncturing schemes are used in M17 stream mode:

1. P_1 leaving 46 from 61 encoded bits
2. P_2 leaving 11 from 12 encoded bits

Scheme P_1 is used for the *link setup frame*, taking 488 bits of encoded data and selecting 368 bits. The $\gcd(368, 488)$ is 8 which, when used to divide, leaves 46 and 61 bits. However, a full puncture pattern requires the puncturing matrix entries count to be divisible by the number of encoding polynomials. For this case a partial puncture matrix is used. It has 61 entries with 46 of them being ones and shall be used 8 times, repeatedly. The construction of the partial puncturing pattern P_1 is as follows:

$$M = \begin{bmatrix} 1 & 0 & 1 & 1 \end{bmatrix} \quad (\text{E.1})$$

$$P_1 = \begin{bmatrix} 1 & M_1 & \dots & M_{15} \end{bmatrix} \quad (\text{E.2})$$

In which M is a standard $2/3$ rate puncture matrix and is used 15 times, along with a leading 1 to form P_1 , an array of length 61.

The first pass of the partial puncturer discards G_1 bits only, second pass discards G_2 , third - G_1 again, and so on. This ensures that both bits are punctured out evenly.

Scheme P_2 is for frames (excluding LICH chunks, which are coded differently). This takes 296 encoded bits and selects 272 of them. Every 12th bit is being punctured out, leaving 272 bits. The full matrix shall have 12 entries with 11 being ones.

The puncturing scheme P_2 is defined by its partial puncturing matrix:

$$P_2 = \begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 1 & 1 & 0 \end{bmatrix} \quad (\text{E.3})$$

The linearized representations are:

P1 = [1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1,
 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1,
 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1, 1, 0, 1, 1]

P2 = [1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 0]

One additional puncturing scheme P_3 is used in the packet mode. The puncturing scheme is defined by its puncturing matrix:

$$P_3 = \begin{bmatrix} 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 0 \end{bmatrix} \quad (\text{E.4})$$

The linearized representation is:

P3 = [1, 1, 1, 1, 1, 1, 1, 0]

Appendix F

Interleaving

For interleaving a Quadratic Permutation Polynomial (QPP) is used. The polynomial

$$\pi(x) = (45x + 92x^2) \bmod 368$$

is used for a 368 bit interleaving pattern QPP.

input index	output index	input index	output index	input index	output index	input index	output index
0	0	92	92	184	184	276	276
1	137	93	229	185	321	277	45
2	90	94	182	186	274	278	366
3	227	95	319	187	43	279	135
4	180	96	272	188	364	280	88
5	317	97	41	189	133	281	225
6	270	98	362	190	86	282	178
7	39	99	131	191	223	283	315
8	360	100	84	192	176	284	268
9	129	101	221	193	313	285	37
10	82	102	174	194	266	286	358
11	219	103	311	195	35	287	127
12	172	104	264	196	356	288	80
13	309	105	33	197	125	289	217
14	262	106	354	198	78	290	170
15	31	107	123	199	215	291	307
16	352	108	76	200	168	292	260
17	121	109	213	201	305	293	29
18	74	110	166	202	258	294	350

input index	output index	input index	output index	input index	output index	input index	output index
19	211	111	303	203	27	295	119
20	164	112	256	204	348	296	72
21	301	113	25	205	117	297	209
22	254	114	346	206	70	298	162
23	23	115	115	207	207	299	299
24	344	116	68	208	160	300	252
25	113	117	205	209	297	301	21
26	66	118	158	210	250	302	342
27	203	119	295	211	19	303	111
28	156	120	248	212	340	304	64
29	293	121	17	213	109	305	201
30	246	122	338	214	62	306	154
31	15	123	107	215	199	307	291
32	336	124	60	216	152	308	244
33	105	125	197	217	289	309	13
34	58	126	150	218	242	310	334
35	195	127	287	219	11	311	103
36	148	128	240	220	332	312	56
37	285	129	9	221	101	313	193
38	238	130	330	222	54	314	146
39	7	131	99	223	191	315	283
40	328	132	52	224	144	316	236
41	97	133	189	225	281	317	5
42	50	134	142	226	234	318	326
43	187	135	279	227	3	319	95
44	140	136	232	228	324	320	48
45	277	137	1	229	93	321	185
46	230	138	322	230	46	322	138
47	367	139	91	231	183	323	275
48	320	140	44	232	136	324	228
49	89	141	181	233	273	325	365
50	42	142	134	234	226	326	318

input index	output index	input index	output index	input index	output index	input index	output index
51	179	143	271	235	363	327	87
52	132	144	224	236	316	328	40
53	269	145	361	237	85	329	177
54	222	146	314	238	38	330	130
55	359	147	83	239	175	331	267
56	312	148	36	240	128	332	220
57	81	149	173	241	265	333	357
58	34	150	126	242	218	334	310
59	171	151	263	243	355	335	79
60	124	152	216	244	308	336	32
61	261	153	353	245	77	337	169
62	214	154	306	246	30	338	122
63	351	155	75	247	167	339	259
64	304	156	28	248	120	340	212
65	73	157	165	249	257	341	349
66	26	158	118	250	210	342	302
67	163	159	255	251	347	343	71
68	116	160	208	252	300	344	24
69	253	161	345	253	69	345	161
70	206	162	298	254	22	346	114
71	343	163	67	255	159	347	251
72	296	164	20	256	112	348	204
73	65	165	157	257	249	349	341
74	18	166	110	258	202	350	294
75	155	167	247	259	339	351	63
76	108	168	200	260	292	352	16
77	245	169	337	261	61	353	153
78	198	170	290	262	14	354	106
79	335	171	59	263	151	355	243
80	288	172	12	264	104	356	196
81	57	173	149	265	241	357	333
82	10	174	102	266	194	358	286

input index	output index	input index	output index	input index	output index	input index	output index
83	147	175	239	267	331	359	55
84	100	176	192	268	284	360	8
85	237	177	329	269	53	361	145
86	190	178	282	270	6	362	98
87	327	179	51	271	143	363	235
88	280	180	4	272	96	364	188
89	49	181	141	273	233	365	325
90	2	182	94	274	186	366	278
91	139	183	231	275	323	367	47

F.1 References

- Trifina Lucian, Tarniceriu Daniela, Munteanu Valeriu. “Improved QPP Interleavers for LTE Standard.” ISSCS 2011 - International Symposium on Signals, Circuits and Systems (2011)

Appendix G

BERT Details

G.1 PRBS Generation

The PRBS uses the ITU standard PRBS9 polynomial: $x^9 + x^5 + 1$

This is the traditional form for a linear feedback shift register (LFSR) used to generate a pseudorandom binary sequence.

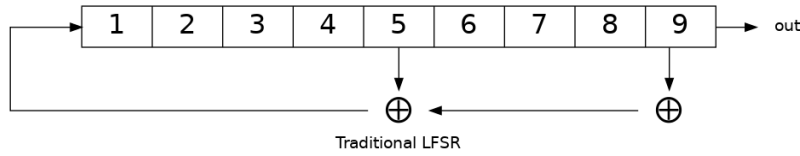


Figure G.1: Traditional form LFSR

However, the M17 LFSR is a slightly different. The M17 PRBS9 uses the generated bit as the output bit rather than the high-bit before the shift.

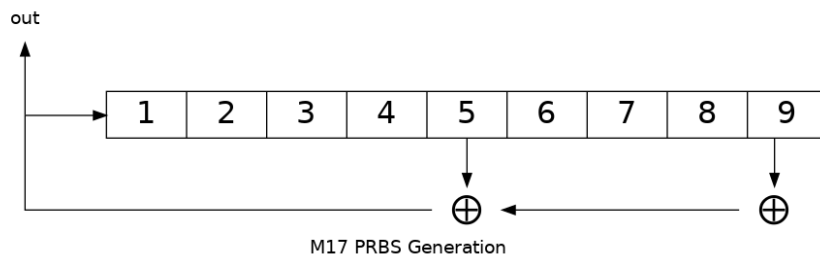


Figure G.2: M17 LFSR

This will result in the same sequence, just shifted by nine bits.

$$M17_PRBS_n = PRBS9_{n+8}$$

The reason for this is that it allows for easier synchronization. This is equivalent to a multiplicative scrambler (a self-synchronizing scrambler) fed with a stream of 0s.

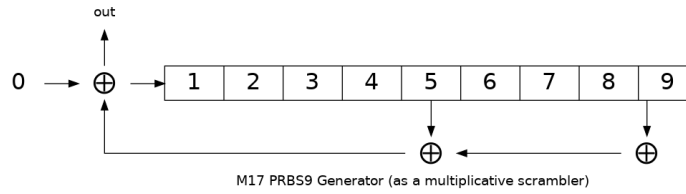


Figure G.3: M17 PRBS9 Generator

```

1  class PRBS9 {
2      static constexpr uint16_t MASK = 0x1FF;
3      static constexpr uint8_t TAP_1 = 8;           // Bit 9
4      static constexpr uint8_t TAP_2 = 4;           // Bit 5
5
6      uint16_t state = 1;
7
8      public:
9      bool generate()
10     {
11         bool result = ((state >> TAP_1) ^ (state >> TAP_2)) & 1;
12         state = ((state << 1) | result) & MASK;
13         return result;
14     }
15     ...
16 };

```

The PRBS9 SHOULD be initialized with a state of 1.

G.2 PRBS Receiver

The receiver detects the frame is a BERT Frame based on the Sync Burst received. If the PRBS9 generator is reset at this point, the sender and receiver should be synchronized at the start. This, however, is not common nor is it required. PRBS generators can be self-synchronizing.

G.2.1 Synchronization

The receiver will synchronize the PRBS by first XORing the received bit with the LFSR taps. If the result of the XOR is a 1, it is an error (the expected feedback bit and the input do not match) and the sync count is reset. The received bit is then also shifted into the LFSR state register. Once a sequence of eighteen (18) consecutive good bits are recovered (twice the length of the LFSR), the stream is considered synchronized.

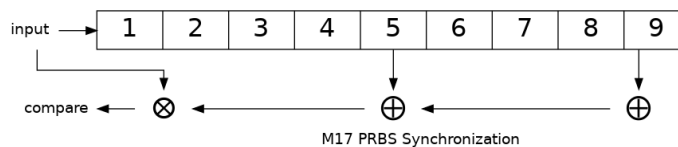


Figure G.4: M17 PRBS9 Synchronization

During synchronization, bits received and bit errors are not counted towards the overall bit error rate.

```

1  class PRBS9 {
2      ...
3      static constexpr uint8_t LOCK_COUNT = 18;    // 18 consecutive good bits.

```

```

4  ...
5  // PRBS Synchronizer. Returns 0 if the bit matches the PRBS, otherwise 1.
6  // When synchronizing the LFSR used in the PRBS, a single bad input bit
7  // will result in 3 error bits being emitted, one for each tap in the LFSR.
8  bool synchronize(bool bit)
9  {
10     bool result = (bit ^ (state >> TAP_1) ^ (state >> TAP_2)) & 1;
11     state = ((state << 1) | bit) & MASK;
12     if (result) {
13         sync_count = 0; // error
14     } else {
15         if (++sync_count == LOCK_COUNT) {
16             synced = true;
17             ...
18         }
19     }
20     return result;
21 }
22 ...
23 };

```

G.2.2 Counting Bit Errors

After synchronization, BERT mode switches to error-counting mode, where the received bits are compared to a free-running PRBS9 generator. Each bit that does not match the output of the free-running LFSR is counted as a bit error.

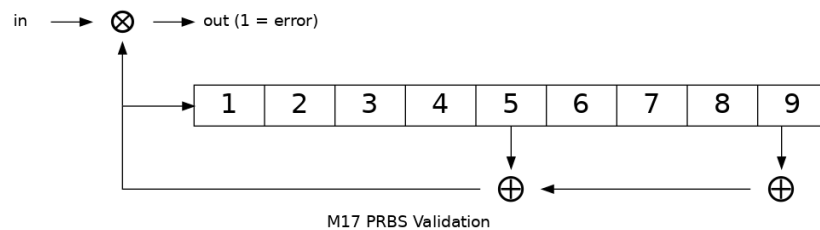


Figure G.5: M17 PRBS9 Validation

```

1  class PRBS9 {
2  ...
3  // PRBS validator. Returns 0 if the bit matches the PRBS, otherwise 1.
4  // The results are only valid when sync() returns true;
5  bool validate(bool bit)
6  {
7      bool result;
8      if (!synced) {
9          result = synchronize(bit);
10     } else {
11         // PRBS is now free-running.
12         result = bit ^ generate();
13         count_errors(result);
14     }
15     return result;
16 }
17 ...
18 };

```

G.2.3 Resynchronization

The receiver must keep track of the number of bit errors over a period of 128 bits. If more than 18 bit errors occur, the synchronization process starts anew. This is necessary in the case of missed frames or other serious synchronization issues.

Bits received and errors which occur during resynchronization are not counted towards the bit error rate.

G.3 References

- ITU O.150 : Digital test patterns for performance measurements on digital transmission equipment
- PRBS (according ITU-T O.150) and Bit-Sequence Tester : VHDL-Modules

Appendix H

File Formats

This appendix documents the file formats used for testing various M17 layers.

H.1 Glossary

H.1.1 Bit numbering, Bit order, Most significant bit (MSB), Least significant bit (LSB)

Bit numbering is how bit positions are identified in a binary number. The least significant bit (LSB) is the bit position representing a value of 1. The most significant bit (MSB) is the bit position representing the highest value position. Bit order refers to the order in which bits are extracted from a binary number. This is important especially when sending binary values one bit at a time, or when constructing multiple-bit symbols. LSB first means the extraction happens from the least significant position first. MSB first means extraction happens from the most significant position first.

H.1.2 Deviation, Frequency Deviation

In this context, deviation how far from the center frequency a carrier is shifted. This can be positive or negative. For M17, the frequency deviation of the four symbols are shown in Physical Layer Table 1.

H.1.3 Deviation Function (Transmit)

A function used to convert symbol values to frequency deviation in RF hardware. This can be used to set hardware registers, create voltages, etc. depending on the hardware used.

H.1.4 Deviation Function (Receive)

A function used to convert frequency deviation in RF hardware to symbol values. This can be used when reading hardware registers, sampling voltages, etc. depending on the hardware used.

H.1.5 Dibit

Two bits used to represent a symbol, as shown in Physical Layer Table 1.

H.1.6 Endianness, Byte order, Big-endian (BE), Little-endian (LE)

Endianness is the order of the bytes in a word of digital data. In this document, we will refer to big-endian (BE) and little-endian (LE). BE means that the most significant byte of a word is at the lowest memory location, while LE means that the least significant byte is at the lowest memory location.

H.1.7 RF Sample Rate

The rate at which deviation values are updated. This will vary depending on the hardware. M17 test software commonly uses 48000 samples per second.

H.1.8 Root-raised-cosine (RRC) Filter

A filter used to in digital communications to help reduce intersymbol interference. The M17 Physical Layer specifies a root-raised-cosine (RRC) filter with $\alpha = 0.5$ Root Raised Cosine

H.1.9 Symbol

An M17 Physical Layer symbol of +3, +1, -1, and -3.

H.1.10 Symbol Rate

The rate at which new symbols are generated. For M17, this is 4800 symbols per second.

H.2 File Extensions

Multiple files are used when testing the different elements of the M17 protocol. File extensions (the three characters after a period in a complete file name) are defined to standardize formats and usage.

Extension	Description	Data Format	Data Rate
aud	Mono audio	Signed 16-bit LE	8000 samples per second
sym	M17 symbols	Signed 8-bit	4800 symbols per second
bin	Packed M17 Dibits	MSB first, Unsigned 8-bit	4800 symbols per second (1200 bytes per second)
rrc	RRC filtered and Scaled M17 symbols	Signed 16-bit LE	48000 samples per second
dev	Deviation values	Varies	Varies

Table H.1: File extensions

H.2.1 aud

Mono audio of signed 16-bit LE at a rate of 8000 samples per second. This is often referred to as a “raw” audio file and contains no embedded header information.

H.2.2 sym

M17 symbols (+3, +1, -1, -3) encoded as signed 8-bit values at rate of 4800 symbols per second.

H.2.3 bin

M17 symbols packed 2 bits per symbol (dibits), 4 symbols per byte (+3 = 01, +1 = 00, -1 = 10, -3 = 11) with the MSB first. These are unsigned 8-bit values at 4800 symbols per second, which is 4 symbols per byte at 1200 bytes per second.

H.2.4 rrc

RRC filtered and scaled M17 symbols. In order to generate a reasonable RRC waveform, the symbol rate (4800 symbols per second) is upsampled by a factor of 10 to an RRC sample rate of 48000 samples per second. Then the upsampled symbols are passed through the RRC filter. The output samples of the RRC filter are multiplied by 7168 to fit within a signed 16-bit LE representation (e.g. a +3 value would be +21504).

H.2.5 dev

Hardware specific deviation values. These would be obtained by passing RRC filtered values through a deviation function. Since these are device specific, it is recommended to use an underscore plus device type as part of the filename. For example, the Semtech SX1276 uses a deviation step size of 61 Hz per bit. An M17 1600 Hz frequency step is equivalent to an SX1276 deviation value change of 26. Since the SX1276 only accepts positive deviation steps, the deviation function for the SX1276 would be $(\text{rrc value} + 3.0) \times 13$. The .dev file specific for the SX1276 would contain those values, and could have a name such as m17test_sx1276.dev

H.3 Example file flows

These show the file types in order of processing for transmit and receive flows. Each “->” symbolizes processing required to move from one file type to the next.

H.3.1 Transmit

aud -> sym -> rrc -> dev

aud -> bin -> rrc -> dev

H.3.2 Receive

dev -> rrc -> sym -> aud

dev -> rrc -> bin -> aud

H.4 To-Do

File formats for packet and voice + data streams.

H.5 References

Bit numbering

Endianness

Root Raised Cosine

Appendix I

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