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ALTERA MAX II EPM240 CPLD Core Board USB Blaster FPGA Programmer For EPM240T100C5N JTAG PLD Development Kit With JTAG Wire

1 order

US \$28.90 ~~US \$90.30~~ -68%

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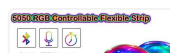


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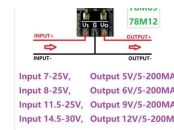
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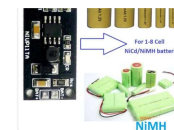
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★ 4.8

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US \$3.12

★ 4.6

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OVERVIEW

CUSTOMER REVIEWS (0)

SPECIFICATIONS

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Product Name:

ALTERA MAX II EPM240 CPLD Core Board & USB Blaster FPGA Programmer for EPM240T100C5N JTAG PLD
Development kit with JTAG wire
Module No : RT294

Packing List:

1 PCS EPM240 core board

1 PCS Altera USB Blaster #with USB wire and 10 pin JTAG wire#



US \$5.50

★ 4.8

316 Sold



US \$2.78

★ 5.0

11 Sold

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Product Description:

EPM240 Board Description:

Onboard EPM240T100C5N chip

50Mhz Active crystal

All pins from CPLD are Labelled and available onboard

Altera Official standards JTAG Interface

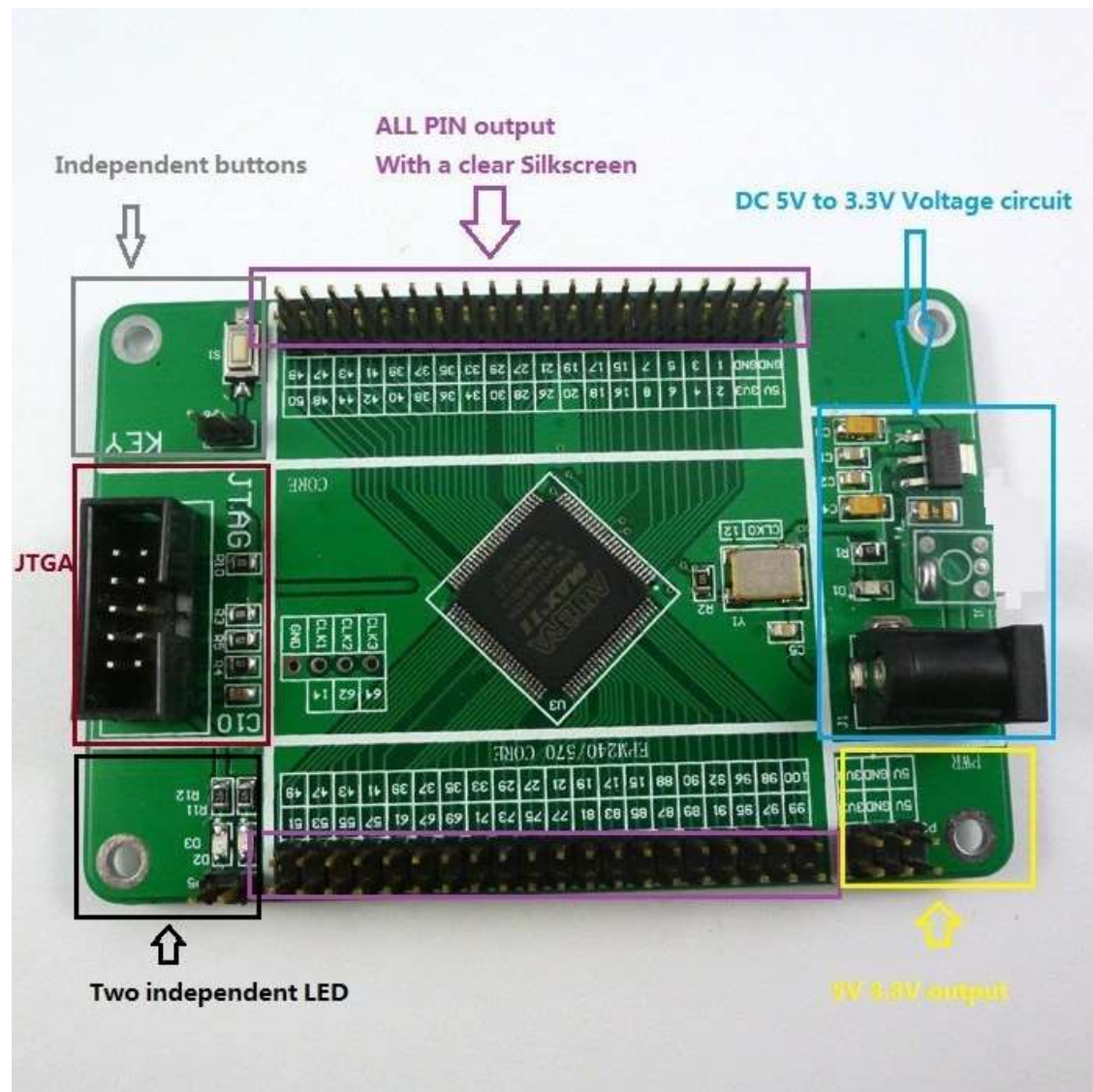
Two 5V 3#3V output to provide power for the peripheral

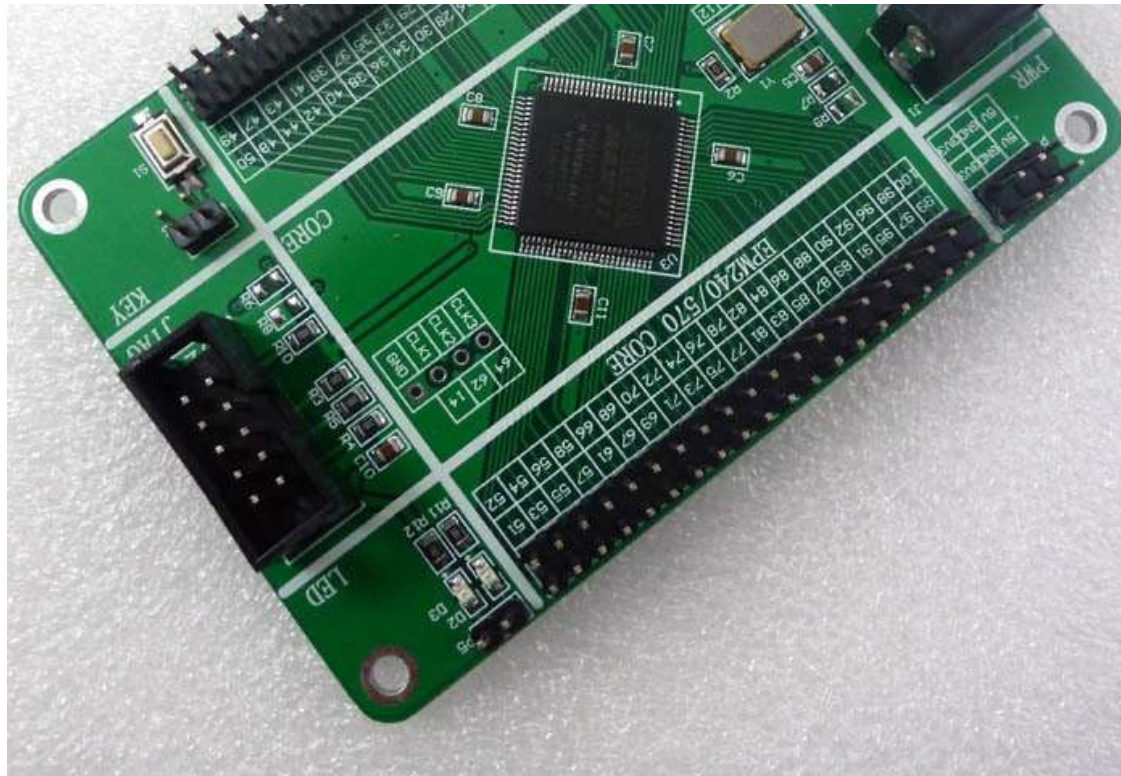
A Independent button for test core board

Two independent LED lights for test core board

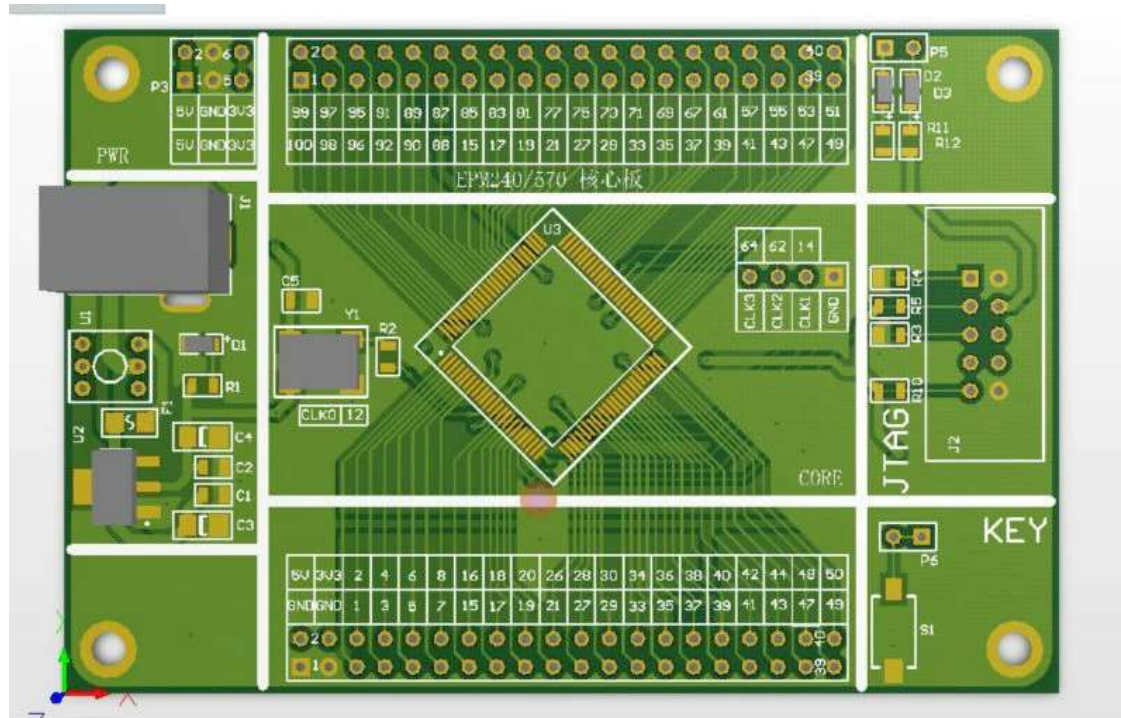
Size : 9cm X 6cm #9:6 is Golden ratio ^_^#

Diagram : Please contact me on ebay message for diagram after your order !





PCB Board 3D View:



Altera USB Blaster:







Feature :

1. Stably support SignalTap II embedded logic analyzer function ,no mistake while crawling data;
2. Fully support all Altera products such as :
 CPLD: MAX3000, MAX7000, MAX9000 and MAXII
 FPGA: Stratix, StratixII, StratixIII, Cyclone, CycloneII, CycloneIII, ACEX1K, APEX20K and FLEX10K etc#
 Active serial configuration device: EPCS1, EPCS4, EPCS16, EPCS64 etc#
 Enhanced configuration devices: EPC1, EPC4#
3. Supports 3 download mode: AS, PS and JTAG;
4. Support and Nios II embedded soft core processor communication and debugging, Rev.C latest version Hardware. Support jtag_uart;
5. Speed: 6 times faster than parallel port download cable ByteBlasterII;
6. Easy to use: MiniUSB interface, easy connection, two status indicator makes debugging more comfortable.
7. Fully compatible with ALTERA USB Blaster, functionality and performance are the same as ALTERA original download cable.
- 8: Voltage : 1.5~5V JTAG IO Voltage .

System configuration:

1, Windows XP, Windows Vista, Windows7 ,USB interface;

2, Quartus II version 4.0 or newer version;

This programmer uses standard 10-Pin ICD10 interface,
the Pin assignment is shown as followed:

10-Pin interface #the triangle mark on cable "" means 1st pin#:

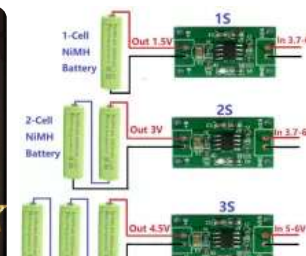
ByteBlaster II Female Plug Signal Names and Programming Modes						
Pin	AS Mode		PS Mode		JTAG Mode	
	Signal Name	Description	Signal Name	Description	Signal Name	Description
1	DCLK	Clock signal	DCLK	Clock signal	TCK	Clock signal
2	GND	Signal ground	GND	Signal ground	GND	Signal ground
3	CONF_DONE	Configuration done	CONF_DONE	Configuration done	TDO	Data from device
4	VCC (TRGT)	Target power supply	VCC (TRGT)	Target power supply	VCC (TRGT)	Target power supply
5	nCONFIG	Configuration control	nCONFIG	Configuration control	TMS	JTAG state machine control
6	nCE	Oyclone chip enable	—	No connect	—	No connect
7	DATAOUT	Active serial data out	nSTATUS	Configuration status	—	No connect
8	nCS	Serial configuration device chip select	—	No connect	—	No connect
9	ASDI	Active serial data in	DATA0	Data to device	TDI	Data to device
10	GND	Signal ground	GND	Signal ground	GND	Signal ground

No Feedback.

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8 Sold



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US \$13.21

5 Sold



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US \$5.79

3 key



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US \$2.57

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Free Shipping

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2 Sold

2A multi-cell synchronous
Buck Li-Ion battery charger



Power supply
voltage DC 5-23V

Charging voltage : 4.2V
8.4V
12.6V

Charging current : 2A
1A
0.5A

2A Multi-Cell Synchronous B...
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US \$5.56

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