High Temperature (150°C) Ultra-Stable X8R Dielectric, 25VDC-100VDC (Commercial & Automotive Grade)

Overview

KEMET's Ultra-Stable X8R dielectric features a 150°C maximum operating temperature, offering the latest in high temperature dielectric technology and reliability for extreme temperature applications. It offers the same temperature capability as conventional X8R, but without the capacitance loss due to applied DC voltage. Ultra-Stable X8R exhibits no change in capacitance with respect to voltage and boasts a minimal change in

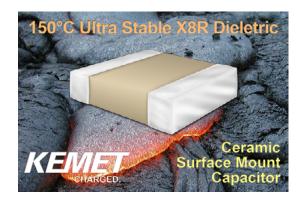
capacitance with reference to ambient temperature. It is a suitable replacement for higher capacitance and larger footprint devices that fail to offer capacitance stability. Capacitance change with respect to temperature is limited to $\pm 15\%$ from -55° C to $+150^{\circ}$ C.

Benefits

- -55°C to +150°C operating temperature range
- · Pb-Free and RoHS compliant
- EIA 0402, 0603, 0805, 1206, 1210 and 1812 case sizes
- DC voltage ratings of 25V, 50V and 100V
- Capacitance offerings ranging from 10pF to 0.22µF
- Available capacitance tolerances of ±1%, ±2%, ±5%, ±10% and ±20%
- · Extremely low ESR and ESL
- · High thermal stability
- High ripple current capability
- No capacitance change with respect to applied rated DC voltage
- · Non-polar device, minimizing installation concerns
- · Offered in both commercial and automotive grades
- 100% pure matte tin-plated termination finish that allowing for excellent solderability.
- SnPb plated termination finish option available upon request (5% min)

Applications

Typical applications include decoupling, bypass and filtering in extreme environments such as down-hole oil exploration, underhood automotive, military and aerospace.



Ordering Information

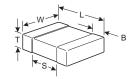
С	1210	С	184	K	3	Н	А	С	AUTO
Ceramic	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance	Voltage	Dielectric	Failure Rate/ Design	Termination Finish ¹	Packaging/Grade (C-Spec) ²
	0402 0603 0805 1206 1210 1812	C = Standard	2 Sig. Digits + Number of Zeros	F = ±1% G = ±2% J = ±5% K = ±10% M = ±20%	3 = 25V 5 = 50V 1 = 100V	H = Ultra Stable X8R	A = N/A	C = 100% Matte Sn L = SnPb (5% min)	Blank = Bulk TU = 7" Reel Unmarked AUTO = Automotive Grade 7"Reel Unmarked

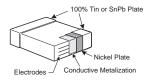
¹ Additional termination finish options may be available. Contact KEMET for details.

² Additional reeling or packaging options may be available. Contact KEMET for details.



Dimensions – Millimeters (Inches)





EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Min.	Mounting Technique
0402	1005	1.00 (.040) ± 0.05 (.002)	$0.50 (.020) \pm 0.05 (.002)$		0.30 (.012) ± 0.10 (.004)	0.30 (.012)	Solder Reflow Only
0603	1608	1.60 (.063) ± 0.15 (.006)	0.80 (.032) ± 0.15 (.006)	for	0.35 (.014) ± 0.15 (.006)	0.70 (.028)	0 11 14
0805	2012	2.00 (.079) ± 0.20 (.008)	1.25 (.049) ± 0.20 (.008)	ole 2	0.50 (0.02) ± 0.25 (.010)	0.75 (.030)	Solder Wave or Solder Reflow
1206	3216	3.20 (.126) ± 0.20 (.008)	1.60 (.063) ± 0.20 (.008)	See Table 2 fi Thickness	0.50 (0.02) ± 0.25 (.010)		Coldel Reliew
1210	3225	3.20 (.126) ± 0.20 (.008)	2.50 (.098) ± 0.20 (.008)	See	0.50 (0.02) ± 0.25 (.010)	N/A	Solder Reflow Only
1812	4532	4.50 (.177) ± 0.30 (.012)	3.20 (.126) ± 0.30 (.012)		0.60 (.024) ± 0.35 (.014)		Soluel Reliow Only

Qualification/Certification

Commercial grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance and Reliability.

Automotive grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC-Q200, please visit their website @www.aecouncil.com.

Environmental Compliance

Pb-Free and RoHS compliant (excluding SnPb termination finish option)

Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range:	-55°C to +150°C
Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC):	±15%
Aging Rate (Max % Cap Loss/Decade Hour):	0%
Dielectric Withstanding Voltage:	250% of rated voltage (5 ± 1 seconds and charge/discharge not exceeding 50mA)
Dissipation Factor (DF) Maximum Limits @ 25°C:	2.5%
Insulation Resistance (IR) Limit @ 25°C:	1000 megohm microfarads or $100G\Omega$ (Rated voltage applied for 120 ± 5 secs @ 25° C)

To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to $G\Omega$ limit. Select the lower of the two limits.

Capacitance and Dissipation Factor (DF) measured under the following conditions:

1MHz ± 100kHz and 1.0 ± 0.2 Vrms if capacitance ≤1000pF.

 $1kHz \pm 50Hz$ and 1.0 ± 0.2 Vrms if capacitance >1000pF

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 & Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON".



Table 1 – (0402 - 1812 Case Sizes)

			S	erie	S			C0402	2	(C0603	3		C080	5		C1206	6		C1210)	C18	812
Cap	Cap		Vol	tage C	ode		3	5	1	3	5	1	3	5	1	3	5	1	3	5	1	5	1
Сар	Code		Vo	ltage l	DC		25	50	100	25	50	100	25	50	100	25	50	100	25	50	100	50	100
			Сар	Tolera	ance		Pr	oduct	Availa	ability	and (Chip T	hickn	ess C	odes S	See Ta	ble 2	for Ch	ip Thi	icknes	s Dim	ensio	ns
100 110	101 111	F F	G G	J	K K	M M	BB BB	BB BB	BB BB														
120	121	F	G	J	K	M	BB	BB	BB														
130	131	F	G	J	K	M	BB	BB	BB														
150 160	151 161	F	G	J	K	M	BB BB	BB BB	BB BB														
180	181	F	G	J	K	M	ВВ	ВВ	ВВ														
200 220	201 221	F	G	J	K	M M	BB BB	BB BB	BB BB														
240	241	F	G	J	K	М	BB	BB	BB														
270 300	271 301	F	G	J	K K	M M	BB BB	BB BB	BB BB														
330	331	F	G	J	K	M	BB	BB	BB														
360 390	361	F F	G G	J	K K	M M	BB BB	BB BB	BB BB														
430	391 431	F	G	J	K	M	BB	BB	BB	СВ	СВ	СВ											
470	471	F	G	J	K	M	BB	BB	BB	СВ	СВ	СВ											
510 560	511 561	F	G	J	K	M M	BB BB	BB BB	BB BB	CB CB	CB CB	CB CB											
620	621	F	G	J	K	М	BB	BB	BB	СВ	СВ	СВ											
680 750	681 751	F	G G	J	K	M M	BB BB	BB BB	BB BB	CB CB	CB CB	CB CB											
820	821	F	G	J	K	M	BB	BB	BB	CB	CB	CB											
910 1,000	911 102	F	G G	J	K K	M M	BB BB	BB BB	BB BB	CB CB	CB CB	CB CB											
1,100	112	F	G	J	K	M	BB	BB	DD	СВ	CB	CB											
1,200	122	F F	G	J	K	M	BB	BB		CB	CB	CB											
1,300 1,500	132 152	F	G	J	K	M M	BB BB	BB BB		CB CB	CB CB	CB CB											
1,600	162	F	G	J	K	М				СВ	СВ	СВ											
1,800 2,000	182 202	F	G	J	K	M M				CB CB	CB CB	CB CB											
2,200	222	F	G	J	K	М				СВ	СВ	СВ	DC	DC	DC								
2,400 2,700	242 272	F	G	J	K	M M				CB CB	CB CB	CB CB	DC DC	DC DC	DC DC								
3,000	302	F	G	J	K	M				СВ	СВ	СВ	DC	DC	DC								
3,300 3,600	332 362	F	G G	J	K K	M M				CB CB	CB CB	CB CB	DC DC	DC DC	DC DC								
3,900	392	F	G	J	K	M				CB	CB	CB	DC	DC	DC								
4,300	432	F	G	J	K	M				CB	CB	CB	DC	DC	DC								
4,700 5,100	472 512	F F	G G	J	K K	M M				CB CB	CB CB	СВ	DC DC	DC DC	DC DC								
5,600	562	F	G	J	K	M				СВ	СВ		DC	DC	DC								
6,200 6,800	622 682	F F	G G	J	K K	M M				CB CB	CB CB		DC DC	DC DC	DC DC	EB	EB	EB					
7,500	752	F	G	J	K	М				СВ			DC	DC	DC	EB	EB	EB					
8,200 9,100	822 912	F F	G G	J	K	M M				CB CB			DC DC	DC DC	DC DC	EB EB	EB EB	EB EB					
10,000	103	F	G	J	K	M				CB			DC	DC	DD	EB	EB	EB					
12,000 15,000	123 153	F	G	J	K	M							DC DC	DC DD	DE DG	EB EB	EB EB	EB EB	FB FB	FB FB	FB FB	GB	GB
18,000	183	F	G	J	K	M							DC	DD	50	EB	EB	EB	FB	FB	FB	GB	GB
22,000 27,000	223 273	F F	G G	J	K	M							DD DF	DF		EB EB	EB EB	EC EE	FB FB	FB FB	FB FB	GB GB	GB GB
33,000	333	F	G	J	K K	M M							DF			EB	EB	EE	FB	FB	FB	GB	GB
47,000	473	F F	G	J	K	M										EC	EE	EH	FB	FB	FE	GB	GB
56,000	563	F	G	ltage l	K K	M	25	20	100	25	20	100	25	20	100	25 E	EF 02	90 EH	55 EB	FB 0 5	FF Q	GB 05	GB Q
Сар	Сар			tage C			3	بن 5	1	3	5	1	3	5	1	3	5	1	3	5	1	<u>د</u> 5	1
Cap	Code			tage C Serie				³ C0402	L	!	C0603	L	!	C0805		-	C1206			C1210			812
<u></u>				er ie	ა			CU4U2		l	C0003	,		C0005			C 1206	'		U1210	,	L 018	012



Table 1 – (0402 - 1812 Case Sizes) con't

			S	erie	S			C040	2		C0603	3		C080	5		C1206	6		C1210)	C18	812
Cap	Cap		Vol	tage C	ode		3	5	1	3	5	1	3	5	1	3	5	1	3	5	1	5	1
Сар	Code		Vo	ltage	DC		25	20	100	25	20	100	25	20	100	25	20	100	25	20	100	20	100
			Сар	Tolera	ance		Pr	oduct	Avail	ability	and (Chip T	hickn	ess C	odes (See Ta	ble 2	for Ch	ip Thi	cknes	s Dim	ensio	ns
68,000	683	F	G	J	K	M										EF	EH		FB	FC	FG	GB	GB
82,000	823	F	G	J	K	M										EH	EH		FC	FF	FH	GB	GB
100,000	104	F	G	J	K	M										EH			FE	FG	FM	GB	GD
120,000	124	F	G	J	K	M													FG	FH		GB	GH
150,000	154	F	G	J	K	M													FH	FM		GD	GN
180,000	184	F	G	J	K	M													FJ			GH	
220,000	224	F	G	J	K	M	İ						İ									GK	
			Vo	ltage	DC		25	20	100	25	20	100	25	20	100	25	20	100	25	20	100	20	100
Сар	Cap Code		Vol	tage C	ode		3	5	1	3	5	1	3	5	1	3	5	1	3	5	1	5	1
			Series			C0402	2		C0603	3		C0805	5		C1206	;		C1210)	C1	812		



<u>Table 2 – Chip Thickness / Packaging Quantities</u>

Thickness	Chip	Thickness ±	Qty per Reel	Qty per Reel	Qty per Reel	Qty per Reel	Qty per Bulk
Code	Size	Range (mm)	7" Plastic	13" Plastic	7" Paper	13" Paper	Cassette
AA AB	01005 0201	0.20 ± 0.02 0.30 ± 0.03			15000 15000		
BB CB	0402 0603	0.50 ± 0.05 0.80 ± 0.07			10000 4000	50000 10000	50000 15000
CC CD	0603	0.80 ± 0.10			4000 4000 4000	10000 10000 10000	15000
DC CD	0603 0805	0.80 ± 0.15 0.78 ± 0.10			4000 4000	10000	
DD DL	0805 0805	0.90 ± 0.10 0.95 ± 0.10	4000	10000	4000	10000	
DE	0805	1.00 ± 0.10	2500	10000			
DF DG	0805 0805	1.10 ± 0.10 1.25 ± 0.15	2500 2500	10000 10000			
DH EB	0805 1206	1.25 ± 0.20	2500 4000	10000 10000	4000	10000	
EK	1206	0.78 ± 0.10 0.80 ± 0.10	2000	8000	4000	10000	
EC EN	1206 1206	0.90 ± 0.10 0.95 ± 0.10	4000 4000	10000 10000			
ED EE	1206	1.00 ± 0.10	2500	10000 10000			
EF	1206 1206	1.10 ± 0.10 1.20 ± 0.15	2500 2500	10000			
EM EG	1206 1206	1.25 ± 0.15 1.60 ± 0.15	2500 2000	10000 8000			
EH	1206	1.60 ± 0.20	2000	8000			
EJ FB	1206 1210	1.70 ± 0.20 0.78 ± 0.10	2000 4000	8000 10000			
FC FD	1210 1210	0.90 ± 0.10 0.95 ± 0.10	4000 4000	10000 10000	Package Qu	antity	
FE FE	1210	1.00 ± 0.10	2500	10000	_		
FF FG	1210 1210	1.10 ± 0.10 1.25 ± 0.15	2500 2500	10000 10000	Based on Fig		
FL FO	1210 1210	1.40 ± 0.15 1.50 ± 0.20	2000 2000	8000 8000	Thickness S	pecifications	
FH	1210	1.55 ± 0.15	2000	8000			
FP FM	1210 1210	1.60 ± 0.20 1.70 ± 0.20	2000 2000	8000 8000			
FJ FN	1210 1210	1.85 ± 0.20 1.85 ± 0.20	2000 2000	8000 8000			
FT	1210	1.90 ± 0.20	1500	4000			
FK FR	1210 1210	2.10 ± 0.20 2.25 ± 0.20	2000 2000	8000 8000			
FS PA	1210 1220	2.50 ± 0.20 0.80 ± 0.10	1000 4000	4000 10000			
MA	1632	0.80 ± 0.10	4000	10000			
NA NA	1706 1706	0.90 ± 0.10 0.90 ± 0.10	4000 4000	10000 10000			
LD LA	1808 1808	0.90 ± 0.10 1.40 ± 0.15	2500 1000	10000 4000			
LB	1808	1.60 ± 0.15	1000	4000			
LC GB	1808 1812	2.00 ± 0.15 1.00 ± 0.10	1000 1000	4000 4000			
GC GD	1812 1812	1.10 ± 0.10 1.25 ± 0.15	1000 1000	4000 4000			
GE	1812	1.30 ± 0.10	1000	4000			
GH GF	1812 1812	1.40 ± 0.15 1.50 ± 0.10	1000 1000	4000 4000			
GG GK	1812 1812	1.55 ± 0.10 1.60 ± 0.20	1000 1000	4000 4000			
GJ GN	1812 1812	1.70 ± 0.15 1.70 ± 0.20	1000 1000	4000 4000			
GL	1812	1.90 ± 0.20	1000	4000			
GM GO	1812 1812	2.00 ± 0.20 2.50 ± 0.20	1000 500	4000 2000			
HB HC	1825 1825	1.10 ± 0.15 1.15 ± 0.15	1000 1000	4000 4000			
HD	1825	1.30 ± 0.15	1000	4000			
HE HF	1825 1825	1.40 ± 0.15 1.50 ± 0.15	1000 1000	4000 4000			
HG JB	1825 2220	1.60 ± 0.20 1.00 ± 0.15	1000 1000	4000 4000			
JC	2220	1.10 ± 0.15	1000	4000			
JD JE	2220 2220	1.30 ± 0.15 1.40 ± 0.15	1000 1000	4000 4000			
JF JP	2220 2220	1.50 ± 0.15 1.60 ± 0.20	1000 1000	4000 4000			
JG	2220	1.70 ± 0.15	1000	4000			
JH JO	2220 2220	1.80 ± 0.15 2.40 ± 0.15	1000 500	4000 2000			
KB KC	2225 2225	1.00 ± 0.15 1.10 ± 0.15	1000 1000	4000 4000			
KD	2225	1.30 ± 0.15	1000	4000			
KE KF	2225 2225	1.40 ± 0.15 1.60 ± 0.20	1000 1000	4000 4000			



Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

Recommended Soldering Profile:

• KEMET recommends following the guidelines outlined in IPC/JEDEC J-STD-020

Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC-7351

EIA Size Code	Metric Size Code	I	Density Level A: Maximum (Most) Land Protrusion (mm)					Media	sity Lev an (Nor rotrusio)	Density Level C: Minimum (Least) Land Protrusion (mm)					
Oode	Ouc	С	Υ	X	V1	V2	С	Υ	X	V1	V2	С	Υ	X	V1	V2	
01005	0402	0.33	0.46	0.43	1.60	0.90	0.28	0.36	0.33	1.30	0.70	0.23	0.26	0.23	1.00	0.50	
0201	0603	0.38	0.56	0.52	1.80	1.00	0.33	0.46	0.42	1.50	0.80	0.28	0.36	0.32	1.20	0.60	
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80	
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20	
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70	
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00	
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90	
1808	4520	2.30	1.75	2.30	7.40	3.30	2.20	1.55	2.20	6.50	2.70	2.10	1.35	2.10	5.80	2.40	
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70	
1825	4564	2.15	1.60	6.90	6.90	7.90	2.05	1.40	6.80	6.00	7.30	1.95	1.20	6.70	5.30	7.00	
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60	
2225	5664	2.70	1.70	6.90	8.10	7.90	2.60	1.50	6.80	7.20	7.30	2.50	1.30	6.70	6.50	7.00	

Density Level A: For low-density Product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC-7351).

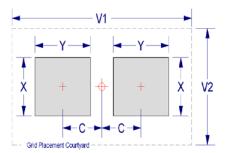




Table 4 – Performance & Reliability: Test Methods and Conditions

Stress	Reference	Test or Inspection Method
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8kg for 60 seconds.
Board Flex	JIS-C-6429	Appendix 2, Note: 2mm (min) for all except 3mm for C0G.
		Magnification 50 X. Conditions:
Oaldanah iita	LOTE OOO	a) Method B, 4 hrs @ 155°C, dry heat @ 235°C
Solderability	J-STD-002	b) Method B @ 215°C category 3
		c) Method D, category 3 @ 260°C
Temperature Cycling	JESD22 Method JA-104	1000 cycles (-55°C to +150°C), Measurement at 24 hrs. +/- 2 hrs after test conclusion.
B: 111 : 171	MIL OTD COOM II 1400	Load Humidity: 1000 hours 85°C/85%RH and Rated Voltage. Add 100K ohm resistor. Measurement at 24 hrs. +/- 2 hrs after test conclusion.
Biased Humidity	MIL-STD-202 Method 103	Low Volt Humidity: 1000 hours 85°C/85%RH and 1.5V. Add 100K ohm resistor. Measurement at 24 hrs. +/- 2 hrs after test conclusion.
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a & 7b not required. Unpowered. Measurement at 24 hrs. +/- 2 hrs after test conclusion.
Thermal Shock	MIL-STD-202 Method 107	-55°C/+150. Note: Number of cycles required-300, maximum transfer time-20 seconds, dwell time-15 minutes. Air-Air.
High Temperature Life	MIL-STD-202 Method 108	1000 hours at 150°C with 1.5 X rated voltage applied.
Storage Life	MIL-STD-202 Method 108	150°C, 0VDC, for 1000 hours.
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.
Resistance to Solvents	MIL-STD-202 Method 215	Add Aqueous wash chemical - OKEM Clean or equivalent.

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature – reels may soften or warp, and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40 degrees C, and maximum storage humidity not exceed 70% relative humidity. In addition, temperature fluctuations should be minimized to avoid condensation on the parts, and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability, chip stock should be used promptly, preferably within 1.5 years of receipt.



Tape & Reel Packaging Information

KEMET offers Multilayer Ceramic Chip Capacitors packaged in 8mm, 12mm and 16mm tape on 7" and 13" reels in accordance with EIA standard 481. This packaging system is compatible with all tape fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

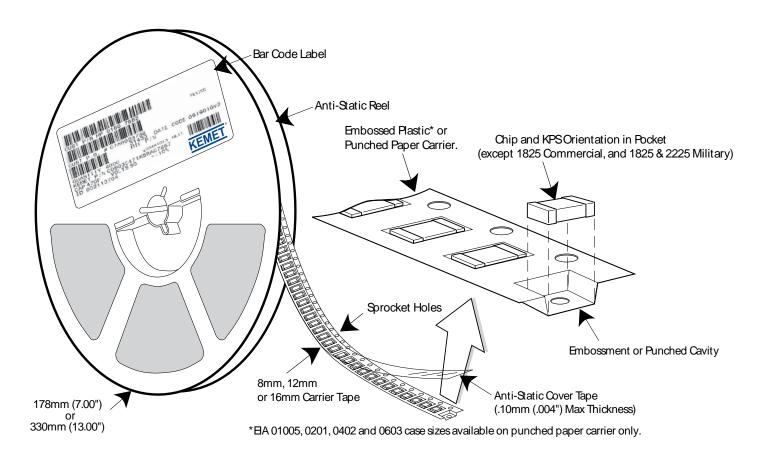


Table 5 – Carrier Tape Configuration (mm)

EIA Case Size	Tape size (W)*	Pitch (P ₁)*
01005 - 0402	8	2
0603 - 1210	8	4
1805 - 1808	12	4
≥ 1812	12	8
KPS 1210	12	8
KPS 1812 & 2220	16	12
Array 0508 & 0612	8	4

^{*}Refer to Figure 1 for W and P, carrier tape reference locations.

^{*}Refer to Table 6 for tolerance specifications.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

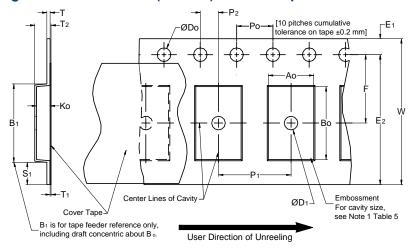


Table 6 - Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

			Constant Dime	ensions — Milli	meters (Inche	es)			
Tape Size	D ₀	D₁ Min. Note 1	E ₁	P ₀	P ₂	R Ref. Note 2	S₁ Min. Note 3	T Max.	T ₁ Max.
8mm		1.0 (0.039)				25.0 (0.984)			
12mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ± 0.10 (0.069 ± 0.004)	4.0 ± 0.10 (0.157 ± 0.004)	2.0 ± 0.05 (0.079 ± 0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16mm		(0.059)				(1.181)			
			Variable Dime	nsions — Milli	meters (Inche	s)			
Tape Size	Pitch	B₁ Max. Note 4	E ₂ Min.	F	P ₁	T ₂ Max	W Max	A ₀ ,B	. & K ₀
8mm	Single (4mm)	4.35 (0.171)	6.25 (0.246)	3.5 ± 0.05 (0.138 \pm 0.002)	4.0 ± 0.10 (0.157 ± 0.004)	2.5 (0.098)	8.3 (0.327)		
12mm	Single (4mm) & Double (8mm)	8.2 (0.323)	10.25 (0.404)	5.5 ± 0.05 (0.217 ± 0.002)	8.0 ± 0.10 (0.315 ± 0.004)	4.6 (0.181)	12.3 (0.484)	No	te 5
16mm	Triple (12mm)	12.1 (0.476)	14.25 (0.561)	5.5 ± 0.05 (0.217 ± 0.002)	8.0 ± 0.10 (0.315 ± 0.004)	4.6 (0.181)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape with or without components shall pass around R without damage (see Figure 5).
- 3. If S,<1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Document 481 paragraph 4.3 (b)).
- 4. B1 dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by A_{o} , B_{o} and K_{o} shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12mm tapes and 10° maximum for 16mm tapes (see Figure 3).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8mm and 12mm wide tape and to 1.0mm maximum for 16mm tape (see Figure 4).
 - (e) for KPS Series product A_0 and B_0 are measured on a plane 0.3mm above the bottom of the pocket.
 - (f) see Addendum in EIA Document 481 for standards relating to more precise taping requirements.



Figure 2 – Punched (Paper) Carrier Tape Dimensions

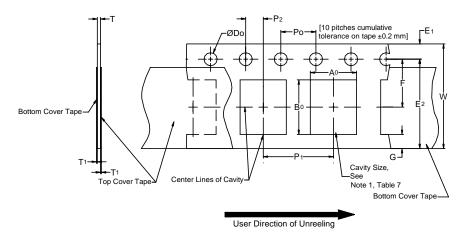


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

		Constant [Dimensions — N	lillimeters (Inche	es)		
Tape Size	D ₀	E ₁	P ₀	P ₂	T₁Max	G Min	R Ref. Note 2
8mm	1.5 +0.10-0.0 (0.059 +0.004, -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (.004) Max.	0.75 (.030)	25 (.984)
		Variable D	imensions — M	illimeters (Inche	s)		
Tape Size	Pitch	E2 Min	F	P ₁	T Max	W Max	A_0B_0
8mm	Half (2mm)	6.25	3.5 ± 0.05	2.0 ± 0.05 (0.079 ± 0.002)	1.1	8.3 (0.327)	Note 5
8mm	Single (4mm)	(0.246)	(0.138 ± 0.002)	4.0 ± 0.10 (0.157 ± 0.004)	(0.098)	8.3 (0.327)	Note 5

- 1. The cavity defined by $A_{or} B_{o}$ and T shall surround the component with sufficient clearance that:
 - a) the component does not protrude beyond either surface of the carrier tape.
 - b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).
 - e) see Addendum in EIA Document 481 for standards relating to more precise taping requirements.
- 2. The tape with or without components shall pass around R without damage (see Figure 5).



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 Kg Minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8mm	0.1 Newton to 1.0 Newton (10gf to 100gf)
12mm & 16mm	0.1 Newton to 1.3 Newton (10gf to 130gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300±10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA-556 and EIA-624.

Figure 3 – Maximum Component Rotation

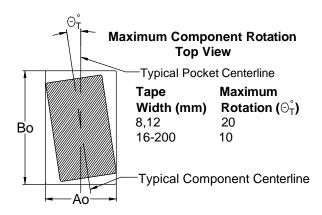


Figure 4 – Maximum Lateral Movement

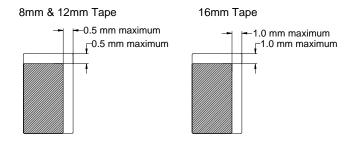


Figure 5 – Bending Radius

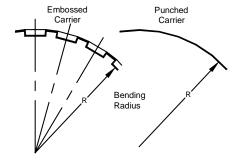
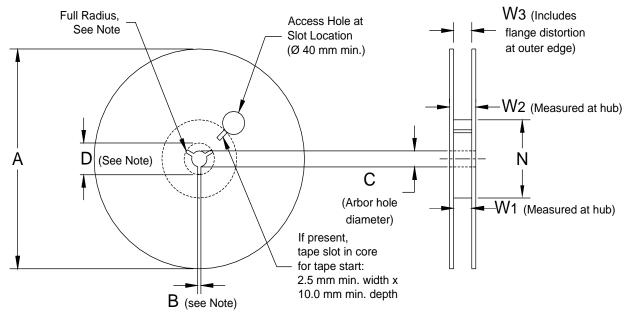




Figure 6 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 - Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)						
Tape Size	A	B Min	С	D Min		
8mm	178 ± 0.20					
12mm	(7.008 ± 0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)		
16mm	330 ± 0.20 (13.000 ± 0.008)					
	Variable Dimensions — Millimeters (Inches)					
Tape Size	N Min	W ₁	W ₂ Max	W_3		
8mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)			
12mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference		
16mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)			



Figure 7 – Tape Leader & Trailer Dimensions

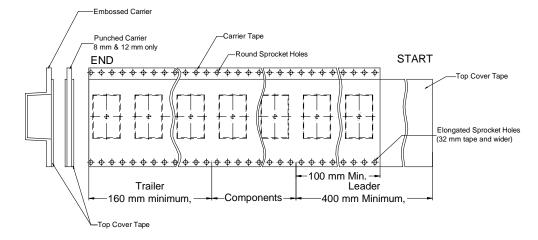


Figure 8 - Maximum Camber

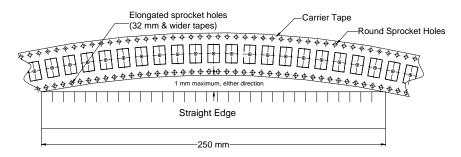




Figure 9 – Bulk Cassette Packaging (Ceramic Chips Only)

Meets Dimensional Requirements IEC-286 and EIAJ 7201

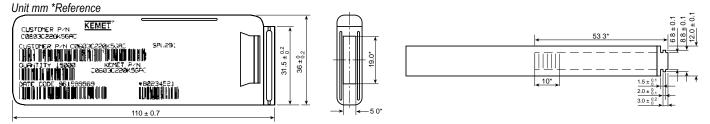


Table 9 - Capacitor Dimensions for Bulk Cassette

Cassette Packaging - Millimeters

EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation minimum	T Thickness	Number of Pcs/Cassette
0402	1005	1.0 ± 0.05	0.5 ± 0.05	0.2 to 0.4	0.3	$0.5 \pm .05$	50,000
0603	1608	1.6 ± 0.07	0.8 ± 0.07	0.2 to 0.5	0.7	0.8 ± .07	15,000

Table 10 – Capacitor Marking

Laser marking is available as an extra-cost option for most KEMET ceramic chips. Such marking is two sided, and includes a K to identify KEMET, followed by two characters (per EIA-198) to identify the capacitance value. Note that marking is not available for any Y5V chip. In addition, the 0603 marking option is limited to the K only. (Marking Optional – Not Available for 0402 Size)

·				<u> </u>				<u> </u>	
Numeral Alpha	Capacitance (pF) For Various Numeral Identifiers								
Character	9	0	1	2	3	4	5	6	7
А	0.1	1	10	100	1000	10000	100000	1000000	10000000
В	0.11	1.1	11	110	1100	11000	110000	1100000	11000000
С	0.12	1.2	12	120	1200	12000	120000	1200000	12000000
D	0.13	1.3	13	130	1300	13000	130000	1300000	13000000
Е	0.15	1.5	15	150	1500	15000	150000	1500000	15000000
F	0.16	1.6	16	160	1600	16000	160000	1600000	16000000
G	0.18	1.8	18	180	1800	18000	180000	1800000	18000000
Н	0.2	2	20	200	2000	20000	200000	2000000	20000000
J	0.22	2.2	22	220	2200	22000	220000	2200000	22000000
K	0.24	2.4	24	240	2400	24000	240000	2400000	24000000
L	0.27	2.7	27	270	2700	27000	270000	2700000	27000000
М	0.3	3	30	300	3000	30000	300000	3000000	30000000
N	0.33	3.3	33	330	3300	33000	330000	3300000	33000000
Р	0.36	3.6	36	360	3600	36000	360000	3600000	36000000
Q	0.39	3.9	39	390	3900	39000	390000	3900000	39000000
R	0.43	4.3	43	430	4300	43000	430000	4300000	43000000
S	0.47	4.7	47	470	4700	47000	470000	4700000	47000000
T	0.51	5.1	51	510	5100	51000	510000	5100000	51000000
U	0.56	5.6	56	560	5600	56000	560000	5600000	56000000
V	0.62	6.2	62	620	6200	62000	620000	6200000	62000000
W	0.68	6.8	68	680	6800	68000	680000	6800000	68000000
Х	0.75	7.5	75	750	7500	75000	750000	7500000	75000000
Υ	0.82	8.2	82	820	8200	82000	820000	8200000	82000000
Z	0.91	9.1	91	910	9100	91000	910000	9100000	91000000
а	0.25	2.5	25	250	2500	25000	250000	2500000	25000000
b	0.35	3.5	35	350	3500	35000	350000	3500000	35000000
d	0.4	4	40	400	4000	40000	400000	4000000	40000000
е	0.45	4.5	45	450	4500	45000	450000	4500000	45000000
f	0.5	5	50	500	5000	50000	500000	5000000	50000000
m	0.6	6	60	600	6000	60000	600000	6000000	60000000
n	0.7	7	70	700	7000	70000	700000	7000000	70000000
t	0.8	8	80	800	8000	80000	800000	8000000	80000000
у	0.9	9	90	900	9000	90000	900000	9000000	90000000



Example shown is 1,000 pF capacitor



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