Parameter	HMC5883L	QMC5883L
I2C addr	Write addr: 0x3c Read addr: 0x3d	If SI pin tie to VDD: Write addr: 0x1a Read addr: 0x1b If SI pin tie to END: Write addr: 0x18 Read addr: 0x19
Read X/Y/Z Register	0x03,0x04/0x07,0x08/0x05,0x06	0x00~0x05
Output data rate	0x00 bit4,bit3,bit2	0x09 bit3,bit2 00 : 10Hz 01 : 50Hz 02 : 100Hz 03 : 200Hz
Ranges	0x01 bit7,bit6,bit5	0x09 bit5,bit4 00 : 2G 01 : 8G
Mode Register	0x02 bit1,bit0 00 : continuous mode 01 : single mode	0x09 bit1,bit0 00 : standby 01 : continuous
Ready Bit	/	0x06 bit0
Set/Reset Period	HW	0x0B
Over Sample Ratio	0x00 bit6,bit5	0x09 bit7,bit6
Temperature output	No	0x07~0x08

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Need Modify

1) I2c addr： write 0x1a，read 0x1b

2) Init_Config:

0x0B = 0x01;

0x20 = 0x40;

0x21 = 0x01;

0x09 = 0x0d/0x1d; /*OSR=512, RNG=+/-2G or +/-8G,
ODR=200Hz, MODE= continuous*/

3) Read output data register: 0x00~0x05

4) Output register: First byte is LSB, second byte is MSB, different before.

5) Data ready register:

0x06[bit0] DRDY , "0": no new data; "1": have new data;

6) Output data sensitivity, Follow to the Range param :

If Range = +/- 2G , 12000 LSB/G

If Range = +/- 8G , 3000 LSB/G